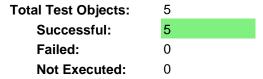
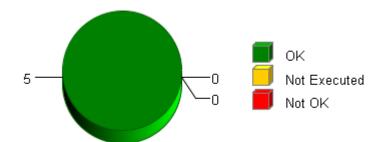


### **Summary**

## **Overall Test Object Results (including Coverage)**



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## **Selected Project Items**

Test Object "CBD\_UnitTest/AstLmt/AstLmt\_Init"

Test Object "CBD\_UnitTest/AstLmt/AstLmt\_Per1"

Test Object "CBD\_UnitTest/AstLmt/AstLmt\_Scom\_GetSteeringAssistDefeat"

Test Object "CBD\_UnitTest/AstLmt/AstLmt\_Scom\_ManualTrqCmd"

Test Object "CBD\_UnitTest/AstLmt/AstLmt\_Scom\_SetSteeringAssistDefeat"

### **Used Test Environments**

TI TMS 570 PLS UDE (Default)

### **Batch Operation Settings**

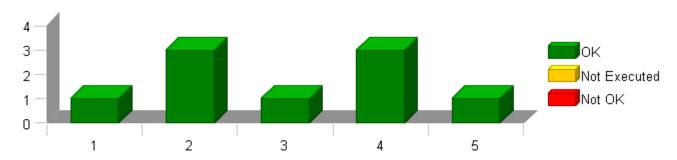
Check Interface: No
Generate Driver: Yes
Execute Test: Yes
Create New Test Run: No

**Instrumentation:** Test Object Only

Coverage: Statement Coverage, Branch Coverage, Decision Coverage, Modified Condition /

Decision Coverage, Multiple Condition Coverage

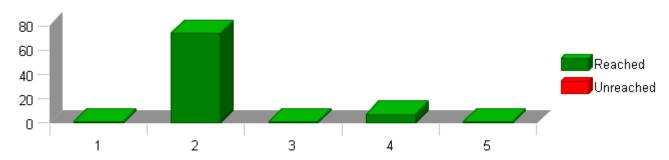
### Test Case Results for Each Test Object (without Coverage)



The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

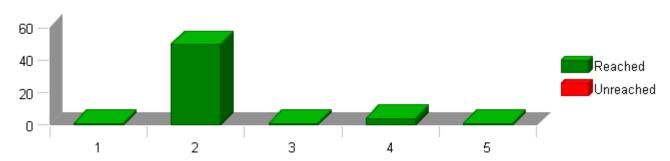


## Statement (C0) Coverage: Total Statements for Each Test Object



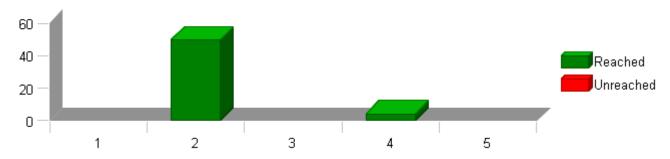
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

### Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

## **Decision Coverage: Total Decision Outcomes for Each Test Object**

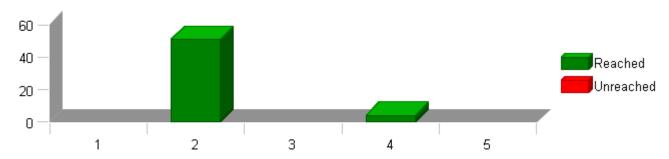


The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.



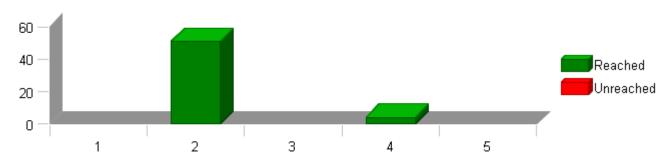
## MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

## MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

## **TEST OVERVIEW REPORT**

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Project AstLmt\_CM

## **Test Object List**

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	DC	MC/DC	МСС	Test Cases Re	esult
	AstLmt_CM	100 %	100 %	100 %	100 %	100 %	9 of 9 passed	•
	CBD_UnitTest	100 %	100 %	100 %	100 %	100 %	9 of 9 passed	~
	AstLmt	100 %	100 %	100 %	100 %	100 %	9 of 9 passed	~
1	AstLmt_Init	100 %	100 %	-	-	-	1 of 1 passed	•
2	AstLmt Per1	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	•
3	AstLmt Scom GetSteeringAssistDefeat	100 %	100 %	-	-	-	1 of 1 passed	•
4	AstLmt Scom ManualTrqCmd	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	•
5	AstLmt Scom SetSteeringAssistDefeat	100 %	100 %	-	-	-	1 of 1 passed	•

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AstLmt\_Scom\_SetSteeringAssistDefeat

 Project
 AstLmt\_CM

 Module
 AstLmt

Test Object AstLmt\_Scom\_SetSteeringAssistDefeat

### Instrumentation: Test Object Only

Statement (C0) Coverage 100 %
Branch (C1) Coverage 100 %

### **Statistics**

Total Testcases	1	
Successful	1	✓
Failed	0	
Not Executed	0	

### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\AstLmt_CM	
Configuration File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml	
Target Environment	TI TMS 570 PLS UDE (Default)	
Kind of Test	Unit Test	
Linker Options		
Source File(s)		
File	\$(PROJECTROOT)\AstLmt_CM\src\Ap_AstLmt.c	
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AstLmt_CM\utp\contract -I\$(PROJECTROOT)\AstLmt_CM\utp\contract\Ap_AstLmt -I\$ (PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler \tms470\include	

Comments/Descrip	otion/Specification
Name	Text
Module 'AstLmt'	UNIT TEST INFORMATION************************************
	Name of Tester:Ajit Shrivastava Code File(s) Under Test:Ap_AstLmt.c Code File(s) Version:9 Module Design Document:Assist_Sum_Limit_CurrentMode_MDD.docx Module Design Document Version:13 Data Dictionary Version:9 Unit Test Plan Version:6 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):942 Total RAM Used (Bytes):8 Special Test Requirements: Test Date:09-05-2014 Comments:"NOTE 1: Inline functions defined in ""GlobalMacro.h"" are not Unit Tested. NOTE 2 : ""CBD_Sandbox_dbg.map"" map file is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj</pre>
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	<pre>\$(ProgramFiles)\pls\UDE 3.2</pre>
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	<pre>\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg</pre>

# TEST DETAILS REPORT AstLmt\_Scom\_SetSteeringAssistDefeat

Workspace File

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Attributes
Name Value

D:\Synergy\_Work\_Area\AstLmt\_CM\UnitTestEnv\config\UDE\_TMS570\_DEBUG.WSP



### Test Case 1: Boundary Test

Specification Performance Metrics:

(With "None" Instrumentation and WithPS Environment)

CPU Cycles:

TS1.1 241.00 Cycles TS1.2 241.00 Cycles

**Description** Vector Description:

TS1.1 SteeringAsstDefeat1\_Cnt\_lgc = True TS1.2 SteeringAsstDefeat1\_Cnt\_lgc = False

Test Step 1.1 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Rte_Inst_Ap_AstLmt	target_Rte_Inst_Ap_AstLmt		
SteeringAsstDefeat_Cnt_lgc	1		
target_Rte_Inst_Ap_AstLmt.Pim_SteerAsstDefeat	target_Pim_SteerAsstDefeat		
Name	Actual Value	Expected Value	Result
target_Pim_SteerAsstDefeat	1	1	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte Call Ap AstLmt SteeringAsstDefeat WriteBlock	1	Rte Call Ap AstLmt SteeringAsstDefeat WriteBlock	1	~

Test Step 1.2 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Rte_Inst_Ap_AstLmt	target_Rte_Inst_Ap_AstLmt		
SteeringAsstDefeat_Cnt_lgc	0		
target_Rte_Inst_Ap_AstLmt.Pim_SteerAsstDefeat	target_Pim_SteerAsstDefeat		
Name	Actual Value	Expected Value	Result
target Pim SteerAsstDefeat	0	0	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_AstLmt_SteeringAsstDefeat_WriteBlock	1	Rte_Call_Ap_AstLmt_SteeringAsstDefeat_WriteBlock	1	~

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AstLmt\_Per1

Project	AstLmt_CM
Module	AstLmt
Test Object	AstLmt_Per1

### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

### **Statistics**

Total Testcases	3	
Successful	3	<b>~</b>
Failed	0	
Not Executed	0	

### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\AstLmt_CM
Configuration File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\AstLmt_CM\src\Ap_AstLmt.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AstLmt_CM\utp\contract -I\$(PROJECTROOT)\AstLmt_CM\utp\contract\Ap_AstLmt -I\$ (PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler \tms470\include

Name	Text
Module 'AstLmt'	**************************************
	Name of Tester:Ajit Shrivastava
	Code File(s) Under Test:Ap_AstLmt.c
	Code File(s) Version:9
	Module Design Document:Assist_Sum_Limit_CurrentMode_MDD.docx Module Design Document Version:13
	Data Dictionary Version: 9
	Unit Test Plan Version:6
	Optimization Level:Level 2
	Compiler (CodeGen) Version:TMS470_4.9.5
	Model Type:Excel Macro
	Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):942
	Total RAM Used (Bytes):6
	Total CALS Used (Bytes):8
	Special Test Requirements:
	Test Date:09-05-2014
	Comments:"NOTE 1: Inline functions defined in ""GlobalMacro.h"" are not Unit Tested.  NOTE 2 : ""CBD Sandbox dbg.map"" map file is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	<pre>\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5</pre>
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false

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AstLmt\_Per1

Attributes					
Name	Value				
Timer Prescale	0				
Timer Resolution	1				
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg				
Workspace File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP				



### **Test Case 1: Matrix Test**

Specification

Performance Metrics:

(With "None" Instrumentation and WithPS

Environment)

TS1.1 1848.00 cycles Shortest Execution Path TS1.2 1948.00 cycles Longest Execution Path

Description

Vector Description:

Test Step 1.1 (Repeat Count = 1)

TS1.1:"Shortest execution Path:

(PosServCCDisable\_Cnt\_T\_lgc = True) = False

(LmPnCtrCCDisable\_Cnt\_T\_lgc = True = False

((AstLmt\_ManualTrqCmdEn\_Cnt\_M\_lgc = True) And (VehSpd\_Kph\_T\_f32 < FLT\_EPSILON)) = True

(DefeatLimitService\_Cnt\_T\_lgc = True) = True

TS1.2"Longest Execution Path:

(PosServCCDisable\_Cnt\_T\_lgc = True) = True

IS1.2"Longest Execution Path:

(PosServCCDisable\_Cnt\_T\_lgc = True)=True

(LrnPnCtrCCDisable\_Cnt\_T\_lgc = True)=True

((AstLmt\_ManualTrqCmdEn\_Cnt\_M\_lgc = True) And (VehSpd\_Kph\_T\_f32 < FLT\_EPSILON))=False

(LrnPnCtrEnable\_Cnt\_T\_lgc = True)=False

(AstLmt\_SteeringAsstDefeat\_Cnt\_M\_lgc = True)=False

(DefeatLimitService\_Cnt\_T\_lgc = True)=False

Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	8.80000019
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
_SumLimPICmpLimit_MtrNm_f32	0
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-3
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-8.80000019
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8.80000019
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.80000019
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.80000019
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.80000019
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8.80000019
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-8.80000019
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0
gt AstLmt Per1 LrnPnCtrEnable Cnt lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-8.80000019
gt AstLmt Per1 OpTrqOvr MtrNm f32.value	-8.80000019
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	1
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-8.80000019
gt AstLmt Per1 PullCompCmd MtrNm f32.value	1
gt AstLmt Per1 ThermalLimitPerc Uls f32.value	1
gt AstLmt Per1 ThermalLimit MtrNm f32.value	8.80000019
gt_AstLmt_Per1_VehSpd_Kph_f32.value	0
gt AstLmt Per1 WheellmbalanceCmd MtrNm f32.value	-8.80000019
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
gt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTDamping MtrNm f32	tgt AstLmt Per1 AssistEOTDamping MtrNm f32
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
gt Rte Inst Ap AstLmt.AstLmt Per1 AssistStallLimit MtrNm f32	tgt AstLmt Per1 AssistStallLimit MtrNm f32
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt AstLmt Per1 AssistVehSpdLimit MtrNm f32
gt Rte Inst Ap AstLmt.AstLmt Per1 CombinedDamping MtrNm f32	tgt AstLmt Per1 CombinedDamping MtrNm f32
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
gt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32	tgt AstLmt Per1 LimitPercentFiltered Uls f32
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt AstLmt Per1 PosServCCDisable Cnt Igc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
gt Rte Inst Ap AstLmt.AstLmt Per1 PreLimitForStall MtrNm f32	tgt AstLmt Per1 PreLimitForStall MtrNm f32
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimit orotali_witNin_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt AstLmt Per1 PreLimit orden MtrNm f32
grate_merap_retineretineretineretineretique_within_loz	tgt_ ottint_ of _ retinitional_minmi_loz

tgt\_AstLmt\_Per1\_PrkAssistCmd\_MtrNm\_f32

tgt\_AstLmt\_Per1\_PullCompCmd\_MtrNm\_f32

tgt\_AstLmt\_Per1\_SumLimTrqCmd\_MtrNm\_f32

target\_AstLmt\_Per1\_TSMitCommand\_MtrNm\_f32

tgt\_Rte\_Inst\_Ap\_AstLmt.AstLmt\_Per1\_PrkAssistCmd\_MtrNm\_f32

 $tgt\_Rte\_Inst\_Ap\_AstLmt.AstLmt\_Per1\_PullCompCmd\_MtrNm\_f32$ 

 $tgt\_Rte\_Inst\_Ap\_AstLmt.AstLmt\_Per1\_SumLimTrqCmd\_MtrNm\_f32$ 

 $tgt\_Rte\_Inst\_Ap\_AstLmt.AstLmt\_Per1\_TSMitCommand\_MtrNm\_f32$ 

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AstLmt\_Per1

Name	Input Value		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_inst_Ap_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32		
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	1	1 ± 0.000030517578125	~
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	<b>✓</b>
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	<b>✓</b>
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>	

Test Step 1.2 (Repeat Count = 1)	<b>✓</b>
Name	Input Value
	1
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	
AstLmt_ManualTrqCmd_MtrNm_M_f32	8.80000019
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	O
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	1
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	3
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-2.5999999
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-7
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	1
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1.20000005
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-7
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	0.5
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	512
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-6
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt AstLmt Per1 PullCompCmd MtrNm f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32
13-1	

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AstLmt\_Per1

Name	Input Value		
tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitF	Perc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_	MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_M	/ltrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32		
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	~
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5.73999977	5.73999977 ± 0.0009765625	<b>✓</b>
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-5.73999977	-5.73999977 ± 0.0009765625	<b>✓</b>
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-5.73999977	-5.73999977 ± 0.0009765625	<b>~</b>
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	8	8 ± 0.0009765625	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	•

AstLmt\_Per1

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Test Case 2: Boundary Test

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AstLmt\_Per1



### Specification

Performance Metrics:

(With "None" Instrumentation and WithPS

CPU Cycles:

TS2.1 1985.00 cycles TS2.2 1879.00 cycles TS2.3 1821.00 cycles TS2.2 1879.00 cycles
TS2.3 1821.00 cycles
TS2.3 1816.00 cycles
TS2.4 1816.00 cycles
TS2.5 1747.00 cycles
TS2.6 1777.00 cycles
TS2.6 1777.00 cycles
TS2.7 1747.00 cycles
TS2.8 1723.00 cycles
TS2.9 1747.00 cycles
TS2.10 1723.00 cycles
TS2.11 1747.00 cycles
TS2.12 1723.00 cycles
TS2.13 1747.00 cycles
TS2.14 1723.00 cycles
TS2.15 1747.00 cycles
TS2.16 1723.00 cycles
TS2.17 1723.00 cycles
TS2.18 1747.00 cycles
TS2.19 1723.00 cycles
TS2.19 1723.00 cycles
TS2.21 1723.00 cycles
TS2.21 1723.00 cycles
TS2.22 1747.00 cycles
TS2.23 1747.00 cycles
TS2.23 1747.00 cycles
TS2.24 1747.00 cycles
TS2.25 1723.00 cycles
TS2.26 1747.00 cycles
TS2.27 1723.00 cycles
TS2.28 1747.00 cycles
TS2.29 1723.00 cycles
TS2.29 1747.00 cycles
TS2.29 1723.00 cycles
TS2.29 1740.00 cycles
TS2.29 1740.00 cycles
TS2.31 1740.00 cycles
TS2.31 1740.00 cycles
TS2.31 1740.00 cycles
TS2.33 1740.00 cycles
TS2.35 1740.00 cycles
TS2.35 1740.00 cycles
TS2.36 1740.00 cycles
TS2.36 1740.00 cycles
TS2.37 1740.00 cycles TS2.34 1742.00 cycles TS2.35 1742.00 cycles TS2.36 1742.00 cycles TS2.37 1742.00 cycles TS2.38 1742.00 cycles TS2.39 1742.00 cycles TS2.40 1742.00 cycles TS2.41 1742.00 cycles TS2.42 1742.00 cycles TS2.40 1742.00 cycles TS2.40 1742.00 cycles TS2.41 1742.00 cycles TS2.42 1742.00 cycles TS2.43 1758.00 cycles TS2.44 1758.00 cycles TS2.45 1760.00 cycles TS2.46 1767.00 cycles TS2.47 1742.00 cycles TS2.48 1742.00 cycles TS2.49 1742.00 cycles TS2.50 1742.00 cycles TS2.50 1742.00 cycles TS2.49 1742.00 cycles TS2.50 1742.00 cycles TS2.50 1742.00 cycles TS2.51 1742.00 cycles TS2.51 1742.00 cycles TS2.53 1742.00 cycles TS2.55 1742.00 cycles TS2.55 1758.00 cycles TS2.55 1758.00 cycles TS2.56 1758.00 cycles TS2.60 1758.00 cycles TS2.61 1723.00 cycles TS2.63 1723.00 cycles TS2.66 1767.00 cycles TS2.66 1767.00 cycles TS2.66 1767.00 cycles TS2.67 1723.00 cycles TS2.68 1767.00 cycles TS2.69 1723.00 cycles TS2.71 1723.00 cycles TS2.71 1723.00 cycles TS2.73 1723.00 cycles TS2.74 1729.00 cycles TS2.75 1729.00 cycles TS2.75 1723.00 cycles TS2.76 1729.00 cycles TS2.77 1723.00 cycles TS2.78 1723.00 cycles TS2.78 1723.00 cycles TS2.79 1723.00 cycles TS2.81 1729.00 cycles TS2.81 1758.00 cycles TS2.82 1723.00 cycles TS2.83 1729.00 cycles TS2.84 1758.00 cycles TS2.85 1767.00 cycles TS2.86 1757.00 cycles TS2.87 1829.00 cycles TS2.88 1719.00 cycles TS2.89 1712.00 cycles TS2.90 1728.00 cycles TS2.91 1714.00 cycles TS2.92 1729.00 cycles TS2.93 1719.00 cycles TS2.93 1719.00 cycles TS2.93 1719.00 cycles TS2.94 1712.00 cycles TS2.94 1712.00 cycles TS2.95 1731.00 cycles TS2.96 1731.00 cycles TS2.97 1729.00 cycles TS2.97 1729.00 cycles TS2.98 1790.00 cycles TS2.99 1758.00 cycles





#### Description Vector Description:

TS2.1AssistCmd MtrNm f32 = Min TS2.2AssistCmd\_MtrNm\_f32 = Max TS2.3AssistCmd\_MtrNm\_f32 = Mid TS2.4AssistCmd\_MtrNm\_f32 = Pos TS2.5AssistCmd\_MtrNm\_f32 = Neg TS2.6AssistEOTDamping MtrNm f32 = Min TS2.7AssistEOTDamping MtrNm f32 = Max TS2.8AssistEOTDamping MtrNm f32 = Mid TS2.9AssistEOTDamping\_MtrNm\_f32 = Mid
TS2.9AssistEOTDamping\_MtrNm\_f32 = Pos
TS2.10AssistEOTDamping\_MtrNm\_f32 = Neg
TS2.11AssistEOTGain\_Uls\_f32 = Min
TS2.12AssistEOTGain\_Uls\_f32 = Mid
TS2.14AssistEOTLimit\_MtrNm\_f32 = Min
TS2.14AssistEOTLimit\_MtrNm\_f32 = Min IS2.14AssistEOTLimit\_MtrNm\_f32 = Min TS2.15AssistEOTLimit\_MtrNm\_f32 = Max TS2.16AssistEOTLimit\_MtrNm\_f32 = Mid TS2.17AssistStallLimit\_MtrNm\_f32 = Min TS2.18AssistStallLimit\_MtrNm\_f32 = Max TS2.19AssistStallLimit\_MtrNm\_f32 = Mid TS2.20AssistVehSpdLimit\_MtrNm\_f32 = Min TS2.21AssistVehSpdLimit\_MtrNm\_f32 = Max TS2.22AssistVehSpdLimit\_MtrNm\_f32 = Mid TS2.23CombinedDamping\_MtrNm\_f32 = Min TS2.24CombinedDamping\_MtrNm\_f32 = Max TS2.25CombinedDamping\_MtrNm\_f32 = Mid TS2.26CombinedDamping\_MtrNm\_f32 = Pos TS2.27CombinedDamping\_MtrNm\_f32 = Neg TS2.28DefeatLimitService\_Cnt\_lgc = Min TS2.29DefeatLimitService\_Cnt\_lgc = Max TS2.30LimitedReturn\_MtrNm\_f32 = Min TS2.31LimitedReturn\_MtrNm\_f32 = Max TS2.32LimitedReturn\_MtrNm\_f32 = Mid TS2.33LimitedReturn\_MtrNm\_f32 = Pos TS2.34LimitedReturn\_MtrNm\_f32 = Neg TS2.35LmPnCtrCCDisable Cnt\_lgc = Min
TS2.36LmPnCtrCCDisable\_Cnt\_lgc = Min
TS2.36LmPnCtrCDisable\_Cnt\_lgc = Max
TS2.38LmPnCtrEnable\_Cnt\_lgc = Min
TS2.38LmPnCtrEnable\_Cnt\_lgc = Max
TS2.39LmPnCtrTCmd\_MtrNm\_f32 = Min
TS2.40LmPnCtrTCmd\_MtrNm\_f32 = Max TS2.41LrnPnCtrTCmd\_MtrNm\_f32 = Mid TS2.42LrnPnCtrTCmd\_MtrNm\_f32 = Pos TS2.43LrnPnCtrTCmd\_MtrNm\_f32 = Neg  $TS2.44OpTrqOvr\_MtrNm\_f32 = Min$ TS2.44OpTrqOvr\_MtrNm\_f32 = Min
TS2.45OpTrqOvr\_MtrNm\_f32 = Max
TS2.46OpTrqOvr\_MtrNm\_f32 = Mid
TS2.47OpTrqOvr\_MtrNm\_f32 = Pos
TS2.48OpTrqOvr\_MtrNm\_f32 = Neg
TS2.49OutputRampMult\_Uls\_f32 = Min
TS2.50OutputRampMult\_Uls\_f32 = Mia
TS2.51OutputRampMult\_Uls\_f32 = Mid
TS2.52PosServCCDisable\_Cnt\_lgc = Min
TS2.53PosServCCDisable\_Cnt\_lgc = Max
TS2.54Powerl\_imitPerr\_Uls\_f32 = Min TS2.54PowerLimitPerc\_UIs\_f32 = Min TS2.55PowerLimitPerc\_UIs\_f32 = Max TS2.56PowerLimitPerc\_UIs\_f32 = Mid TS2.57PrkAssistCmd\_MtrNm\_f32 = Min TS2.58PrkAssistCmd\_MtrNm\_f32 = Max TS2.59PrkAssistCmd\_MtrNm\_f32 = Mid TS2.60PrkAssistCmd\_MtrNm\_f32 = Pos TS2.61PrkAssistCmd\_MtrNm\_f32 = Neg TS2.62PullCompCmd\_MtrNm\_f32 = Min TS2.63PullCompCmd\_MtrNm\_f32 = Max TS2.64PullCompCmd\_MtrNm\_f32 = Mid TS2.65PullCompCmd\_MtrNm\_f32 = Pos TS2.66PullCompCmd\_MtrNm\_f32 = Neg TS2.67ThermalLimitPerc\_Uls\_f32 = Min TS2.68ThermalLimitPerc\_Uls\_f32 = Max TS2.69ThermalLimitPerc\_Uls\_f32 = Mid TS2.70ThermalLimit\_MtrNm\_f32 = Min TS2.71ThermalLimit\_MtrNm\_f32 = Max TS2.72ThermalLimit\_MtrNm\_f32 = Mid TS2.73WheellmbalanceCmd\_MtrNm\_f32 = Min TS2.74WheellmbalanceCmd\_MtrNm\_f32 = Max TS2.75WheellmbalanceCmd\_MtrNm\_f32 = Mid TS2.76WheelImbalanceCmd\_MtrNm\_f32 = Pos TS2.77WheelImbalanceCmd\_MtrNm\_f32 = Neg TS2.778k\_SumLimPlCmpLimit\_MtrNm\_f32 = Min TS2.78k\_SumLimPlCmpLimit\_MtrNm\_f32 = Min
TS2.79k\_SumLimPlCmpLimit\_MtrNm\_f32 = Max
TS2.80k\_SumLimPlCmpLimit\_MtrNm\_f32 = Min
TS2.81VehSpd\_Kph\_f32 = Min
TS2.82VehSpd\_Kph\_f32 = Max
TS2.83VehSpd\_Kph\_f32 = Pos
TS2.84ManualTrqCmdEn\_Cnt\_M\_lgc = Min
TS2.85ManualTrqCmdEn\_Cnt\_M\_lgc = Max
TS2.85ManualTrqCmd\_MtrNm\_M\_f32 = Max
TS2.86ManualTrqCmd\_MtrNm\_M\_f32 = Max
TS2.88ManualTrqCmd\_MtrNm\_M\_f32 = Neg
TS2.89ManualTrqCmd\_MtrNm\_M\_f32 = Neg
TS2.99ManualTrqCmd\_MtrNm\_M\_f32 = Pos
TS2.91SteeringAsstDefeat\_Cnt\_M\_lgc = Min
TS2.92SteeringAsstDefeat\_Cnt\_M\_lgc = Max
TS2.93TSMitCommand\_MtrNm\_f32=Min
TS2.94TSMitCommand\_MtrNm\_f32=Min
TS2.94TSMitCommand\_MtrNm\_f32=Max





TS2.95TSMitCommand\_MtrNm\_f32=Zero TS2.96TSMitCommand\_MtrNm\_f32=Neg TS2.97TSMitCommand\_MtrNm\_f32=Pos TS2.98All Min TS2.99All max

Fest Step 2.1 (Repeat Count = 1)	Input Value		
stLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-7.36000013		
AstLmt SteeringAsstDefeat Cnt M Igc	0		
Rte_Inst_Ap_AstLmt	tgt Rte Inst Ap AstLmt		
_SumLimPlCmpLimit_MtrNm_f32	8.10000038		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-3		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-3		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.100000001		
pt AstLmt Per1 AssistStallLimit MtrNm f32.value	1.79999995		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	1.5		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-5		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	3		
pt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
pt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LmPnCtrEnable_Cnt_tgc.value	4		
gt_AstLmt_Per1_ChrPriCthTofnid_withNin_i32.value	5		
pt_AstLmt_Per1_OpTrqOvr_MtrNm_i32.value	0.100000001		
pt_AstLmt_Per1_OutputRampMult_Ois_is2.value	1		
gt_AstLmt_Per1_PosservCcDisable_Cnt_lgc.value	0.100000001		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-3		
	2		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	0.100000001		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value pt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.0999999		
	42.0999985		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	-4.5		
gt_AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32.value		22	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_f	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_t		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_N	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_C	_ <del>-</del> -	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_0		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3:		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_0	·	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	<del>-</del>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNn	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNn		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mtr1	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls	_	
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd		
lame	Actual Value	Expected Value	Res
	0.10000001	0.100000001 ± 0.000030517578125	
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value			
	8.80000019	8.80000019 ± 0.0009765625	
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value		8.80000019 ± 0.0009765625 4.5 ± 0.0009765625	



Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~		
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~		

Test Step 2.2 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	5.36999989		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt SteeringAsstDefeat Cnt M lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	7.19999981		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.9000001		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.200000003		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	1.8999998		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	1.60000002		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	4		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	6		
tgt AstLmt Per1 OutputRampMult Uls f32.value	0.200000003		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt AstLmt Per1 PowerLimitPerc Uls f32.value	0.200000003		
tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value	-4		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.20000003		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.20000005		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	38.2000008		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-5		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mti		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U	<del>_</del> -	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lc	gc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cr	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	1_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNi	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M	trNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.20000003	0.200000003 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	5	5 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	1	1 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.20000003	0.200000003 ± 0.0009765625	٠,



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>	

Test Step 2.3 (Repeat Count = 1) Name	Input Value		
	0		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32	-4.3499999		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPlCmpLimit_MtrNm_f32	6.30000019		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.79999995		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	0		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-5		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.20000003		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.300000012		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	1.70000005		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-7		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	6		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	7		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-5.5999999		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	4		
t_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.29999995		
t_AstLmt_Per1_VehSpd_Kph_f32.value	26.7999992		
t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-6		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt AstLmt Per1 AssistEOTDamping M		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	<del>-</del>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mti		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt AstLmt Per1 LimitedReturn MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Ci		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_le		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt AstLmt Per1 LrnPnCtrTCmd MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32	1 132	
		f2.2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cr	<del>_</del> -	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNi	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mi		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	<u>f</u> 32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	
gt_AstEmt_reri_Emit erecrit intered_dis_los_value	8.80000019	8.80000019 ± 0.0009765625	
gt_AstLmt_Per1_PreLimit or3tan_withtin_f32.value	8.80000019	8.80000019 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.0900000036	0.090000036 ± 0.0009765625	



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Add m. Manual Forgine Methy M. 192  Add m. Steering-Assel Defeat, Ch. M. Uge  Ref. first, A. P. Add m. 192  Add m. Steering-Assel Defeat, Ch. M. Uge  Ref. first, A. P. Add m. 192  S. Add m. Steering-Assel Defeat, Ch. M. Uge  Ref. first, A. P. Add m. 192  S. Add m. Steering-Assel Defeat, Ch. M. Uge  Ref. first, A. P. Add m. 192  S. Add m. Steering-Assel Defeat, Ch. M. Uge  Ref. first, A. P. Add m. 192  S. Add m. Perl Assel Cort Manual Methy m. 192 value	2.4 (Repeat Count = 1)	Input Value	
Asterni, Manual Trigothol (Millinn, M.) (2)  ARIEM (1997) (2)  ARI	Transcription Cost M. Inc.	•	
Ast.ml   SelempAssinDetest On M   50			
Sp. Asstume  Sp.			
S. SumLamPri-CropLints, Minhin, 192   sq. Astum, Perl. Assiscification, Minhin, 192 value   sq. Astum, Perl. Assistification, Minhin, 192 value   sq. Astum, Perl. CombinedDampin, Minhin, 192 value   sq. Astum, Perl. Liminoricition, Minhin, 192 value   sq. Astum, Perl. Assistion, Minhin, 192 value   sq. Astum, Perl. Postericition   sq. Astum, Perl. Assistion, Minhin, 192 value   sq. Astum, Perl. Postericition   sq. Astum, Perl. Assistion, Minhin, 192 value   sq. Astum, Perl. Postericition   sq. Astum, Perl. Assisticom, Minhin, 192 value   sq. Astum, Perl. Minhin, 192 value   sq. Rein, Ind. Astum, Astum, Perl. Assistciorid, Minhin, 192   sq. Rein, Ind. Astum, Astum, Perl. Assistciorid, Minhin, 192   sq. Rein, Ind. Astum, Astum, Perl. Assistciorid, Minhin, 192   sq. Rein, Ind. Astum, Astum, Perl. Liminoridentale, Orl. 193   sq. Rein, Ind. Astum, A			
27000005			
2, Asturn. Perl. AssistChot Markinn, 123 value   0, Asturn. Perl. AssistChot Markinn, 125 value   0, Asturn. Perl. ChristonEchaping, Mirkinn, 125 value   0, Asturn. Perl. LimberChristonEchaping, Mirkinn, 125 value   0, Asturn. Perl. LimberChristonEchaping, Mirkinn, 125 value   1, Asturn. Perl. LimberChristonEchaping, Mirkinn, 125 value   1, Asturn. Perl. ChristonEchaping, Mirkinn, 125 value   0, Asturn. Perl. DispersonChristonEchaping, Mirkinn, 125 value   0, Asturn. Perl. Purchasticton, Mirkinn, 125 value   0, Asturn. Perl. Purchasticton, Mirkinn, 125 value   1, Asturn. Perl. Purc			
3, Astum Perl AssistEOT Clam   19, 25 value   0, 200000012   0, 200000012   0, 200000012   0, 200000012   0, 200000012   0, 2000000000   0, 20000000000000000			
Q. Astum. Perl. AssistSOTLam, Mirkm, T.S.2 value   0,00000000   0, detam. Perl. AssistSollam, Mirkm, T.S.2 value   1,000000000   0, detam. Perl. AssistSollam, Mirkm, T.S.2 value   1,000000000   0,00000000   0,00000000   0,00000000			
Astum. Perl AssistSoll-Lim, Mirkim, 132-value   0.40000000000000000000000000000000000			
Q.Asturn.Pert_AssistShallmin, MrNm, 152 value   2,0990909   3,48 turn.Pert_Combined-bamping, MrNm, 152 value   4,899995   3,48 turn.Pert_Combined-bamping, MrNm, 152 value   5,9 Asturn.Pert_LimitedResturn.JMrNm, 152 value   6   1,79999995   3,48 turn.Pert_LimitedResturn.JMrNm, 152 value   7,9 Asturn.Pert_LimitedResturn.JMrNm, 152 value   7,9 Asturn.Pert_Disturn.Pert_LimitedResturn.JMrNm, 152 value   7,9 Asturn.Pert_Disturn.Pert_LimitedResturn.JMrNm, 152 value   7,9 Asturn.Pert_Disturn.Pert_LimitedResturn.JMrNm, 152 value   7,9 Asturn.Pert_Disturn.JMrNm, 152 value   7,9 Asturn.JMrNm,			
AstLim_Pert   CombinesDumping Minhm, 192 value   1, 299999999999999999999999999999999999			
Statum   Pert   Combined Damping   Minhm   132 value			
gl. Astl.m.Pert   DefeatLimisService, Crt.   gc. value			
Q. AstLim.   Pert  Iminedretum, MnNm, _152 value   0   0   0   0   0   0   0   0   0			
SASTEM_Pert_LimPhoCitiCoDisable_Cnt_lgcvalue			
Saltum  Pert   LimPhOtErable  Cnt, Igc-value   1   1   1   1   1   1   1   1   1			
Salt.ml. Perl _ UniPrictif Crid _ Mirkm _ 132 value			
AstLIM_Perl_OpTiQOr_MRh7m_132 value   1, 1			
gl. AstLmL Pert   DoutputRampMult_Uis_132-value   0.40000006   0.1   0.0000006   0.1   0.0000006   0.000000006   0.00000006   0.00000006   0.00000006   0.00000006   0.00000006   0.00000006   0.00000006   0.00000006   0.00000006   0.00000006   0.00000006   0.00000006   0.00000006   0.0000006   0.00000006   0.00000006   0.0000006   0.0000006   0.0000006   0.0000006   0.00000006   0.0000006   0.0000006   0.00000006   0.00000006   0.0000006   0.0000006   0.0000006   0.00000006   0.0000006   0.0000006			
AstLim_Pert   PosSerVCCDisable_Cnt_lgc_value   0   0   0   0   0   0   0   0   0	- : :		
J. AstLIM_Pert_PowerLimiPerc_UIs_122_value   0.400000006       J. AstLIM_Pert_PullCompCmd_MtrNm_152_value   1.6     J. AstLIM_Pert_PullCompCmd_MtrNm_152_value   5     J. AstLIM_Pert_PullCompCmd_MtrNm_152_value   1.4     J. AstLIM_Pert_PullCompCmd_MtrNm_152_value   2.4000001     J. AstLIM_Pert_PullCompCmd_MtrNm_152_value   1.4     J. AstLIM_Pert_Primabilimi_MtrNm_152_value   1.4     J. AstLIM_Pert_Primabilimi_MtrNm_152_value   1.4     J. AstLIM_Pert_Primabilimi_MtrNm_152_value   1.4     J. Re_Inst_Post_AssLIM_AstLIM_Pert_AssistCOM_MtrNm_152_   1.4     J. Re_Inst_Post_AssLIM_AstLIM_Pert_AssistCOM_MtrNm_152_   1.4     J. Re_Inst_Post_AssLIM_AstLIM_Pert_AssistCOM_INST_10_   1.4     J. Re_Inst_Post_AssLIM_AstLIM_Pert_CombinedDamping_MtrNm_152_   1.4     J. Re_Inst_Post_AssLIM_AstLIM_Pert_LimiPercontFiltered_UIs_132_   1.4     J. Re_Inst_Post_AssLIM_AstLIM_Pert_DispCripCond_UINNm_132_   1.4     J. Re_Inst_Post_AssLIM_AstLIM_Pert_PowerLimiPerc_UIs_132_   1.4     J. Re_Inst_Post_AssLIM_AstLIM_Pert_Post_MinNm_132_   1.4     J. Re_Inst_Post_AssLIM_AstLIM_Pert_Po			
AstLIM_Pert_PriAssistCord_MtrNm_f32_value   5     AstLIM_Pert_ThemalLimitPerc_Uls_f32_value   5     AstLIM_Pert_ThemalLimitPert_Uls_f32_value   0.40000006     AstLIM_Pert_ThemalLimitPerc_Uls_f32_value   14.5     AstLIM_Pert_WheelimbalianceCnd_MtrNm_f32_value   14.5     AstLIM_Pert_WheelimbalianceCnd_MtrNm_f32_value   14.5     AstLIM_Pert_WheelimbalianceCnd_MtrNm_f32_value   14.5     AstLIM_Pert_WheelimbalianceCnd_MtrNm_f32_value   7.1999981     AstLIM_Pert_WheelimbalianceCnd_MtrNm_f32_value   7.1999981     AstLIM_Pert_AssistCord_MtrNm_f32     AstLIM_Pert_Ass			
SalLmt_Pert_PullCompCmd_MtrNm_r32 value   5			
Q. AstLmt. Pert   ThermalLimit Perc. Uls_132 value   2,4000001   3, AstLmt. Pert   ThermalLimit, Within, 132 value   14.5   3, AstLmt. Pert   WheelimbalanceCmd, Mirkin, 132 value   14.5   3, AstLmt. Pert   AssistCord_Mirkin, 132 value   14.5   3, AstLmt. Pert   AssistEOTGamping, Mirkin, 132   3, Rite_Inst, 3p, AstLmt. AstLmt. Pert   AssistEOTGamping, Mirkin, 132   3, Rite_Inst, 3p, AstLmt. AstLmt. Pert   AssistEOTGamping, Mirkin, 132   3, Rite_Inst, 3p, AstLmt. AstLmt. Pert   AssistEOTGamping, Mirkin, 132   3, Rite_Inst, 3p, AstLmt. AstLmt. Pert   AssistEOTGamping, Mirkin, 132   3, Rite_Inst, 3p, AstLmt. AstLmt. Pert   AssistEOTGamping, Mirkin, 132   3, Rite_Inst, 3p, AstLmt. AstLmt. Pert   AssistEOTGamping, Mirkin, 132   3, Rite_Inst, 3p, AstLmt. AstLmt. Pert   AssistEOTGamping, Mirkin, 132   3, Rite_Inst, 3p, AstLmt. AstLmt. Pert   AssistVehSpdLimit, Mirkin, 132   3, Rite_Inst, 3p, AstLmt. AstLmt. Pert   AssistVehSpdLimit, Mirkin, 132   3, Rite_Inst, 3p, AstLmt. AstLmt. Pert   AssistVehSpdLimit, Mirkin, 132   3, Rite_Inst, 3p, AstLmt. AstLmt. Pert   CombinedDamping, Mirkin, 132   3, Rite_Inst, 3p, AstLmt. AstLmt. Pert   LimitPercenFiltered Uls_132   3, Rite_Inst, 3p, AstLmt. AstLmt. Pert   District Mirkin, 132   3, Rite_Inst, 3p, AstLmt. AstLmt. Pert   District Mirkin, 132   3, Rite_Inst, 3p, AstLmt. AstLmt. Pert   PrelLimitForGov. Mirkin, 132   3, Rite_Inst, 3p, AstLmt. AstLmt. P			
2.4   AstLmt. Pert   ThermalLimit, MirNm   32.2 value   1.4   1.4   1.5   1.			
3.4.StLmt.Pert_VehSpd_Kph_f32 value   14.5			
3. AstLmt   Perf   WheelimbalanceCmd   MirNm   f32 value   7.19999981   1. Rte   Inst. Ap   AstLmt AstLmt   Perf   AssistCond   MirNm   f32   1. Rte   Inst. Ap   AstLmt   AstLmt   Perf   AssistCond   MirNm   f32   1. Rte   Inst. Ap   AstLmt   AstLmt   Perf   AssistCond   MirNm   f32   1. Rte   Inst. Ap   AstLmt   AstLmt   Perf   AssistEOTDamping   MirNm   f32   1. Rte   Inst. Ap   AstLmt   AstLmt   Perf   AssistEOTDamping   MirNm   f32   1. Rte   Inst. Ap   AstLmt   AstLmt   Perf   AssistEOTDaming   MirNm   f32   1. Rte   Inst. Ap   AstLmt   AstLmt   Perf   AssistEOTDaming   MirNm   f32   1. Rte   Inst. Ap   AstLmt   AstLmt   Perf   AssistSteOTDaming   MirNm   f32   1. Rte   Inst. Ap   AstLmt   AstLmt   Perf   AssistVeNSpdLimin   MirNm   f32   1. Rte   Inst. Ap   AstLmt   AstLmt   Perf   CombinedDamping   MirNm   f32   1. Rte   Inst. Ap   AstLmt   AstLmt   Perf   CombinedDamping   MirNm   f32   1. Rte   Inst. Ap   AstLmt   AstLmt   Perf   CombinedDamping   MirNm   f32   1. Rte   Inst. Ap   AstLmt   AstLmt   Perf   LimitePercentFiltered   Uls   f32   1. Rte   Inst. Ap   AstLmt   AstLmt   Perf   LimitePercentFiltered   Uls   f32   1. Rte   Inst. Ap   AstLmt   AstLmt   Perf   LimitePercentFiltered   Uls   f32   1. Rte   Inst. Ap   AstLmt   AstLmt   Perf   LimiteReturm   MirNm   f32   1. Rte   Inst. Ap   AstLmt   AstLmt   Perf   LimiteDricTCObable   Cnt. Igc   1. Rte   Inst. Ap   AstLmt   AstLmt   Perf   LimiteDricTCDobable   Cnt. Igc   1. Rte   Inst. Ap   AstLmt   AstLmt   Perf   LimiteDricTCObable   Cnt. Igc   1. Rte   Inst. Ap   AstLmt   AstLmt   Perf   DipTqOv_Mirthm   f32   1. Rte   Inst. Ap   AstLmt   AstLmt   Perf   PopTqOv_Mirthm   f32   1. Rte   Inst. Ap   AstLmt   AstLmt   Perf   PopTqOv_Mirthm   f32   1. Rte   Inst. Ap   AstLmt   AstLmt   Perf   PretimitForeStall   Mirthm   f32   1. Rte   Inst. Ap   AstLmt   AstLmt   Perf   PretimitForeStall   Mirthm   f32   1. Rte   Inst. Ap   AstLmt   AstLmt   Perf   PretimitForeStall   Mirthm   f32   1. Rte   Inst. Ap   AstLmt   AstLmt   Perf   South   South   AstLm			
gt Rte Inst Ap AstLmt AstLmt Per1 AssistCond_MtrNm_f32         tgt_AstLmt_Per1 Instruct_Cond_mtrNm_f32         tgt_AstLmt_Per1 Instruct_Cond_mtrNm_f32<			
gt Rte Inst Ap			
gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   AssistEOTGain   Uis   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   AssistEOTILimit   Mtn/m   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   AssistEoTILimit   Mtn/m   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   AssistEoTILimit   Mtn/m   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   AssistStaliLimit   Mtn/m   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   AssistStaliLimit   Mtn/m   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   CombinedDamping   Mtn/m   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   LimiteGetatimitService   Cnt   gc   gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   LimiteGetatimitService   Cnt   gc   gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   LimiteGetatim   Mtn/m   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   LimiteGetatim   Mtn/m   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   LimiteGetatim   Mtn/m   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   LimiteGetatim   Mtn/m   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   LimiteGetatim   Mtn/m   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   LimiteGetatim   Mtn/m   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   LimiteGetatim   Mtn/m   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   DirTQO'mtn/m   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   DirTQO'mtn/m   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   DirTQO'mtn/m   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   DirTQO'mtn/m   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   Provertimiter   Uis   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   Privasisic   Mtn/m   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   Privasisic   Mtn/m   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   Privasisic   Mtn/m   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   ShiftCommand   Mtn/m   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   ThermalLimit   Mtn/m   f32   gt   Rte   Inst   Ap   AstLmt   AstLmt			
gt Rte Inst Ap, AstLmt AstLmt, Perl AssistEOTLimit, MtrNm; f32         tgt_AstLmt_Perl AssistStalliLimit, MtrNm; f32         tgt_AstLmt_Perl AssistStalliLimit, MtrNm; f32           gt Rte Inst Ap, AstLmt AstLmt, Perl AssistVehSpdLimit, MtrNm; f32         tgt_AstLmt_Perl AssistStalliLimit, MtrNm; f32         tgt_AstLmt_Perl AssistStalliLimit, MtrNm; f32           gt Rte Inst Ap, AstLmt AstLmt, Perl _CombinedDamping, MtrNm; f32         tgt_AstLmt_Perl_DefeatLimitService_Cnt_Igc         tgt_AstLmt_Perl_DefeatLimitService_Cnt_Igc           gt Rte Inst Ap, AstLmt AstLmt, Perl _LimitPercentFiltered_UIs; f32         tgt_AstLmt_Perl_DefeatLimitService_Cnt_Igc         tgt_AstLmt_Perl_DefeatLimitService_Cnt_Igc           gt Rte Inst Ap, AstLmt AstLmt, Perl _LimitPercentFiltered_UIs; f32         tgt_AstLmt_Perl_LimitPercentFiltered_UIs; f32           gt Rte Inst Ap, AstLmt AstLmt_Perl _LimitPercentFiltered_UIs; f32         tgt_AstLmt_Perl_LimitPercentFiltered_UIs; f32           gt Rte Inst Ap, AstLmt AstLmt_Perl _LimitPercentFiltered_UIS; f32         tgt_AstLmt_Perl_LimitPercentFiltered_UIs; f32           gt Rte Inst Ap, AstLmt AstLmt_Perl_OpTrQVr_MtrNm; f32         tgt_AstLmt_Perl_LimitPercentFiltered_UIS; f32           gt Rte Inst Ap, AstLmt AstLmt_Perl_PoseervCDisable_Cnt_Igc         tgt_AstLmt_Perl_PoseervCDisable_Cnt_Igc           gt Rte_Inst Ap, AstLmt AstLmt_Perl_PowerLimitPerc_UIS; f32         tgt_AstLmt_Perl_PoseervCDisable_Cnt_Igc           gt Rte_Inst Ap, AstLmt AstLmt_Perl_Prel-InitTorque_MtrNm; f32         tgt_AstLmt_Perl_Prel-InitTorque_MtrNm; f32           gt Rte_Inst Ap,			
gr. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistStallLimit_MtrNm_f32   tgt_AstLmt_Perl_AssistStallLimit_MtrNm_f32   tgt_AstLmt_Perl_AssistStallLimit_MtrNm_f32   tgt_AstLmt_Perl_AssistStallLimit_MtrNm_f32   tgt_AstLmt_Perl_CombinedDamping_MtrNm_f32   tgt_AstLmt_Perl_CombinedDamping_MtrNm_f32   tgt_AstLmt_Perl_CombinedDamping_MtrNm_f32   tgt_AstLmt_Perl_DefeatLimitService_Cnt_Igc   tgt_AstLmt_Perl_DefeatLimitService_Cnt_Igc   tgt_AstLmt_Perl_DefeatLimitService_Cnt_Igc   tgt_AstLmt_Perl_LimitPercentFiltered_Us_f32   tgt_AstLmt_Perl_LimitPercentFiltered_Us_f32   tgt_AstLmt_Perl_LimitPercentFiltered_Us_f32   tgt_AstLmt_Perl_LimitPercentFiltered_Us_f32   tgt_AstLmt_Perl_LimitBercentFiltered_Us_f32   tgt_AstLmt_Perl_PorctrCCDisable_Cnt_Igc   tgt_AstLmt_Perl_PorctrCTCmd_MtrNm_f32   tgt_AstLmt_Perl_PowerLimitBerc_Us_f32   tgt_AstLmt_Perl_PowerLimitBerc_Us_f32   tgt_AstLmt_Perl_PowerLimitBerc_Us_f32   tgt_AstLmt_Perl_PowerLimitBerc_Us_f32   tgt_AstLmt_Perl_PowerLimitBerc_Us_f32   tgt_AstLmt_Perl_Pret_ImitForque_MtrNm_f32   tgt_AstLmt_Perl_Pret_ImitForque_MtrNm_f32   tgt_AstLmt_Perl_Pret_ImitForque_MtrNm_f32   tgt_AstLmt_Perl_Pret_ImitForque_MtrNm_f32   tgt_AstLmt_Perl_Pret_ImitForque_MtrNm_f32   tgt_AstLmt_Perl_Pret_ImitForque_MtrNm_f32   tgt_AstLmt_Perl_Pret_ImitForque_MtrNm_f32   tgt_AstLmt_Perl_Pret_ImitForque_MtrNm_f32   tgt_AstLmt_Perl_Tret_ImitImitPerc_Us_f32   tgt_AstLmt_Perl_Tret_ImitImitPerc_Us_f32   tgt_AstLmt_Perl_Tret_ImitImitPerc_Us_f32   tgt_AstLmt_Perl_Tret_ImitImitPerc_			
gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistVehSpdLimit_MtrNm_f32         tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32         tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32           gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_DefeatLimitService_Cnt_Igc         tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc         tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc           gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimitGreentFiltered_Uls_f32         tgt_AstLmt_Per1_DefeatLimitGreentFiltered_Uls_f32           gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimitGreentFiltered_Uls_f32         tgt_AstLmt_Per1_LimitGreentFiltered_Uls_f32           gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimPnCtrCDisable_Cnt_Igc         tgt_AstLmt_Per1_LimPnCtrCTCblable_Cnt_Igc           gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimPnCtrTCmd_MtrNm_f32         tgt_AstLmt_Per1_LimPnCtrTCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_OptrQvr_MtrNm_f32         tgt_AstLmt_Per1_DeftrGroble_Cnt_Igc           gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_OptrQvr_MtrNm_f32         tgt_AstLmt_Per1_OptrQvr_MtrNm_f32           gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PoservCDisable_Cnt_Igc         tgt_AstLmt_Per1_OptrQvr_MtrNm_f32           gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForStall_MtrNm_f32         tgt_AstLmt_Per1_PowerLimitForCDisall_MtrNm_f32           gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForGall_MtrNm_f32         tgt_AstLmt_Per1_PreLimitForGall_MtrNm_f32           gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_SumLimTrqComd_MtrNm_f32         tgt_AstLmt_Per1_PreLimitForGall_MtrNm_f32           gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_SumLimiTrqComd_MtrNm_f32         tgt_Ast			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32         tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc         tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc         tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc         tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc         tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32         tgt_AstLmt_Per1_ImitPercEntFiltered_Uls_f32         tgt_AstLmt_Per1_DercEntFiltered_Uls_f32         tgt_AstLmt_Per1_DercEntFiltered_Uls_			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc         tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFillered_Uls_f32         tgt_AstLmt_Per1_LimitPercentFillered_Uls_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32         tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_Igc         tgt_AstLmt_Per1_LmPnCtrCCDisable_Cnt_Igc           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32         tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DopTrqOvr_MtrNm_f32         tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32         tgt_AstLmt_Per1_DopTrqOvr_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc         tgt_AstLmt_Per1_DopTrqOvr_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32         tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32         tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PricLimitTorque_MtrNm_f32         tgt_AstLmt_Per1_PricLimitTorque_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32         tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TimitTrqCmd_MtrNm_f32         tgt_AstLmt_Per1_TimitImitTrqCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32			
tgt_Rte_Inst_Ap_AstLmt_AptLmt_Perrl_LimitPercentFiltered_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_LimitedRetum_MtnMm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_LimitedRetum_MtnMm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_LimitedRetum_MtnMm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_LimitedRetum_MtnMm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_LimitedRetum_MtnMm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_LimitedRetum_MtnMm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_LimitedRetum_MtnMm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_OptrqOvr_MtnNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_OptrqOvr_MtnNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_OptrqOvr_MtnNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_PrelimitForStall_MtnNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_PrelimitForStall_MtnNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_PrelimitForque_MtnNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_PrelimitTorque_MtnNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_PrelimitTorque_MtnNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_PrelimitTorque_MtnNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_SumLimitTrqCmd_MtnNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_SumLimitTrqCmd_MtnNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_TowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_TowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_TowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_TowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Perrl_VehSpd_Kph_f32 gt_AstLmt_Perrl_TimitPercentFiltered_Uls_f32.value  A			
gt_Rte_inst_Ap_AstLmt_AptLattLmt_Per1_LimitedReturn_MtrNm_f32         tgt_AstLmt_Ap_LimitedReturn_MtrNm_f32         tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32           gt_Rte_inst_Ap_AstLmt_AstLmt_Per1_LimPnCtrCDisable_Cnt_igc         tgt_AstLmt_Per1_LimPnCtrCDisable_Cnt_igc         tgt_AstLmt_Per1_LimPnCtrCDisable_Cnt_igc           gt_Rte_inst_Ap_AstLmt_AstLmt_Per1_LimPnCtrTCmd_MtrNm_f32         tgt_AstLmt_Per1_LimPnCtrTCmd_MtrNm_f32         tgt_AstLmt_Per1_LimPnCtrTCmd_MtrNm_f32           gt_Rte_inst_Ap_AstLmt_AstLmt_Per1_OutputRampMult_Uls_f32         tgt_AstLmt_Per1_OptrqOvr_MtrNm_f32         tgt_AstLmt_Per1_OutputRampMult_Uls_f32           gt_Rte_inst_Ap_AstLmt_AstLmt_Per1_PosServCCDisable_Cnt_igc         tgt_AstLmt_Per1_OutputRampMult_Uls_f32         tgt_AstLmt_Per1_OutputRampMult_Uls_f32           gt_Rte_inst_Ap_AstLmt_AstLmt_Per1_PowerLimitPerc_Uls_f32         tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32         tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32           gt_Rte_inst_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32         tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32         tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32           gt_Rte_inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32         tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32         tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32           gt_Rte_inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32         tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32         tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32           gt_Rte_inst_Ap_AstLmt_AstLmt_Per1_TremalLimitPerc_Uls_f32         tgt_AstLmt_Per1_TremalLimitPerc_Uls_f32			
tgt_AstLmt_Ap_AstLmt_AstLmt_Perl_LmPnCtrCCDisable_Cnt_lgc   tgt_AstLmt_Perl_LmPnCtrEnable_Cnt_lgc   tgt_AstLmt_Ap_AstLmt_Ap_AstLmt_Perl_LmPnCtrEnable_Cnt_lgc   tgt_AstLmt_Perl_LmPnCtrEnable_Cnt_lgc   tgt_AstLmt_Perl_LmPnCtrEnable_Cnt_lgc   tgt_AstLmt_Perl_LmPnCtrEnable_Cnt_lgc   tgt_AstLmt_Perl_LmPnCtrEnable_Cnt_lgc   tgt_AstLmt_Perl_LmPnCtrEnable_Cnt_lgc   tgt_AstLmt_Perl_DpTrqOvr_MtrNm_f32   tgt_AstLmt_Perl_OpTrqOvr_MtrNm_f32   tgt_AstLmt_Perl_OpTrqOvr_MtrNm_f32   tgt_AstLmt_Perl_OptrqOvr_MtrNm_f32   tgt_AstLmt_Perl_OptrqOvr_MtrNm_f32   tgt_AstLmt_Perl_OptrqOvr_MtrNm_f32   tgt_AstLmt_Perl_OptrqOvr_MtrNm_f32   tgt_AstLmt_Perl_PosservCCDisable_Cnt_lgc   tgt_AstLmt_Perl_TosmitCommand_MtrNm_f32   tgt_AstLmt_Perl_TosmitLimitTynt_MtrNm_f32   tgt_AstLmt_Perl_TosmitLimitTynt_MtrNm_f32   tgt_AstLmt_Perl_TosmitLimitTynt_MtrNm_f32   tgt_AstLmt_Perl_Vospd_Kph_f32   tgt_AstLmt_Perl			
tgt_Rte_Inst_Ap_AstLmt_Per1_LmPnCtrEnable_Cnt_Igc  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OpTrqOvr_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OpTrqOvr_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OpTrqOvr_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OptrqOvr_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OptrqOvr_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCDisable_Cnt_Igc  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCDisable_Cnt_Igc  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Powert.imitPerc_Uls_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Powert.imitForStall_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PillCompCmd_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PillCompCmd_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TSMitCommand_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TSMitCommand_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TremalLimitPerc_Uls_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TremalLimit_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TremalLimit_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TremalLimit_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TremalLimit_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TremalLimit_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Tred_miltMin_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Tred_miltMin_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Tred_miltMin_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Tred_miltMin_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Tred_miltMin_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Tred_miltMin_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Tred_miltMin_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Tred_miltMin_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Tred_miltMin_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_P			
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OutputRampMult_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OutputRampMult_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OutputRampMult_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PowerLimitPorcy_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SullcompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SullcompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TsMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TsMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TsMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Uls_f32.value gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value gt_AstLmt_P			
gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OpTrqOvr_MtrNm_f32         tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OutputRampMult_UIs_f32         tgt_AstLmt_Per1_OutputRampMult_UIs_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosservCCDisable_Cnt_Igc         tgt_AstLmt_Per1_PosservCCDisable_Cnt_Igc           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PowerLimitPerc_UIs_f32         tgt_AstLmt_Per1_PosservCCDisable_Cnt_Igc           gt_Rte_Inst_Ap_AstLmt_Per1_PretLimitForStall_MtrNm_f32         tgt_AstLmt_Per1_PowerLimitPerc_UIs_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PretLimitForStall_MtrNm_f32         tgt_AstLmt_Per1_PretLimitForStall_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PretLimitTorque_MtrNm_f32         tgt_AstLmt_Per1_PretLimitTorque_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PullCompCmd_MtrNm_f32         tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32         tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TSMitCommand_MtrNm_f32         tgt_AstLmt_Per1_TsMitCommand_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitPerc_UIs_f32         tgt_AstLmt_Per1_ThermalLimitPerc_UIs_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitMtrNm_f32         tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_VehSpd_Kph_f32         tgt_AstLmt_Per1_TretlimitMin_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32         tg			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PoservCCDisable_Cnt_lgc tgt_AstLmt_Per1_PoservLimitForc_Uls_f32 tgt_AstLmt_Per1_PoservCCDisable_Cnt_lgc tgt_AstLmt_Per1_PoservCCDisable_Cnt_lgc tgt_AstLmt_Per1_PoservCCDisable_Cnt_lgc tgt_AstLmt_Per1_PoservCCDisable_Cnt_lgc tgt_AstLmt_Per1_PoservCCDisable_Cnt_lgc tgt_AstLmt_Per1_PoservCCDisable_Cnt_lgc tgt_AstLmt_Por1_PoservCCDisable_Cnt_lgc tgt_AstLmt_Por1_PoservLimitForculs_f32 tgt_AstLmt_Por1_PoservCDisable_Cnt_lgc tgt_AstLmt_Por1_PoservCCDisable_Cnt_lgc tgt_AstLmt_Por1_PoservCCDisable_Cnt_lgc tgt_AstLmt_Por1_PoservLimitForStall_MtrNm_f32 tgt_AstLmt_Por1_ToutintNotin_MtrNm_f32 tgt_AstLmt_Por1_ToutintNotin_MtrNm_f32 tgt_AstLmt_Por1_UnitNotin_MtrNm_f32 tgt_AstLmt_Por1_UnitNotin_UnitNotin_UnitNotin_UnitNotin_UnitNotin_UnitNotin_UnitNotin			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc         tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32         tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32         tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32         tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32         tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32         tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32         tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32         tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32         tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32         tgt_AstLmt_Per1_TremalLimit_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32         tgt_AstLmt_Per1_VehSpd_Kph_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32         tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32           Actual Value         Expected Value           gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value         7         7 ± 0.0009765625           gt_AstL			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32         tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32         tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32         tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32         tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32         tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32         tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TsMitCommand_MtrNm_f32         tgt_AstLmt_Per1_TsMitCommand_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32         tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32         tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32         tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32         tgt_AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32           Name         Actual Value         Expected Value           gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value         7         7 ± 0.0009765625           gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value         7         7 ± 0.0009765625			
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TretmalLimit_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TretmintMin_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TretimitMin_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  stg_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  stg_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  stg_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value  gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value  7			
gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32         tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32         tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PullCompCmd_MtrNm_f32         tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32         tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TSMitCommand_MtrNm_f32         tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitPerc_Uls_f32         tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TrqLimitMin_MtrNm_f32         tgt_AstLmt_Per1_TrmalLimit_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TrqLimitMin_MtrNm_f32         tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_VehSpd_Kph_f32         tgt_AstLmt_Per1_VehSpd_Kph_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32         tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32           Name         Actual Value         Expected Value           gt_AstLmt_Per1_InitForStall_MtrNm_f32.value         0.400000006         0.400000006 ± 0.000030517578125           gt_AstLmt_Per1_PreLimitForque_MtrNm_f32.value         7 ± 0.0009765625			
gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32         tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PullCompCmd_MtrNm_f32         tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32         tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TSMitCommand_MtrNm_f32         tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitPerc_Uls_f32         tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TrqLimitMin_MtrNm_f32         tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_VehSpd_Kph_f32         tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_VehSpd_Kph_f32         tgt_AstLmt_Per1_VehSpd_Kph_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32         tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32           Name         Actual Value         Expected Value           gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value         0.400000006         0.400000006 ± 0.000030517578125           gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value         7         7 ± 0.0009765625           gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value         7         7 ± 0.0009765625			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32         tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32         tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32         target_AstLmt_Per1_TSMitCommand_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_UIs_f32         tgt_AstLmt_Per1_ThermalLimitPerc_UIs_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32         tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32         tgt_AstLmt_Per1_VehSpd_Kph_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32         tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32           Name         Actual Value         Expected Value           gt_AstLmt_Per1_InitFercentFiltered_UIs_f32.value         0.400000006         0.400000006 ± 0.000030517578125           gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value         7 ± 0.0009765625           gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value         7 ± 0.0009765625		1	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32         tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32         target_AstLmt_Per1_TSMitCommand_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_UIs_f32         tgt_AstLmt_Per1_ThermalLimitPerc_UIs_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32         tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32         tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32         tgt_AstLmt_Per1_VehSpd_Kph_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32         tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32           Name         Actual Value         Expected Value           gt_AstLmt_Per1_ImitPercentFiltered_Uls_f32.value         0.400000006         0.400000006 ± 0.000030517578125           gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value         7         7 ± 0.0009765625           gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value         7         7 ± 0.0009765625			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32         target_AstLmt_Per1_TSMitCommand_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32         tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32         tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32         tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32         tgt_AstLmt_Per1_VehSpd_Kph_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32         tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32           Name         Actual Value         Expected Value           gt_AstLmt_Per1_ImitPercentFiltered_Uls_f32.value         0.400000006         0.400000006 ± 0.000030517578125           gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value         7         7 ± 0.0009765625           gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value         7         7 ± 0.0009765625			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32         tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32         tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32         tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32         tgt_AstLmt_Per1_VehSpd_Kph_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32         tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32           Name         Actual Value         Expected Value           gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value         0.400000006         0.400000006 ± 0.000030517578125           gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value         7         7 ± 0.0009765625           gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value         7         7 ± 0.0009765625			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32       tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32         gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32       tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32         gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32       tgt_AstLmt_Per1_VehSpd_Kph_f32         gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32       tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32         Name       Actual Value       Expected Value         gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value       0.400000006       0.400000006 ± 0.000030517578125         gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value       7       7 ± 0.0009765625         gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value       7       7 ± 0.0009765625			
gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TrqLimitMin_MtrNm_f32         tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32         tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32         tgt_AstLmt_Per1_VehSpd_Kph_f32         tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32           Vame         Actual Value         Expected Value           gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value         0.400000006         0.400000006 ± 0.000030517578125           gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value         7         7 ± 0.0009765625           gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value         7         7 ± 0.0009765625			
tgt_Rte_Inst_Ap_AstLmt_Per1_VehSpd_Kph_f32 tgt_Rte_Inst_Ap_AstLmt_Per1_VehSpd_Kph_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Name Actual Value  gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.400000006 0.400000006 ± 0.000030517578125 gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 7 ± 0.0009765625 gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 7 ± 0.0009765625			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32         tgt_AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32           Name         Actual Value         Expected Value           gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value         0.400000006         0.400000006 ± 0.000030517578125           gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value         7         7 ± 0.0009765625           gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value         7         7 ± 0.0009765625			
Name         Actual Value         Expected Value           gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value         0.400000006         0.400000006 ± 0.000030517578125           gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value         7         7 ± 0.0009765625           gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value         7         7 ± 0.0009765625		1	
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value       0.400000006       0.400000006 ± 0.000030517578125         gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value       7       7 ± 0.0009765625         gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value       7       7 ± 0.0009765625	_ASILMI.ASILMI_Peri_wineeiimbalanceCmd_MtrNm_f32		
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value       7       \$\pmu\$ 0.0009765625         gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value       7       \$\pmu\$ 0.0009765625		·	Resi
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 7			
v			
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value 2.79999995 2.79999995 ± 0.0009765625	_SumLimTrqCmd_MtrNm_f32.value	2.79999995 ± 0.0009765625	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>	

Innut Value		
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1		
0		
8		
-1.20000005		
0.5		
1		
0.5		
-7		
6		
0.5		
2.5		
38.2999992		
-8		
	m f32	
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	32	
	_	
0		
	lgc	
	2	
	gc	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_AstLmt_Peri_PowerLimitPerc_Uis_rs2 tgt_AstLmt_Peri_PowerLimitPorStall_MtrNm_f32		
0		
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f		
0	32	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f	32 2	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3:	32 2 2	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3	32 2 2 32	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_	32 2 2 2 332 m_f32	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN	32 2 2 2 332 m_f32	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3:	32 2 2 2 332 m_f32	
tgl_AstLmt_Per1_PreLimitTorque_MtrNm_f:  tgl_AstLmt_Per1_PrkAssistCmd_MtrNm_f3:  tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3:  tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN  tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3:  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	32 2 2 2 332 m_f32	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	32 2 2 (32 m_f32	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3: tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr	32 2 2 332 m_f32 ? Nm_f32	Rec
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3: tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr Actual Value	32 2 2 332 m_f32 2 Nm_f32 Expected Value	Resi
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3: tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr Actual Value 0.5	Nm_f32  Expected Value  0.5 ± 0.000030517578125	Resi
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3: tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr Actual Value  0.5 8.80000019	Nm_f32  Expected Value  0.5 ± 0.00030517578125  8.80000019 ± 0.0009765625	Resi
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3: tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr Actual Value 0.5	Nm_f32  Expected Value  0.5 ± 0.000030517578125	Resi
	0 8 -1.20000005 0.5 1 0.5 1 0.5 -7 6 0.5 2.5 38.2999992 -8 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrN tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f3 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3 tgt_AstLmt_Per1_CombinedDamping_MtrNr tgt_AstLmt_Per1_LimitPercentFiltered_Uls_igt_AstLmt_Per1_LimitPercentFiltered_Uls_igt_AstLmt_Per1_LimitPercentFiltered_Uls_igt_AstLmt_Per1_LimitPerCtrEcolisable_Cnt_igt_AstLmt_Per1_LimPnCtrEcolisable_Cnt_igt_AstLmt_Per1_LimPnCtrTCmd_MtrNm_f3 tgt_AstLmt_Per1_LimPnCtrTCmd_MtrNm_f3 tgt_AstLmt_Per1_OptrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OutputRampMult_Uls_f32 tgt_AstLmt_Per1_OptsevrCCDisable_Cnt_igt_AstLmt_Per1_OutputRampMult_Uls_f32 tgt_AstLmt_Per1_OptsevrCCDisable_Cnt_igt_AstLmt_Per1_OptsevrCCDisable_Cnt_igt_AstLmt_Per1_OptsevrCCDisable_Cnt_igt_AstLmt_Per1_OutputRampMult_Uls_f32	0 2.25999999 0 tgt Rte_Inst_Ap_AstLmt 4.5 -2.5999999 -2.5999999 -7 0.400000006 0.5 2.2000005 1.89999998 1 0 0 7 1 0 0 8 -1.20000005 0.5 1 1 0.5 -7 6 0.5 2.5 38.2999992 -8 tgt_AstLmt_Per1_AssistComd_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPerCortCord_MtrNm_f32 tgt_AstLmt_Per1_LimitPerCortCord_Igc tgt_AstLmt_Per1_LimitPerCortCord_Igc tgt_AstLmt_Per1_LimitPerCortCord_Igc tgt_AstLmt_Per1_LimitPerCortCord_Igc tgt_AstLmt_Per1_LimitPerCortTord_MtrNm_f32





Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~	

Test Step 2.6 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt ManualTrqCmd MtrNm M f32	-1.76999998		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	3.5999999		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.5		
tgt_AstLmt_Per1_13/miccommand_winvin_i3z.value	-3		
tgt_AstLmt_Per1_AssistCmd_MtrNm_i32.value	-3 -8.80000019		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.600000024		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.29999995		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	2		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	8		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.5999999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	36.4000015		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	1		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_N	//trNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNi		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_C		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt AstLmt Per1 LimitPercentFiltered U	<del>_</del> -	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrCCDisable 0	_	
tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrEnable Cnt Igc	tgt AstLmt Per1 LrnPnCtrEnable Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_C	_ <del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_UIs_t		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNi		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_N		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd	_MtrNm_f32	
Name	Actual Value	Expected Value	Res
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	3	3 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1.20000005	-1.20000005 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.720000029	-0.720000029 ± 0.0009765625	
	0.60000024	0.600000024 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.7 (Repeat Count = 1)	Innut Value		
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1.75000000		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1.75999999		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt k_SumLimPlCmpLimit_MtrNm_f32	tgt_Rte_Inst_Ap_AstLmt 2.70000005		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.4000001		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-4		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	8.80000019		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.60000019		
gt_AstLmt_Per1_AssistEOTGain_Ois_ioz.value	0.69999988		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.400001		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.0999999		
gt_AstLmt_Per1_Assistverisput.imt_withth_is2.value	3		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LmPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32.value	-2.0999999		
gt_Ast_mt_Fer1_cmFrotirCmd_withtin_isz.value	-4.5		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.699999988		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	1.20000005		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8		
pt AstLmt Per1 ThermalLimitPerc Uls f32.value	0.69999988		
gt_AstLmt_Per1_ThermalLimit_dre_dis_id2.value	2.70000005		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	26.6000004		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	2		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_N		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNi	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_M		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_C		
gt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32	tgt_AstLmt_Per1_LimitPercentFiltered_l	<del>_</del> -	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm		
gt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrCCDisable Cnt Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_0	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt AstLmt Per1 LrnPnCtrEnable Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNi	•	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt AstLmt Per1 OutputRampMult Uls		
pt Rte Inst Ap AstLmt.AstLmt Per1 PosServCCDisable Cnt Igc	tgt AstLmt Per1 PosServCCDisable C		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_	-·	
gt Rte Inst Ap AstLmt.AstLmt Per1 PreLimitForStall MtrNm f32	tgt AstLmt Per1 PreLimitForStall MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN	_	
gt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNn	_	
gt Rte Inst Ap AstLmt.AstLmt Per1 SumLimTrqCmd MtrNm f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_N	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt AstLmt Per1 ThermalLimit MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f		
gt Rte Inst Ap AstLmt.AstLmt Per1 VehSpd Kph f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd	MtrNm f32	
Jame			Doc:
	Actual Value	Expected Value	Resu
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.699999988 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-8.80000019	-8.80000019 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	-0.48999998 0.699999988	-0.49000001 ± 0.0009765625 0.699999988 ± 0.0009765625	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>	

Test Step 2.8 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32	-8.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
S_SumLimPlCmpLimit_MtrNm_f32	1.79999995		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.29999995		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-5		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	0		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.800000012		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.5		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.20000005		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	4.19999981		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
yt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012		
pt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	2		
pt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-1.20000005		
yt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.79999995		
at AstLmt Per1 VehSpd Kph f32.value	34.7000008		
t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_t	132	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_UIs_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrI	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_I	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrN	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_	_Cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cn	t_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LmPnCtrTCmd_MtrN	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Ul	s_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_	Cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls	_f32	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_Mtr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrI	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrN		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrN	_	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_	_	
pt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimitPerc Uls f32	tgt_AstLmt_Per1_ThermalLimitPerc_U		
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNn	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt AstLmt Per1 TrqLimitMin MtrNm		
gt_Rte_Inst_Ap_AstLint.AstLint_Fer1_TrqLinittviii_witrviii_is2 gt_Rte_Inst_Ap_AstLint.AstLint_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	_102_	
		d MtrNm f22	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCm		-
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-4.5	-4.5 ± 0.0009765625	
	-3.60000014	-3.5999999 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-3.00000014	0.0000000 = 0.00001 00020	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>~</b>

Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	8.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
<_SumLimPlCmpLimit_MtrNm_f32	0.89999976		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.20000005		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6.19999981		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	1.5		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.89999976		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.5999999		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.2999995		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	3.2999995		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.899999976		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.899999976		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	3		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.899999976		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.9000001		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	65.9000015		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	4.0999999		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt		
gt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTGain Uls f32	tgt AstLmt Per1 AssistEOTGain Uls f3:	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mti	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cni		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_UI		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr		
gt Rte Inst Ap AstLmt.AstLmt Per1 LmPnCtrEnable Cnt Igc	tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_lg		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_132	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	20	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cn	— ·	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_I	MtrNm_f32	
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.899999976	0.899999976 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	6.19999981	6.19999981 ± 0.0009765625	
lgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-2.66000032	-2.66000009 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.809999943	-0.810000002 ± 0.0009765625	
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.899999976	0.899999976 ± 0.0009765625	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>	

Test Step 2.10 (Repeat Count = 1) Name	Input Value		
	The state of the s		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-7.5		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPICmpLimit_MtrNm_f32 C_SumLimPICmpLimit_MtrNm_f32 C_SumLimPICmpLimit_MtrNm_f32	5.63000011		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-7		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-2.5999999		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.89999976		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	1		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0.69999988		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.4000001		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	6		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	3.2999995		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	4.5		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.10000001		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	3		
pt_AstLmt_Per1_VehSpd_Kph_f32.value	25.5		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	5.19999981		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	)	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt AstLmt Per1 AssistEOTDamping MtrNm f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_UIs_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt AstLmt Per1 LimitedReturn MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_I		
	tgt AstLmt Per1 LrnPnCtrTCmd MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32		1_132	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	£2.2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M	trNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_t	732	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
lame	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.100000001	0.100000001 ± 0.000030517578125	
gt_AstLmt_Per1_cimitretcent intered_ors_roz.value	7	7 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitPorque_MtrNm_f32.value	3.29999995	3.29999995 ± 0.0009765625	
	0.330000013	0.330000013 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value			



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>	

Test Step 2.11 (Repeat Count = 1) Name	Input Value		
	0		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt ManualTrqCmd MtrNm M f32	7.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	6.23999977		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2		
	-2 -8		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value			
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	2		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	1.10000002		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0.800000012		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.5		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	7.5999999		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	3.2999995		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.20000003		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.20000003		
pt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	3.0999999		
yt_AstLmt_Per1_VehSpd_Kph_f32.value	75.8000031		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	6		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	Nm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN		
		_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtrl		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgd		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt	<del>-</del> -	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_	<u>f</u> 32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_t	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt AstLmt Per1 WheellmbalanceCmd M	ltrNm f32	
Name	Actual Value	Expected Value	Res
			Kest
gt_AstLmt_Per1_LimitPercentFiltered_UIs_f32.value	0.200000003	0.200000003 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
	-8.80000019	-8.80000019 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value			
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	-0.160000013 -0.160000011 0.800000012	-0.159999996 ± 0.0009765625 0.800000012 ± 0.0009765625	





Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.12 (Repeat Count = 1)			•
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-6.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	7.44999981		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.89999998		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	1.60000002		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	1		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	1.20000005		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0.89999976		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.5999999		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	8		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1 -2		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	0.300000012		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PowerLimitrerc_Ois_is2.value	6		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3.29999995		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	3.20000005		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	28.2000008		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	lm f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_t	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt	lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3:	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	<u>lgc</u>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_	<u>f</u> 32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_t	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_Mi	rNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	1.60000002	1.60000002 ± 0.0009765625	,
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1	-1 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.300000012	-0.300000012 ± 0.0009765625	
	1		

0.899999976

tgt\_AstLmt\_Per1\_TrqLimitMin\_MtrNm\_f32.value

0.899999976 ± 0.0009765625



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>	

ManualTrqCmdEn_Cnt_M_lgc  ManualTrqCmd_MtrNm_M_f32  SteeringAsstDefeat_Cnt_M_lgc  :_Ap_AstLmt implCmpLimit_MtrNm_f32  sstLmt_Per1_TSMitCommand_MtrNm_f32.value  mt_Per1_AssistEotDamping_MtrNm_f32.value  mt_Per1_AssistEotDamping_MtrNm_f32.value  mt_Per1_AssistEotDamping_MtrNm_f32.value  mt_Per1_AssistEotDamping_MtrNm_f32.value  mt_Per1_AssistStallLimit_MtrNm_f32.value  mt_Per1_AssistStallLimit_MtrNm_f32.value  mt_Per1_AssistVehSpdLimit_MtrNm_f32.value  mt_Per1_CombinedDamping_MtrNm_f32.value  mt_Per1_DefeatLimitService_Cnt_lgc.value  mt_Per1_DefeatLimitService_Cnt_lgc.value  mt_Per1_LimitedReturn_MtrNm_f32.value  mt_Per1_LmPnCtrCcDisable_Cnt_lgc.value  mt_Per1_LmPnCtrTCmd_MtrNm_f32.value  mt_Per1_DpTrqOvr_MtrNm_f32.value  mt_Per1_OpTrqOvr_MtrNm_f32.value  mt_Per1_PosServCcDisable_Cnt_lgc.value  mt_Per1_PosServCcDisable_Cnt_lgc.value  mt_Per1_PowerLimitPerc_Uls_f32.value  mt_Per1_PowerLimitPerc_Uls_f32.value  mt_Per1_PllCompCmd_MtrNm_f32.value  mt_Per1_ThermalLimitPerc_Uls_f32.value  mt_Per1_ThermalLimitPerc_Uls_f32.value  mt_Per1_ThermalLimitMtrNm_f32.value  mt_Per1_ThermalLimitMtrNm_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_NheellmbalanceCmd_MtrNm_f32.value  mt_Per1_WheellmbalanceCmd_MtrNm_f32.value  Jnst_Ap_AstLmt.AstLmt_Per1_AssistEotDamping_MtrNm_f32  Jnst_Ap_AstLmt.AstLmt_Per1_AssistEotDain_Uls_f32	Input Value  0 6.55999994 0 tgt_Rte_Inst_Ap_AstLmt 1.24000001 -1.799999995 2 4 0.5 1.29999995 0.40000006 0.69999988 -1.20000005 0 -1 1 0 -2 -3 0.40000006 1 0.40000006 7 5.5999999 0.40000006 3.2999995 85.4000015 8		
ManualTrqCmd_MtrNm_M_f32  SteeringAsstDefeat_Cnt_M_lgc t_Ap_AstLmt imPlCmpLimit_MtrNm_f32  sstLmt_Per1_TSMitCommand_MtrNm_f32.value mt_Per1_AssistEOTDamping_MtrNm_f32.value mt_Per1_AssistEOTDamping_MtrNm_f32.value mt_Per1_AssistEOTGain_Uls_f32.value mt_Per1_AssistEOTLimit_MtrNm_f32.value mt_Per1_AssistStallLimit_MtrNm_f32.value mt_Per1_AssistStallLimit_MtrNm_f32.value mt_Per1_AssistVehSpdLimit_MtrNm_f32.value mt_Per1_CombinedDamping_MtrNm_f32.value mt_Per1_DefeatLimitService_Cnt_lgc.value mt_Per1_LimitedReturn_MtrNm_f32.value mt_Per1_LimpCtrCCDisable_Cnt_lgc.value mt_Per1_LrmPnCtrCDisable_Cnt_lgc.value mt_Per1_LmPnCtrTCmd_MtrNm_f32.value mt_Per1_OpTrqOvr_MtrNm_f32.value mt_Per1_OpTrqOvr_MtrNm_f32.value mt_Per1_PosServCCDisable_Cnt_lgc.value mt_Per1_PosServCDisable_Cnt_lgc.value mt_Per1_PowerLimitPerc_Uls_f32.value mt_Per1_PowerLimitPerc_Uls_f32.value mt_Per1_PrkAssistCmd_MtrNm_f32.value mt_Per1_PrhamalLimit_MtrNm_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_WheellmbalanceCmd_MtrNm_f32.value lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	6.55999994 0 tgt_Rte_Inst_Ap_AstLmt 1.24000001 -1.79999995 2 4 0.5 1.29999995 0.40000006 0.699999988 -1.20000005 0 -1 1 0 0 -2 -3 0.400000006 1 0.400000006 7 5.5999999 0.40000006 3.29999995 85.4000015 8		
SteeringAsstDefeat_Cnt_M_lgc t_Ap_AstLmt imPlCmpLimit_MtrNm_f32 sstLmt_Per1_TSMitCommand_MtrNm_f32.value mt_Per1_AssistCmd_MtrNm_f32.value mt_Per1_AssistEOTDamping_MtrNm_f32.value mt_Per1_AssistEOTDamping_MtrNm_f32.value mt_Per1_AssistEOTLimit_MtrNm_f32.value mt_Per1_AssistStallLimit_MtrNm_f32.value mt_Per1_AssistStallLimit_MtrNm_f32.value mt_Per1_AssistVehSpdLimit_MtrNm_f32.value mt_Per1_CombinedDamping_MtrNm_f32.value mt_Per1_DefeatLimitService_Cnt_lgc.value mt_Per1_LimitedReturn_MtrNm_f32.value mt_Per1_LimitedReturn_MtrNm_f32.value mt_Per1_LimPnCtrCcDisable_Cnt_lgc.value mt_Per1_LimPnCtrCnd_MtrNm_f32.value mt_Per1_LimPnCtrCnd_MtrNm_f32.value mt_Per1_OpTrqOvr_MtrNm_f32.value mt_Per1_OptrqOvr_MtrNm_f32.value mt_Per1_PosServCcDisable_Cnt_lgc.value mt_Per1_PosServCDisable_Cnt_lgc.value mt_Per1_PowerLimitPerc_Uls_f32.value mt_Per1_PrkAssistCmd_MtrNm_f32.value mt_Per1_PrhermalLimitPerc_Uls_f32.value mt_Per1_ThermalLimitPerc_Uls_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_WheellmbalanceCmd_MtrNm_f32.value lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	0 tgt_Rte_Inst_Ap_AstLmt 1.24000001 -1.79999995 2 4 0.5 1.29999995 0.40000006 0.699999988 -1.20000005 0 -1 1 0 -2 -3 0.400000006 1 0.400000006 7 5.5999999 0.40000006 3.29999995 85.4000015 8		
t_Ap_AstLmt imPlCmpLimit_MtrNm_f32 sstLmt_Per1_TSMitCommand_MtrNm_f32.value mt_Per1_AssistCmd_MtrNm_f32.value mt_Per1_AssistEOTDamping_MtrNm_f32.value mt_Per1_AssistEOTGain_Uls_f32.value mt_Per1_AssistEOTLimit_MtrNm_f32.value mt_Per1_AssistStallLimit_MtrNm_f32.value mt_Per1_AssistVehSpdLimit_MtrNm_f32.value mt_Per1_CombinedDamping_MtrNm_f32.value mt_Per1_CombinedDamping_MtrNm_f32.value mt_Per1_DefeatLimitService_Cnt_Igc.value mt_Per1_LimitedReturn_MtrNm_f32.value mt_Per1_LimitedReturn_MtrNm_f32.value mt_Per1_LrmPnCtrCDisable_Cnt_Igc.value mt_Per1_LrmPnCtrCDisable_Cnt_Igc.value mt_Per1_LrmPnCtrTCmd_MtrNm_f32.value mt_Per1_OpTrqOvr_MtrNm_f32.value mt_Per1_OptrqOvr_MtrNm_f32.value mt_Per1_PosServCCDisable_Cnt_Igc.value mt_Per1_PosServCDisable_Cnt_Igc.value mt_Per1_PowerLimitPerc_Uls_f32.value mt_Per1_PrkAssistCmd_MtrNm_f32.value mt_Per1_PremalLimitPerc_Uls_f32.value mt_Per1_ThermalLimitPerc_Uls_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_WheellmbalanceCmd_MtrNm_f32.value lnst_Ap_AstLmt.AstLmt_Per1_AssistCod_MtrNm_f32 lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_Rte_Inst_Ap_AstLmt 1.24000001 -1.79999995 2 4 0.5 1.29999995 0.40000006 0.69999988 -1.20000005 0 -1 1 0 -2 -3 0.40000006 1 0.40000006 7 5.5999999 0.40000006 3.2999995 85.4000015 8		
imPlCmpLimit_MtrNm_f32 sstLmt_Per1_TSMitCommand_MtrNm_f32.value mt_Per1_AssistCmd_MtrNm_f32.value mt_Per1_AssistEOTDamping_MtrNm_f32.value mt_Per1_AssistEOTGain_Uls_f32.value mt_Per1_AssistEOTLimit_MtrNm_f32.value mt_Per1_AssistStallLimit_MtrNm_f32.value mt_Per1_AssistStallLimit_MtrNm_f32.value mt_Per1_CombinedDamping_MtrNm_f32.value mt_Per1_DefeatLimitService_Cnt_Igc.value mt_Per1_LimitedReturn_MtrNm_f32.value mt_Per1_LimitedReturn_MtrNm_f32.value mt_Per1_LimitedReturn_MtrNm_f32.value mt_Per1_LimPnCtrCcDisable_Cnt_Igc.value mt_Per1_LimPnCtrCnd_MtrNm_f32.value mt_Per1_DopTrqOvr_MtrNm_f32.value mt_Per1_OpTrqOvr_MtrNm_f32.value mt_Per1_OptrqOvr_MtrNm_f32.value mt_Per1_PosServCcDisable_Cnt_Igc.value mt_Per1_PosServCDisable_Cnt_Igc.value mt_Per1_PowerLimitPerc_Uls_f32.value mt_Per1_PowerLimitPerc_Uls_f32.value mt_Per1_PrkAssistCmd_MtrNm_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_WheellmbalanceCmd_MtrNm_f32.value lnst_Ap_AstLmt.AstLmt_Per1_AssistCod_MtrNm_f32 lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	1.24000001 -1.79999995 2 4 0.5 1.29999995 0.40000006 0.69999988 -1.2000005 0 -1 1 0 -2 -3 0.40000006 1 0.40000006 7 5.599999 0.40000006 3.2999995 85.4000015 8		
astLmt_Per1_TSMitCommand_MtrNm_f32.value  mt_Per1_AssistCmd_MtrNm_f32.value  mt_Per1_AssistEOTDamping_MtrNm_f32.value  mt_Per1_AssistEOTGain_Uls_f32.value  mt_Per1_AssistEOTLimit_MtrNm_f32.value  mt_Per1_AssistStallLimit_MtrNm_f32.value  mt_Per1_AssistVehSpdLimit_MtrNm_f32.value  mt_Per1_CombinedDamping_MtrNm_f32.value  mt_Per1_DefeatLimitService_Cnt_lgc.value  mt_Per1_LimitedReturn_MtrNm_f32.value  mt_Per1_LimPnCtrCcDisable_Cnt_lgc.value  mt_Per1_LimPnCtrCcDisable_Cnt_lgc.value  mt_Per1_LimPnCtrEnable_Cnt_lgc.value  mt_Per1_DopTrqOvr_MtrNm_f32.value  mt_Per1_OpTrqOvr_MtrNm_f32.value  mt_Per1_OptrqOvr_MtrNm_f32.value  mt_Per1_PosServCcDisable_Cnt_lgc.value  mt_Per1_PowerLimitPerc_Uls_f32.value  mt_Per1_PowerLimitPerc_Uls_f32.value  mt_Per1_PthAssistCmd_MtrNm_f32.value  mt_Per1_ThermalLimitPerc_Uls_f32.value  mt_Per1_ThermalLimitPers_Uls_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_VehSpd_Kph_f32.value  mt_Per1_VehSpd_Kph_f32.value  mt_Per1_WheellmbalanceCmd_MtrNm_f32.value  lnst_Ap_AstLmt.AstLmt_Per1_AssistCod_MtrNm_f32  lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	-1.79999995 2 4 0.5 1.29999995 0.40000006 0.699999988 -1.2000005 0 -1 1 0 -2 -3 0.40000006 1 0.40000006 7 5.599999 0.40000006 3.2999995 85.4000015 8		
mt_Per1_AssistCmd_MtrNm_f32.value  mt_Per1_AssistEOTDamping_MtrNm_f32.value  mt_Per1_AssistEOTGain_Uls_f32.value  mt_Per1_AssistEOTLimit_MtrNm_f32.value  mt_Per1_AssistStallLimit_MtrNm_f32.value  mt_Per1_AssistVehSpdLimit_MtrNm_f32.value  mt_Per1_CombinedDamping_MtrNm_f32.value  mt_Per1_DefeatLimitService_Cnt_lgc.value  mt_Per1_LimitedReturn_MtrNm_f32.value  mt_Per1_LimPnCtrCcDisable_Cnt_lgc.value  mt_Per1_LimPnCtrCcDisable_Cnt_lgc.value  mt_Per1_LimPnCtrEnable_Cnt_lgc.value  mt_Per1_DopTrqOvr_MtrNm_f32.value  mt_Per1_OpTrqOvr_MtrNm_f32.value  mt_Per1_OptrqOvr_MtrNm_f32.value  mt_Per1_PosServCcDisable_Cnt_lgc.value  mt_Per1_Posert.imitPerc_Uls_f32.value  mt_Per1_Powert.imitPerc_Uls_f32.value  mt_Per1_PthAssistCmd_MtrNm_f32.value  mt_Per1_ThermalLimitPerc_Uls_f32.value  mt_Per1_ThermalLimitPerc_Uls_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_VehSpd_Kph_f32.value  mt_Per1_VehSpd_Kph_f32.value  mt_Per1_WheellmbalanceCmd_MtrNm_f32.value  lnst_Ap_AstLmt.AstLmt_Per1_AssistCod_MtrNm_f32  lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	2 4 0.5 1.29999995 0.40000006 0.699999988 -1.2000005 0 -1 1 0 -2 -3 0.40000006 1 0.40000006 7 5.5999999 0.40000006 3.2999995 85.4000015 8		
mt_Per1_AssistEOTDamping_MtrNm_f32.value mt_Per1_AssistEOTGain_Uls_f32.value mt_Per1_AssistEOTLimit_MtrNm_f32.value mt_Per1_AssistStallLimit_MtrNm_f32.value mt_Per1_AssistStallLimit_MtrNm_f32.value mt_Per1_CombinedDamping_MtrNm_f32.value mt_Per1_DefeatLimitService_Cnt_lgc.value mt_Per1_LimitedReturn_MtrNm_f32.value mt_Per1_LimitedReturn_MtrNm_f32.value mt_Per1_LimPnCtrCoDisable_Cnt_lgc.value mt_Per1_LimPnCtrEnable_Cnt_lgc.value mt_Per1_LimPnCtrTCmd_MtrNm_f32.value mt_Per1_OpTrqOvr_MtrNm_f32.value mt_Per1_OpTrqOvr_MtrNm_f32.value mt_Per1_OutputRampMult_Uls_f32.value mt_Per1_PosServCoDisable_Cnt_lgc.value mt_Per1_PosServCoDisable_Cnt_lgc.value mt_Per1_PowerLimitPerc_Uls_f32.value mt_Per1_PowerLimitPerc_Uls_f32.value mt_Per1_PillCompCmd_MtrNm_f32.value mt_Per1_ThermalLimitPerc_Uls_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_WheellmbalanceCmd_MtrNm_f32.value lnst_Ap_AstLmt.AstLmt_Per1_AssistCod_MtrNm_f32 lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	4 0.5 1.29999995 0.40000006 0.69999988 -1.2000005 0 -1 1 0 -2 -3 0.40000006 1 0.40000006 7 5.5999999 0.40000006 3.2999995 85.4000015 8		
mt_Per1_AssistEOTGain_Uls_f32.value mt_Per1_AssistEOTLimit_MtrNm_f32.value mt_Per1_AssistStallLimit_MtrNm_f32.value mt_Per1_AssistStallLimit_MtrNm_f32.value mt_Per1_CombinedDamping_MtrNm_f32.value mt_Per1_CombinedDamping_MtrNm_f32.value mt_Per1_DefeatLimitService_Cnt_lgc.value mt_Per1_LimitedReturn_MtrNm_f32.value mt_Per1_LimpnCtrCCDisable_Cnt_lgc.value mt_Per1_LimpnCtrCmd_MtrNm_f32.value mt_Per1_LimpnCtrCmd_MtrNm_f32.value mt_Per1_OpTrqOvr_MtrNm_f32.value mt_Per1_OptrqOvr_MtrNm_f32.value mt_Per1_OutputRampMult_Uls_f32.value mt_Per1_PosServCCDisable_Cnt_lgc.value mt_Per1_PosServCDisable_Cnt_lgc.value mt_Per1_PowerLimitPerc_Uls_f32.value mt_Per1_PowerLimitPerc_Uls_f32.value mt_Per1_PillCompCmd_MtrNm_f32.value mt_Per1_ThermalLimitPerc_Uls_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_WheellmbalanceCmd_MtrNm_f32.value lnst_Ap_AstLmt.AstLmt_Per1_AssistCod_MtrNm_f32 lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	0.5 1.29999995 0.40000006 0.699999988 -1.2000005 0 -1 1 0 -2 -3 0.40000006 1 0.40000006 7 5.5999999 0.40000006 3.2999995 85.4000015 8		
mt_Per1_AssistEOTLimit_MtrNm_f32.value  mt_Per1_AssistStallLimit_MtrNm_f32.value  mt_Per1_AssistVehSpdLimit_MtrNm_f32.value  mt_Per1_CombinedDamping_MtrNm_f32.value  mt_Per1_DefeatLimitService_Cnt_lgc.value  mt_Per1_LimitedReturn_MtrNm_f32.value  mt_Per1_LimpnCtrCCDisable_Cnt_lgc.value  mt_Per1_LimpnCtrCmd_MtrNm_f32.value  mt_Per1_LimpnCtrCmd_MtrNm_f32.value  mt_Per1_OpTrqOvr_MtrNm_f32.value  mt_Per1_OptrqOvr_MtrNm_f32.value  mt_Per1_OutputRampMult_Uls_f32.value  mt_Per1_PosServCCDisable_Cnt_lgc.value  mt_Per1_PowerLimitPerc_Uls_f32.value  mt_Per1_PowerLimitPerc_Uls_f32.value  mt_Per1_PrkAssistCmd_MtrNm_f32.value  mt_Per1_PtlCompCmd_MtrNm_f32.value  mt_Per1_ThermalLimitPerc_Uls_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_VehSpd_Kph_f32.value  mt_Per1_VehSpd_Kph_f32.value  mt_Per1_WheellmbalanceCmd_MtrNm_f32.value  linst_Ap_AstLmt.AstLmt_Per1_AssistCod_MtrNm_f32  linst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	1.2999995 0.40000006 0.69999988 -1.2000005 0 -1 1 0 -2 -3 0.40000006 1 0.40000006 7 5.5999999 0.40000006 3.2999995 85.4000015 8		
mt_Per1_AssistStallLimit_MtrNm_f32.value  mt_Per1_AssistVehSpdLimit_MtrNm_f32.value  mt_Per1_CombinedDamping_MtrNm_f32.value  mt_Per1_DefeatLimitService_Cnt_lgc.value  mt_Per1_LimitedReturn_MtrNm_f32.value  mt_Per1_LimitPnCtrCCDisable_Cnt_lgc.value  mt_Per1_LimitPnCtrCmd_MtrNm_f32.value  mt_Per1_LimitPnCtrCmd_MtrNm_f32.value  mt_Per1_OpTrqOvr_MtrNm_f32.value  mt_Per1_OptrqOvr_MtrNm_f32.value  mt_Per1_OptrqOvr_MtrNm_f32.value  mt_Per1_PosServCCDisable_Cnt_lgc.value  mt_Per1_PosServCDisable_Cnt_lgc.value  mt_Per1_PowerLimitPerc_Uls_f32.value  mt_Per1_PrkAssistCmd_MtrNm_f32.value  mt_Per1_PillCompCmd_MtrNm_f32.value  mt_Per1_ThermalLimitPerc_Uls_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_VehSpd_Kph_f32.value  mt_Per1_VehSpd_Kph_f32.value  mt_Per1_WheellmbalanceCmd_MtrNm_f32.value  linst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32  linst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	0.40000006 0.699999988 -1.20000005 0 -1 1 0 -2 -3 0.40000006 1 0.40000006 7 5.5999999 0.40000006 3.2999995 85.4000015 8		
mt_Per1_AssistVehSpdLimit_MtrNm_f32.value  mt_Per1_CombinedDamping_MtrNm_f32.value  mt_Per1_DefeatLimitService_Cnt_lgc.value  mt_Per1_LimitedReturn_MtrNm_f32.value  mt_Per1_LimitedReturn_MtrNm_f32.value  mt_Per1_LmPnCtrCcDisable_Cnt_lgc.value  mt_Per1_LmPnCtrTCmd_MtrNm_f32.value  mt_Per1_OpTrqOvr_MtrNm_f32.value  mt_Per1_OutputRampMult_Uls_f32.value  mt_Per1_PosServCcDisable_Cnt_lgc.value  mt_Per1_PowerLimitPerc_Uls_f32.value  mt_Per1_PowerLimitPerc_Uls_f32.value  mt_Per1_PrkAssistCmd_MtrNm_f32.value  mt_Per1_PtllCompCmd_MtrNm_f32.value  mt_Per1_ThermalLimitPerc_Uls_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_WehSpd_Kph_f32.value  mt_Per1_VehSpd_Kph_f32.value  mt_Per1_WheellmbalanceCmd_MtrNm_f32.value  lnst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32  lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	0.699999988 -1.20000005 0 -1 1 0 -2 -3 0.400000006 1 0.400000006 7 5.5999999 0.40000006 3.2999995 85.4000015 8		
mt_Per1_CombinedDamping_MtrNm_f32.value mt_Per1_DefeatLimitService_Cnt_lgc.value mt_Per1_LimitedReturn_MtrNm_f32.value mt_Per1_LimitedReturn_MtrNm_f32.value mt_Per1_LrnPnCtrCcDisable_Cnt_lgc.value mt_Per1_LrnPnCtrEnable_Cnt_lgc.value mt_Per1_OpTrqOvr_MtrNm_f32.value mt_Per1_OpTrqOvr_MtrNm_f32.value mt_Per1_OutputRampMult_Uls_f32.value mt_Per1_PosServCcDisable_Cnt_lgc.value mt_Per1_PowerLimitPerc_Uls_f32.value mt_Per1_PowerLimitPerc_Uls_f32.value mt_Per1_PrkAssistCmd_MtrNm_f32.value mt_Per1_TrkermalLimitPerc_Uls_f32.value mt_Per1_ThermalLimitPerc_Uls_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_WheellmbalanceCmd_MtrNm_f32.value lnst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	-1.20000005 0 -1 1 0 -2 -3 0.400000006 1 0.400000006 7 5.5999999 0.40000006 3.29999995 85.4000015 8		
mt_Per1_DefeatLimitService_Cnt_lgc.value mt_Per1_LimitedReturn_MtrNm_f32.value mt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value mt_Per1_LrnPnCtrEnable_Cnt_lgc.value mt_Per1_LrnPnCtrTCmd_MtrNm_f32.value mt_Per1_OpTrqOvr_MtrNm_f32.value mt_Per1_OutputRampMult_Uls_f32.value mt_Per1_OutputRampMult_Uls_f32.value mt_Per1_PosServCCDisable_Cnt_lgc.value mt_Per1_PowerLimitPerc_Uls_f32.value mt_Per1_PrkAssistCmd_MtrNm_f32.value mt_Per1_PrkAssistCmd_MtrNm_f32.value mt_Per1_PullCompCmd_MtrNm_f32.value mt_Per1_ThermalLimitPerc_Uls_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_WheellmbalanceCmd_MtrNm_f32.value lnst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	0 -1 1 0 -2 -3 0.400000006 1 0.400000006 7 5.5999999 0.40000006 3.29999995 85.4000015 8		
mt_Per1_LimitedReturn_MtrNm_f32.value  mt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value  mt_Per1_LrnPnCtrEnable_Cnt_lgc.value  mt_Per1_LrnPnCtrTCmd_MtrNm_f32.value  mt_Per1_OpTrqOvr_MtrNm_f32.value  mt_Per1_OutputRampMult_Uls_f32.value  mt_Per1_PosServCCDisable_Cnt_lgc.value  mt_Per1_PowerLimitPerc_Uls_f32.value  mt_Per1_PrkAssistCmd_MtrNm_f32.value  mt_Per1_PullCompCmd_MtrNm_f32.value  mt_Per1_ThermalLimitPerc_Uls_f32.value  mt_Per1_ThermalLimitPerc_Uls_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_WhSpd_Kph_f32.value  mt_Per1_WheellmbalanceCmd_MtrNm_f32.value  lnst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32  lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	-1 1 0 -2 -3 0.400000006 1 0.400000006 7 5.5999999 0.40000006 3.2999995 85.4000015 8		
mt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value mt_Per1_LrnPnCtrEnable_Cnt_lgc.value mt_Per1_LrnPnCtrTCmd_MtrNm_f32.value mt_Per1_OpTrqOvr_MtrNm_f32.value mt_Per1_OutputRampMult_Uls_f32.value mt_Per1_OutputRampMult_Uls_f32.value mt_Per1_PosServCCDisable_Cnt_lgc.value mt_Per1_PowerLimitPerc_Uls_f32.value mt_Per1_PrkAssistCmd_MtrNm_f32.value mt_Per1_PullCompCmd_MtrNm_f32.value mt_Per1_ThermalLimitPerc_Uls_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_WheelImbalanceCmd_MtrNm_f32.value lnst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	1 0 -2 -3 0.400000006 1 0.40000006 7 5.5999999 0.40000006 3.29999995 85.4000015 8		
mt_Per1_LrnPnCtrEnable_Cnt_lgc.value  mt_Per1_LrnPnCtrTCmd_MtrNm_f32.value  mt_Per1_OpTrqOvr_MtrNm_f32.value  mt_Per1_OutputRampMult_Uls_f32.value  mt_Per1_PosServCCDisable_Cnt_lgc.value  mt_Per1_PowerLimitPerc_Uls_f32.value  mt_Per1_PrkAssistCmd_MtrNm_f32.value  mt_Per1_PullCompCmd_MtrNm_f32.value  mt_Per1_ThermalLimitPerc_Uls_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_VehSpd_Kph_f32.value  mt_Per1_WheelImbalanceCmd_MtrNm_f32.value  lnst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32  lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	0 -2 -3 0.400000006 1 0.40000006 7 5.5999999 0.40000006 3.29999995 85.4000015 8		
mt_Per1_LrnPnCtrTCmd_MtrNm_f32.value  mt_Per1_OpTrqOvr_MtrNm_f32.value  mt_Per1_OutputRampMult_Uls_f32.value  mt_Per1_PosServCCDisable_Cnt_lgc.value  mt_Per1_PowerLimitPerc_Uls_f32.value  mt_Per1_PrkAssistCmd_MtrNm_f32.value  mt_Per1_PullCompCmd_MtrNm_f32.value  mt_Per1_ThermalLimitPerc_Uls_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_VehSpd_Kph_f32.value  mt_Per1_WheelImbalanceCmd_MtrNm_f32.value  lnst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32  lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	-2 -3 0.40000006 1 0.40000006 7 5.5999999 0.40000006 3.2999995 85.400015 8		
mt_Per1_OpTrqOvr_MtrNm_f32.value  mt_Per1_OutputRampMult_Uls_f32.value  mt_Per1_PosServCcDisable_Cnt_lgc.value  mt_Per1_PowerLimitPerc_Uls_f32.value  mt_Per1_PrkAssistCmd_MtrNm_f32.value  mt_Per1_PullCompCmd_MtrNm_f32.value  mt_Per1_ThermalLimitPerc_Uls_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_VehSpd_Kph_f32.value  mt_Per1_WheelImbalanceCmd_MtrNm_f32.value  lnst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32  lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	-3 0.40000006 1 0.40000006 7 5.5999999 0.40000006 3.29999995 85.4000015		
mt_Per1_OutputRampMult_Uls_f32.value mt_Per1_PosServCCDisable_Cnt_lgc.value mt_Per1_PowerLimitPerc_Uls_f32.value mt_Per1_PrkAssistCmd_MtrNm_f32.value mt_Per1_PullCompCmd_MtrNm_f32.value mt_Per1_ThermalLimitPerc_Uls_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_WheelImbalanceCmd_MtrNm_f32.value lnst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	0.40000006 1 0.40000006 7 5.5999999 0.40000006 3.29999995 85.4000015 8		
mt_Per1_PosServCCDisable_Cnt_lgc.value mt_Per1_PowerLimitPerc_Uls_f32.value mt_Per1_PrkAssistCmd_MtrNm_f32.value mt_Per1_PullCompCmd_MtrNm_f32.value mt_Per1_ThermalLimitPerc_Uls_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_WheelImbalanceCmd_MtrNm_f32.value lnst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	1 0.40000006 7 5.599999 0.40000006 3.2999995 85.4000015 8		
mt_Per1_PowerLimitPerc_Uls_f32.value  mt_Per1_PrkAssistCmd_MtrNm_f32.value  mt_Per1_PullCompCmd_MtrNm_f32.value  mt_Per1_ThermalLimitPerc_Uls_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_VehSpd_Kph_f32.value  mt_Per1_WheelImbalanceCmd_MtrNm_f32.value  lnst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32  lnst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	0.40000006 7 5.599999 0.40000006 3.2999995 85.4000015 8		
mt_Per1_PrkAssistCmd_MtrNm_f32.value  mt_Per1_PullCompCmd_MtrNm_f32.value  mt_Per1_ThermalLimitPerc_Uls_f32.value  mt_Per1_ThermalLimit_MtrNm_f32.value  mt_Per1_VehSpd_Kph_f32.value  mt_Per1_WheelImbalanceCmd_MtrNm_f32.value  Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32  Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	7 5.5999999 0.40000006 3.29999995 85.4000015 8		
mt_Per1_PullCompCmd_MtrNm_f32.value mt_Per1_ThermalLimitPerc_Uls_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_WheelImbalanceCmd_MtrNm_f32.value Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	5.5999999 0.400000006 3.29999995 85.4000015 8		
mt_Per1_ThermalLimitPerc_Uls_f32.value mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_WheelImbalanceCmd_MtrNm_f32.value Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	0.40000006 3.2999995 85.4000015 8		
mt_Per1_ThermalLimit_MtrNm_f32.value mt_Per1_VehSpd_Kph_f32.value mt_Per1_WheellmbalanceCmd_MtrNm_f32.value Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	3.29999995 85.4000015 8		
mt_Per1_VehSpd_Kph_f32.value mt_Per1_WheellmbalanceCmd_MtrNm_f32.value Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	85.4000015 8		
mt_Per1_WheellmbalanceCmd_MtrNm_f32.value Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	8		
mt_Per1_WheellmbalanceCmd_MtrNm_f32.value Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32			
Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32			
Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
	tgt_AstLmt_Per1_AssistEOTDamping_MtrNn	n f32	
	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	2	
Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32		
Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm		
Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm	_	
Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_Ige		
Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f3		
Inst_Ap_AstLmt.AstLmt_Per1_Limite Greent intered_ois_132	tgt_AstLmt_Per1_Limiterercentrintered_ois_ic		
Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lg	gc	
Inst_Ap_AstLmt_AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	2	
Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	2	
Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lg	gc	
Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	-	
Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f3		
Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32		
_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32		
_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32		
Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f3	32	
_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm	m_f32	
_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32		
_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrN	Nm_f32	
	Actual Value	Expected Value	Res
mt_Per1_LimitPercentFiltered_Uls_f32.value	0.40000006	0.400000006 ± 0.000030517578125	
mt Per1 PreLimitForStall MtrNm f32.value	3.64000058	3.6400001 ± 0.0009765625	
mt_Per1_PreLimitTorque_MtrNm_f32.value	3.6400058	3.6400001 ± 0.0009765625	
mt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.160000011	0.159999996 ± 0.0009765625	
mt_Per1_TrqLimitMin_MtrNm_f32.value	0.400000011	0.400000006 ± 0.0009765625	





Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	•

Test Step 2.14 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32	-5.55999994		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt 6.38000011		
k_SumLimPlCmpLimit_MtrNm_f32			
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.70000005		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	3.0999999		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0.5		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	0.800000012		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	8		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	3.4000001		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	76.5999985		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-1.20000005		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_	f32	
	tgt AstLmt Per1 AssistEOTDamping		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTDamping_MtrNm_f32		<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_Mtrl		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_	_	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_		
tgt_Rte_Inst_Ap_AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrN	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable	_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cn	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_Mtrl	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_U	ls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_	_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_UIs_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls	s_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target AstLmt Per1 TSMitCommand	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_L	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm		
		_102	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCm	nd MtrNm f32	
			1_
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	
		2 0000000 + 0 0000765625	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3.0999999	3.0999999 ± 0.0009765625	
	-3	-3 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value			,



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	•

Test Step 2.15 (Repeat Count = 1) Name	Input Value		
	·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	5.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPlCmpLimit_MtrNm_f32	0.239999995		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.60000002		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	4		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	6		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.80000019		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0.60000024		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	0.89999976		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-3		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2		
t_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	3.5		
t_AstLmt_Per1_VehSpd_Kph_f32.value	425.299988		
t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-2.0999999		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt AstLmt Per1 AssistEOTDamping		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_	<del>-</del>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_0		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt AstLmt Per1 LimitedReturn MtrNi		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_	_	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_lgc	tgt_AstLint_Fer1_tillFricticCobsable_		
	tgt AstLmt Per1 LrnPnCtrTCmd MtrN		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32		_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Ul		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_	_ <del></del>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_Mtr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNi	<del>_</del>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mtr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_	MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_U	ls_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm	n_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmo	d_MtrNm_f32	
lame	Actual Value	Expected Value	Res
	0.600000024	0.600000024 ± 0.000030517578125	
at Astlimt Per1 LimitPercentFiltered Uls f32 value		3.0000000E- ± 0.000000011010120	
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value		6 63999987 + 0 0009765625	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	6.63999939	6.63999987 ± 0.0009765625	
		6.63999987 ± 0.0009765625 -6.63999987 ± 0.0009765625 -0.360000014 ± 0.0009765625	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.16 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-4.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPICmpLimit_MtrNm_f32	1.36000001 -1.5		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	7		
	0.300000012		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.5		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0.69999988		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	1.7999995		
tgt AstLmt Per1 CombinedDamping MtrNm f32.value	-6.5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt AstLmt Per1 LrnPnCtrEnable Cnt lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrCmd_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt AstLmt Per1 PowerLimitPerc Uls f32.value	0.69999988		
tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value	-2.0999999		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	3.5999999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	153.100006		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-4.5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mti	·Nm f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtrl	- Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls	s_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg	С	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f:	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3:	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_t	732	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_t	732	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_M	/ltrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.699999988 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-5	-5 ± 0.0009765625	٠,
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-3.5	-3.5 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.69999988	0.699999988 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.17 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-1.5		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat Cnt_M_lgc	1		
	tgt Rte Inst Ap AstLmt		
Rte_Inst_Ap_AstLmt k_SumLimPlCmpLimit_MtrNm_f32	2.48000002		
	-1.3999998		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value			
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	1.70000005		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.20000005		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	12.5		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-1		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3:	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M	ltrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNr	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNn	1 f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mi		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cr		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32	tgt_AstLmt_Per1_LimitPercentFiltered_L	<del>_</del> -	
tgt_Rte_Inst_Ap_AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm		
tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrCCDisable C	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt AstLmt Per1 LrnPnCtrEnable Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNn	•	
tgt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_		
	tgt AstLmt Per1 PosServCCDisable C		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PosservCCDisable_C		
tgt_Rte_Inst_Ap_AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimitPerc_UIs_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	1.20000005	1.20000005 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1.20000005	-1.20000005 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.24000001	-0.239999995 ± 0.0009765625	•
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0	0 ± 0.0009765625	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	•	

Test Step 2.18 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt ManualTrqCmd MtrNm M f32	1.5		
AstLmt SteeringAsstDefeat Cnt M Igc	0		
Rte Inst Ap AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	3.5999999		
	-1.29999995		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value			
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.80000012		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	1.79999995		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.29999995		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4.0999999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	510.899994		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-1.20000005		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mi	rNm f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3.	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt AstLmt Per1 DefeatLimitService Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt AstLmt Per1 LimitPercentFiltered Ul		
tgt_Rte_Inst_Ap_AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32	_102	
	tgt_AstLmt_Per1_OutputRampMult_Uls_f	22	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
tgt_Rte_Inst_Ap_AstLmt_Per1_PowerLimitPerc_UIs_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3:	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_I	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	2.0999999	2.0999999 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	1.9200008	1.91999996 ± 0.0009765625	٠,
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.540000021	0.540000021 ± 0.0009765625	
	1.7999995	1.79999995 ± 0.0009765625	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.19 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32	-7		
AstLmt SteeringAsstDefeat Cnt M lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt 4.71999979		
k_SumLimPlCmpLimit_MtrNm_f32			
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3.2999995		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.89999976		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	1.8999998		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.5		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.4000001		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	3.2999995		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	1.20000005		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4.19999981		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	425.700012		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-2.0999999		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f	32	
	tgt AstLmt Per1 AssistEOTDamping		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32		<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_N	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_I		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_0		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNr	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_	Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls	s_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_0	Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_Mtrl	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNi	_	
tgt Rte Inst Ap AstLmt.AstLmt Per1 SumLimTrqCmd MtrNm f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target AstLmt Per1 TSMitCommand	_	
tgt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimitPerc Uls f32	tgt_AstLmt_Per1_ThermalLimitPerc_UI	<del>-</del>	
tgt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimit MtrNm f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLint.AstLint_Peri_ThermatLinit_MitNin_is2 tgt_Rte_Inst_Ap_AstLint.AstLint_Peri_TrqLinitMin_MtrNm_f32	tgt AstLmt Per1 TrqLimitMin MtrNm		
tgt_Rte_inst_Ap_AstLmt.AstLmt_Peri_TrqLimitwiin_wtrviin_is2  tgt_Rte_inst_Ap_AstLmt.AstLmt_Peri_TrqLimitwiin_wtrviin_is2	tgt_AstLmt_Per1_frqLimitiviii_ivitiviii_ tgt_AstLmt_Per1_VehSpd_Kph_f32	IOL	
		MtrNm f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmo		
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.40000006	0.400000006 ± 0.000030517578125	•
	4.5	4.5 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value			
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_t32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-4.5	-4.5 ± 0.0009765625	•
		-4.5 ± 0.0009765625 -1.79999995 ± 0.0009765625	•



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	•

Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	7		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	5.84000015		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.10000002		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	1.3999998		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	0		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	3.2999995		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value			
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	0.5 -3		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	2.2999995		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4.30000019		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	373.299988		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-4		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M	trNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32		
	tgt_AstLmt_Per1_ThermalLimit_MtrNm_t		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	04	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_venSpd_Rpn_t32 tgt_Rte_Inst_Ap_AstLmt.AstLmt Per1_WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32		
Name			Basul
	Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_UIs_f32.value	0.5	0.5 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	6.8499999 -6.8499999	6.8499999 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.8499999	-6.8499999 ± 0.0009765625 0 ± 0.0009765625	
gr_Astern_reli_ounitimitiqoniu_witiMII_i32.value	-0	0 I 0.00097 03023	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Fest Step 2.21 (Repeat Count = 1)	Input Value			
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1			
AstLmt_ManualTrqCmd_MtrNm_M_f32		-6		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1			
Rte_Inst_Ap_AstLmt		tgt_Rte_Inst_Ap_AstLmt		
_SumLimPlCmpLimit_MtrNm_f32		6.96000004		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value		-1		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value		3.2999995		
pt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value		-1		
pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value		0.200000003		
pt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2.0999999			
pt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	1.5			
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.80000019			
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-1			
pt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-6.5	-6.5		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0			
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1			
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	3.29999995			
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5.5999999			
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024	0.60000024		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024	0.600000024		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-4	-4		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3.4000001			
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024			
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4.4000001			
t_AstLmt_Per1_VehSpd_Kph_f32.value	324.600006			
t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-5			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3	2		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_I			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_t			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32		tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32		tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N	_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_C			
pt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32	tgt_AstLmt_Per1_LimitPercentFiltered_			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrCCDisable (	_		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc				
	tgt AstLmt Per1 LrnPnCtrTCmd MtrN	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32		_		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3:			
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls			
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_0			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_			
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32		tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNn	_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNr	_		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mtrl	_		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_N			
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls	_		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_	<u>f</u> 32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f	32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd	_MtrNm_f32		
lame	Actual Value	Expected Value	Res	
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125		
at AstLmt Per1 PreLimitForStall MtrNm f32.value	3.29999995	3.29999995 ± 0.0009765625		
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value gt_AstLmt_Per1_PreLimitTorgue_MtrNm_f32.value	3.29999995 3.29999995	3.29999995 ± 0.0009765625 3.29999995 ± 0.0009765625		
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	3.29999995 3.29999995 1.98000002	3.29999995 ± 0.0009765625 3.29999995 ± 0.0009765625 1.98000002 ± 0.0009765625		



Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~		
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>		

Name	
Section   Sect	
Astlum! Scientinp-Assillation   Display   Disp	
Ris_Inst_Ap_AstLmt	
SumLimPCropLimi Mirkin, 132   gat. Ast.mir, Pert, AssistErOT Mirkin, 132 value   gat. Ast.mir, Pert, AssistSulatini, Mirkin, 132 value   gat. Ast.mir, Pert, AssistSulatini, Mirkin, 132 value   gat. Ast.mir, Pert, Defeat. Irmited Review (and in the second seco	
got Ast.ml Pert _ TSMICOmmand_MitNim_152 value	
g. AstLm. Pert _AssistCOT (MYNn_ 152 value   5.599999   g. AstLm. Pert _AssistCOT (Comp. MYNn_ 152 value   2   2   2   2   2   2   2   2   2	
gl. Aslt.mt Pert   AssistEOTDamping   MRNm, 152 value   2   2   2   2   2   2   2   2   2	
Q. AstLim, Perl. AssistEOTGain, Us. 132 value   2 20000005	
Ast.ml. Perl	
Jastimil Pert   AssistiveNpdit imil, Mrkhm, 132 value   5.5	
Ast.mt   Peri   AssistVehSpdLimit   MtrNm   52 value   2	
AstLmt_Per1_CombinedDamping_MirNm_132 value   2   2   3,2999995   3   3,2999995   3   3,2999995   3   3,2999995   3   3,2999995   3   3,2999995   3   3,2999995   3   3,2999995   3   3,2999995   3   3,2999995   3   3,2999995   3   3,2999995   3   3,2999995   3   3,2999995   3   3,2999995   3   3,2999995   3   3,2999995   3   3,2999999   3   3,2999999   3   3,2999999   3   3,2999999   3   3,2999999   3   3,2999999   3   3,2999999   3   3,2999999   3   3,2999999   3   3,2999999   3   3,2999999   3   3,2999999   3   3,2999999   3   3,29999999   3   3,29999999   3   3,29999999   3   3,29999999   3   3,29999999   3   3,29999999   3   3,299999999   3   3,2999999999   3   3,299999999999   3,2999999999999999999999999999999999999	
gl AstLIM_Pert_LimitedRetum_MrtNin_f32_value         0           gl AstLIM_Pert_LimitedRetum_MrtNin_f32_value         3           gl AstLIM_Pert_LimitedRetum_MrtNin_f32_value         1           gl AstLIM_Pert_LimitedRetum_MrtNin_f32_value         0           gl AstLIM_Pert_ImitedToChosable_Cnt_ligo_value         5           gl AstLIM_Pert_OptTroOv_MitNim_f32_value         5           gl AstLIM_Pert_OptTroOv_MitNim_f32_value         1           gl AstLIM_Pert_PosSexCCDisable_Cnt_ligo_value         1           gl AstLIM_Pert_PowerLiminPerc_Uis_f32_value         0           gl AstLIM_Pert_PrixAssistCom_MitNim_f32_value         5           gl AstLIM_Pert_PrixAssistCom_MitNim_f32_value         45           gl AstLIM_Pert_ThermalLimitPerc_Uis_f32_value         0           gl AstLIM_Pert_ThermalLimitPerc_Uis_f32_value         45           gl AstLIM_Pert_Vert_Dept_Cyb.fi_f32_value         45           gl AstLIM_Pert_Netibilibalinacom_MitNim_f32_value         45           gl Rie_Inst_Ap_AstLIM_AstLIM_Pert_AssistCort Damping_MitNim_f32         tgt_AstLIM_Pert_AssistCort Damping_MitNim_f32           gl Rie_Inst_Ap_AstLIM_AstLIM_Pert_AssistCort Damping_MitNim_f32         tgt_AstLIM_Pert_AssistCort Damping_MitNim_f32           gl Rie_Inst_Ap_AstLIM_AstLIM_Pert_AssistCort Damping_MitNim_f32         tgt_AstLIM_Pert_AssistCort Damping_MitNim_f32           gl Rie_Inst_Ap_AstLIM_AstLIM_P	
J. AstLmt. Perl _ LimitedRetum_MtrNm_f32.value   3.29999995     J. AstLmt. Perl _ LimPriCtrCDisable _ Cnt.   gevalue   1     J. AstLmt. Perl _ LimPriCtrCDisable _ Cnt.   gevalue   0     J. AstLmt. Perl _ LimPriCtrCrind_ MtrNm_f32.value   5.599999     J. AstLmt. Perl _ LimPriCtrCrind_ MtrNm_f32.value   1     J. AstLmt. Perl _ Dovignov_MtrNm_f32.value   0.69999988     J. AstLmt. Perl _ PosServ. CCDisable _ Cnt.   gevalue   1     J. AstLmt. Perl _ PosServ. CCDisable _ Cnt.   gevalue   1     J. AstLmt. Perl _ PosServ. CCDisable _ Cnt.   gevalue   1     J. AstLmt. Perl _ PosServ. CCDisable _ Cnt.   gevalue   1     J. AstLmt. Perl _ PrkAssistCornd_MtrNm_f32.value   5     J. AstLmt. Perl _ PrkAssistCornd_MtrNm_f32.value   4.5     J. AstLmt. Perl _ PuliCompCmd_MtrNm_f32.value   4.5     J. AstLmt. Perl _ ThermalLimiRprc_Uls_ f32.value   0.69999988     J. AstLmt. Perl _ ThermalLimiRprc_Uls_ f32.value   0.69999988     J. AstLmt. Perl _ Nelsyl Kph. f32.value   4.5     J. Rel. Inst. Ap. AstLmt. AstLmt. Perl _ AssistEOTGam_Uls_ f32     J. Rel. Inst. Ap. AstLmt. AstLmt. Perl _ AssistEOTGam_Uls_ f32     J. Rel. Inst. Ap. AstLmt. AstLmt. Perl _ AssistEOTGam_Uls_ f32     J. Rel. Inst. Ap. AstLmt. AstLmt. Perl _ AssistEOTGam_Uls_ f32     J. Rel. Inst. Ap. AstLmt. AstLmt. Perl _ AssistEOTGam_Uls_ f32     J. Rel. Inst. Ap. AstLmt. AstLmt. Perl _ AssistEOTGam_Uls_ f32     J. Rel. Inst. Ap. AstLmt. AstLmt. Perl _ AssistEOTGam_Uls_ f32     J. Rel. Inst. Ap. AstLmt. AstLmt. Perl _ AssistEOTGam_Uls_ f32     J. Rel. Inst. Ap. AstLmt. AstLmt. Perl _ AssistEOTGam_Uls_ f32     J. Rel. Inst. Ap. AstLmt. AstLmt. Perl _ AssistEOTGam_Uls_ f32     J. Rel. Inst. Ap. AstLmt. AstLmt. Perl _ DesireLimitService_ Cnt.   go     J. Rel. Inst. Ap. AstLmt. AstLmt. Perl _ DesireLimitService_ Cnt.   go     J. Rel. Inst. Ap. AstLmt. AstLmt. Perl _ LimiPercentFiltered_ Uls_ f32     J. Rel. Inst. Ap. AstLmt. As	
1, AstLmt_Perl_LmPnCtrCoDisable_Cnt_lgcvalue   1   0   0   0   0   0   0   0   0   0	
J. AstLmT. Pert   LmPhCritTcnde   Cnt   Igc. value   5.599999     J. AstLmT. Pert   LmPhCritTcnde   Mixhm   132 value   5.5999999     J. AstLmt. Pert   OutpurRampMilt, Uls   132 value   1	
AstLmt Per1 LmPnCtrTCmd MtrNm_f32 value	
J. AstLmt. Pert   OptrqOv_MtrNm_f32 value   0.699999988   1.     J. AstLmt. Pert   OrtputRampMult_Uis_182 value   0.699999988   1.     J. AstLmt. Pert   PosservcCDisable_Cnt_Ug. value   1.     J. AstLmt. Pert   PosservcCDisable_Cnt_Ug. value   1.     J. AstLmt. Pert   PowerLmmtPerc_Uis_182 value   0.699999988   1.     J. AstLmt. Pert   PowerLmmtPerc_Uis_182 value   0.699999988   1.     J. AstLmt. Pert   PowerLmmtPerc_Uis_182 value   0.699999988   1.     J. AstLmt. Pert   ThermalLimitPerc_Uis_182 value   0.699999988   1.     J. AstLmt. Pert   ThermalLimit MrNm_f32 value   0.699999988   1.     J. AstLmt. Pert   ThermalLimit MrNm_f32 value   4.5   0.699999988   1.     J. AstLmt. Pert   ThermalLimit MrNm_f32 value   4.5   0.699999988   1.     J. AstLmt. Pert   VenSpd_Kpn_F32 value   4.5   0.699999988   1.     J. AstLmt. Pert   ThermalLimit MrNm_f32 value   4.5   0.699999988   1.     J. AstLmt. Pert   ThermalLimit MrNm_f32 value   4.5   0.699999988   1.     J. AstLmt. Pert   VenSpd_Kpn_F32 value   4.5   0.699999988   1.     J. AstLmt. Pert   ThermalLimit MrNm_f32 value   4.5   0.699999988   1.     J. AstLmt. Pert   ThermalLimit MrNm_f32 value   4.5   0.699999988   1.     J. Rel. Inst. Ap_AstLmt. AstLmt. Pert_AssistEOTIO MrNm_f32 value   4.5   0.6999999988   1.     J. Rel. Inst. Ap_AstLmt. AstLmt. Pert_AssistEOTIO MrNm_f32 value   1.     J. Rel. Inst. Ap_AstLmt. AstLmt. Pert_AssistEOTIO MrNm_f32 value   1.     J. Rel. Inst. Ap_AstLmt. AstLmt. Pert_AssistEOTIO MrNm_f32 value   1.     J. Rel. Inst. Ap_AstLmt. AstLmt. Pert_Combined Demping, MrNm_f32 value   1.     J. Rel. Inst. Ap_AstLmt. AstLmt. Pert_LimiteDercombinitiered_Uis_f32 value   1.     J. Rel. Inst. Ap_AstLmt. AstLmt. Pert_LimiteGroun_MrNm_f32 value   1.     J. Rel. Inst. Ap_AstLmt. AstLmt. Pert_Dercombinitier	
31, AstLmt, Pert   PosSenvCCDisable, Cnt.   Jgc. value   1	
1, AstLmt_Per1   PosservCCDisable_Cnt_lgc_value   1	
AstLmt   Perf   PowerLimitPerc   Uis	
AstLmt_Pert_PowerLimitPerc_Uis_r32.value   0.699999988       AstLmt_Pert_PrkAssistComd_MirNm_r32.value   5	
31, AstLmt_Per1_ThermalLimitPerc_Uls_f32.value   0.689999988	
AstLmt   Per1_ThermalLimitPerc_UIs_f32.value	
AstLmt_Per1_ThermalLimitPerc_Uis_f32.value	
11. AstLmt_Perl_VehSpd_Kph_f32.value 12. AstLmt_Perl_VehSpd_Kph_f32.value 13. AstLmt_Perl_WheelImbalanceCmd_MtrNm_f32.value 13. Rte_Inst_Ap_AstLmtAptLmt_Perl_AssistComd_MtrNm_f32 14. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistComd_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistComd_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistEOTDamping_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistEOTDamping_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistStOTGain_Uls_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistStoTlLimit_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistStallLimit_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistStallLimit_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistStallLimit_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistStallLimit_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_CombinedDamping_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_DefeatLimitService_Cnt_Igc 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_DefeatLimitService_Cnt_Igc 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_LimitedRetur_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_LimitedRetur_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_LimitedRetur_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_LimPrCtrCoDisable_Cnt_Igc 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_LimPrCtrCoDisable_Cnt_Igc 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_LimPrCtrCoM_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_LimPrCtrTCmd_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_Comprov_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_Optrqov_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_Optrqov_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_Optrqov_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_Optrqov_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_PowerLimitPerc_Uls_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_PowerLimitForGtall_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_PowerLimitForGtall_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_PreLimitForGtall_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_PreLimitForGtall_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_PreLimitForGtall_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_PreLimitForGtall_MtrNm_f32	
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gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistEOTGain_UIs_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistEOTLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistEOTLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistStallLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistStallLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistStallLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistStallLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_DefeatLimitService_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_DefeatLimitService_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimitPercentFiltered_UIs_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimitedReturn_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimitedReturn_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimitedReturn_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimPnCtrCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimPnCtrCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimPnCtrCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_DeftrTCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_OpTrqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_OpTrqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_OpTrqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_DescrotCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_DescrotCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_DescrotCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PoservCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PoservCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PoservCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForcStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForcMill_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForcMill_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForcMill_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForcMill_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForcMill_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForcMill_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForcMill_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForcMill_MtrNm_f32 gt_Rte_Inst_Ap_AstL	
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tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_DefeatLimitService_Cnt_Igc tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_DefeatLimitService_Cnt_Igc tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_Derfection_MtrNm_f32 tgt_AstLmt_Per1_Derfectio	
tgt_AstLmt_AstLmt_Per1_CombinedDamping_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCTDmd_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCTDmd_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DpTrqCvy_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqCvy_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqCvy_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DpTrqCvy_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt_Per1_DefeatLimitService_Cnt_Igc gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_Igc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_Optqovr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PoservCCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitForStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitForStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_A	
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DopTrqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OptrqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosservCCDisable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_M	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrCCDisable_Cnt_lgc  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrEnable_Cnt_lgc  tgt_AstLmt_Per1_LrmPnCtrEnable_Cnt_lgc  tgt_AstLmt_Per1_LrmPnCtrEnable_Cnt_lgc  tgt_AstLmt_Per1_LrmPnCtrEnable_Cnt_lgc  tgt_AstLmt_Per1_LrmPnCtrEnable_Cnt_lgc  tgt_AstLmt_Per1_LrmPnCtrTCmd_MtrNm_f32  tgt_AstLmt_Per1_LrmPnCtrTCmd_MtrNm_f32  tgt_AstLmt_Per1_DpTrqOvr_MtrNm_f32  tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32  tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32  tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32  tgt_AstLmt_Per1_OptrqOvr_MtrNm_f32  tgt_AstLmt_Per1_OutputRampMult_Uls_f32  tgt_AstLmt_Per1_OutputRampMult_Uls_f32  tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc  tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc  tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc  tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32  tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32  tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32  tgt_AstLmt_Per1_PowerLimitForStall_MtrNm_f32  tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  tgt_AstLmt_Per1_TormalLimitPerc_Uls_f32  tgt_AstLmt_Per1_TormalLimitPerc_Uls_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
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tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
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target_AstLmt_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimit_MtrNm_f32 target_AstLmt_Per1_ThermalLimitMtrNm_f32 target_AstLmt_Per1_ThermalLimitMtrNm_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimitMtrNm_f32 target_AstLmt_Per1_ThermalLimitMtrNm_f32 target_AstLmt_Per1_ThermalLimitMtrNm_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimitMtrNm_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimit_MtrNm_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimit_MtrNm_f32 tar	
gt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_	
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gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32	
gt_Rte_Inst_Ap_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	
	Per
Name Actual Value Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 5.5999999 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value -1.01999986 -1.01999998 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value -0.713999867 -0.713999987 ± 0.0009765625	
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value 1.60000002 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.23 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt ManualTrqCmd MtrNm M f32	-5		
AstLmt SteeringAsstDefeat Cnt M Igc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.0599999987		
	-0.800000012		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.800000012		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value			
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2.29999995		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	1.70000005		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.0999999		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4.5999999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	345.100006		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-7		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mi		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Ci		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrEnable Cnt Igc	tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_lg		
	tgt AstLmt Per1 LrnPnCtrTCmd MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32		_132	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	200	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_t		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cr	_ ·	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_UIs_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3.	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	1	1 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1	-1 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.800000012	-0.800000012 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	1.70000005	1.70000005 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.24 (Repeat Count = 1) Name	Input Value		
	0		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	5		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt Rte Inst Ap AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.899999976		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.699999988		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-2		
tgt AstLmt Per1 AssistEOTDamping MtrNm f32.value	-4		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5		
tgt AstLmt Per1 AssistEOTLimit MtrNm f32.value	2.4000001		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	1.79999995		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.20000005		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2		
tgt AstLmt Per1 OpTrqOvr MtrNm f32.value	-3		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.899999976		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt AstLmt Per1 PowerLimitPerc Uls f32.value	0.899999976		
tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value	-7		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.899999976		
tgt AstLmt Per1 ThermalLimit MtrNm f32.value	4.69999981		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	234.199997		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-8		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_N		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNi		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNr	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_C	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_I	Jls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_0	Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_	lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNr	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_C	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_t	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNn	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_N	ftrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls	<u>_</u> f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd	_MtrNm_f32	
Name	Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.89999976	0.899999976 ± 0.000030517578125	•
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-8.80000019	-8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-1.61999989	-1.62 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	1.79999995	1.79999995 ± 0.0009765625	٠,



Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~		
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>		

Test Step 2.25 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-4		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
s_SumLimPlCmpLimit_MtrNm_f32	1.74000001		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.600000024		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-3		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-5		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2.5		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	1.8999998		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.2999995		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	0		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-3		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.100000001		
gt AstLmt Per1 PrkAssistCmd MtrNm f32.value	-8		
gt AstLmt Per1 PullCompCmd MtrNm f32.value	7		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.100000001		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4.8000019		
t_AstLmt_Per1_VehSpd_Kph_f32.value	123.800003		
st_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	1.20000005		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr1	ulm f32	
		VIII_132	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	ra a	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_1		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt	<del>-</del> ×	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_t	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	<u>lgc</u>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_	<u>f</u> 32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_Venopd_rpn_i32	trNm f32	
			Pos
Name	Actual Value	Expected Value	Resi
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.100000001	0.100000001 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3	3 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-3	-3 ± 0.0009765625	
	0.000000040	0.000000046 : 0.000075	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	-0.300000012 1.8999998	-0.300000012 ± 0.0009765625 1.89999998 ± 0.0009765625	



Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~		
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>		

Test Step 2.26 (Repeat Count = 1)	Input Value		
	· ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	4		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
_SumLimPlCmpLimit_MtrNm_f32	2.57999992		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.5		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-4		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-6		
yt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2.5999999		
pt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.4000001		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	1.5		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-3		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
pt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.20000003		
pt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	1		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8		
pt AstLmt Per1 ThermalLimitPerc Uls f32.value	0.20000003		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4.9000001		
yt_AstLmt_Per1_VehSpd_Kph_f32.value	500.700012		
pt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	2		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	m f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	<u></u>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f	22	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistOtalicInit_wtirNii_i32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNr		
		_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_l		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_	lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_l	gc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_t	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3:	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN	m_f32	
	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3:	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32	Nm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mt		Rose
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_Thermall.imitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_Thermall.imit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Name	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr Actual Value	Expected Value	Resi
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr Actual Value 0.200000003	Expected Value 0.2000000003 ± 0.000030517578125	Resi
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr Actual Value 0.200000003 4	Expected Value 0.200000003 ± 0.000030517578125 4 ± 0.0009765625	Resi
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr Actual Value 0.200000003	Expected Value 0.2000000003 ± 0.000030517578125	Resi



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.27 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-3		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPICmpLimit_MtrNm_f32	3.42000008		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.400000006		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2.70000005		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.0999999		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.5		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-2.5999999		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.30000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	2		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	5		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	400.899994		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M		
tgt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTGain Uls f32	tgt AstLmt Per1 AssistEOTGain Uls f3	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt AstLmt Per1 LimitPercentFiltered U	— -	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Ci		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_lgc	tgt AstLmt Per1 LmPnCtrEnable Cnt lg		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_132	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	roo.	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNi	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mi	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-5	-5 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-1.5	-1.5 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2.0999999	2.0999999 ± 0.0009765625	



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.28 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt ManualTrqCmd MtrNm M f32	3		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	4.26000023		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.30000012		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6		
	-8		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value			
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.899999976		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2.79999995		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.20000005		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.5999999		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-5		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-6		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.40000006		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.40000006		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	3		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.40000006		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	5.0999999		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	300.5		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	4		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtrl		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrCCDisable Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrEnable Cnt Ig		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_		
	tgt AstLmt Per1 OpTrqOvr MtrNm f32	_132	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32		22	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_UIs_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3:		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_t		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_t		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	/ltrNm_f32	
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.40000006	0.400000006 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7.20000029	7.19999981 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	7.20000029	7.19999981 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.880000055	0.879999995 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2.20000005	2.20000005 ± 0.0009765625	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.29 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-2		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	5.099999		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.200000003		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	1		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2.9000001		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.29999995		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.70000005		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	5.19999981		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	200.300003		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	5		
		f2.2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_Mtrl		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered	_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrN	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable	_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cn	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_Mtrl	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_U	ls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_	_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls	:_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_Mti	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt AstLmt Per1 PrkAssistCmd MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target AstLmt Per1 TSMitCommand	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_U	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt AstLmt Per1 TrqLimitMin MtrNm		
		_102	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	nd MtrNm f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCm		1_
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	,
ig_ totall_iteria_i totall_iteria_iovalue			
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-7	-7 ± 0.0009765625	•
	-7 -3.5	-7 ± 0.0009765625 -3.5 ± 0.0009765625	· ·



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.30 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc			
AstLmt_ManualTrqCmd_MtrNm_M_f32	2.25999999		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	O		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPlCmpLimit_MtrNm_f32	5.94000006		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.100000001		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	5		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	7		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	3.5		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.9000001		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	3.2999995		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	4		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.20000003		
at AstLmt Per1 PosServCCDisable Cnt Igc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.20000003		
gt AstLmt Per1 PrkAssistCmd MtrNm f32.value	-2.0999999		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-3		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.20000003		
st_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	5.80000019		
at AstLmt Per1 VehSpd Kph f32.value	58.2000008		
: - : -	-4.5		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value		422	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping	, <del>-</del> -	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_UI		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_Mi	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_Mt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService	_Cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltere	d_Uls_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_Mtrl	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable	e_Cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_C	nt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_Mt	rNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_l	Jls_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_U		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_M		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_Mi	<del>-</del>	
gt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt_AstLmt_Per1_PrkAssistCmd_Mtr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_Mtr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_N		
pt Rte Inst Ap AstLmt.AstLmt Per1 TSMitCommand MtrNm f32	target_AstLmt_Per1_TSMitCommand	_	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_		
t Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimit MtrNm f32	tgt_AstLmt_Per1_ThermalLimit_MtrN	_	
	tgt_AstLmt_Per1_mermaiLimit_ivitiv		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32		11_102	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	and Marklan 600	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCi		
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.20000003	0.200000003 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1.20000005	-1.20000005 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.24000001	-0.239999995 ± 0.0009765625	
gt_AstEmt_Ferr_SumEliminqCmd_withVm_i32.value	0.2400001		



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	•

Test Step 2.31 (Repeat Count = 1) Name	Input Value		
	0		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32	-1.76999998		
AstLmt SteeringAsstDefeat Cnt M Igc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	6.78000021		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	8		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	3.5999999		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	3.4000001		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.30000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.30000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	5.9000001		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	42.5999985		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-6.5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mi		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_Lmneuretum_wurvin_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_132	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	200	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_t		
tgt_Rte_Inst_Ap_AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cr	<del></del>	
tgt_Rte_Inst_Ap_AstLmt_Per1_PowerLimitPerc_UIs_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3.	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.30000012	0.300000012 ± 0.000030517578125	
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	6	6 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	
	3	3 ± 0.0009765625	



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	•

Test Step 2.32 (Repeat Count = 1) Name	Input Value		
	· ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1.75999999		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
s_SumLimPlCmpLimit_MtrNm_f32	7.61999989		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0.100000001		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	7		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-1.20000005		
pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.89999976		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	3.70000005		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.0999999		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	3.5		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	6		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	0		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.40000006		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.40000006		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-6.5		
pt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-5		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.40000006		
yt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6		
pt_AstLmt_Per1_VehSpd_Kph_f32.value	38.099985		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3.2999995		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mti	Nm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrI		
		_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f:		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt	<del></del>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3:		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_t	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn	n_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_t	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	AtrNm f32	
lame	Actual Value	Expected Value	Res
	0.400000006	· ·	IVE2
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value		0.400000006 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	
AL ASILINI PARI PRALIMITIONNIA INTINIM 137 VOLIA	-4.5	-4.5 ± 0.0009765625	
	1.0000007	4 7000000E + 0 0000705005	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	-1.80000007 3.0999999	-1.79999995 ± 0.0009765625 3.0999999 ± 0.0009765625	



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.33 (Repeat Count = 1) Name	Input Value		
	·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-8.5		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPICmpLimit_MtrNm_f32	8.46000004		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0.200000003		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	8		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	3.79999995		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.20000005		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	3.5999999		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	7		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	1.5		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	3.29999995		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	3.29999995		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-6		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.0999999		
t_AstLmt_Per1_VehSpd_Kph_f32.value	26.7000008		
t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	5.5999999		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrN	lm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt AstLmt Per1 AssistEOTDamp	ning MtrNm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_	Uls f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLir		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamp		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitServi		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFilte		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt AstLmt Per1 LimitedReturn M		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisa	<del>-</del>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt AstLmt Per1 LrnPnCtrTCmd		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrN		
gt_Rte_inst_Ap_AsiLint.AsiLint_Fer1_OptriqOvi_intinini_i32 gt_Rte_inst_Ap_AsiLint.AsiLint_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMul	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uis_I32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisa		
	tgt_AstLmt_Per1_PosServCCDIsa		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_UIs_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_N	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_I	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitComma		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPer		
pt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_M		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_Mtr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f3		
	tgt_AstLmt_Per1_WheelImbalance	eCmd_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32		Expected Value	Res
	Actual Value		Ites
Name	Actual Value 0.5	0.5 ± 0.000030517578125	Res
Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value		0.5 ± 0.000030517578125 8 ± 0.0009765625	Res
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Name  gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value  gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value  gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0.5		Res
Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	0.5 8	8 ± 0.0009765625	INCO



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>	

Innut Value		
•		
·		
0		
1		
3.2999995		
5.5999999		
0.600000024		
0		
0.600000024		
5.5999999		
-7		
0.600000024		
6.19999981		
3.5		
-1		
tat AstLmt Per1 AssistCmd MtrNm f32		
tgt_AstLmt_Per1_Opt1qOv1_MtiNin_is2 tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc  tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32  tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32  tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32  tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32  tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  target_AstLmt_Per1_TSMitCommand_MtrNm_f32		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc  tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32  tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32  tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32  tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32  tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  target_AstLmt_Per1_TSMitCommand_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc  tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32  tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32  tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32  tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32  tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  target_AstLmt_Per1_TSMitCommand_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc  tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32  tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32  tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32  tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32  tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  target_AstLmt_Per1_TSMitCommand_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc  tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32  tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32  tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32  tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32  tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  target_AstLmt_Per1_TSMitCommand_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_AstLmt_Per1_VehSpd_Kph_f32  tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	Rec	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc  tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32  tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32  tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32  tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32  tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_AstLmt_Per1_VehSpd_Kph_f32  tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Actual Value  Expected Value		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc  tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32  tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32  tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32  tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32  tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_AstLmt_Per1_TydLimitMin_MtrNm_f32  tgt_AstLmt_Per1_VehSpd_Kph_f32  tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Actual Value  0.6000000024  0.6000000024		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc  tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32  tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32  tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32  tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32  tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  target_AstLmt_Per1_TSMitCommand_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_TqLimitMin_MtrNm_f32  tgt_AstLmt_Per1_TvehSpd_Kph_f32  tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Actual Value  0.600000024  3.29999995  3.29999995  2.20000000000000000000000000000000000	Resi	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc  tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32  tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32  tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32  tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32  tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_AstLmt_Per1_TydLimitMin_MtrNm_f32  tgt_AstLmt_Per1_VehSpd_Kph_f32  tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Actual Value  0.6000000024  0.6000000024		
	1 3.29999995 5.5999999 0.600000024 0 0.600000024 5.5999999 -7 0.600000024 6.19999981 3.5 -1 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_AstLmt_Per1_LimPnCtrCCDisable_Cnt_lgc tgt_AstLmt_Per1_LimPnCtrEnable_Cnt_lgc tgt_AstLmt_Per1_LimPnCtrTCmd_MtrNm_f32 tgt_AstLmt_Per1_LimPnCtrTCmd_MtrNm_f32 tgt_AstLmt_Per1_LimPnCtrTCmd_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>~</b>

Test Step 2.35 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-7.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
<_SumLimPlCmpLimit_MtrNm_f32	0.959999979		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0.40000006		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.30000012		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4		
gt AstLmt Per1 AssistStallLimit MtrNm f32.value	3.4000001		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	3.7999995		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	4		
	0		
gt_AstLmt_Per1_LrnPnCtrCcDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value			
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-1		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-8.10000038		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.30000019		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	253.600006		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-2		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	lm_f32	
gt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTGain Uls f32	tgt AstLmt Per1 AssistEOTGain Uls f32	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_t	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3:		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_t	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN	lm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 TrqLimitMin MtrNm f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_Mi	rNm f32	
			Poo
Name	Actual Value	Expected Value	Resi
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.699999988 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2.0999999	2.0999999 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	
	I a conservation of the co		

3.4000001

tgt\_AstLmt\_Per1\_TrqLimitMin\_MtrNm\_f32.value

3.4000001 ± 0.0009765625



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>~</b>

Test Step 2.36 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt ManualTrqCmd MtrNm M f32	7.5		
AstLmt SteeringAsstDefeat Cnt M Igc	0		
Rte_Inst_Ap_AstLmt	tgt Rte Inst Ap AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	1.32000005		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0.5		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-4.5		
tgt AstLmt Per1 AssistEOTDamping MtrNm f32.value	3.29999995		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.0999999		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.5		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	3.9000001		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	0.800000012		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	6.40000012		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value tgt_AstLmt_Per1_VehSpd_Kph_f32.value	342.100006		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3.	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_N		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f:	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Ci	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_L	Jls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_0	Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_	lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNr	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_C	<del>_</del> -	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_N		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls	<del>-</del> '	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_ tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f;		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_rrqLmttwiii1_wttviii1_is	52	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WenSpd_Rpri_is2  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm f32	
Name			Page
	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	4.5	4.5 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625 0 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3.5	3.5 ± 0.0009765625	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~	

Test Step 2.37 (Repeat Count = 1) Name	Input Value		
	· ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-6.5		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
SumLimPlCmpLimit_MtrNm_f32     Total 10     Total	1.67999995		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0.600000024		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.19999981		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.5999999		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	6		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.899999976		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.89999976		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-3		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	2		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.89999976		
pt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.5		
yt AstLmt Per1 VehSpd Kph f32.value	484.799988		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-4.5		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt AstLmt Per1 AssistEOTDamping Mtr	Nm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	<u>.</u>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr1	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_		
		•	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls tgt_AstLmt_Per1_LimitedReturn_MtrNm_f;		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	0		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	r32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtrl	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_M	trNm_f32	
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.89999976	0.899999976 ± 0.000030517578125	
gt_AstLmt_Per1_LimitPercentFiltered_Ois_is2.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	6.5		
·	0	6.5 ± 0.0009765625	
	T U	0 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>	

Test Step 2.38 (Repeat Count = 1)	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	6.55999994		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
x_SumLimPlCmpLimit_MtrNm_f32	2.03999996		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0.69999988		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	3.2999995		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-1		
pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.30000019		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.70000005		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.0999999		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-5		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	3.29999995		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-4		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3.20000005		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.100000001		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.5999999		
pt_AstLmt_Per1_VehSpd_Kph_f32.value	155.899994		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-5		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	m f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	···_·-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNi	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_l		
		-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_	igc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	20	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_I	gc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32		rNm_f32	
	tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32		Expected Value	Resi
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Name	Actual Value	Expected Value	Resi
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	Actual Value 0.100000001	0.100000001 ± 0.000030517578125	Resu
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32  Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	Actual Value 0.100000001 5	0.100000001 ± 0.000030517578125 5 ± 0.0009765625	Resi
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	Actual Value 0.100000001	0.100000001 ± 0.000030517578125	Res



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>	

Test Step 2.39 (Repeat Count = 1)	Input Value		
	·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-5.55999994 1		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPICmpLimit_MtrNm_f32	2.4000001		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0.800000012		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-2		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.4000001		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.7999995		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.19999981		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	3.29999995		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	8		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
pt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003		
pt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-5		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	4		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.6999981		
t AstLmt Per1 VehSpd Kph f32.value	278.600006		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-6		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt AstLmt Per1 AssistEOTDamping Mtr	Nm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtrl	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls tgt_AstLmt_Per1_LimitedReturn_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	V		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	<u>132</u>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_t	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3	2	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	ltrNm_f32	
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125	1.03
gt_AstLmt_Per1_LimitPercentFiltered_Ois_isz.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5.5999999	5.5999999 ± 0.0009765625	
	0	0 ± 0.0009765625	
at Actumt Peri PrelimitTorque MtrNm f32 value		10 ± 0.00031 03023	1
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.40 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	5.5		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt SteeringAsstDefeat Cnt M lgc	0		
Rte_Inst_Ap_AstLmt	tgt Rte Inst Ap AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	2.75999999		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0.89999976		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-1		
tgt AstLmt Per1 AssistEOTDamping MtrNm f32.value	-3		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
tgt AstLmt Per1 AssistEOTLimit MtrNm f32.value	4.5		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.9000001		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.30000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.30000012		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.80000019		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	312.399994		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3.2999995		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn	— -	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Ci		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	_132	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OpTrqOvt_MtNnin_i32	R2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3	<del></del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt AstLmt Per1 PreLimitForStall MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForGtail_within1_i32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitPorStail_MtrNr	_	
tgt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt_AstLmt_Per1_PreLimitrorque_withtin	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
tgt Rte Inst Ap AstLmt.AstLmt Per1 SumLimTrqCmd MtrNm f32	tgt AstLmt Per1 SumLimTrqCmd MtrNi		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mi	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	2.6400001	2.6400001 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3.900001	3.9000001 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	•

Test Step 2.41 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0 -4.5		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
<_SumLimPlCmpLimit_MtrNm_f32	3.11999989		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-2		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-4		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.89999976		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.5999999		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.4000001		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-1		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	0		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2		
pt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.40000006		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
at AstLmt Per1 PowerLimitPerc Uls f32.value	0.40000006		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-7		
pt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6		
at AstLmt Per1 ThermalLimitPerc Uls f32.value	0.40000006		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.9000001		
t AstLmt Per1 VehSpd Kph f32.value	328.299988		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	5.5999999		
		2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3.		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_N	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_M	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Ci		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_L		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_0	Cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_	lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNr	n_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_C	nt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f	32	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNr	n_f32	
gt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm	_	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target AstLmt Per1 TSMitCommand M	_	
pt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimitPerc Uls f32	tgt AstLmt Per1 ThermalLimitPerc Uls	_	
pt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimit MtrNm f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_		
pt Rte Inst Ap AstLmt.AstLmt Per1 TrgLimitMin MtrNm f32	tgt AstLmt Per1 TrqLimitMin MtrNm f		
		C. C	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	MtcNm f22	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_		-
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.40000006	0.400000006 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2	2 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
	0	0 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009703023	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.42 (Repeat Count = 1) Name	Input Value		
	· ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	4.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
_SumLimPlCmpLimit_MtrNm_f32	3.48000002		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.10000002		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-3		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-5		
yt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.69999981		
pt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.0999999		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.5		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-2		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
pt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	1.5		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-3		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
pt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5		
t_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-8		
t_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	7		
t_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7		
t_AstLmt_Per1_VehSpd_Kph_f32.value	429.399994		
t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	1		
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mti	Nm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistOtaliLmit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrI		
		_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f:		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3:		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_t	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn	n_f32	
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_t	32	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3	32	
yt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
at Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt AstLmt Per1 WheelImbalanceCmd M	AtrNm f32	
lame	Actual Value	Expected Value	Res
			Nes
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3	3 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	1.5	1.5 ± 0.0009765625	
	0.75	0.75 . 0.0000767777	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.75 4.0999999	0.75 ± 0.0009765625 4.0999999 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.43 (Repeat Count = 1) Name	Input Value		
	0		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-3.5		
AstLmt_ManualTrqCmd_MtrNm_M_f32	0		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	3.83999991		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.20000005		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.80000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.19999981		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.5999999		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2.5999999		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	1		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.0999999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	52.7999992		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	2		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mi		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
tgt_Rte_Inst_Ap_AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Ci		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt AstLmt Per1 LrnPnCtrTCmd MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32	_102	
	tgt_AstLmt_Per1_OutputRampMult_Uls_t	R2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Ois_		
tgt_Rte_Inst_Ap_AstLmt_Per1_PosServCCDisable_Cnt_Igc			
tgt_Rte_Inst_Ap_AstLmt_Per1_PowerLimitPerc_UIs_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	2.51999998	2.51999998 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.1999981	4.19999981 ± 0.0009765625	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~	

Test Step 2.44 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-7		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte Inst Ap AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	4.1999981		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.29999995		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	2		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
tgt AstLmt Per1 AssistEOTLimit MtrNm f32.value	5.4000001		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.80000019		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.19999981		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	1		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrCcDisable_Cnt_lgc.value	0		
	-4		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value			
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-8.80000019 0.300000012		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value			
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1 0 200000012		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012 7		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value			
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.69999981		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	37.5		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mi	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_t		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cn	<del></del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt	rNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3.	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.30000012	0.300000012 ± 0.000030517578125	
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	5.29999924	5.30000019 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	5.29999924	5.30000019 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	1.4400006	1.44000006 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.80000019	4.80000019 ± 0.0009765625	



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.45 (Repeat Count = 1) Name	Input Value		
	· ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	7		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPICmpLimit_MtrNm_f32 C_Start Actions Dead _TCMitCommand_MtrNm_f32.value	4.55999994		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.39999998		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	3		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.899999976		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.5		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.9000001		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.30000019		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	2		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-5		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-5		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	8.80000019		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.40000006		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.40000006		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	8		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-1		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.40000006		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.80000019		
pt_AstLmt_Per1_VehSpd_Kph_f32.value	83.9000015		
pt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-1.20000005		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt AstLmt Per1 AssistEOTDamping M		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	<del>-</del>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt AstLmt Per1 LimitPercentFiltered U	<del>_</del> -	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt AstLmt Per1 LimitedReturn MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LmPnCtrCCDisable_Ci		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_k		
	tgt AstLmt Per1 LrnPnCtrTCmd MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32		1 132	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	F22	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cr	<del>_</del> -	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNi	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.40000006	0.40000006 ± 0.000030517578125	1.00
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitPor3tali_MttNm_f32.value gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-5	-5 ± 0.0009765625	
	-5 -2	-2 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value			



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	•

Test Step 2.46 (Repeat Count = 1) Name	Input Value		
	0		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32	-6		
_			
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1 test Disc land An Apil mit		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	4.92000008		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.5		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.10000001		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.4000001		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	3		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	0		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.9000001		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	73.6999969		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-2.0999999		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mti		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cl		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrEnable Cnt le		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm		
	tgt AstLmt Per1 OpTrqOvr MtrNm f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32		f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_ tgt_AstLmt_Per1_PosServCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	-	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNi	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mi		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	4	4 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5	5 ± 0.0009765625	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	6		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPICmpLimit_MtrNm_f32	5.28000021		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.60000002		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	7		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.20000003		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.6999981		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.0999999		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.5		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	4		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-7		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	1.5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	500.200012		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-4.5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	·Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrI		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt AstLmt Per1 LimitPercentFiltered Ul		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	.·· <del>·</del>	
gt Rte Inst Ap AstLmt.AstLmt Per1 OutputRampMult Uls f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f:	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3:		
gt_Rte_Inst_Ap_AstLint.AstLint_Fei1_FowerLinitFei0_0is_i32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt AstLmt Per1 PreLimitTorque MtrNm		
gt_Rte_inst_Ap_AstLmt.AstLmt_Fer1_FreLimitTorque_withMin_is2	tgt AstLmt Per1 PrkAssistCmd MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmto_MtrNm_f32	tgt_AstLmt_Per1_PtrAssistCmd_MtrNm_		
	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	target AstLmt Per1_SumLim1rqCmd_witrNn	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32		_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_t		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	# N	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N		
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-7	-7 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-4.20000029	-4.19999981 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5.0999999	5.0999999 ± 0.0009765625	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Name	Input Value		
	·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-5		
AstLmt_SteeringAsstDefeat_Cnt_M_Igc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	5.63999987		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.70000005		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	8		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.80000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.19999981		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2.5999999		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt AstLmt Per1 PowerLimitPerc Uls f32.value	0.69999988		
tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value	-4.5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.69999988		
tgt AstLmt Per1 ThermalLimit MtrNm f32.value	8.1000038		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	58.2000008		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-6.5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_t		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_	lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_	lgc .	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f	32	
tgt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3:	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_FowerLimitFetC_0is_i32	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimit 0/3tail_within_i32	tgt_AstLmt_Per1_FreLimit_orstall_withtin_		
tgt_Rte_inst_Ap_AstLmt.AstLmt_Per1_PreLimitrorque_intrinin_is2 tgt_Rte_inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32			
	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	!	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_Mi	trNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.699999988 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	6	6 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt AstLmt Per1 SumLimTrqCmd MtrNm f32.value	0	0 ± 0.0009765625	

5.19999981

tgt\_AstLmt\_Per1\_TrqLimitMin\_MtrNm\_f32.value

5.19999981 ± 0.0009765625



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.49 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32	5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	6		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.79999995		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	7		
tqt AstLmt Per1 AssistEOTDamping MtrNm f32.value	-1.20000005		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.900001		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.30000019		
tgt AstLmt Per1 AssistVehSpdLimit MtrNm f32.value	5.69999981		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	6		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	1		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	1		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.19999981		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	42.2999992		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3.2999995		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	<del>_</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul		
tgt_Rte_Inst_Ap_AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCcDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_ tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	_132	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f	22	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
tgt_Rte_inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt AstLmt Per1 PowerLimitPerc Uls f3	— -	
tgt_Rte_inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt AstLmt Per1 PreLimitTorque MtrNm	_	
tgt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt AstLmt Per1 SumLimTrqCmd MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt	_	
tgt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimitPerc Uls f32	tgt AstLmt Per1 ThermalLimitPerc Uls		
tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt AstLmt Per1 ThermalLimit MtrNm f		
tgt_Rte_Inst_Ap_AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3:		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_I	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	1	1 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5.30000019	5.30000019 ± 0.0009765625	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.50 (Repeat Count = 1) Name	Input Value		
	0		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-4		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	6.36000013		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.8999998		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	8		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.4000001		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.80000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	7		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	2		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	2		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.899999976		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	3.29999995		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.899999976		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.30000019		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	38.4000015		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	5.5999999		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	Nm f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr1		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt AstLmt Per1 DefeatLimitService Cnt		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitretCentrintered_Oil		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn		
	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	132	
tgt_Rte_Inst_Ap_AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	20	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	ftrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.899999976	0.899999976 ± 0.000030517578125	
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	8	8 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5.4000001	5.4000001 ± 0.0009765625	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	•

### WIND VALUE TO A STATE OF THE PLAN STATE OF T	Test Step 2.51 (Repeat Count = 1)	Input Value		
State   Manual Tracking   Methods				
National Association   Control   Light				
Start ImPriChyphint, Minhins, 122	·			
Samt.mpt   Morthm, 152		-		
1, Ast.m. Perl.   2, Ast.m.   2, A	:-			
Astern Perl   AssistCortan   Jimms   12 value				
Asturn   Perl   AssistEOTORing   1,95   22 value				
Astline   Perl   AssistCOTIAN   Ullis   72 value				
J. AstLim Perl AssistChillim, Mehhm 152 value   5.0000001   1, AstLim Perl AssistVehöpcLim, Mehhm 152 value   5.5   5.5   1, AstLim Perl Children				
Astimpt Perl   AssistStallismi, Minkm, 102 value		0.600000024		
Ast.m. Perl   CommenceTomping   Minim   St2 value	gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.0999999		
Ast.m.   Perl.   CombinedCamping, Mehrh.   122 value		5.5		
J. AstLim. Perl. Limite/Gesturn. Mixthm. 123 value   1	gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.9000001		
	gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value			
J. AstLm. Perl L.	gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
J. AstLim. Perl L. ImProCiff Code Minim. 192 value   1   3   3   3   3   3   3   3   3   3	gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	3		
	gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
Astumt Perf   Ostprück Minhm, 132 value   1, Astumt Perf   Postprück Onto   1, Astumt   Perf   Postprück Onto   1, Astumt   Perf   Postprück Onto   1, Astumt   Perf   Postprück Onto   1, Astumt   Perf   Postprück Onto   1, Astumt   Perf   Postprück   1, Astumt   Perf   Postprück   1, Astumt   Perf   Postprück   1, Astumt   Perf   Postprück   1, Astumt   Perf	gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
Jastim   Pert   OutpuRsamMut   Us   52 value   0.5     Jastim   Pert   PosesvcCDisable   Cnt   Us   52 value   0.100000001     Jastim   Pert   ThemalLimide   Themis   Tastim   Tasti	yt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	3		
2] AstLmL Pert   PosSeno CODisable, Cn. Igo: value   0   0   0   0   0   0   0   0   0	gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6.5		
J. Ast.ml. Perl _ PowerLimitPerc_Uis_   G2 value   0.100000001     J. Ast.ml. Perl _ PirAssistCmd_MnNm_  S2 value   0.100000001     J. Ast.ml. Perl _ PirAssistCmd_MnNm_  S2 value   0.100000001     J. Ast.ml. Perl _ ThermalLimitPerc_Uis_   G2 value   0.100000001     J. Ast.ml. Perl _ ThermalLimitPerc_Uis_   G2 value   0.100000001     J. Ast.ml. Perl _ VheSpd_Kph_  S2 value   0.100000001     J. Ast.ml. Perl _ VheSpd_Kph_  S2 value   0.100000001     J. Ast.ml. Perl _ WheelinbalanceCmd_MnNm_  G2 value   0.100000001     J. Ast.ml. Perl _ WheelinbalanceCmd_MnNm_  G2 value   0.100000001     J. Rel _ Inst_Ap_Ast.ml. Ast.ml. Perl _ AssistCoTDamping_MnNm_  G2   0.100000001     J. Rel _ Inst_Ap_Ast.ml. Ast.ml. Perl _ AssistCoTDamping_MnNm_  G2   0.100000001     J. Rel _ Inst_Ap_Ast.ml. Ast.ml. Perl _ AssistCoTLimit_MnNm_  G2   0.100000001     J. Rel _ Inst_Ap_Ast.ml. Ast.ml. Perl _ AssistVehSpd_Limit_MnNm_  G2   0.100000001     J. Rel _ Inst_Ap_Ast.ml. Ast.ml. Perl _ AssistVehSpd_Limit_MnNm_  G2   0.100000001     J. Rel _ Inst_Ap_Ast.ml. Ast.ml. Perl _ LimitPercentFiltered_Uis_ G2   0.10000000000000000000000000000000000	gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
	gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
	gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.100000001		
AstLmt   Pert   ThermatLimitPerc_Uls_132 value	gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5.5999999		
AstLmL Pert   ThermalLimit, MtrNm_f32.value	gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-7		
AstLmL Pert   ThermalLimit, MtrNm_f32.value	at AstLmt Per1 ThermalLimitPerc Uls f32.value	0.100000001		
AstLmt_Pert   WebSpd , Kph_ 132 value				
AstLmt Pert   WheelmbalanceCmd MtrNm;				
12, Rte, Inst, Ap, AstLmtAstLmt, Perl, AssistEOTDamping, MirNim, [32]   tg, AstLmt, Perl, AssistEOTGain, Uis, [32]   tg, AstLmt, Perl, LimiteOfecture, Mirkmt, [32]   tg, AstLmt, Perl, AssistEOTGain, Uis, [32]   tg, AstLmt, Perl, Ass				
Internation			Nm f32	
12   12   13   15   15   15   15   15   15   15			VIII_102	
gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_AssistStalliLimit_MtrNm_f32         tgt_AstLmt. Per1_AssistStalliLimit_MtrNm_f32           gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_AssistStAllimit_MtrNm_f32         tgt_AstLmt. Per1_AssistStAllimit_MtrNm_f32           gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_DefeatLimitService_Cnt.lgc         tgt_AstLmt. Per1_DefeatLimitService_Cnt.lgc           gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_LimitPercentFiltered_Uls_f32         tgt_AstLmt. Per1_DefeatLimitService_Cnt.lgc           gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_LimitPercentFiltered_Uls_f32         tgt_AstLmt. Per1_LimitPercentFiltered_Uls_f32           gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_LimitPercentFiltered_Uls_f32         tgt_AstLmt. Per1_LimitPercentFiltered_Uls_f32           gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_LimitPercentFiltered_Uls_f32         tgt_AstLmt. Per1_LimitPercentFiltered_Uls_f32           gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_DefectColsable_Cnt.lgc         tgt_AstLmt. Per1_LimitPercentFiltered_Uls_f32           gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_OpTrqOvr_MtrNm_f32         tgt_AstLmt. Per1_OpTrqOvr_MtrNm_f32           gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_Post-InitPerc Uls_f32         tgt_AstLmt. Per1_DotputRampMult_Uls_f32           gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_Post-InitPercs_Uls_f32         tgt_AstLmt. Per1_Post-InitPerc_Uls_f32           gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_PreLimitTorque_MtrNm_f32         tgt_AstLmt. Per1_Post-InitTorque_MtrNm_f32           gt. Rte. Inst. Ap. AstLmt. AstLmt. Per1_Sulf. Command_MtrNm_f32 <td< td=""><td></td><td></td><td>f2.2</td><td></td></td<>			f2.2	
gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   AssistVehSpdLimit   MtrNm   f32   tg   AstLmt   Pert   CombinedDamping   MtrNm   f32   tg   AstLmt   Pert   CombinedDamping   MtrNm   f32   tg   AstLmt   Pert   CombinedDamping   MtrNm   f32   tg   AstLmt   Pert   Defeat   MtrNm   f32   tg   AstLmt   Pert   Defeat   MtrNm   f32   tg   AstLmt   Pert   Limited   MtrNm   f32   tg   AstLmt   Pert   Doptro   MtrNm   f32   tg   AstLmt   Pert   PowerLimitPerc   Uls   f32   tg   AstLmt   Pert   PreLimitForGall   MtrNm   f32   tg   AstLmt   Pert   Tramillimited   tg   AstLmt   Pert   Tramillimited   tg   AstLmt   Pert   Tramillimited   tg   tg   AstLmt   Pert   Tramillimited   tg   tg   AstL				
gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   DefeatLimitService   Ort.   Igo   Igo   Rte   Inst   Ap   AstLmt   AstLmt   Pert   DefeatLimitService   Ort.   Igo   Igo   Italian   Inst   Ap   AstLmt   AstLmt   Pert   DefeatLimitService   Ort.   Igo				
			_	
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_i32 tgt_AstLmt_AstLmt_Per1_LimiteReturn_MtrNm_i32 tgt_AstLmt_AstLmt_Per1_LimiteReturn_MtrNm_i32 tgt_AstLmt_Per1_LimitedReturn_MtrNm_i32 tgt_AstLmt_Per1_Pir_LimitedReturn_MtrNm_i32 tgt_AstLmt_Per1_Pir_LimitedReturn_MtrNm_i32 tgt_AstLmt_Per1_Pir_LimitedReturn_MtrNm_i32 tgt_AstLmt_Per1_Pir_LimitedReturn_MtrNm_i32 tgt_AstLmt_Per1_OutputRempluit_Uls_i32 tgt_AstLmt_Per1_OutputRempMult_Uls_i32 tgt_AstLmt_Per1_PowerLimitedReturn_i32 tgt_AstLmt_Per1_PowerLimitedReturn_i32 tgt_AstLmt_Per1_PowerLimitedReturn_i32 tgt_AstLmt_Per1_PowerLimitedReturn_i32 tgt_AstLmt_Per1_PowerLimitedReturn_i32 tgt_AstLmt_Per1_PowerLimitedReturn_i32 tgt_AstLmt_Per1_PreLimitedReturn_i32 tgt_AstLmt_Per1_PreLimited_Uls_i32 tgt_AstLmt_Per1_PreLimitedReturn_i32 tgt_AstLmt_Per1_InternalLimitedReturn_i32 tgt_AstLmt_Per1_ThermalLimitedReturn_i32 tgt_AstLmt_Per1_ThermalLimitedReturn_i32 tgt_AstLmt_Per1_ThermalLimitedReturn_i32 tgt_AstLmt_Per1_ThermalLimitedReturn_i32 tgt_AstLmt_Per1_ThermalLimitedReturn_i32 tgt_AstLmt_Per1_ThermalLimitedReturn_i32 tgt_AstLmt_Per1_PreLimited_Uls_i32.value tgt_AstLmt				
tgt_astLmt_Per1_LimitedReturn_MtrNm_f32   tgt_astLmt_Per1_Directroctorial_MtrNm_f32   tgt_astLmt_Per1_Tirectroctorial_MtrNm_f32   tgt_astLmt_Per1_Directroctorial_MtrNm_f32			•	
tg   tg   tg   tg   tg   tg   tg   tg				
tgt_AstLmt_Ap_AstLmt_Apt_mt_Per1_OpTrqOvr_MtrNm_f32   tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32   tgt_AstLmt_Ap_AstLmt_Ap_AstLmt_Per1_DosServCCDisable_Cnt_lgc   tgt_AstLmt_Ap_AstLmt_Apt_MtrNm_f32   tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc   tgt_AstLmt_Ap_AstLmt_Ap_AstLmt_Apt_MtrNm_f32   tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32   tgt_AstLmt_Ap_AstLmt_Ap_AstLmt_Apt_MtrNm_f32   tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32   tgt_AstLmt_Ap_AstLmt_Apt_MtrNm_f32   tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32   tgt_AstLmt_Ap_AstLmt_AstLmt_Per1_PreLimitForstall_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForque_MtrNm_f32   tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32   tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32   tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32   tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32   tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32   tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32   tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32   tgt_AstLmt_Per1_TsmitLimitPerc_Uls_f32   tgt_AstLmt_Per1_TsmitLimitPerc_Uls_f32   tgt_AstLmt_Per1_TsmitLimitPerc_Uls_f32   tgt_AstLmt_Per1_TsmitLimitPerc_Uls_f32   tgt_AstLmt_Per1_TretimitMin_MtrNm_f32   tgt_AstLmt_Per1_TretimitMin_MtrNm_f32   tgt_AstLmt_Per1_TretimitMin_MtrNm_f32   tgt_AstLmt_Per1_TretimitMin_MtrNm_f32   tgt_AstLmt_Per1_TretimitMin_MtrNm_f32   tgt_AstLmt_Per1_TretimitMin_MtrNm_f32   tgt_AstLmt_Per1_VehSpd_Kph_f32   tgt_AstLmt_Per1_PreL				
			f32	
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc   tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc   tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc   tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc   tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32   tgt_AstLmt_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_PreAssistCmd_MtrNm_f32   tgt_AstLmt_Per1_PreCompCmd_MtrNm_f32   tgt_AstLmt_Per1_PreCompCmd_MtrNm_f32   tgt_AstLmt_Per1_PreCompCmd_MtrNm_f32   tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32   tgt_AstLmt_Per1_TownImTrqCmd_MtrNm_f32   tgt_AstLmt_Per1_TownImtMin_MtrNm_f32   tgt_AstLmt_Per1_TownImtMin_MtrNm_f32   tgt_AstLmt_Per1_VownImtMin_MtrNm_f32   tgt_AstLmt_Per1_VownImtMin_MtrNm_f				
trace   inst_Ap_AstLmt_AstLmt_Per1_PowerLimitPerc_Uls_f32   tgt_AstLmt_Per1_PowerLimitForStall_MtrNm_f32   tgt_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForque_MtrNm_f32   tgt_AstLmt_Per1_SumLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32   target_AstLmt_Per1_TSMitCommand_MtrNm_f32   tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32   tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32   tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32   tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32   tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32   tgt_AstLmt_Per1_ThermalLimitMtrNm_f32   tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_VehSpd_Kph_f32   tgt_AstLmt_Per1_VehSpd_Kph_f32   tgt_AstLmt_Per1_VehSpd_Kph_f32   tgt_AstLmt_Per1_VehSpd_Kph_f32   tgt_AstLmt_Per1_UshSpd_Kph_f32   tgt_AstL				
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32   tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32   tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32   tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32   tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32   tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32   tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32   tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32   tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32   tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32   tgt_AstLmt_Per1_ThermalLimitMtrNm_f32   tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32   tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32   tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32   tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32   tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32   tgt_AstLmt_Per1_UndelImbalanceCmd_MtrNm_f32   tgt_AstLmt_Per1_UndelImbalanceCmd_MtrNm_f32   tgt_AstLmt_Per1_UndelImbalanceCmd_MtrNm_f32   tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value   0.100000001   0.10				
trace   Inst   Ap   AstLmt   AstLmt   Per1   PreLimitTorque   MtrNm   f32   tgt   AstLmt   Per1   TsMitCommand   MtrNm   f32   target   AstLmt   Per1   TsMitCommand   MtrNm   f32   target   AstLmt   Per1   TsMitCommand   MtrNm   f32   target   AstLmt   Per1   ThermalLimit   MtrNm   f32   tgt   AstLmt   Per1   TrqLimitMin   MtrNm   f32   tgt   AstLmt   Per1   TrqLimitMin   MtrNm   f32   tgt   AstLmt   Per1   TrqLimitMin   MtrNm   f32   tgt   AstLmt   Per1   VehSpd   Kph   f32   tgt   AstLmt   Per1   VehSpd   Kph   f32   tgt   AstLmt   Per1   WheellmbalanceCmd   MtrNm   f32   tgt   AstLmt   Per1   PreLimitForStall   MtrNm   f32   tgt   AstLmt   Per1   PreLimitTorque   MtrNm   f32   tgt   AstLmt   Per1   SumLimTqCmd   MtrNm	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32         tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32         tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32         tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32         target_AstLmt_Per1_TSMitCommand_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32         tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TremalLimit_MtrNm_f32         tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32         tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32         tgt_AstLmt_Per1_VehSpd_Kph_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32         tgt_AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32           Name         Actual Value         Expected Value           gt_AstLmt_Per1_InitPercentFiltered_Uls_f32.value         0.100000001         0.100000001 ± 0.000030517578125           gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value         3         3 ± 0.0009765625           gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value         1.5 ± 0.0009765625	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32         tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32         tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32         target_AstLmt_Per1_TSMitCommand_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32         tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32         tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32         tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32         tgt_AstLmt_Per1_VehSpd_Kph_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32         tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32           Name         Actual Value         Expected Value           gt_AstLmt_Per1_InitPercentFiltered_Uls_f32.value         0.100000001         0.100000001 ± 0.000030517578125           gt_AstLmt_Per1_PreLimitForGtall_MtrNm_f32.value         3         3 ± 0.0009765625           gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value         1.5         1.5 ± 0.0009765625	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32         tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32         target_AstLmt_Per1_TSMitCommand_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32         tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TretimitMin_MtrNm_f32         tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32         tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32         tgt_AstLmt_Per1_VehSpd_Kph_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32         tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32           Name         Actual Value         Expected Value           gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value         0.100000001         0.100000001 ± 0.000030517578125           gt_AstLmt_Per1_PreLimitForQue_MtrNm_f32.value         3         3 ± 0.0009765625           gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value         1.5         1.5 ± 0.0009765625	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3	32	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value target_AstLmt_Per1_TsMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_TermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_TermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_TermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_TermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_TermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_TermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_TermalLimitMtrNm_f32 tgt_AstLmt_Per1_TermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TermalLimitMtrNm_f32 tgt_AstLmt_Per1_TermalLimitMtrNm_f32 tgt_AstLmt_Per1_TermalLimitMtrNm_f32 tgt_AstLmt_Per1_TermalLimitMtrNm_f32 tgt_AstLmt_Per1_TermalLimitMtrNm_f32 tgt_AstLmt_Per1_TermalLimitMtrNm_f32 tgt_AstLmt_Per1_TermalLimitImtrNm_f32 tgt_AstLmt_Per1_TermalLimitImtrNm	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f	32	
target_AstLmt_Per1_TSMitCommand_MtrNm_f32  target_AstLmt_Per1_TSMitCommand_MtrNm_f32  target_AstLmt_Per1_TSMitCommand_MtrNm_f32  target_AstLmt_Per1_TSMitCommand_MtrNm_f32  tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32  tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32  tgt_AstLmt_Per1_TqLimitMin_MtrNm_f32  tgt_AstLmt_Per1_VehSpd_Kph_f32  tgt_AstLmt_Per1_VehSpd_Kph_f32  tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  tgt_AstLmt_Per1_UnitPercentFiltered_Uls_f32.value  tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value  tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value  target_AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_AstLmt_Per1_WheelImballanceCmd_MtrNm_f32  tgt_AstLmt_Per1_UnitPerc_Uls_f32  tgt_AstLmt_Per1_UnitPerc_Uls_f32  tgt_AstLmt_Per1_LimitPerc_Uls_f32  tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value  tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value  target_AstLmt_Per1_TsMitCommand_MtrNm_f32  tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32  tgt_AstLmt_Per1_Torque_MtrNm_f32  tgt_AstLmt_Per1_Torque_MtrNm_f32  tgt_AstLmt_Per1_Torque_MtrNm_f32  tgt_AstLmt_Per1_SumLimitTorque_MtrNm_f32  tgt_AstLmt_Per1_Torque_MtrNm_f32	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32       tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32         gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TqLimitMin_MtrNm_f32       tgt_AstLmt_Per1_TqLimitMin_MtrNm_f32         gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32       tgt_AstLmt_Per1_VehSpd_Kph_f32         gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32       tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32         Iame       Actual Value       Expected Value       Res         gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value       0.100000001       0.100000001 ± 0.000030517578125         gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value       3       3 ± 0.0009765625         gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value       1.5       1.5 ± 0.0009765625		target_AstLmt_Per1_TSMitCommand_Mtrl	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32       tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32         gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TqLimitMin_MtrNm_f32       tgt_AstLmt_Per1_TqLimitMin_MtrNm_f32         gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32       tgt_AstLmt_Per1_VehSpd_Kph_f32         gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32       tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32         Iame       Actual Value       Expected Value       Res         gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value       0.100000001       0.100000001 ± 0.000030517578125         gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value       3       3 ± 0.0009765625         gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value       1.5       1.5 ± 0.0009765625	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3	32	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.100000001 0.100000001 ± 0.000030517578125 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 3 ± 0.0009765625 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 1.5 ± 0.0009765625		tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3:	2	
tgt_Rtle_Inst_Ap_AstLmt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.100000001 0.100000001 0.100000001 ± 0.000030517578125 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 3 3 ± 0.0009765625 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 1.5 1.5 ± 0.0009765625		tgt_AstLmt_Per1_TrqLimitMin_MtrNm f32		
the content of the				
Name         Actual Value         Expected Value         Res           gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value         0.100000001         0.100000001 ± 0.000030517578125         0.100000001         0.100000001 ± 0.0000765625         0.0009765625         0			trNm f32	
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value     0.100000001     0.100000001 ± 0.000030517578125       gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value     3     3 ± 0.0009765625       gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value     3     3 ± 0.0009765625       gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value     1.5     1.5 ± 0.0009765625				Pos
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value       3       3 ± 0.0009765625         gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value       3       3 ± 0.0009765625         gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value       1.5       1.5 ± 0.0009765625				11031
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value       3       3 ± 0.0009765625         gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value       1.5       1.5 ± 0.0009765625				
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value 1.5 ± 0.0009765625	·			
	gr_Asicht_FetT_PtecimitTorque_withM_t32.value	J		
	at Apt mt Dord SumlimTraCond Machine 500 ustra	1.5		



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.52 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-3		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	7.07999992		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2.0999999		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.19999981		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	4		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	4		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	3.2999995		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-1		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-8		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.5		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	14.6000004		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-2		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	rNm f32	
tgt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTGain Uls f32	tgt AstLmt Per1 AssistEOTGain Uls f3:	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mti	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt AstLmt Per1 LimitPercentFiltered UI		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	_	
gt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrCCDisable Cnt Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr		
gt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrEnable Cnt Igc	tgt_AstEmt_i eri_Emi notroobisable_ori		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm		
gt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32	_102	
gt_Rte_inst_Ap_AstLint.AstLint_Per1_Op1rqOvi_MtiNin_is2 gt_Rte_Inst_Ap_AstLint.AstLint_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_Op1rqOvi_MtrNm_i32 tgt_AstLmt_Per1_OutputRampMult_Uls_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_I	MtrNm_f32	
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2.0999999	2.0999999 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5.5999999	5.5999999 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	•

Test Step 2.53 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt ManualTrqCmd MtrNm M f32	3		
AstLmt SteeringAsstDefeat Cnt M Igc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	7.4400006		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2.20000005		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-4.5		
tgt AstLmt Per1 AssistEOTDamping MtrNm f32.value	3.29999995		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.30000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.69999981		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.0999999		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	1		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012 8.60000038		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value tgt_AstLmt_Per1_VehSpd_Kph_f32.value	38.700008		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	•	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_N		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cr	ıt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_L	ls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_l	gc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNn	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_C		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNr		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_ tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_r32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_\rqLimitMin_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_venSpd_Rpn_t32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm f32	
			Dage
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.30000012	0.300000012 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	1.5	5 ± 0.0009765625 1.5 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5.69999981	5.69999981 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.54 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-2		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
_SumLimPlCmpLimit_MtrNm_f32	7.80000019		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2.2999995		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.899999976		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.4000001		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.80000019		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.19999981		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	6		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	6		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.40000006		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-3		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	2.2999995		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.40000006		
at AstLmt Per1 ThermalLimit MtrNm f32.value	8.6999981		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	36.7999992		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-4.1999981		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	Vm f32	
gt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTGain Uls f32	tgt AstLmt Per1 AssistEOTGain Uls f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt AstLmt Per1 LimitPercentFiltered Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt		
gt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrEnable Cnt Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_CmrqOvr_MtrNm_f32	J2	
gt Rte Inst Ap AstLmt.AstLmt Per1 OutputRampMult Uls f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3:	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	igo	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	<b>500</b>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_t		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_M	rNm_f32	
Name	Actual Value	Expected Value	Resi
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.40000006	0.400000006 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-8.80000019	-8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-2.32000017	-2.31999993 ± 0.0009765625	
		I	

5.80000019

tgt\_AstLmt\_Per1\_TrqLimitMin\_MtrNm\_f32.value

5.80000019 ± 0.0009765625



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.55 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	2		
AstLmt_ManualTrqCmd_MtrNm_M_f32  AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt k_SumLimPlCmpLimit_MtrNm_f32	tgt_Rte_Inst_Ap_AstLmt 8.15999985		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2.400001		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	3.2999995		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-1		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001		
	6.5		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.9000001		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.3000001		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6.5 1		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	7		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value			
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	7		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	1		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-4		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3.4000001		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.0999999		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	26.8999996		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-5		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_t	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN	lm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_	lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_	_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3:	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_t		
gt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3		
gt Rte Inst Ap AstLmt.AstLmt Per1 SumLimTrqCmd MtrNm f32	tgt AstLmt Per1 SumLimTrqCmd MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt AstLmt Per1 ThermalLimitPerc Uls f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt AstLmt Per1 ThermalLimit MtrNm f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt AstLmt Per1 TrqLimitMin MtrNm f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt AstLmt Per1 VehSpd Kph f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_Mi	trNm f32	
			Desi
Name	Actual Value	Expected Value	Resi
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	1	1 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	7	7 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	3.5	3.5 ± 0.0009765625	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.56 (Repeat Count = 1) Name	Input Value		
	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	7 20000012		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-7.36000013 0		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	· ·		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
<_SumLimPlCmpLimit_MtrNm_f32			



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.57 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	5.36999989		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPlCmpLimit_MtrNm_f32	0.40000006		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2.5999999		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-1		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-3		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.30000012		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.69999981		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.0999999		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.5		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.40000006		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5.5999999		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.69999988		
yt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.30000019		
pt AstLmt Per1 VehSpd Kph f32.value	65.3000031		
t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f	ন্ত্	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt AstLmt Per1 AssistEOTDamping		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_	<del>-</del>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN		
		_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_N	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_0		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNr	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrN	lm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Ul	s_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_	Cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_Mtr	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNi	m_f32	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrN	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mtr	*Nm f32	
at Rte Inst Ap AstLmt.AstLmt Per1 TSMitCommand MtrNm f32	target_AstLmt_Per1_TSMitCommand_	_	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_U		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt AstLmt Per1 TrqLimitMin MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
	tgt_AstLmt_Per1_venSpd_kpn_i32 tgt_AstLmt_Per1_WheelImbalanceCmd	d MtrNm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32			
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.699999988 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	1.20000005	1.20000005 ± 0.0009765625	
		-1.20000005 ± 0.0009765625	
	-1.20000005		
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	-1.20000005 -0.840000033 6.0999999	-0.839999974 ± 0.0009765625 6.0999999 ± 0.0009765625	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>	

Test Step 2.58 (Repeat Count = 1) Name	Input Value			
	•			
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0 -4.3499999			
AstLmt_ManualTrqCmd_MtrNm_M_f32	-4.3499999			
AstLmt_SteeringAsstDefeat_Cnt_M_lgc				
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt			
<_SumLimPICmpLimit_MtrNm_f32 <a href="mailto:specific-street">specific-street</a> Add mt. Part. TSMitCommand. MtrNm. f32 yellus.	0.82999983			
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2.70000005			
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-2			
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-4			
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006			
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.80000019			
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.19999981			
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.5999999			
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-1			
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0			
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2.0999999			
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1			
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0			
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2.0999999			
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5			
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012			
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1			
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5			
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	8.80000019			
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6.6999981			
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012			
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.4000001			
pt AstLmt Per1 VehSpd Kph f32.value	25.1000004			
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-1.20000005			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt AstLmt Per1 AssistEOTDamping MtrNm f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tqt_AstLmt_Per1_LimitedReturn_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	0			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32			
gt_Rte_inst_Ap_AstLint.AstLint_Fet1_FixAssistCintu_within_i32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32				
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	Race		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Actual Value  Expected Value	Resi		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Actual Value  0.8000000012  0.8000000012   Expected Value 0.8000000012	Resi		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Actual Value  0.800000012	Resi		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Actual Value  0.8000000012  0.8000000012   Expected Value 0.8000000012	Resi		



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>~</b>

Test Step 2.59 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32	3.47000003		
AstLmt SteeringAsstDefeat Cnt M lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	1.25999999		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2.7999995		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-3		
tqt AstLmt Per1 AssistEOTDamping MtrNm f32.value	-5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5		
tgt_AsstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.9000001		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.3000019		
tgt AstLmt Per1 AssistVehSpdLimit MtrNm f32.value	6.6999981		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.89999976		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	0		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.89999976		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.5		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	75.6999969		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-2.0999999		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M	trNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNn	1_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNn	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M	trNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cr	ıt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U	ls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_I	gc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNn	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Ci		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNi	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNn	1_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_		
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.899999976	0.899999976 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	4.5	4.5 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-4.5	-4.5 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-4.04999971	-4.05000019 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6.30000019	6.30000019 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.60 (Repeat Count = 1) Name	Input Value		
	0		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32	2.25999999		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt Rte Inst Ap AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	1.69000006		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2.900001		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024		
tgt AstLmt Per1 AssistEOTLimit MtrNm f32.value	7		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.4000001		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.80000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.100000001		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	1.5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.10000001		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.5999999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	28.8999996		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-4.5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_N	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNi	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_M	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_C	<del>_</del> -	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
tgt_Rte_Inst_Ap_AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_0		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNr tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt AstLmt Per1 OutputRampMult Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_0		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt AstLmt Per1 PowerLimitPerc Uls	— -	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_FowerLimitFerc_Ois_i		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForGue_MtrNm_f32	tgt AstLmt Per1 PreLimitForstall_MtN	_	
tgt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNn	_	
tgt Rte Inst Ap AstLmt.AstLmt Per1 SumLimTrgCmd MtrNm f32	tgt AstLmt Per1 SumLimTrqCmd MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_N	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd	_MtrNm_f32	
Name	Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.699999988 ± 0.000030517578125	
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	6.19000006	6.19000006 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	6.1900006	6.19000006 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.619000018	0.619000018 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6.4000001	6.4000001 ± 0.0009765625	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>	

Test Step 2.61 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt ManualTrqCmd MtrNm M f32	-1.76999998		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt 2.11999989		
<_SumLimPlCmpLimit_MtrNm_f32 Instruct_Act   mt_Port_TSMitCommand_MtrNlm_f32_value	3		
rarget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-5		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-5 -7		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value			
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.0999999		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.5		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.9000001		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-4		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	3.2999995		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	3.2999995		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-8		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-2.5999999		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.6999981		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	85.1999969		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	rNm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr		
		_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mti		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_UI		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	<u>f</u> 32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn	1_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt	·Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	AtrNm f32	
Name	Actual Value	Expected Value	Resi
			Kest
gt_AstLmt_Per1_LimitPercentFiltered_UIs_f32.value	0.800000012	0.800000012 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	
1 A II 1 B 4 B 11 IIT		3.29999995 ± 0.0009765625	1
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	3.29999995		
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.660000026 6.5	0.660000026 ± 0.0009765625 6.5 ± 0.0009765625	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~	

Test Step 2.62 (Repeat Count = 1) Name	Input Value		
	0		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1.75999999		
AstLmt_ManualTrqCmd_MtrNm_M_f32	0		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	2.54999995		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.19999981		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.5999999		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	1		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.899999976		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	3		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.80000019		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	76.5999985		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	4		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_N		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNi		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_C		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32	tgt_AstLmt_Per1_LimitPercentFiltered_t		
tgt_Rte_Inst_Ap_AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrCCDisable 0		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_C	·	
tgt_Rte_Inst_Ap_AstLmt_Per1_PowerLimitPerc_UIs_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_t		
tgt_Rte_Inst_Ap_AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNi		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_N		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd	_MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.899999976	0.899999976 ± 0.000030517578125	
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	6	6 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	2.05000019	2.04999995 ± 0.0009765625	٠,
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.615000069	0.61500001 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6.5999999	6.5999999 ± 0.0009765625	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>	

Test Step 2.63 (Repeat Count = 1) Name	Input Value		
	·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-8.5		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPICmpLimit_MtrNm_f32	2.98000002		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.9000001		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-7		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	1		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.89999976		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.30000019		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.6999981		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.0999999		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	2		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.40000006		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
at AstLmt Per1 PowerLimitPerc Uls f32.value	0.100000001		
gt AstLmt Per1 PrkAssistCmd MtrNm f32.value	4		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8.80000019		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.400000006		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.900001		
at AstLmt Per1 VehSpd Kph f32.value	425.399994		
	5.19999981		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value		f2.2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_Mtr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_Mtrl		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrN	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable	_Cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cn	t_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_Mtrl	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_U	ls_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_	_Cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls	s_f32	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_Mt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_Mtr	Nm_f32	
gt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrN		
gt Rte Inst Ap AstLmt.AstLmt Per1 PullCompCmd MtrNm f32	tgt AstLmt Per1 PullCompCmd MtrN	_	
gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mt	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand	_	
pt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimitPerc Uls f32	tgt_AstLmt_Per1_ThermalLimitPerc_U		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_etc_ois_i32	tgt_AstLmt_Per1_ThermalLimit_MtrNr	_	
gt_Rte_Inst_Ap_AstLint.AstLint_Per1_TrqLimitMin_MtrNm_f32	tgt AstLmt Per1 TrqLimitMin MtrNm		
gt_Rte_inst_Ap_AsiLint.AsiLint_Fer1_inqLinittwiin_within1_is2 gt_Rte_inst_Ap_AsiLint.AsiLint_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_TqLimitwiii1_WithViii1 tgt_AstLmt_Per1_VehSpd_Kph_f32	_102	
		d MtcNm f22	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCm		
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.40000006	0.400000006 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1	-1 ± 0.0009765625	
	-0.40000006	-0.400000006 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.40000000	0.40000000 ± 0.00001 00020	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>	

Test Step 2.64 (Repeat Count = 1)	In a Malan		
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	8.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	3.41000009		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.79999995		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	2		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.4000001		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.80000019		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.19999981		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	3		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
tgt AstLmt Per1 PosServCCDisable Cnt Igc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003		
tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value	5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	0		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	363.799988		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	6		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	Nm f22	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtrl		
tgt_Rte_Inst_Ap_AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cni		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgd		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_t	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_M	trNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitFerStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	3.40000019	3.40000019 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	J.4000001	5.7000001 I 0.0009703023	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>	

Test Step 2.65 (Repeat Count = 1)	Input Value		
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc			
AstLmt_ManualTrqCmd_MtrNm_M_f32	-7.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPlCmpLimit_MtrNm_f32	3.8399991		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.70000005		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	1		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.20000003		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.5		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.9000001		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.30000019		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-3		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-3		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	4		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt AstLmt Per1 PowerLimitPerc Uls f32.value	0.30000012		
gt AstLmt Per1 PrkAssistCmd MtrNm f32.value	6		
gt AstLmt Per1 PullCompCmd MtrNm f32.value	1.5		
at AstLmt Per1 ThermalLimitPerc Uls f32.value	0.600000024		
pt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.10000038		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	246.100006		
st_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	Nm f32	
		_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_UIs_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtrl		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cni		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lge		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt	_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	!	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_	<u>f</u> 32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_t	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr	_	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt AstLmt Per1 TrgLimitMin MtrNm f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt AstLmt Per1 WheelImbalanceCmd M	ltrNm f32	
			Pos
Name	Actual Value	Expected Value	Resi
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3	3 ± 0.0009765625	
A A-H a-t Dard Darlins IT-su AVAIL 600 1			
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-3	-3 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	-3 -1.80000007 6.9000001	-1.79999995 ± 0.0009765625 6.9000001 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	•

tgt_Rte_Inst_Ap_AstLmt 4.2699998		
Pos		
Res		
Res 8125		



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	•

Test Step 2.67 (Repeat Count = 1)			~
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-6.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	4.69999981		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.5		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.69999981		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7.0999999		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.5		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	2		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	6		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
tgt AstLmt Per1 PosServCCDisable Cnt Igc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5		
tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value	8		
	4		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value			
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.30000019		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	84.5		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-1.20000005		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtrl	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn	:_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lge		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	<u>f</u> 32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_		
tgt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f		
tgt Rte Inst Ap AstLmt.AstLmt Per1 PullCompCmd MtrNm f32	tgt AstLmt Per1 PullCompCmd MtrNm 1		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt AstLmt Per1 TrqLimitMin MtrNm f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
		HrNm f22	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N		
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	,
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-5	-5 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-4	-4 ± 0.0009765625	,
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	7.0999999	7.0999999 ± 0.0009765625	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~	

Test Step 2.68 (Repeat Count = 1) Name	Input Value		
	•		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	6.55999994		
AstLmt_SteeringAsstDefeat_Cnt_M_Igc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	5.13000011		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.4000001		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.80000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7.19999981		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.5999999		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	3		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	7		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.899999976		
tgt AstLmt Per1 PosServCCDisable Cnt Igc.value	1		
tgt AstLmt Per1 PowerLimitPerc Uls f32.value	0.600000024		
tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value	-1.20000005		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	1		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.39999962		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	27.7000008		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-2.0999999		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_I	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_t		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_N	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_C		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm	1_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_0	Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrN	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_0	Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mtrl		
tgt Rte Inst Ap AstLmt.AstLmt Per1 TSMitCommand MtrNm f32	target_AstLmt_Per1_TSMitCommand_N	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt AstLmt Per1 TrqLimitMin MtrNm f		
		32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd	MtrNm f22	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32			
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	1	1 ± 0.000030517578125	
tot Act mt Dord Drol imitEorCtall Minkley 500 unive	4	4 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value			
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	•
		0 ± 0.0009765625 0 ± 0.0009765625	•



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~	

Test Step 2.69 (Repeat Count = 1) Name	Input Value			
	1			
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32	-5.55999994			
AstLmt SteeringAsstDefeat Cnt M Igc	0			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt 5.55999994			
k_SumLimPlCmpLimit_MtrNm_f32				
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.29999995			
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	5			
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	7			
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.60000024			
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.9000001			
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7.30000019			
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.69999981			
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	4			
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1			
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-7			
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0			
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1			
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-7			
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	8			
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.100000001			
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0			
tgt AstLmt Per1 PowerLimitPerc Uls f32.value	0.69999988			
tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value	-2.0999999			
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6			
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5			
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.5			
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	2.9000001			
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-4.5			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_	f32		
	tgt AstLmt Per1 AssistEOTDamping			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32		<del>-</del>		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_Mtr	_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_Mtrl			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_	_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrN	m_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable	_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cn	t_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_Mtrl	Nm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f	32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_U	ls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_	_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls	_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_Mt	rNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_Mtr	Nm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrN	_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrN	_		
tgt Rte Inst Ap AstLmt.AstLmt Per1 SumLimTrgCmd MtrNm f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mt			
tgt_Rte_Inst_Ap_AstLmt_Per1_TSMitCommand_MtrNm_f32	target AstLmt Per1 TSMitCommand	_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_UIs_f32	tgt_AstLmt_Per1_ThermalLimitPerc_U	<del>-</del>		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_HtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNr	_		
tgt_Rte_Inst_Ap_AstLint.AstLint_Peri_ThermalLinit_WithNin_isz	tgt AstLmt Per1 TrqLimitMin MtrNm			
tgt_Rte_inst_Ap_AstLmt.AstLmt_Peri_TrqLimitwiin_wtiNm_is2 tgt_Rte_inst_Ap_AstLmt.AstLmt_Peri_VehSpd_Kph_f32	tgt_AstLmt_Per1_TrqLimitwiin_withmi	_102		
		d MtcNm f22		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCm			
Name	Actual Value	Expected Value	Resu	
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.699999988 ± 0.000030517578125		
	7	7 ± 0.0009765625	•	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value				
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-7	-7 ± 0.0009765625	•	
		-7 ± 0.0009765625 -0.699999988 ± 0.0009765625	•	



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.70 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	5.5		
AstLmt_SteeringAsstDefeat_Cnt_M_Igc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPlCmpLimit_MtrNm_f32	5.98999977		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.20000005		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	6		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	8		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7.400001		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.80000019		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-8		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-8		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1.20000005 0.20000003		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012		
gt_AstLmt_Per1_FowerLimitPer0_0is_i32.value	-4.5		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	7		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_ThermalLimit_ete_ois_i52.value	0		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	85.1999969		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-6.5		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtrl	Nm f32	
gt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTGain Uls f32	tgt AstLmt Per1 AssistEOTGain Uls f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr1	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt AstLmt Per1 LimitPercentFiltered Uls	•	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt	_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtrl	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_M	trNm_f32	
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	6	6 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	

0

tgt\_AstLmt\_Per1\_TrqLimitMin\_MtrNm\_f32.value

0 ± 0.0009765625



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.71 (Repeat Count = 1)	Input Value		
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-4.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPlCmpLimit_MtrNm_f32	6.42000008		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	7		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.10000038		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7.5		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.9000001		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	6		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	1		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	1		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.89999976		
gt AstLmt Per1 PrkAssistCmd MtrNm f32.value	-6.5		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_ThermalLimit_de_ois_i32.value	8.80000019		
	53.4000015		
gt_AstLmt_Per1_VehSpd_Kph_f32.value			
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3.29999995		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	N	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn	t_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt	_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_t		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt AstLmt Per1 ThermalLimitPerc Uls f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 VehSpd Kph f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_inst_Ap_AstLint.AstLint_Peri_venSpd_Rpii_is2 gt_Rte_inst_Ap_AstLint.AstLint_Peri_venSpd_Rpii_is2	tgt_AstLmt_Per1_WheelImbalanceCmd_N	ItrNm f32	
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.89999976	0.899999976 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	1	1 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.300000012	0.300000012 ± 0.0009765625	
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	7.5	7.5 ± 0.0009765625	



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.72 (Repeat Count = 1) Name	Input Value		
	·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	4.5		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPlCmpLimit_MtrNm_f32	6.8499999		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	8		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.89999976		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.19999981		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7.5999999		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	7		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	2		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	2		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.400000006		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	3.2999995		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	5.5		
t_AstLmt_Per1_VehSpd_Kph_f32.value	12.6000004		
pt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	5.599999		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt AstLmt Per1 AssistEOTDamping Mt	·Nm f32	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtrl		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt AstLmt Per1 LimitedReturn MtrNm f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn		
yt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrEnable Cnt Ig		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt AstLmt Per1 LrnPnCtrTCmd MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	_132	
		22	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3:		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_M	/trNm_f32	
lame	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8	8 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	
		5 _ 0.00001 000E0	



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.73 (Repeat Count = 1) Name	Input Value		
	· ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-3.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
S_SumLimPlCmpLimit_MtrNm_f32	7.28000021		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.89999998		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-4.5		
pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.30000019		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7.6999981		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.10000038		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	8		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	3		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	3		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
pt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.20000003		
gt AstLmt Per1 PrkAssistCmd MtrNm f32.value	5.5999999		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2.0999999		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.40000006		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.099999		
t_AstLmt_Per1_VehSpd_Kph_f32.value	510.799988		
yt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-8.80000019		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	lm f32	
		111_132	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	22	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_	_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt AstLmt Per1 TrqLimitMin MtrNm f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt AstLmt Per1 WheelImbalanceCmd Mt	rNm f32	
			Dag
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.40000006	0.400000006 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3	3 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	3	3 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	1.5	1.5 ± 0.0009765625	
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2.0999999	2.0999999 ± 0.0009765625	



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.74 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	3.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPlCmpLimit_MtrNm_f32	7.71000004		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.79999995		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.39999962		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7.80000019		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.19999981		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	4		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	3 20000005		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	3.29999995		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	4		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
pt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.20000005		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	425.5		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8.80000019		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	NM_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	roo.	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_t		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3 tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc			
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_CtrrqOvr_MtrNm_f32	<b>32</b>	
gt Rte Inst Ap AstLmt.AstLmt Per1 OutputRampMult Uls f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3:	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_		
gt Rte Inst Ap AstLmt.AstLmt Per1 PowerLimitPerc Uls f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	igo	
gt_Rte_inst_Ap_AstLint.AstLint_Per1_PowerLinittPerc_ois_i32 gt_Rte_inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_	f32	
gt_Rte_inst_Ap_AstLint.AstLint_Per1_PreLimitForStail_witNm_i32 gt_Rte_inst_Ap_AstLmt.AstLint_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStail_MtrNm_t		
gt_Rte_inst_Ap_AstLint.AstLint_Per1_PreLimit1orque_Mithin_i32 gt_Rte_inst_Ap_AstLint.AstLint_Per1_PreAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MitNm_i		
gt_Rte_inst_Ap_AstLint.AstLint_Per1_PrkAssistCintd_MtrNm_i32 gt_Rte_inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32			
gt_Rte_inst_Ap_AstLint.AstLint_Fer1_FullCompCinta_MtiNin1_i32 gt_Rte_inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target AstLmt Per1 TSMitCommand MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_etc_0is_io2	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLint.AstLint_Per1_TrqLinittviiiT_witrviiiT_is2 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_inst_Ap_AstLmt.AstLmt_Ferr_verispu_Rpir_isz gt_Rte_inst_Ap_AstLmt.AstLmt_Perr_verispu_Rpir_isz	tgt_AstLmt_Per1_veriSpd_kpir_i32  tgt_AstLmt_Per1_WheelImbalanceCmd_Mi	rNm f32	
Vame	Actual Value	Expected Value	Pos
		· ·	Resi
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2.0999999	2.0999999 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	

2.20000005

tgt\_AstLmt\_Per1\_TrqLimitMin\_MtrNm\_f32.value

2.20000005 ± 0.0009765625



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.75 (Repeat Count = 1)	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-2.5		
AstLmt_ManualTrqCmd_MtrNm_M_f32	0		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
<_SumLimPlCmpLimit_MtrNm_f32	8.14000034		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.70000005		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3.2999995		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.300000012		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.5		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7.9000001		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.30000019		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.69999988		
gt AstLmt Per1 PosServCCDisable Cnt Igc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.400000006		
gt AstLmt Per1 PrkAssistCmd MtrNm f32.value	5		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.2999995		
at AstLmt Per1 VehSpd Kph f32.value	373.100006		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	0	- 100	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDampin	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_U		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_N	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_M		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimi		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDampin		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService	e_Cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltere	ed_Uls_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_Mtr	·Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisab	le_Cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_0	Cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_M	trNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_	Uls_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisabl		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_L	<del>_</del>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_N		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_N	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_Mt		
gt Rte Inst Ap AstLmt.AstLmt Per1 PullCompCmd MtrNm f32	tgt_AstLmt_Per1_PullCompCmd_Mt	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_f		
gt Rte Inst Ap AstLmt.AstLmt Per1 TSMitCommand MtrNm f32	target_AstLmt_Per1_TSMitComman	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_		
t Rte Inst Ap AstLint.AstLint Per1 ThermalLimit MtrNm f32	tgt_AstLmt_Per1_ThermalLimit_Mtr1		
	tgt_AstLmt_Per1_TrefmaiLimit_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32		III_IJZ	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	tonad Markins \$20	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceC		1_
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	5	5 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	3.5	3.5 ± 0.0009765625	
gt_Astemt_Ferr_Sumemirridoma_within_i32:value	0.0		





Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~	

Test Step 2.76 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt ManualTrqCmd MtrNm M f32	2.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	8.56999969		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.60000002		
tgt_AstLmt_Per1_13/inicommand_winvin_i3z.value	-6.5		
	5.5999999		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value			
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.60000038		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.39999962		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	6		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	6		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	4		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.4000001		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	324.200012		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	1.5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mi	trNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Ci		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm		
	tgt AstLmt Per1 OpTrqOvr MtrNm f32	_132	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32		pp.	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_t		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cr	<del></del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3.	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resi
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.699999988 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	6.5	6.5 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2.4000001	2.4000001 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	•

Test Step 2.77 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt ManualTrqCmd MtrNm M f32	-1.5		
AstLmt SteeringAsstDefeat Cnt M Igc	0		
Rte Inst Ap AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.600000024		
	-1.5		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	3.29999995		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value			
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.69999981		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.10000038		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.5		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.89999976		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.5		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	456.299988		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-2.5999999		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	Nm f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtrl	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt AstLmt Per1 DefeatLimitService Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt AstLmt Per1 LimitPercentFiltered Uls		
tgt_Rte_Inst_Ap_AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrEnable Cnt Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32	102	
	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	22	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_t		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	ltrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	7	7 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	7	7 ± 0.0009765625	,
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	6.29999971	6.30000019 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.78 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.39999998		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.0999999		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.19999981		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.60000038		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	3.29999995		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	8		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	6		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	8		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.89999976		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.5999999		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	345.399994		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-1.20000005		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	lm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNi	n_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_I	gc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_	<u>f</u> 32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_	lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_UIs_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_Mt	rNm_f32	
Name	Actual Value	Expected Value	Resi
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.899999976	0.899999976 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5.5999999	5.5999999 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	
		T	

2.5999999

tgt\_AstLmt\_Per1\_TrqLimitMin\_MtrNm\_f32.value

2.5999999 ± 0.0009765625



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	•

Test Step 2.79 (Repeat Count = 1)	Invest Value		
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-7		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt 8.80000019		
k_SumLimPlCmpLimit_MtrNm_f32	-1.29999995		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	6		
· · · ·	0.69999988		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.19999981		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.30000019		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.69999981		
tgt AstLmt Per1 CombinedDamping MtrNm f32.value	5.5999999		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_tmPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	7		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt AstLmt Per1 PowerLimitPerc Uls f32.value	0.80000012		
tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value	-1.20000005		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.100000001		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.70000005		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	234.5		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-2.0999999		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	rNm f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt AstLmt Per1 AssistEOTGain Uls f3:	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mti	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_UI		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrTCmd MtrNm f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt AstLmt Per1 PreLimitForStall MtrNn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt AstLmt Per1 PreLimitTorque MtrNm	f32	
tgt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt AstLmt Per1 PrkAssistCmd MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_I	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	4	4 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1.20000005	-1.20000005 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.24000001	-0.239999995 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2.70000005	2.70000005 ± 0.0009765625	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~	

Test Step 2.80 (Repeat Count = 1) Name	Input Value		
	0		
AstLmt_ManualTrqCmdEn_Cnt_M_Igc AstLmt_ManualTrqCmd_MtrNm_M_32	7		
AstLmt SteeringAsstDefeat Cnt M Igc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	5.5		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	7		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.30000019		
tgt_AstLmt_Per1_AssistCOTLiffit_MtrNm_f32.value	8.39999962		
tgt AstLmt Per1 AssistVehSpdLimit MtrNm f32.value	7.0999999		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	4		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	-2.0999999		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_OutputRampindit_Ois_isz.value	1		
	0.899999976		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3.29999995 0.200000003		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	2.79999995		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value			
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	123.599998		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3.29999995	f00	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping	<del>-</del> -	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Ci		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_Mti	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_U		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable	=	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_UI	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_M	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_Mtrf	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_M	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCn	md_MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.899999976	0.899999976 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	
	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value			
tgt_AstLmt_Per1_FreLimitrorque_wirrNm_f32.value	0	0 ± 0.0009765625	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>	

Test Step 2.81 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt ManualTrqCmd MtrNm M f32	4		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.300000012		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.10000002		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
tgt AstLmt Per1 AssistEOTLimit MtrNm f32.value	4.5		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.9000001		
tgt_AstLmt_Per1_AssiststalicImit_within_i32.value	4.3000001		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_Lmnledr\edum_with\m_i32.value	0		
gt_AstLmt_Per1_LmPnCtrEnable_Cnt_lgc.value	1		
	8.80000019		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_Op1rqOvt_mithtn=isz.value tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
	0.300000012		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-6		
tgt AstLmt Per1 PullCompCmd MtrNm f32.value	5		
	0.300000012		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	6.80000012		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	0		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	-7		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mi	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_UIs_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn	<del>_</del> -	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Ci		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_k		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	_132	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32		raa.	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_t		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cr	<del></del>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_		
Name	Actual Value	Expected Value	Resi
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.30000012	0.300000012 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	2.6400001	2.6400001 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3.9000001	3.9000001 ± 0.0009765625	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.82 (Repeat Count = 1) Name	Input Value		
	· ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-3 0		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	•		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
S_SumLimPlCmpLimit_MtrNm_f32	0.579999983		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-2		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-4		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.899999976		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.5999999		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.4000001		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-1		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.40000006		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
pt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.40000006		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-7		
gt AstLmt Per1 PullCompCmd MtrNm f32.value	6		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.40000006		
pt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.900001		
t_AstLmt_Per1_VehSpd_Kph_f32.value	512		
st_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-8		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	Im f32	
		MIII_132	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	22	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_1		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt	- •	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_	<u>f</u> 32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_t	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
at Rte Inst Ap AstLmt.AstLmt Per1 TrgLimitMin MtrNm f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
pt Rte Inst Ap AstLmt.AstLmt Per1 VehSpd Kph f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_M	rNm f32	
		1	
Name	Actual Value	Expected Value	Resi
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.40000006	0.400000006 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5.11999989	5.11999989 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-5.11999989	-5.11999989 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-1.60000002	-1.60000002 ± 0.0009765625	
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4	4 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.83 (Repeat Count = 1)	Installed		
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	3		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.860000014		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.899999976		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-5		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.10000001		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.69999981		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.0999999		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.5		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	1		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-3		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-8		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	7		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	125.260002		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	1		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr1		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_	Igc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt AstLmt Per1 LimitPercentFiltered Uls	•	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt AstLmt Per1 LimitedReturn MtrNm f3	_	
gt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrCCDisable Cnt Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt		
gt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrEnable Cnt Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgd		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_		
gt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 OutputRampMult Uls f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	2	
gt Rte Inst Ap AstLmt.AstLmt Per1 PosServCCDisable Cnt Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLint.AstLint_Fei1_FowerLinitFei0_0is_i32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
gt_Rte_Inst_Ap_AstLint.AstLint_Per1_PreLimitForStail_MtiNint_132  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_		
gt_Rte_inist_Ap_AstLmt.AstLmt_Ferr_FreLimitrorque_withini_is2	tgt_AstLmt_Per1_FreLimitTorque_MitNin_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmt_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f		
gt_Rte_inst_Ap_AstLint.AstLint_Peri_PullCompCinta_MtiNint_is2 gt_Rte_inst_Ap_AstLint.AstLint_Peri_SumLimTrqCinta_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm		
	target AstLmt Per1 TSMitCommand Mtrl	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32		_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f:		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3.		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	111 (00	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_M	_	
Name	Actual Value	Expected Value	Resi
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3	3 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	1	1 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.5	0.5 ± 0.0009765625	
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.0999999	4.0999999 ± 0.0009765625	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.84 (Repeat Count = 1) Name	Input Value		
	· ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-2		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
S_SumLimPlCmpLimit_MtrNm_f32	1.13999999		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.800000012		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-4		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-6		
pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.80000019		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.19999981		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.5999999		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-3		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
pt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
pt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	1		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8		
yt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.099999		
t_AstLmt_Per1_VehSpd_Kph_f32.value	75.3000031		
yt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	2.2999995		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	lm f32	
		NIII_132	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	200	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_1		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt	- •	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	<u>lgc</u>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_	<u>f</u> 32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt AstLmt Per1 ThermalLimitPerc Uls f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt AstLmt Per1 TrqLimitMin MtrNm f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_M	rNm f32	
			Pac
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	2.51999998	2.51999998 ± 0.0009765625	
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.19999981	4.19999981 ± 0.0009765625	



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

### Sealthn All Assault From Minhor M. 192  **Authn J. Seelen (Assault Manual Technolom Minhor M. 192  **Authn J. Seelen (Assault M. 192  **Authn J. Seelen	Test Step 2.85 (Repeat Count = 1) Name	Input Value		
Sealing Manual Toping Janking Jul 20   2   2   2   2   2   2   2   2   2		· ·		
State   Sering Assistance   Sering Assistanc				
Semilipric Prof.   pm.   Mrhm.   132				
Samulan/Complanti, Marthur, 152   Samulan/Complanti, Marthur, 152 value   Sale Astern, Perl., 1548Comman, 15				
Quartern Perl _ SNACCOMMANN_ 122 value   0.99999988   0.45 cmt. Perl _ AssetSCT03mpingMirhim_52 value   0.70 cmt. Perl _ AssetSCT03mpingMirhim_52 value   0.40000010   0.45 cmt. Perl _ AssetSCT03min_Mirhim_52 value   0.49999981   0.45 cmt. Perl _ AssetSVerlegot_im_Mirhim_52 value   0.49999981   0.45 cmt. Perl _ CommissComp. Dec. value   0.45 cmt. Perl _ PullsComp. Dec. value   0.45 cmt. Perl _				
Statum   Pert   AssistCortagning   Minhm   Stavable   5				
Q.Asturn.Perl. AssistFOTamping, Jahrhum, D2 value   0, 200000012   0, 45turn.Perl. AssistFOTamping, Jahrhum, D2 value   4,00000013   0, 45turn.Perl. AssistFOTamping, Jahrhum, D2 value   4,00000013   0, 45turn.Perl. AssistStatill im, Jahrhum, D2 value   4,00000013   0, 45turn.Perl. AssistStatill im, Jahrhum, D2 value   4,00000013   0, 45turn.Perl. CombinedStamping, Jahrhum, D2 value   4,00000013   0, 45turn.Perl. DistributionStamping, Jahrhum, D2 value   4,100000013   0, 45turn.Perl. DistributionStamping, Jahrhum, D2 value   4,100000013   0, 45turn.Perl. DistributionStatill im, Jahrhum, D2 value   1, 45turn.Perl. DistributionStatill im, Jahrhum, D2 value   1, 45turn.Perl. DistributionStatill im, Jahrhum, D2 value   1, 45turn.Perl. DistributionStatill im, Jahrhum, D2 value   0, 45turn.Perl. DistributionStatill im, D2 value   0, 45turn.Perl. DistributionStatill im, D2 value   0, 50000005   0, 45turn.Perl. DistributionStatill im, D2 value   0, 45turn.Perl. DistributionStatill im, D2 value   0, 50000005   0, 45turn.Perl. DistributionStatill im, D2 value   0, 45tu				
Astump Pert   AssistCOTAM  MINN   E2 value				
Astum. Perl AssisSOTLIM, MrNm, 122-value   4,0000001				
Q.Astim.Perl   Assistitation  MrNm, 132 value				
Astumer   Perf   Desire   De	·			
Astum Pert   CombinedDamping   Mirkm   123 value				
2, Astum Pert   DefeatLimistervice, Ont. jov.volve   1   2, 24   3, 29999995   3, 299999999999999999999999999999999999				
20   Astum   Pert   LimitedReturn   Mrkim   132 value   2   2   2   2   2   2   2   2   2				
Q. Astum. Pert   LmPPCCIFCDable Cnt   go.value   0   1   2   3   29999995   1   1   3   1   3   29999995   1   1   3   3   3   3   3   3   3   3				
Astum				
AstLmt.Pert L. DriPCOFTCOM_MINNim_G2 value   2,399999988   3,4st.mt.Pert OptingVm_MINNim_G2 value   0,899999988   3,4st.mt.Pert OptingVm_MINNim_G2 value   0,899999988   3,4st.mt.Pert OptingVm_MINNim_G2 value   0,899999988   3,4st.mt.Pert PosServCCDisable_Cnt_glo-value   0,899999988   3,4st.mt.Pert PosServCotton_WinningVm_G2 value   0,899999988   3,4st.mt.Pert PosServCotton_WinningVm_G2 value   0,899999988   3,4st.mt.Pert PosServCotton_WinningVm_G2 value   0,84st.mt.Pert PosServCotton_WinningVm_G2 value   3,4st.mt.Pert PosServCotton_WinningVm_G2 value   3,4st.mt.Pert PosServCotton_WinningVm_G2 value   0,84st.mt.Pert PosServCotton_WinningVm_G2 value   0,84st.mt.Pert PosServCotton_WinningVm_G2 value   0,4st.mt.Pert P				
Salt.ml   Perl   OptnYour, Mirkm   132 value   0				
Ast.ml. Perf.   OstpulSamphult, Us.   52 value     0   0   0   0   0   0   0   0   0				
AstLmt Pert   PosSenoCDisable   Cnt   go.value   0   0   0   0   0   0   0   0   0				
J. AstLm. Perl _ PowerLimiPerc_Uis_   32 value   0.69999988   1,   AstLm. Perl _ PirAssistCnd_MNNin_    52 value   1.20000005   1,   AstLm. Perl _ PirAssistCnd_MNNin_    52 value   1.20000005   1,   AstLm. Perl _ PirassistCnd_MNNin_    52 value   1.20000005   1,   AstLm. Perl _ PirassistCnd_MNNin_    52 value   1.79999981   1,   AstLm. Perl _ VehSpd_Kpt_    32 value   37 4000015   3.2000001   3.20000001   3.20000001   3.20000001   3.20000001   3.20000000001   3.20000001   3.20000001   3.2000000000001   3.2000000000000000000000000000000000000				
gl_AstLmt_Pert_PrikAssistCnd_MinNm_f32 value		•		
AstLmL Perl_PullCompCmd_MirNm_132.value				
2], AstLmt_Pert_ThermatLimit Perc_Uls_122_value   0.699999988    2], AstLmt_Pert_WenSpd_Kpt_122_value   7.19999981    2], AstLmt_Pert_WenSpd_Kpt_122_value   37.4000015    3], AstLmt_Pert_WenSpd_Kpt_122_value   37.4000015    3], AstLmt_Pert_WenSpd_Kpt_122_value   37.4000015    3], AstLmt_Pert_AssistCond_MirNm_132_value   3.4000001    3], Rel_Inst_Ap_AstLmt.AstLmt_Pert_AssistCOTDamping_MirNm_132_value   15.8000001    4], Rel_Inst_Ap_AstLmt.AstLmt_Pert_AssistCOTDamping_MirNm_132_value   15.8000001    4], Rel_Inst_Ap_AstLmt.AstLmt_Pert_AssistCOTSam_Uls_122_value   15.8000001    4], Rel_Inst_Ap_AstLmt.AstLmt_Pert_AssistSOTSam_Uls_122_value   15.8000001    4], Rel_Inst_Ap_AstLmt.AstLmt_Pert_AssistSOTSam_Uls_122_value   15.8000001    4], Rel_Inst_Ap_AstLmt.AstLmt_Pert_CombinedDamping_MirNm_132_value   15.8000001    4], Rel_Inst_Ap_AstLmt.AstLmt_Pert_DefeatLimiService_Cnt_Igo_value_LimiLevice_Uls_122_value   15.8000001    4], Rel_Inst_Ap_AstLmt.AstLmt_Pert_LimiPercentFilered_Uls_122_value   15.80000001    4], Rel_Inst_Ap_AstLmt.AstLmt_Pert_LimiPercentFilered_Uls_122_value   15.80000001    4], Rel_Inst_Ap_AstLmt.AstLmt_Pert_LimiPercentFilered_Uls_122_value   15.8000000000000000000000000000000000000				
AstImt_Pert   ThermalLumit_MtrNm_f32 value				
AstLIM_Pert   VehSpd , Kph , G2 value   3.4000015				
3.48.Lmt   Pert   WheelimbalanceCmd MirNm   f32 value   3.4000001   tgl. AstLmt   Pert   AssistCond   MirNm   f32   tgl. AstLmt   Pert   AssistCond   MirNm   f32   tgl. Rel. Inst.   Ap. AstLmt   AstLmt   Pert   AssistCond   MirNm   f32   tgl. Rel. Inst.   Ap. AstLmt   AstLmt   Pert   AssistCond   MirNm   f32   tgl. Rel. Inst.   Ap. AstLmt   AstLmt   Pert   AssistCond   MirNm   f32   tgl. AstLmt   Pert   DefeatLimitService   Cnt.   tgl.   tgl. AstLmt   Pert   DefeatLimitService   tgl. AstLmt   tgl.				
It   It   It   It   It   It   It   It		37.4000015		
Ite   Inst.   Ap   AstLmt.   Astl.mt.   AssistEOTDamping   MirNm   132   tg   AstLmt.   Pert   AssistEOTDamping   MirNm   132   tg   AstLmt.   Pert   AssistEOTGain   Uis   Test   Uis   AstLmt.   Pert   AssistEOTGain   Uis   Test   Uis   U				
Ig   AstLm  Ap   AstLm  AstLm  Per    AssistEOTGain   Uis   f32   Ig   AstLm  Per    AssistEOTGain   Uis   f32   Ig   AstLm  AstLm  Per  AssistEOTLimit, MirNm   f32   Ig   AstLm  Per    AssistEOTLimit, MirNm  f32   Ig   AstLm  Per    CombinedDamping, MirNm  f32   Ig   AstLm  Per    CombinedDamping   MirNm  f32   Ig   AstLm  Per    DefeatLimitService   Cnt   Igc   Ig   AstLm  Per    LimitPercontFiltered   Uis   f32   Ig   AstLm  Per    DefeatLimitService   Cnt   Igc   Ig   AstLm  Per    DefeatLimitService   Cnt   Igc   Ig   AstLm  Per    DefeatLimitService   Uis   AstLm  Per    DefeatLimitService   Uis   AstLm  Per    DefeatLimitService   Uis   Uis   AstLm  Per    D				
gt Rte Inst Ap AstLmt AstLmt Per1 AssistEOTLimit MtrNm [32]         tgt AstLmt Per1 AssistStallLimit MtrNm [32]         tgt AstLmt Per1 AssistStallLimit MtrNm [32]           gt Rte Inst Ap AstLmt AstLmt Per1 AssistStallLimit MtrNm [32]         tgt AstLmt Per1 AssistStallLimit MtrNm [32]           gt Rte Inst Ap AstLmt AstLmt Per1 CombinedDamping MtrNm [32]         tgt AstLmt Per1 DefeatLimitService Cnt Igc           gt Rte Inst Ap AstLmt AstLmt Per1 LimitePercentFiltered Uls [32]         tgt AstLmt Per1 DefeatLimitService Cnt Igc           gt Rte Inst Ap AstLmt AstLmt Per1 LimitePercentFiltered Uls [32]         tgt AstLmt Per1 LimitePercentFiltered Uls [32]           gt Rte Inst Ap AstLmt AstLmt Per1 LimitePercentFiltered Uls [32]         tgt AstLmt Per1 LimitePercentFiltered Uls [32]           gt Rte Inst Ap AstLmt AstLmt Per1 LimiteProfictrom MtrNm [32]         tgt AstLmt Per1 LimiteRefeum MtrNm [32]           gt Rte Inst Ap AstLmt AstLmt Per1 LimiteRefeum MtrNm [32]         tgt AstLmt Per1 LimiteRefeum MtrNm [32]           gt Rte Inst Ap AstLmt AstLmt Per1 [Per CortCrObable Cnt Igc         tgt AstLmt Per1 LimiteRefeum MtrNm [32]           gt Rte Inst Ap AstLmt AstLmt Per1 [Per OutputRampMult Uls [32]         tgt AstLmt Per1 [Per OutputRampMult Uls [32]           gt Rte Inst Ap AstLmt AstLmt Per1 Per SeeverCObisable Cnt Igc         tgt AstLmt Per1 [Per SeeverCobisable Cnt Igc           gt Rte Inst Ap AstLmt AstLmt Per1 Per Verification Limit MtrNm [32]         tgt AstLmt Per1 [Per SeeverCobisable Cnt Igc           gt Rte Inst Ap AstLmt AstLmt Per1 [Per Per Imiter Co	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32		lm_f32	
gt Rte Inst.Ap, AstLmt AstLmt. Per1_AssistStallLimit_MtrNm_f32         tgt_AstLmt. Per1_AssistStallLimit_MtrNm_f32           gt Rte Inst.Ap, AstLmt AstLmt. Per1_AssistStAllLimit_MtrNm_f32         tgt_AstLmt. Per1_AssistStAllLimit_MtrNm_f32           gt Rte Inst.Ap, AstLmt. AstLmt. Per1_DefeatLimitService_Cnt_lgc         tgt_AstLmt. Per1_DefeatLimitService_Cnt_lgc           gt Rte Inst.Ap, AstLmt. AstLmt. Per1_LimitGerentFiltered_Uls_f32         tgt_AstLmt. Per1_DefeatLimitService_Cnt_lgc           gt Rte Inst.Ap, AstLmt. AstLmt. Per1_LimitGerentFiltered_Uls_f32         tgt_AstLmt. Per1_LimitGerentFiltered_Uls_f32           gt Rte Inst.Ap, AstLmt. AstLmt. Per1_LimitGerentFiltered_Uls_f32         tgt_AstLmt. Per1_LimitGerentFiltered_Uls_f32           gt Rte Inst.Ap, AstLmt. AstLmt. Per1_LimPnCtrCollable_Cnt_lgc         tgt_AstLmt. Per1_LimPnCtrCollable_Cnt_lgc           gt Rte Inst.Ap, AstLmt. AstLmt. Per1_OpTrqOvr_MtrNm_f32         tgt_AstLmt. Per1_OpTrqOvr_MtrNm_f32           gt Rte Inst.Ap, AstLmt. AstLmt. Per1_OpTrqOvr_MtrNm_f32         tgt_AstLmt. Per1_OpTrqOvr_MtrNm_f32           gt Rte Inst.Ap, AstLmt. AstLmt. Per1_Poseer/CDisable_Cnt_lgc         tgt_AstLmt. Per1_Poser-Club_ist           gt Rte Inst.Ap, AstLmt. AstLmt. Per1_PreLimitTorque_MtrNm_f32         tgt_AstLmt. Per1_Power-LimitTorque_MtrNm_f32           gt Rte Inst.Ap, AstLmt. AstLmt. Per1_PreLimitTorque_MtrNm_f32         tgt_AstLmt. Per1_PreLimitTorque_MtrNm_f32           gt Rte Inst.Ap, AstLmt. AstLmt. Per1_SulficTordn_MtrNm_f32         tgt_AstLmt. Per1_SulficTornand_MtrNm_f32				
gt Rte Inst.Ap, AstLmt AstLmt, Perl _AssistVehSpdLimit, MtrNm_f32         tgt_AstLmt_AstLmt_Perl _CombinedDamping, MtrNm_f32         tgt_AstLmt_Perl _LmtrlErend_Uls_f32         tgt_AstLmt_Perl _DetroCtrDend_MtrNtm_f32         tgt_AstLmt_Perl _DetroCtrDend_MtrNtm_f32         tgt_AstLmt_Perl _DetroCtrDend_MtrlAtm_f32         tgt_AstLmt_Perl _PreLimitForque_MtrlAtm_f32         tgt_AstLmt_Perl _PreLimitForque_MtrlAtm_f32         tgt_AstLmt_Perl _PreLimitForque_MtrlAtm_f32         tgt_AstLmt_Perl _PreLimitForque_MtrlAtm_f32         tgt_AstLmt_Perl _PreLimitForque_MtrlAtm_f32<				
gt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   CombinedDamping   MtrNm   f32   tgt   AstLmt   Pert   DefeatLimitService   Cnt   lgc   tgt   Rte   Inst   Ap   AstLmt   AstLmt   Pert   DefeatLimitService   Cnt   lgc   tgt   AstLmt   Pert   DefeatLimitService   Cnt   lgc   tgt   AstLmt   Pert   LimitPercentFiltered   Uls   f32   tgt   AstLmt   Pert   DopTrqOvr   MtrNmt   f32   tgt   AstLmt   Pert   PosservCCDisable   Cnt   tgc   tgt   AstLmt   Pert   PosservCDDisable				
gt Rte_Inst_Ap_AstLmtAstLmt_Perl_DefeatLimitService_Cnt_Igc         tgt_AstLmt_Perl_DefeatLimitService_Cnt_Igc           gt_Rte_Inst_Ap_AstLmtAstLmt_Perl_LimitPercentFiltered_Uis_f32         tgt_AstLmt_Perl_LimitPercentFiltered_Uis_f32           gt_Rte_Inst_Ap_AstLmtAstLmt_Perl_LimitedReturn_MtrNm_f32         tgt_AstLmt_Perl_LimitedReturn_MtrNm_f32           gt_Rte_Inst_Ap_AstLmtAstLmt_Perl_LimPnCtrCcDisable_Cnt_Igc         tgt_AstLmt_Perl_LimPnCtrCcDisable_Cnt_Igc           gt_Rte_Inst_Ap_AstLmtAstLmt_Perl_LimPnCtrCcDisable_Cnt_Igc         tgt_AstLmt_Perl_LimPnCtrCcd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmtAstLmt_Perl_OpertCrCcd_MtrNm_f32         tgt_AstLmt_Perl_LimPnCtrCcd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmtAstLmt_Perl_OutputRampMult_Uis_f32         tgt_AstLmt_Perl_Pi_OutputRampMult_Uis_f32           gt_Rte_Inst_Ap_AstLmtAstLmt_Perl_PosServCcDisable_Cnt_Igc         tgt_AstLmt_Perl_PosServCcDisable_Cnt_Igc           gt_Rte_Inst_Ap_AstLmtAstLmt_Perl_PosServCcDisable_Cnt_Igc         tgt_AstLmt_Perl_PosServCcDisable_Cnt_Igc           gt_Rte_Inst_Ap_AstLmtAstLmt_Perl_PosServCcDisable_Cnt_Igc         tgt_AstLmt_Perl_PosServCcDisable_Cnt_Igc           gt_Rte_Inst_Ap_AstLmtAstLmt_Perl_PrelimitForStall_MtrNm_f32         tgt_AstLmt_Perl_PosServCcDisable_Cnt_Igc           gt_Rte_Inst_Ap_AstLmtAstLmt_Perl_PrelimitForStall_MtrNm_f32         tgt_AstLmt_Perl_PrelimitForStall_MtrNm_f32           gt_Rte_Inst_Ap_AstLmtAstLmt_Perl_SumLimitPerc_Uis_f32         tgt_AstLmt_Perl_PrelimitForStall_MtrNm_f32           gt_Rte_Inst_Ap_AstLmtAstLmt_Perl_TistMitC	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNi	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_if32         tgt_AstLmt_Per1_LimitPercentFiltered_Uls_if32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNmt_f32         tgt_AstLmt_Per1_LimitedReturn_MtrNmt_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc         tgt_AstLmt_Per1_LmPnCtrCDisable_Cnt_lgc           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTcmd_MtrNm_if32         tgt_AstLmt_Per1_LmPnCtrTcmd_MtrNm_if32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_if32         tgt_AstLmt_Per1_LmPnCtrTcmd_MtrNm_if32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OptrqOvr_MtrNm_if32         tgt_AstLmt_Per1_OptrqOvr_MtrNm_if32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosSenvCCDisable_Cnt_lgc         tgt_AstLmt_Per1_OptrqOvr_MtrNm_if32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ProsenvCDisable_Cnt_lgc         tgt_AstLmt_Per1_PosenvCDisable_Cnt_lgc           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_if32         tgt_AstLmt_Per1_PosenvCDisable_Cnt_lgc           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_if32         tgt_AstLmt_Per1_PreLimitPerc_Uls_if32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForque_MtrNm_if32         tgt_AstLmt_Per1_PreLimitForque_MtrNm_if32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimitPerc_Uls_if32         tgt_AstLmt_Per1_SumLimitTerqud_MtrNm_if32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPern_Uls_if32         tgt_AstLmt_Per1_ThermalLimitPern_Uls_if32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TreLimitMin_MtrNm	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32         tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32         tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCoolisable_Cnt_igc         tgt_AstLmt_Per1_LmPnCtrCroolisable_Cnt_igc         tgt_AstLmt_Per1_LmPnCtrCroolisable_Cnt_igc           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_UmPnCtrCrod_MtrNm_f32         tgt_AstLmt_Per1_UmPnCtrCrod_MtrNm_f32         tgt_AstLmt_Per1_UmPnCtrCrod_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32         tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32         tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OptrqOvr_MtrNm_f32         tgt_AstLmt_Per1_OptrqOvr_MtrNm_f32         tgt_AstLmt_Per1_OptrqOvr_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc         tgt_AstLmt_Per1_OptrqOvr_MtrNm_f32         tgt_AstLmt_Per1_PosServCCDisable_Cnt_Igc           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32         tgt_AstLmt_Per1_PosServCCDisable_Cnt_Igc         tgt_AstLmt_Per1_PosServCCDisable_Cnt_Igc           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32         tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32         tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForGor_MtrNm_f32         tgt_AstLmt_Per1_SumLimitPerc_Uls_f32         tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32         tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32         tgt_AstLmt_Per1_ThermalLimitMtrNm_f32 </td <td>gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc</td> <td>tgt_AstLmt_Per1_DefeatLimitService_Cnt_I</td> <td>gc</td> <td></td>	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_I	gc	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LmPnCtrCCDisable_Cnt_Igc tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LmPnCtrcable_Cnt_Igc tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LmPnCtrcable_Cnt_Igc tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LmPnCtrcable_Cnt_Igc tgt_AstLmt_Per1_LmPnCtrCable_Cnt_Igc tgt_AstLmt_Per1_LmPnCtrCable_Cnt_Igc tgt_AstLmt_Per1_DpTrqCvr_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_OpTrqCvr_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_OpTrqCvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqCvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqCvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqCvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqCvr_MtrNm_f32 tgt_AstLmt_Per1_DpSservCCDisable_Cnt_Igc tgt_AstLmt_Per1_DpSservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PosservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PosservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PosservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PosservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PosservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PosservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PosservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PosservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PosservCCDisable_Cnt_Igc tgt_AstLmt_Per1_OpTrqCvr_MtrNm_f32 tgt_AstLmt_Per1_PosservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PosservCCDisable_Cnt_Igc tgt_AstLmt_Per1_OpTrqCvr_MtrNm_f32 tgt_AstLmt_Per1_PosservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PosservCCDisable_IntrNm_f32 tgt_AstLmt_Per1_PosservCCDisable_IntrNm_f32 tgt_AstLmt_Per1_PosservCCDisable_IntrNm_f32 tgt_AstLmt_Per1_TopT_IntrIntrIntrNm_f32 tgt_AstLmt_Per1_TopT_IntrIntrIntrIntrNm_f32 tgt_AstLmt_Per1_TopT_IntrIntrIntrIntrNm_f32 tgt_AstLmt_Per1_TopT_IntrIntrIntrIntrNm_f32 tgt_AstLmt_Per1_IntrIntrIntrIntrIntrNm_f32 tgt_AstLmt_Per1_IntrIntrIntrIntrIntrIntrIntrIntrIntrIntr	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_	_f32	
tgt_AstLmt_Ap_AstLmt_AptLmt_Per1_LmPnCtrEnable_Cnt_lgc  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OpTrqOvr_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OpTrqOvr_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OpTrqOvr_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OpServCCDisable_Cnt_lgc  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCCDisable_Cnt_lgc  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCCDisable_Cnt_lgc  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCCDisable_Cnt_lgc  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosTimitForStall_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PullCompCmd_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TstMitCommand_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TstMitCommand_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitPerc_Uls_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitPerc_Uls_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitMtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitMtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitMtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitMtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TopLimitMin_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_VehSpd_Kph_f32  gt_AstLmt_Per1_TopLimitForStall_MtrNm_f32  gt_AstLmt_Per1_TopLimitForStall_MtrNm_f32  gt_AstLmt_Per1_TopLimitForStall_MtrNm_f32  gt_AstLmt_Per1_Trel_imitForStall_MtrNm_f32  gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value  gt_AstLmt_Per1_End_InitPerc_Uls_f32  gt_AstLmt_Per1_Det_InitPerc_Uls_f32  gt_AstLmt_Per1_SumLimTrQCmd_MtrNm_f32.value  gt_AstLmt_Per1_SumLimTrQCmd_MtrNm_f32.value  gt_AstLmt_Per1_SumLimTrQCmd_MtrNm_f32.value  gt_AstLmt_Per1_SumLimTrQCmd_MtrNm_f32.value  gt_Ast	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3	2	
the linst Ap_AstLmt AstLmt Per1_LmPnCtrTCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosservCCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosservCCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosservCCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosservCCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosservCCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosservCCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForQue_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForQue_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForQue_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForQue_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitMtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_AstLmt_Per1_PreLimitMin_MtrNm_f32 gt_AstLmt_Per1_PreLimitMin_far2 gt_AstLmt_Per1_PreLimitMin_far2 gt_AstLmt_Per1_PreLimitMin_far2 gt_AstLmt_Per1_PreLimit	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc		_lgc	
gt_Rte_Inst_Ap_AstLmt_Per1_OpTrqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_Ap_AstLmt_Per1_OptrqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_Ap_AstLmt_Per1_PosServCCDisable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCCDisable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCCDisable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCCDisable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCCDisable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Pret_imitPorc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Pret_imitPorcytall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Pret_imitForque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Pret_imitForque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Pret_imitForque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Pret_imitForque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_StiliCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Trept_imitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Trept_imitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Trept_imitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Trept_imitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Trept_imitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 dt_AstLmt_Per1_UnitMin_MtrNm_f32 dt_AstLmt_Per1_UnitMin_MtrNm_f32 dt_AstLmt_Per1_UnitMin_MtrNm_f32 dt_AstLmt_Per1_UnitMin_MtrNm_f32 dt_AstLmt_Per1_UnitMin_MtrNm_f32 dt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value dt_AstLmt_Per1_UnitMin_MtrNm_f32 dt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value dt_AstLmt_Per1_Pert_imitTorque_MtrNm_f32.value dt_AstLmt_Per1_Pert_imitTorque_MtrNm_f32.value dt_AstLmt_Per1_Pert_imitTorque_MtrNm_f32.value dt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value dt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value dt_AstLmt_Per	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OutputRampMult_Uls_f32   tgt_AstLmt_Per1_DutputRampMult_Uls_f32   tgt_AstLmt_Per1_PosServCCDisable_Cnt_Igc   tgt_AstLmt_Per1_PosServColor_Igc   tgt_AstLmt_Per1_PosServColor_Igc   tgt_AstLmt_Per1_PosServColor_Igc   tgt_AstLmt_Per1_PosServColor_Igc   tgt_AstLmt_PosServColor_Igc   tgt_AstLmt_PosServColor_Igc   tgt_AstLmt_PosServColor_Igc   tgt_AstLmt_PosServColor_Igc   tgt_AstLmt_PosServColor_Igc   tgt_AstLmt_PosServColor_Igc   tgt_AstLmt_PosServColor_Igc   tgt_AstLmt_PosServColor_Igc   tgt_AstLmt_PosSe	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCCDisable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TnermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_TnermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_TnermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TnermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TnermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TnermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TnermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TnermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TnermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TnermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TnermalLimit_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_UnitifForStall_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value tgt_AstLmt_Per1_PreLi	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PowerLimitPerc_Uls_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PullCompCmd_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PullCompCmd_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TSMitCommand_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TSMitCommand_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermallLimitPerc_Uls_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermallLimit_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TreqLimitMin_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TreqLimitMin_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TreqLimitMin_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TreqLimitMin_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TreqLimitMin_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  gt_AstLmt_Per1_UnitPercentFiltered_Uls_f32.value  gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value  gt_AstLmt_Per1_PreLimitForGtall_MtrNm_f32.value  gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value  gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value  gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value  gt_AstLmt_Per1_SumLimTrQCmd_MtrNm_f32.value  gt_AstLmt_Per1_SumLimTrQCmd_MtrNm_f32.value  gt_AstLmt_Per1_SumLimTrQCmd_MtrNm_f32.value  gt_AstLmt_Per1_SumLimTrQCmd_MtrNm_f32.value  gt_AstLmt_Per1_SumLimTrQCmd_MtrNm_f32.value  gt_AstLmt_Per1_SumLimTrQCmd_MtrNm_f32.value  gt_AstLmt_Per1_SumLimTrQCmd_MtrNm_f32.value  gt_AstLmt_Per1_SumLimTrQCmd_MtrNm_f32.value  gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	2	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitMtrNm_f32 tgt_AstLmt_Per1_ThermalLimitMtrNm_f32 tgt_AstLmt_Per1_TreqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TreqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TreqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_UnitPercentFiltered_Uls_f32.value tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrac_d_UltrNm_f32.value tgt_AstLmt_Per1_SumLimTrac_d_UltrNm_f32.value tgt_AstLmt_Per1_SumLimTrac_d_Ul	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32         tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32         tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32         tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32         tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32         target_AstLmt_Per1_TSMitCommand_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32         tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32         tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_vehSpd_Kph_f32         tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32         tgt_AstLmt_Per1_VehSpd_Kph_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32         tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32           Name         Actual Value         Expected Value         Res           gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value         5         5 ± 0.0009765625           gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value         3.29999995         3.29999994 ± 0.0009765625	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32         tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32         tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32         tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32         target_AstLmt_Per1_TSMitCommand_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32         tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32         tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32         tgt_AstLmt_Per1_VehSpd_Kph_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32         tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32           Name         Actual Value         Expected Value           gt_AstLmt_Per1_InitPercentFiltered_Uls_f32.value         0.699999988         0.699999988 ± 0.000030517578125           gt_AstLmt_Per1_PreLimitPorque_MtrNm_f32.value         3.29999995         3.29999995 ± 0.0009765625           gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value         2.30999994         2.30999994 ± 0.0009765625	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PullCompCmd_MtrNm_f32         tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32         tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TSMitCommand_MtrNm_f32         target_AstLmt_Per1_TSMitCommand_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitPerc_Uls_f32         tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TrqLimitMin_MtrNm_f32         tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_VehSpd_Kph_f32         tgt_AstLmt_Per1_VehSpd_Kph_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32         tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32           Name         Actual Value         Expected Value         Res           gt_AstLmt_Per1_InitPercentFiltered_Uls_f32.value         0.699999988         0.699999988 ± 0.000030517578125         5 ± 0.0009765625           gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value         3.29999995         3.29999995 ± 0.0009765625         3.29999994 ± 0.0009765625	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32         tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32         target_AstLmt_Per1_TSMitCommand_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32         tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32         tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_vehSpd_Kph_f32         tgt_AstLmt_Per1_VehSpd_Kph_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32         tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32           Name         Actual Value         Expected Value         Res           gt_AstLmt_Per1_InitPercentFiltered_Uls_f32.value         0.699999988         0.699999988 ± 0.000030517578125         5 ± 0.0009765625           gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value         3.29999995         3.29999995 ± 0.0009765625         3.29999994 ± 0.0009765625	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32       target_AstLmt_Per1_TSMitCommand_MtrNm_f32         gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32       tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32         gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32       tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32         gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32       tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32         gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32       tgt_AstLmt_Per1_VehSpd_Kph_f32         gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32       tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32         Name       Actual Value       Expected Value       Res         gt_AstLmt_Per1_InitPercentFiltered_Uls_f32.value       0.699999988       0.69999998 ± 0.000030517578125         gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value       5       5 ± 0.0009765625         gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value       3.29999995       3.2999999 ± 0.0009765625	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitPerc_Uls_f32         tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TreptimitImit_MtrNm_f32         tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TreptimitMin_MtrNm_f32         tgt_AstLmt_Per1_TreptimitMin_MtrNm_f32           gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_VehSpd_Kph_f32         tgt_AstLmt_Per1_VehSpd_Kph_f32           gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32         tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32           Name         Actual Value         Expected Value         Res           gt_AstLmt_Per1_InitPercentFiltered_Uls_f32.value         0.699999988         0.699999988 ± 0.000030517578125         5 ± 0.0009765625           gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value         3.29999995         3.29999995 ± 0.0009765625         3.29999994 ± 0.0009765625	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_	<u>f</u> 32	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_UmitPercentFiltered_Uls_f32.value 0.699999988 0.69999988 0.69999988 0.69999988 0.69999988 0.69999988 0.699999988 0.699999988 0.690999988 0.699999988 0.699999988 0.6999999999999999999999999999999999999		target_AstLmt_Per1_TSMitCommand_MtrN	lm_f32	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Name Actual Value Expected Value Res gt_AstLmt_Per1_ImitPercentFiltered_Uls_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 5 5 5 0.0009765625 gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value 2.30999994 2.30999994 ± 0.0009765625	gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3	2	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Name Actual Value Expected Value Res gt_AstLmt_Per1_ImitPercentFiltered_Uls_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 5 5 5 0.0009765625 gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value 2.30999994 2.30999994 ± 0.0009765625				
tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32  Name  Actual Value Expected Value Res gt_AstLmt_Per1_ImitPercentFiltered_Uls_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 5 5 5 0.0009765625 gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value 2.30999994 2.30999994 2.30999994 2.30999994 5 0.000765625				
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32    Actual Value   Expected Value   Res				
Name         Actual Value         Expected Value         Res           gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value         0.699999988         0.699999988 ± 0.000030517578125         9.699999988 ± 0.000030517578125         9.699999988         0.699999988 ± 0.000030517578125         9.699999988         9.699999988 ± 0.000030517578125         9.699999988         9.699999988 ± 0.000030517578125         9.699999999999999999999999999999999999			rNm f32	
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value       0.699999988       0.699999988 ± 0.000030517578125         gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value       5       5 ± 0.0009765625         gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value       3.29999995       3.29999995 ± 0.0009765625         gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value       2.30999994       2.30999994 ± 0.0009765625			1	Resi
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value       5       5 ± 0.0009765625         tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value       3.29999995       3.29999995 ± 0.0009765625         tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value       2.30999994       2.30999994 ± 0.0009765625				11031
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value       3.29999995       3.29999995 ± 0.0009765625         gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value       2.30999994       2.30999994 ± 0.0009765625				
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value 2.30999994 ± 0.0009765625				
	gt_AstLmt_Per1_SumLimTrqCmd_mtrNm_f32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.30000019	4.30000019 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.86 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt ManualTrqCmd MtrNm M f32	-16		
	0		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt 1.70000005		
c_SumLimPlCmpLimit_MtrNm_f32			
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.600000024		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.4000001		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.80000019		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-5.30000019		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	3.4000001		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2.0999999		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012		
pt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.3000019		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	5.37e-009		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	4.5		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtrl	Nm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	NIII_102	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_0is_is2 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTGairI_0is_i32	f22	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtrl		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_	•	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtrl	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3	32	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3:	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt AstLmt Per1 WheelImbalanceCmd M	ltrNm f32	
lame	Actual Value	Expected Value	Res
		· ·	I/C2
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
·	0.00000010		
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-8.80000019 3.53000033	-8.80000019 ± 0.0009765625	
·	-8.80000019 -3.52000022 4.4000001	-8.80000019 ± 0.0009765625 -3.51999998 ± 0.0009765625 4.4000001 ± 0.0009765625	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.87 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt ManualTrqCmd MtrNm M f32	15.9995003		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt Rte Inst Ap AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	1.98000002		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.5		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	1		
	0.5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.0999999		
	4.5		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.9000001		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6 1		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value			
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.89999976		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.89999976		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	4.5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.899999976		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.4000001		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	5.38000009e-008		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	5.5999999		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_	<u>f</u> 32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_	<u>f</u> 32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtrl	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt AstLmt Per1 LrnPnCtrCCDisable Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt AstLmt Per1 LrnPnCtrEnable Cnt Ige		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt AstLmt Per1 LrnPnCtrTCmd MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimit 0/3tail_within_52	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PirAssistCmtd_withMin_i32  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_1		
tgt Rte Inst Ap AstLmt.AstLmt Perl SumLimTrqCmd MtrNm f32	tgt_AstLmt_Per1_FullCompCmd_MtrNm_i		
tgt_Rte_inst_Ap_AstLmt.AstLmt_Peri_SumLimTrqCmd_wtrNm_r32 tgt_Rte_inst_Ap_AstLmt.AstLmt Peri_SumLimTrqCmd_wtrNm_r32	target_AstLmt_Per1_TSMitCommand_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	W. A.L. (600	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N		
Name	Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.89999976	0.899999976 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	•
@C			



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>	

Test Step 2.88 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	2.25999999		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.40000006		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-8		
tgt_AstEmt_Per1_AssistEOTDamping_MtrNm_f32.value	2		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024		
gt AstLmt Per1 AssistEOTLimit MtrNm f32.value	5.19999981		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.5999999		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-7		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2		
gt AstLmt Per1 OpTrqOvr MtrNm f32.value	-2		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt AstLmt Per1 PowerLimitPerc Uls f32.value	0.100000001		
gt AstLmt Per1 PrkAssistCmd MtrNm f32.value	5.5999999		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.100000001		
gt AstLmt Per1 ThermalLimit MtrNm f32.value	7.5		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	3.77999996e-008		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	6		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	<del>-</del>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mi	<del>-</del>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
gt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32	tgt AstLmt Per1 LimitPercentFiltered U	— ·	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_l		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm		
gt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt AstLmt Per1 PosServCCDisable Cr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt AstLmt Per1 PreLimitForStall MtrNr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt AstLmt Per1 PreLimitTorque MtrNn	_	
gt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
gt Rte Inst Ap AstLmt.AstLmt Per1 SumLimTrqCmd MtrNm f32	tgt AstLmt Per1 SumLimTrqCmd MtrNi		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target AstLmt Per1 TSMitCommand M	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt AstLmt Per1 ThermalLimitPerc Uls	_	
gt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimit MtrNm f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt AstLmt Per1 VehSpd Kph f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm f32	
lame	Actual Value	Expected Value	Resu
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.100000001	0.100000001 ± 0.000030517578125	Nest
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8	8 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625 0 ± 0.0009765625	
gt_AstLmt_Per1_TrqLimitridcmd_intrinin_isz.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.5999999	4.5999999 ± 0.0009765625	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>	

Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-1.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	2.53999996		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.300000012		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	1		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.30000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.69999981		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.0999999		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3.2999995		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.5999999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	6.38000008e-009		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3:	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M	1trNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNn	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mi	trNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M	trNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cr	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_L	Jls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNn	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_C	nt_lgc	
tgt Rte Inst Ap AstLmt.AstLmt Per1 PowerLimitPerc UIs f32	tgt AstLmt Per1 PowerLimitPerc Uls f	·	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNr	_	
tgt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt AstLmt Per1 PrkAssistCmd MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
tgt Rte Inst Ap AstLmt.AstLmt Per1 SumLimTrqCmd MtrNm f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_N	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_UIs_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_		
tgt Rte Inst Ap AstLmt.AstLmt Per1 TrqLimitMin MtrNm f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125	Acou
tgt_AstLmt_Per1_LimitPercentFiltered_Uis_i32.value	1.5	1.5 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitForStail_mtrNm_t32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1.5	-1.5 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.300000012	-0.300000012 ± 0.0009765625	
GC OCCUPE OF LOGUE HANDON OF THE PROPERTY OF T	-0.00000012	-0.000000012 I 0.0000700020	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~	

Test Step 2.90 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32	1.60000002		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPlCmpLimit_MtrNm_f32	2.81999993		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.200000003		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	2		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	4		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.4000001		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.80000019		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.19999981		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	1		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.30000012		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.6999981		
t_AstLmt_Per1_VehSpd_Kph_f32.value	7.27999989e-008		
pt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt AstLmt Per1 AssistEOTDamping Mt	rNm f32	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	<del>-</del>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtri		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_UI		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt AstLmt Per1 LimitedReturn MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt AstLmt Per1 LrnPnCtrTCmd MtrNm		
gt_Ne_inst_Ap_AstLint.AstLint_Per1_EnriProcti Pend_withtin_i32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	_132	
		22	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cn	— ·	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	MtrNm_f32	
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2	2 ± 0.0009765625	
gt_AstLmt_Per1_PreLimit or3tall_withth_f32.value gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	1.60000002	1.60000002 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.480000019	0.479999989 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.91 (Repeat Count = 1)			~
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	3.0999999		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.100000001		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	1		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.30000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.69999981		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.0999999		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3.29999995		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.5999999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	503.5		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtrl	Nm f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	_	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr1	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls	•	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt AstLmt Per1 LrnPnCtrTCmd MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	2	
tgt Rte Inst Ap AstLmt.AstLmt Per1 PosServCCDisable Cnt Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_withNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrI		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_UIs_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3:		
tgt_Rte_Inst_Ap_AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_M		
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3	3 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-3	-3 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.600000024	-0.600000024 ± 0.0009765625	•
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.69999981	4.69999981 ± 0.0009765625	•



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~	

Test Step 2.92 (Repeat Count = 1) Name	Input Value		
	· ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-7		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPICmpLimit_MtrNm_f32	3.38000011		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	2		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	4		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.4000001		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.80000019		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.19999981		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	1		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.30000012		
yt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.6999981		
pt_AstLmt_Per1_VehSpd_Kph_f32.value	37.5999985		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt AstLmt Per1 AssistEOTDamping Mtr	Nm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	···· <u>-</u> ··-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr1	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_		
gt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32		•	
	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	132	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtrl	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_M	trNm_f32	
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	
gt_AstLmt_Per1_LimitPercentFiltered_Ois_isz.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2	2 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_132.value gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0		
	Į <b>V</b>	0 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>

Test Step 2.93 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt 2.25999999		
k_SumLimPICmpLimit_MtrNm_f32	-3		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	2		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	0.600000024		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	5.19999981		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.5999999		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5		
	-7		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2		
	1		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value			
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2 0.100000001		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value			
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.100000001 5.5999999		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value			
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.100000001 7.5		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value			
gt_AstLmt_Per1_VehSpd_Kph_f32.value	3.77999996e-008		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	6		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_UIs_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cr	<del></del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_I		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_Igc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32			
gt_Rte_inst_Ap_AstLint.AstLint_Ferr_LinFinotiriCind_MitNin_is2 gt_Rte_inst_Ap_AstLint.AstLint_Ferr_LinFinotiriCind_MitNin_is2	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	1_132	
		<b>600</b>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_ tgt_AstLmt_Per1_PosServCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	0		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	Markles 522	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_		
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.100000001	0.100000001 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8	8 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.5999999	4.5999999 ± 0.0009765625	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>	

Test Step 2.94 (Repeat Count = 1)	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-1.5		
AstLmt_ManualTrqCmd_MtrNm_M_f32	0		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPlCmpLimit_MtrNm_f32	2.53999996		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	3		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	1		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.30000019		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.69999981		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.0999999		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-3		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-3		
pt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5		
pt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.20000003		
at AstLmt Per1 PosServCCDisable Cnt Igc.value	0		
at AstLmt Per1 PowerLimitPerc Uls f32.value	0.200000003		
at AstLmt Per1 PrkAssistCmd MtrNm f32.value	6		
pt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3.29999995		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.20000003		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.5999999		
	6.3800008e-009		
yt_AstLmt_Per1_VehSpd_Kph_f32.value	7		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value		200	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_	<del>-</del>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_N	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_0	Cnt_lgc	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_	_Ulsf32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNr	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_	_Cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt	t_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrN	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls	s_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_	Cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_Mtrl		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNr		
pt Rte Inst Ap AstLmt.AstLmt Per1 PullCompCmd MtrNm f32	tgt_AstLmt_Per1_PullCompCmd_MtrN	<del>_</del>	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mtr		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_	_	
yt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Ui		
pt_Rte_Inst_Ap_AstLint.AstLint_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm	_	
	tgt_AstLmt_Peri_ThermalLimit_MitNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32		IUC	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	d Milablas 622	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmo		
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.20000003	0.200000003 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	1.5	1.5 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1.5	-1.5 ± 0.0009765625	
	0.000000040	-0.300000012 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.300000012	-0.300000012 ± 0.0009703023	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>	

Test Step 2.95 (Repeat Count = 1)	Input Value		
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1.60000002		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPlCmpLimit_MtrNm_f32	2.81999993		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	2		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	4		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.4000001		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.80000019		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.19999981		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	1		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.30000012		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7		
gt AstLmt Per1 PullCompCmd MtrNm f32.value	5.5999999		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.30000012		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.69999981		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	7.27999989e-008		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	Im #22	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtrl	NII_132	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_UIs_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	500	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_t		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	<del>-</del>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt	_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_t	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 VehSpd Kph f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_inst_Ap_AstLint.AstLint_Peri_venSpd_Rpri_is2 gt_Rte_inst_Ap_AstLint.AstLint_Peri_venSpd_Rpri_is2		trNm f32	
	tgt_AstLmt_Per1_WheelImbalanceCmd_M		
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2	2 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	1.60000002	1.60000002 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.480000019	0.479999989 ± 0.0009765625	
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.80000019	4.80000019 ± 0.0009765625	



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>

Test Step 2.96 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1.5		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	3.0999999		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.5		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	1		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.30000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.69999981		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.099999		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt AstLmt Per1 PowerLimitPerc Uls f32.value	0.200000003		
tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value	6		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3.29999995		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.20000003		
tgt AstLmt Per1 ThermalLimit MtrNm f32.value	7.599999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	503.5		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_N		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_L		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C	:nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_l	gc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNn	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_C		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNr	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	_MtrNm_f32	
Name	Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125	
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	3	3 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-3	-3 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.600000024	-0.600000024 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.6999981	4.69999981 ± 0.0009765625	٠,



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>	

Test Step 2.97 (Repeat Count = 1)	Input Value		
Name	0		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-7		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt SteeringAsstDefeat Cnt M lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPICmpLimit_MtrNm_f32	3.38000011		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.5		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	2		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	4		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.400001		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.8000019		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.19999981		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	1		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_tmPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_tmPnCtrTCmd_MtrNm_f32.value	-4		
gt_AstLmt_Per1_chrenot romd_within_i32.value gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.30000019		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PosServCcDisable_Cnt_igt.value	0.300000012		
gt_AstLmt_Fer1_FowerEmitt ere_ors_roz.value	7		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5.599999		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.30000012		
gt_AstLmt_Per1_ThermalLimit_etc_ois_132.value	7.69999981		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	37.5999985		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	)	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_N tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTGain_Ois_ic		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNn	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt AstLmt Per1 LimitPercentFiltered L		
gt_Rte_inst_Ap_AstLint.AstLint_Ferr_Limitrercentrilitered_0is_is2 gt_Rte_Inst_Ap_AstLint.AstLint_Ferr_LimitredReturn_MtrNim_f32		_	
gt_Rte_inst_Ap_AsiLini.AsiLiniFer1_LininedReturn_MitNin_i32 gt_Rte_inst_Ap_AsiLmt.AsiLmt_Per1_LmPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LimitedReturn_MtrNm_ tgt_AstLmt_Per1_LrnPnCtrCCDisable_C	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_l		
gt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrTCmd MtrNm f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_OutputRampMult_Uls_ tqt_AstLmt_Per1_PosServCCDisable_C		
	0		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f. tgt_AstLmt_Per1_PreLimitForStall_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStail_MtrN tgt_AstLmt_Per1_PreLimitTorque_MtrNr	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32		_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm tgt_AstLmt_Per1_PullCompCmd_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32			
	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M	<del>-</del>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	02	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	Manhlon 600	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_		
Name	Actual Value	Expected Value	Resu
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.30000012	0.300000012 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2	2 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.80000019	4.80000019 ± 0.0009765625	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	<b>✓</b>	

Test Step 2.98 (Repeat Count = 1) Name	Input Value		
	· ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-16		
AstLmt_ManualTrqCmd_MtrNm_M_f32	0		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPICmpLimit_MtrNm_f32 C_SumLimPICmpLimit_MtrNm_f32 C_SumLimPICmpLimit_MtrNm_f32			
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-3		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	0		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	0		
pt_AstLmt_Per1_VehSpd_Kph_f32.value	0		
pt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-8.80000019		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt AstLmt Per1 AssistEOTDamping	MtrNm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_N		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_I	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_0		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt AstLmt Per1 LimitedReturn MtrNr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt AstLmt Per1 LrnPnCtrTCmd MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3	_	
gt_Rte_Inst_Ap_AsiLinit.AsiLinit_rei1_OptriqOvi_withthi_i32 gt_Rte_Inst_Ap_AsiLinit.AsiLinit_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_0		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_Mtrl	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNi		
gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mtr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_UI	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmc	d_MtrNm_f32	
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0	0 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-3	-3 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0	0 ± 0.0009765625	
UL ASILIIL FELL SUITLIITTUCITU MILIMIT 132.Value			



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	•

Test Step 2.99 (Repeat Count = 1)			٧
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	15.9995003		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	8.80000019		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	1		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1		
tgt AstLmt Per1 PosServCCDisable Cnt Igc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	1		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8.80000019		
	1		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value			
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	512		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8.80000019		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtrl	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f		
tgt Rte Inst Ap AstLmt.AstLmt Per1 PullCompCmd MtrNm f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
tgt Rte Inst Ap AstLmt.AstLmt Per1 TrqLimitMin MtrNm f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt Rte Inst Ap AstLmt.AstLmt Per1 VehSpd Kph f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_inst_Ap_AstLmt.AstLmt_Per1_venSpd_Rpn_132 tgt_Rte_inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32		ItrNm f32	
	tgt_AstLmt_Per1_WheelImbalanceCmd_N		
Name	Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	1	1 ± 0.000030517578125	,
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	,
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	

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AstLmt\_Per1

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	•



#### **Test Case 3: Path Coverage Test**

```
Performance Metrics:
Specification
                           (With "None" Instrumentation and WithPS
                           Environment)
                          CPU Cycles:
TS3.1 1985.00 cycles
TS3.2 1867.00 cycles
TS3.3 1821.00 cycles
TS3.4 1745.00 cycles
TS3.5 1747.00 cycles
                          TS3.5 1747.00 cycles
TS3.6 1723.00 cycles
TS3.7 1747.00 cycles
TS3.7 1747.00 cycles
TS3.8 1723.00 cycles
TS3.10 1723.00 cycles
TS3.11 1747.00 cycles
TS3.12 1723.00 cycles
TS3.13 1770.00 cycles
TS3.14 1804.00 cycles
TS3.15 1851.00 cycles
TS3.16 1790.00 cycles
TS3.17 1757.00 cycles
                          TS3.18 1702.00 cycles
TS3.19 1718.00 cycles
TS3.20 1745.00 cycles
Description
                          Vector Description:
                          TS3.1"( PosServCCDisable_Cnt_T_lgc == TRUE )=True
                          and
                          ( LrnPnCtrCCDisable_Cnt_T_lgc == TRUE )=True
                          and
                          (PullCompCmd_MtrNm_T_f32 >= k_SumLimPlCmpLimit_MtrNm_f32) = True
                          ( (ManualTrqCmdEn_Cnt_M_lgc == TRUE) =False && (VehSpd_Kph_T_f32 < FLT_EPSILON) )
                          and
                          ( LrnPnCtrEnable_Cnt_T_lgc == TRUE )=True
                          and
                          (PreLimitTorque_MtrNm_T_f32 >= 0)=True
                          (AbsPreLimitTorque_MtrNm_T_f32<= AssistEOTLimit_MtrNm_T_f32) = True
                          and
                          (PreLimitForStall_MtrNm_T_f32 <= AssistVehSpdLimit_MtrNm_T_f32)=True
                          ( DefeatLimitService_Cnt_T_lgc == TRUE )=True
                          and
                          TS3.2"(PullCompCmd_MtrNm_T_f32 >= k_SumLimPlCmpLimit_MtrNm_f32) = False
                          (PullCompCmd_MtrNm_T_f32 <= -k_SumLimPlCmpLimit_MtrNm_f32) = false
                          and
                          ((ManualTrqCmdEn_Cnt_M_lgc == TRUE) =True && (VehSpd_Kph_T_f32 < FLT_EPSILON))=False
                          and
                          (Sign_f32_m(AssistPowerLimit_MtrNm_T_f32) >= D_ZERO_CNT_S8)=False and(ThermalLimitPerc_Uls_T_f32 >= PowerLimitPerc_Uls_T_f32) = True" TS3.3"((HwTrqPolarity_Cnt_T_f32 * SignMRFMtrVel_Cnt_T_f32 * SignPreLimitForPower_Cnt_T_f32) >= D_ZERO_ULS_F32)=False
                          and
                          (AssistPowerLimit_MtrNm_T_f32 >= Abs_f32_m(k_SumLimOvrSpdMtrTrq2QLmt_MtrNm_f32))=False
                          (AbsPreLimitForPower_MtrNm_T_f32 <= ForPowerLimit_MtrNm_T_f32 )=True" TS3.4"( DefeatLimitService_Cnt_T_lgc == TRUE )= False
                          and
                          (PreLimitForStall_MtrNm_T_f32 <= AssistStallLimit_MtrNm_T_f32) = True
                          and
                          (AbsLimitedTorque_MtrNm_T_f32 <= ThermalLimit_MtrNm_T_f32) = True"
                           TS3.5"( LrnPnCtrEnable_Cnt_T_lgc == TRUE )=False
                          and
                          (AbsPreLimitTorque_MtrNm_T_f32<= AssistEOTLimit_MtrNm_T_f32) = False
                          ànd
                          (PreLimitForStall_MtrNm_T_f32 <= 0)=False
                          and
                          (PreLimitForPower_MtrNm_T_f32 <= 0)=False"
TS3.6"( LrnPnCtrCCDisable_Cnt_T_lgc == TRUE )=False
                          and
                          (AbsPreLimitForPower_MtrNm_T_f32 <= AssistPowerLimit_MtrNm_T_f32 )=False" TS3.7( PosServCCDisable_Cnt_T_lgc == TRUE )=False TS3.8"(PreLimitTorque_MtrNm_T_f32 >= 8.8) = True
                          and
                          (PreLimitForStall_MtrNm_T_f32 >= 8.8)=True"
                           TS3.9"(PreLimitForStall_MtrNm_T_f32 <= AssistVehSpdLimit_MtrNm_T_f32)=False
                          TS3.10(PostLimitForAssistSumCC_MtrNm_T_f32 >= 8.8)=True
                          TS3.11"(PreLimitTorque_MtrNm_T_f32 >= 0)=False
                          and
                          and(PreLimitTorque_MtrNm_T_f32 <= -8.8) = True
                          TS3.12(PullCompCmd_MtrNm_T_f32 <= -k_SumLimPlCmpLimit_MtrNm_f32) = True
                          TS3.12(PullCompCmd_MtrNm_T_f32 <= -k_SumLimPlCmpLimit_MtrNm_f32) = True
TS3.13(PreLimitForStall_MtrNm_T_f32 <= AssistStallLimit_MtrNm_T_f32) = False
TS3.14(LimitPercentFiltered_Uls_T_f32 >= 1)=True
TS3.15(AbsLimitedTorque_MtrNm_T_f32 <= ThermalLimit_MtrNm_T_f32) = False
TS3.16(LimitPercentFiltered_Uls_T_f32 <= 0) = True
TS3.17"( (ManualTrqCmdEn_Cnt_M_lgc == TRUE)=True && (VehSpd_Kph_T_f32 < FLT_EPSILON) )=True
(SteeringAsstDefeat_Cnt_M_lgc == TRUE)=False"
TS3.18(SteeringAsstDefeat_Cnt_M_lgc == TRUE)=True
TS3.19(TrqLimitMin_MtrNm_T_f32 >= 8.8)=True
TS3.20(PreLimitForStall_MtrNm_T_f32 <= 0)=True
```





Test Step 3.1 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AstLmt ManualTrqCmdEn Cnt M lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	5		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	4		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5		
tgt AstLmt Per1 LrnPnCtrCCDisable Cnt lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5		
tgt AstLmt Per1 OpTrqOvr MtrNm f32.value	7		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	1		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	22	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_I		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3:		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_	.lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3.		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Peri_i SMitCommand_mtrNm_132 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Peri_i SMitCommand_mtrNm_132	target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3.		
tgt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimit MtrNm f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt AstLmt Per1 TrqLimitMin MtrNm f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_Mt	rNm_f32	
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.699999988 ± 0.000030517578125	~
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	~
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	<b>✓</b>
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6	6 ± 0.0009765625	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	•	





Test Step 3.2 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AstLmt ManualTrgCmdEn Cnt M lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-1		
AstLmt SteeringAsstDefeat Cnt M Igc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	6		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	6		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	6		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value tgt AstLmt Per1 PosServCCDisable Cnt Igc.value	0.699999988		
	0.600000024		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	5		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	5		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	6		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_N		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNi	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_M	ltrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N	/ltrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_C	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_	Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_0	Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNi	m_f32	
tgt_Rte_Inst_Ap_AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_C		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_UIs_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNn		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	target_AstLmt_Per1_TSMitCommand_N tgt_AstLmt_Per1_ThermalLimitPerc_Uls		
tgt Rte Inst Ap AstLmt.AstLmt Per ThermalLimit MtrNm f32	tgt_AstLmt_Per1_ThermalLimitPerc_Ois	_	
tgt Rte Inst Ap AstLmt.AstLmt Per TrqLimitMin MtrNm f32	tgt_AstLmt_Per1_TrefmaiLimit_MtrNm_f	_	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	<u>-</u>	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt AstLmt Per1 WheelImbalanceCmd	MtrNm f32	
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	Nesult
tgt_AstLmt_Per1_PreLimitFercentFiltered_0is_is2.value	6	6 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimit orstall_withtin_iss.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	J
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5	5 ± 0.0009765625	
27 7. 2. 7. 4			

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte Call Astlimt Per1 CP1 ChecknointReached	1	Rte Call Astlimt Per1 CP1 ChecknointReached	1	-	





Test Step 3.3 (Repeat Count = 1)			•
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	2		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	7		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	3		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7		
tgt AstLmt Per1 LrnPnCtrCCDisable Cnt lgc.value	1		
	1		
tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_lgc.value	7		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.400000006		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	8		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt	:_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_UI	s_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_t	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg	ıc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3	— ·	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	n f32	
tgt Rte Inst Ap AstLmt.AstLmt Per1 PreLimitTorque MtrNm f32	tgt AstLmt Per1 PreLimitTorque MtrNm	_	
tgt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn		
tgt Rte Inst Ap AstLmt.AstLmt Per1 TSMitCommand MtrNm f32	target_AstLmt_Per1_TSMitCommand_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_UIs_f32	tgt AstLmt Per1 ThermalLimitPerc Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt AstLmt Per1 VehSpd Kph f32		
		Marklan 500	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N		-
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.69999988 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	4	4 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3	3 ± 0.0009765625	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>~</b>





Test Step 3.4 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AstLmt ManualTrqCmdEn Cnt M lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-2		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	6		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	6		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	0 5		
tgt AstLmt Per1 LrnPnCtrCCDisable Cnt lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt AstLmt Per1 LrnPnCtrTCmd MtrNm f32.value	3		
tgt AstLmt Per1 OpTrgOvr MtrNm f32.value	5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	348		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	22	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_I		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3:		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_	.lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3.		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3.		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt AstLmt Per1 TrqLimitMin MtrNm f32		
tgt Rte Inst Ap AstLmt.AstLmt Per1 VehSpd Kph f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_Mt	rNm_f32	
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	~
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3	3 ± 0.0009765625	<b>~</b>
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	~
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	<b>✓</b>
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2	2 ± 0.0009765625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	-





Test Step 3.5 (Repeat Count = 1)			¥
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	3		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	7		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7		
tgt AstLmt Per1 LrnPnCtrCCDisable Cnt lgc.value	1		
tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	4		
tgt AstLmt Per1 OutputRampMult Uls f32.value	0.5		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt AstLmt Per1 PowerLimitPerc Uls f32.value	0.40000006		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6		
tgt AstLmt Per1 PullCompCmd MtrNm f32.value	7		
tgt AstLmt Per1 ThermalLimitPerc Uls f32.value	0.600000024		
tgt AstLmt Per1 ThermalLimit MtrNm f32.value	7		
	34		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value			
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	6	0	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_N	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_M	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N		
tgt_Rte_Inst_Ap_AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_C		
tgt_Rte_Inst_Ap_AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_l		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm	<u>_</u> f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_0	Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_	<u>lg</u> c	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNr	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_C	cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_t	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_N	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt AstLmt Per1 VehSpd Kph f32		
tgt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd	MtrNm f32	
Name	Actual Value		Resu
	0.600000024	Expected Value	Nesul
tgt_AstLmt_Per1_LimitPercentFiltered_UIs_f32.value		0.600000024 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	5.5	5.5 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	2.75	2.75 ± 0.0009765625	•
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3	3 ± 0.0009765625	1

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>	





Test Step 3.6 (Repeat Count = 1)			~
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-3		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	5		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	6		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	6		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	245		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mi	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt		
tgt_Rte_Inst_Ap_AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
tgt_Rte_Inst_Ap_AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Ci		
tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_132	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	200	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_t		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	target_AstLmt_Per1_TSMitCommand_Mi		
	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3 tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32		MtrNm f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_		D
Name	Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.69999988 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	7	7 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	4.9000001	4.9000001 ± 0.0009765625	•
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4	4 ± 0.0009765625	•

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>	





Test Step 3.7 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AstLmt ManualTrqCmdEn Cnt M lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	4		
AstLmt SteeringAsstDefeat Cnt M Igc	0		
Rte Inst Ap AstLmt	tgt Rte Inst Ap AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	5		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	2		
tgt AstLmt Per1 AssistEOTDamping MtrNm f32.value	3		
tgt AstLmt Per1 AssistEOTGain Uls f32.value	0.40000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5		
tgt AstLmt Per1 AssistStallLimit MtrNm f32.value	7		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	7		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	6		
tgt AstLmt Per1 OutputRampMult Uls f32.value	0.5		
tgt AstLmt Per1 PosServCCDisable Cnt Igc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	3		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	166		
tgt_AstLmt_Per1_WheelimbalanceCmd_MtrNm_f32.value	4		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_t	f2.2	
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrI	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrN tgt_AstLmt_Per1_AssistVehSpdLimit_l		
tgt_Rte_Inst_Ap_AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_	_	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_		
tgt_Rte_Inst_Ap_AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt AstLmt Per1 LimitedReturn MtN		
		_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_		
	tgt_AstLmt_Per1_LrnPnCtrEnable_Cn	— -	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrN	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Ul tgt_AstLmt_Per1_PosServCCDisable	_	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDIsable_ tgt_AstLmt_Per1_PosServCCDIsable_		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PowerLimitPerc_Uls_f32		_	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_U	_	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_	_132	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	d Machine 622	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCm		
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.40000006	0.400000006 ± 0.000030517578125	~
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2	2 ± 0.0009765625	~
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	~
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	~
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3	3 ± 0.0009765625	<b>~</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call Astlimt Per1 CP1 ChecknointReached	1	Rte Call Astlimt Per1 CP1 ChecknointReached	1	-





Test Step 3.8 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AstLmt ManualTrqCmdEn Cnt M lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-4		
AstLmt SteeringAsstDefeat Cnt M Igc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	5		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	4		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	7		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	437		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul	s_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg	jc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_t	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cn	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_UIs_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	1_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_I	MtrNm_f32	
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.69999988 ± 0.000030517578125	~
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	~
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	~
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	7.04000044	7.03999996 ± 0.0009765625	<b>✓</b>
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6	6 ± 0.0009765625	_

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call Astlimt Per1 CP1 ChecknointReached	1	Rte Call Astlimt Per1 CP1 ChecknointReached	1	-





Test Step 3.9 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt ManualTrgCmdEn Cnt M lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	6		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	6		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	6		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1 0.600000024		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_PtiAssistCmt_within_is2.value	5		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	367		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	6		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mi		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cn	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U	s_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Ci	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lc	gc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_t		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	target_AstLmt_Per1_TSMitCommand_Mt tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TreImatLimit_MinMin_132  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3.		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_l	MtrNm f32	
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	i vesuit
tgt_AstLmt_Per1_PreLimitFercentFiltered_0is_i32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt AstLmt Per1 PreLimitTorque MtrNm f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5	5 ± 0.0009765625	
<u> </u>		1	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call Astl mt Per1 CP1 ChecknointReached	1	Rte Call Astl mt Per1 CP1 CheckpointReached	1	





Test Step 3.10 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	5		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	6		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	6		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	75		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtrl	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt	:_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul	s_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_t	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg	IC .	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	MtrNm_f32	
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.69999988 ± 0.000030517578125	~
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	~
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	~
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	<b>✓</b>
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4	4 ± 0.0009765625	

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>	





Test Step 3.11 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	6		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	7		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	3		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	7		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	24		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mi	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_CombinedDamping_Mt tgt_AstLmt_Per1_DefeatLimitService_Cn		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt AstLmt Per1 LimitedReturn MtrNm		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LmPnCtrCCDisable Cnt Igc	tgt_AstLmt_Per1_LmPnCtrCCDisable_Ci		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LmPnCtrEnable Cnt Igc	tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_lg		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrTCmd MtrNm f32	tgt AstLmt Per1 LrnPnCtrTCmd MtrNm		
tgt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32	_102	
tgt Rte Inst Ap AstLmt.AstLmt Per1 OutputRampMult Uls f32	tgt AstLmt Per1 OutputRampMult Uls	132	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cr		
tgt Rte Inst Ap AstLmt.AstLmt Per1 PowerLimitPerc UIs f32	tgt AstLmt Per1 PowerLimitPerc Uls f3		
tgt Rte Inst Ap AstLmt.AstLmt Per1 PreLimitForStall MtrNm f32	tgt AstLmt Per1 PreLimitForStall MtrNn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mi	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3.		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_I	MtrNm_f32	
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.699999988 ± 0.000030517578125	<b>~</b>
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	<b>V</b>
tgt AstLmt Per1 PreLimitTorque MtrNm f32.value	-8.80000019	-8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-8.80000019	-8.80000019 ± 0.0009765625	<b>V</b>
	3	3 ± 0.0009765625	~
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3	3 ± 0.0009765625	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call Astlimt Per1 CP1 ChecknointReached	1	Rte Call Astlimt Per1 CP1 ChecknointReached	1	-





Test Step 3.12 (Repeat Count = 1)			•
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-8.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.800000012		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.800000012		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.5		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.20000005		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	4		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt AstLmt Per1 PowerLimitPerc Uls f32.value	0.800000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	2		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012		
tgt AstLmt Per1 ThermalLimit MtrNm f32.value	2.79999995		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	34		
	3		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	Non- £22	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	<u>f</u> 32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_t		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
tgt Rte Inst Ap AstLmt.AstLmt Per1 TrqLimitMin MtrNm f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt Rte Inst Ap AstLmt.AstLmt Per1 VehSpd Kph f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt AstLmt Per1 WheelImbalanceCmd M	ltrNm f32	
Name			Page
	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	,
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-4.5	-4.5 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-3.60000014	-3.5999999 ± 0.0009765625	,
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.800000012	0.800000012 ± 0.0009765625	•

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>





Test Step 3.13 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt ManualTrgCmdEn Cnt M lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	7.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.200000003		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	2		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	1.10000002		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0.800000012		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.5		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	7		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	3.2999995		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.20000003		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	3.0999999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	75		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	6		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3:		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_CombinedDamping_Mti tgt_AstLmt_Per1_DefeatLimitService_Cni		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_UI	— <del>-</del>	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_Limitref centrificed_ois_is2  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitPercentPittered_Oi		
tgt Rte Inst Ap AstLmt.AstLmt Peri LimitedReturn_within_isz	tgt_AstLmt_Per1_LmntedReturn_withhin_		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrEnable Cnt lo		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32	_102	
tgt Rte Inst Ap AstLmt.AstLmt Per1 OutputRampMult Uls f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt AstLmt Per1 PosServCCDisable Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt AstLmt Per1 PreLimitForStall MtrNn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target AstLmt Per1 TSMitCommand Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_I	MtrNm_f32	
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.20000003	0.200000003 ± 0.000030517578125	~
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8	8 ± 0.0009765625	~
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	<b>✓</b>
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.800000012	0.800000012 ± 0.0009765625	•
SC SCENIC OI _ TIQUINIUMII_MILTMI_102.Value	0.00000012	0.00000012 1 0.0003703023	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call Astl mt Per1 CP1 ChecknointReached	1	Rte Call Astl mt Per1 CP1 CheckpointReached	1	





Test Step 3.14 (Repeat Count = 1)			V
Name	Input Value		
AstLmt ManualTrqCmdEn Cnt M lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	2		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt Rte Inst Ap AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.89999976		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	3.29999995		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-1		
tgt AstLmt Per1 AssistEOTGain Uls f32.value	0.100000001		
tgt AstLmt Per1 AssistEOTLimit MtrNm f32.value	6.5		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.9000001		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.30000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
tgt AstLmt Per1 PosServCCDisable Cnt Igc.value	0.3		
tgt_AstLmt_Per1_PosserveeDisable_cnt_gc.value	1		
	-4		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	3		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_ThermalLimitrerc_ors_i32.value	7.0999999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	26		
	-5		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	£22	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_	_132	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_UIs_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	en n	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_1		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_	.132	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	;	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNr	n_f32	
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	1	1 ± 0.000030517578125	~
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	~
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	7	7 ± 0.0009765625	~
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	3.5	3.5 ± 0.0009765625	~
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5.9000001	5.9000001 ± 0.0009765625	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call Astlimt Per1 CP1 ChecknointReached	1	Rte Call Astlimt Per1 CP1 ChecknointReached	1	-





Test Step 3.15 (Repeat Count = 1)			· ·
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	8.80000019		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	1		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	1		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	1		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	1		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	512		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8.80000019		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_UI		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	/ltrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	1	1 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	8	8 ± 0.0009765625	•
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	8	8 ± 0.0009765625	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	<b>✓</b>





Test Step 3.16 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-8.80000019		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	0		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	0		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	0		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-8.80000019		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M	trNm_f32	
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNn	1_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_I	•	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNn		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_ tgt_AstLmt_Per1_ThermalLimit_MtrNm_f	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_I nermailLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32			
	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	4	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	MtrNm f32	
tgt_Rte_Inst_Ap_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_		B
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0	0 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-2	-2 ± 0.0009765625	<b>V</b>
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0	0 ± 0.0009765625	-4
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0	0 ± 0.0009765625	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call Astlimt Per1 CP1 ChecknointReached	1	Rte Call Astlimt Per1 CP1 CheckpointReached	1	





Test Step 3.17 (Repeat Count = 1)			V
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-8.80000019		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.800000012		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.4000001		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.80000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	3		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.30000019		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	5.37e-009		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	4		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	·Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtrl	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul	s_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg	С	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_UIs_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3:	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_t	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_t	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	/trNm_f32	
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	~
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	~
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-8.80000019	-8.80000019 ± 0.0009765625	~
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-3.52000022	-3.51999998 ± 0.0009765625	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call Astl mt Per1 CP1 ChecknointReached	1	Rte Call Astl mt Per1 CP1 CheckpointReached	1	





Test Step 3.18 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-8.80000019		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.800000012		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.4000001		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.80000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012 3		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012		
tgt AstLmt Per1 ThermalLimit MtrNm f32.value	7.30000019		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	5.37e-009		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	4		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mti	Nm f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtrl	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul	s_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg	С	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	<u>f</u> 32	
tgt_Rte_Inst_Ap_AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt	— -	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3:		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_t		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_t tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TreImatLimit_MitNim_132  tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_TrqLimitwiin_witrNm_i32		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	MrNm f32	
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	Nesult
tgt_AstLmt_Per1_cimitPercentPillered_Ois_132.value	8.80000019	8.80000019 ± 0.0009765625	
tgt AstLmt Per1 PreLimitForstall_MitNin_i32.value	-8.80000019	-8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-3.52000022	-3.51999998 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.400001	4.400001 ± 0.0009765625	
27 ·	1		

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call Astlimt Per1 CP1 ChecknointReached	1	Rte Call Astlimt Per1 CP1 ChecknointReached	1	-





Test Step 3.19 (Repeat Count = 1)			V
Name	Input Value		
AstLmt ManualTrqCmdEn Cnt M lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	15.9995003		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	8.80000019		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	1		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	1		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	1		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	512		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8.80000019		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	Al 500	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_UIs_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_Assistverispot.init_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_UIs_f32		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt AstLmt Per1 LimitedReturn MtrNm f		
tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt AstLmt Per1 LrnPnCtrCCDisable Cn		
tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrEnable Cnt Ig		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32	102	
tgt Rte Inst Ap AstLmt.AstLmt Per1 OutputRampMult Uls f32	tgt AstLmt Per1 OutputRampMult Uls f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt AstLmt Per1 PosServCCDisable Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt Rte Inst Ap AstLmt.AstLmt Per1 PreLimitForStall MtrNm f32	tgt AstLmt Per1 PreLimitForStall MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32		
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	1	1 ± 0.000030517578125	~
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	<b>v</b>
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	~
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	~
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call Astl mt Per1 CP1 ChecknointReached	1	Rte Call Astl mt Per1 CP1 CheckpointReached	1	

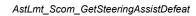




Test Step 3.20 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt ManualTrqCmdEn Cnt M lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	2		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.899999976		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	0		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.5		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.9000001		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.30000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	1 -4		
tgt_AstLmt_Per1_PtiAssistCmt_within_i32.value	3		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
tgt AstLmt Per1 ThermalLimit MtrNm f32.value	7.0999999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	26		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	Vm f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	<del>-</del>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3:		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	<u>lgc</u>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_inst_Ap_AstLmt.AstLmt_Per1_InermaiLimit_MtrNm_f32 tgt_Rte_inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32		
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	1	1 ± 0.000030517578125	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uis_132.value tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitForstail_withtin_isz.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5.9000001	5.9000001 ± 0.0009765625	
	1.00000	1.130001. 2 0.0300100020	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call Astlimt Per1 CP1 ChecknointReached	1	Rte Call Astlimt Per1 CP1 ChecknointReached	1	-





Project AstLmt\_CM Module AstLmt

**Test Object**  $AstLmt\_Scom\_GetSteeringAssistDefeat$ 

#### Instrumentation: Test Object Only

Statement (C0) Coverage 100 % Branch (C1) Coverage 100 %

#### **Statistics**

Total Testcases	1	
Successful	1	~
Failed	0	
Not Executed	0	

#### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\AstLmt_CM
Configuration File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\AstLmt_CM\src\Ap_AstLmt.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AstLmt_CM\utp\contract -I\$(PROJECTROOT)\AstLmt_CM\utp\contract\Ap_AstLmt -I\$ (PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler \tms470\include

Name	Text
Module 'AstLmt'	**************************************
	Name of Tester:Ajit Shrivastava Code File(s) Under Test:Ap_AstLmt.c Code File(s) Version:9 Module Design Document:Assist_Sum_Limit_CurrentMode_MDD.docx Module Design Document Version:3 Data Dictionary Version:9 Unit Test Plan Version:6 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):942 Total RAM Used (Bytes):6 Total CALS Used (Bytes):8 Special Test Requirements: Test Date:09-05-2014 Comments:"NOTE 1: Inline functions defined in ""GlobalMacro.h"" are not Unit Tested. NOTE 2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg

# TEST DETAILS REPORT AstLmt\_Scom\_GetSteeringAssistDefeat

Workspace File

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Attributes
Name Value

D:\Synergy\_Work\_Area\AstLmt\_CM\UnitTestEnv\config\UDE\_TMS570\_DEBUG.WSP



#### Test Case 1: Boundary Test

Specification Performance Metrics:

(With "None" Instrumentation and WithPS Environment)

Elivironment

CPU Cycles:

TS1.1 20.00 Cycles TS1.2 20.00 Cycles

**Description** Vector Description:

TS1.1 Rte\_Pim\_SteerAsstDefeat = True TS1.2 Rte\_Pim\_SteerAsstDefeat = False

Test Step 1.1 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
SteeringAsstDefeat_Cnt_lgc	tgt_SteeringAsstDefeat_Cnt_lgc	
tgt_Pim_SteerAsstDefeat	1	
tgt_Rte_Inst_Ap_AstLmt.Pim_SteerAsstDefeat	tgt_Pim_SteerAsstDefeat	
Name	Actual Value Expected Value	Result
tgt_SteeringAsstDefeat_Cnt_lgc	1	<b>✓</b>

Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
SteeringAsstDefeat_Cnt_lgc	tgt_SteeringAsstDefeat_Cnt_lgc		
tgt_Pim_SteerAsstDefeat	0		
tgt_Rte_Inst_Ap_AstLmt.Pim_SteerAsstDefeat	tgt_Pim_SteerAsstDefeat		
Name	Actual Value	Expected Value	Result
tgt_SteeringAsstDefeat_Cnt_lgc	0	0	~

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AstLmt\_Scom\_ManualTrqCmd

 Project
 AstLmt\_CM

 Module
 AstLmt

Test Object AstLmt\_Scom\_ManualTrqCmd

#### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
<b>Decision Coverage</b>	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

#### **Statistics**

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

#### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\AstLmt_CM
Configuration File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\AstLmt_CM\src\Ap_AstLmt.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AstLmt_CM\utp\contract -I\$(PROJECTROOT)\AstLmt_CM\utp\contract\Ap_AstLmt -I\$ (PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler \tms470\include

Comments/Descrip	otion/Specification
Name	Text
Module 'AstLmt'	**************************************
	Name of Tester:Ajit Shrivastava Code File(s) Under Test:Ap_AstLmt.c Code File(s) Version:9 Module Design Document:Assist_Sum_Limit_CurrentMode_MDD.docx Module Design Document Version:13 Data Dictionary Version:9 Unit Test Plan Version:6 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):942 Total RAM Used (Bytes):6 Total CALS Used (Bytes):6 Special Test Requirements: Test Date:09-05-2014 Comments:"NOTE 1: Inline functions defined in ""GlobalMacro.h"" are not Unit Tested. NOTE 2 : ""CBD_Sandbox_dbg.map"" map file is embedded for reference."

Access to	
Attributes	
Name	Value
Compiler Install Path	<pre>\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5</pre>
Float Precision	9
InitObjDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj</pre>
InitSrcDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\src</pre>
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>
Makefile Template	<pre>\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl</pre>
Target Install Path	<pre>\$(ProgramFiles)\pls\UDE 3.2</pre>
Time Unit	Cycles
Timer Enabled	false

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Attributes	
Name	Value
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



#### Test Case 1: Matrix Test

Specification Performance Metrics:

(With "None" Instrumentation and WithPS

Environment) CPU Cycles:

TS1.1 269.00 Cycles Shortest Execution Path TS1.2 274.00 Cycles Longest Execution Path

Description Vector Description:

TS1.1 "Shortest Execution Path:
( TRUE == EnableManualCtrl ) =False"
TS1.2 "Longest Execution Path:
( TRUE == EnableManualCtrl ) = True
( VehSpd\_Kph\_T\_f32 < FLT\_EPSILON ) = True"

Test Step 1.1 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1		
EnableManualCtrl	0		
MtrTrqCmd_MtrNm_f32	3		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Ve	hSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	54		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	0	~
AstLmt_ManualTrqCmd_MtrNm_M_f32	0	0 ± 0.0009765625	<b>✓</b>
AstLmt Scom ManualTrgCmd()	1	1	•

Test Step Call	Trace				<b>✓</b>
<b>Actual Function</b>		Count	Expected Function	Count	Result
Rte_Read_Ap_AstLr	nt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~

Test Step 1.2 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	2		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	-6		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Ve	ehSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	0		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	✓
AstLmt_ManualTrqCmd_MtrNm_M_f32	-6	-6 ± 0.0009765625	✓
AstLmt_Scom_ManualTrqCmd()	1	1	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	



#### **Test Case 2: Boundary Test**

Specification

Performance Metrics:

(With "None" Instrumentation and WithPS

Environment)

CPU Cycles:

269.00 Cycles 272.00 Cycles 275.00 Cycles 275.00 Cycles 274.00 Cycles 274.00 Cycles 274.00 Cycles 274.00 Cycles 272.00 Cycles 272.00 Cycles 269.00 Cycles 272.00 Cycles TS2.1 TS2.2 TS2.3 TS2.4 TS2.5 TS2.6 TS2.7 TS2.8 TS2.9 TS2.10 TS2.11

#### Vector Description: Description

TS2.1 EnableManualCtrl = min TS2.2 EnableManualCtrl = max

TS2.2 EnableManualCtrl = max
TS2.3 MtrTrqCmd\_MtrNm\_f32 = min
TS2.4 MtrTrqCmd\_MtrNm\_f32 = zero
TS2.5 MtrTrqCmd\_MtrNm\_f32 = zero
TS2.6 MtrTrqCmd\_MtrNm\_f32 = neg
TS2.7 MtrTrqCmd\_MtrNm\_f32 = pos
TS2.8 VehSpd\_Kph\_f32 = min
TS2.9 VehSpd\_Kph\_f32 = max
TS2.10 VehSpd\_Kph\_f32 = pos
TS2.11 All min
TS2.12 All max

Test Step 2.1 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-15.9949999		
EnableManualCtrl	0		
MtrTrqCmd_MtrNm_f32	3.4000001		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Vel	hSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	54.6699982		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	0	<b>✓</b>
AstLmt_ManualTrqCmd_MtrNm_M_f32	0	0 ± 0.0009765625	•
AstLmt Scom ManualTrgCmd()	1	1	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~

Test Step 2.2 (Repeat Count = 1)			V
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-12.9949999		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	-6.19999981	-6.19999981	
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Ve	ehSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	29.9200001		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	~
AstLmt_ManualTrqCmd_MtrNm_M_f32	-12.9949999	-12.9949999 ± 0.0009765625	<b>✓</b>
AstLmt_Scom_ManualTrqCmd()	34	34	~

Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~		

Test Step 2.3 (Repeat Count = 1)		
Name	Input Value	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-9.99499989	
EnableManualCtrl	1	

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Name	Input Value			
MtrTrqCmd_MtrNm_f32	-16			
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f3	2_data		
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	3.45999993e-008			
Name	Actual Value	Expected Value	Result	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	~	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-16	-16 ± 0.0009765625	<b>✓</b>	
AstLmt_Scom_ManualTrqCmd()	1	1	<b>~</b>	

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~	

Test Step 2.4 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-6.99499989		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	15.9995003		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_V	ehSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	6.38000017e-008		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	~
AstLmt_ManualTrqCmd_MtrNm_M_f32	15.9995003	15.9995003 ± 0.0009765625	~
AstLmt_Scom_ManualTrqCmd()	1	1	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1		

Test Step 2.5 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-3.99499989		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	0		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Ve	ehSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	7.27999989e-008		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	~
AstLmt_ManualTrqCmd_MtrNm_M_f32	0	0 ± 0.0009765625	~
AstLmt_Scom_ManualTrqCmd()	1	1	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte Read Ap AstLmt VehSpd Kph f32	1	Rte Read Ap AstLmt VehSpd Kph f32	1	<b>✓</b>	

Test Step 2.6 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-0.995000005		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	-2.5		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Ve	ehSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	5.28000008e-008		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	~
AstLmt_ManualTrqCmd_MtrNm_M_f32	-2.5	-2.5 ± 0.0009765625	✓
AstLmt_Scom_ManualTrqCmd()	1	1	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~	



Test Step 2.7 (Repeat Count = 1)			~
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	2.00500011		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	4.55999994		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Vel	nSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	8.38000034e-008		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	~
AstLmt_ManualTrqCmd_MtrNm_M_f32	4.55999994	4.55999994 ± 0.0009765625	~
Astlimt Scom ManualTraCmd()	1	1	

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~	

Test Step 2.8 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	5.00500011		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	15.1199999		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Ve	ehSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	0		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	~
AstLmt_ManualTrqCmd_MtrNm_M_f32	15.1199999	15.1199999 ± 0.0009765625	<b>~</b>
AstLmt_Scom_ManualTrqCmd()	1	1	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~

Test Step 2.9 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	8.00500011		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	-3.67000008		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Ve	ehSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	512		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	0	~
AstLmt_ManualTrqCmd_MtrNm_M_f32	8.00500011	8.00500011 ± 0.0009765625	~
AstLmt_Scom_ManualTrqCmd()	34	34	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~



Test Step 2.10 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	11.0050001		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	9.82999992		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Veh	Spd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	123.260002		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	-
AstLmt_ManualTrqCmd_MtrNm_M_f32	11.0050001	11.0050001 ± 0.0009765625	•
AstLmt_Scom_ManualTrqCmd()	34	34	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~

Test Step 2.11 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-16		
EnableManualCtrl	0		
MtrTrqCmd_MtrNm_f32	-16		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_V	ehSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	0		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	0	~
AstLmt_ManualTrqCmd_MtrNm_M_f32	0	0 ± 0.0009765625	~
AstLmt_Scom_ManualTrqCmd()	1	1	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~

Test Step 2.12 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	15.9995003		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	15.9995003		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Ve	ehSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	512		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	~
AstLmt_ManualTrqCmd_MtrNm_M_f32	15.9995003	15.9995003 ± 0.0009765625	<b>✓</b>
AstLmt_Scom_ManualTrqCmd()	34	34	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~



#### Test Case 3: Path Coverage Test

**Specification** Performance Metrics:

AstLmt\_Scom\_ManualTrqCmd

(With "None" Instrumentation and WithPS

Environment)

CPU Cycles:

TS3.1 269.00 Cycles TS3.2 272.00 Cycles TS3.3 275.00 Cycles

**Description** Vector Description:

TS3.1 (TRUE == EnableManualCtrl) = False
TS3.2 "(TRUE == EnableManualCtrl) = True
and(VehSpd\_Kph\_T\_f32 < FLT\_EPSILON)=False"
TS3.3 (VehSpd\_Kph\_T\_f32 < FLT\_EPSILON)=True

Test Step 3.1 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1		
EnableManualCtrl	0		
MtrTrqCmd_MtrNm_f32	3		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_\	/ehSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	54		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	0	~
AstLmt_ManualTrqCmd_MtrNm_M_f32	0	0 ± 0.0009765625	<b>✓</b>
AstLmt_Scom_ManualTrqCmd()	1	1	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~

Test Step 3.2 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	2		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	-6		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data		
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	29		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	✓
AstLmt_ManualTrqCmd_MtrNm_M_f32	2	2 ± 0.0009765625	<b>✓</b>
AstLmt_Scom_ManualTrqCmd()	34	34	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~

Test Step 3.3 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	3		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	-16		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Ve	tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	3.45999993e-008		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	~
AstLmt_ManualTrqCmd_MtrNm_M_f32	-16	-16 ± 0.0009765625	~
AstLmt_Scom_ManualTrqCmd()	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~

AstLmt\_Scom\_ManualTrqCmd

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AstLmt\_Init

 Project
 AstLmt\_CM

 Module
 AstLmt

 Test Object
 AstLmt\_Init

#### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

#### **Statistics**

Total Testcases	1	
Successful	1	~
Failed	0	
Not Executed	0	

#### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\AstLmt_CM
Configuration File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\AstLmt_CM\src\Ap_AstLmt.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AstLmt_CM\utp\contract -I\$(PROJECTROOT)\AstLmt_CM\utp\contract\-I\$(PROJECTROOT)\AstLmt_CM\utp\contract\-I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler \tms470\include

Name	Text
Module 'AstLmt'	**************************************
	Name of Tester:Ajit Shrivastava Code File(s) Under Test:Ap_AstLmt.c Code File(s) Version:9 Module Design Document:Assist_Sum_Limit_CurrentMode_MDD.docx Module Design Document Version:3 Data Dictionary Version:9 Unit Test Plan Version:6 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):942 Total RAM Used (Bytes):6 Total CALS Used (Bytes):8 Special Test Requirements: Test Date:09-05-2014 Comments:"NOTE 1: Inline functions defined in ""GlobalMacro.h"" are not Unit Tested. NOTE 2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj</pre>
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570.tpl
Target Install Path	<pre>\$(ProgramFiles)\pls\UDE 3.2</pre>
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	<pre>\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg</pre>

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AstLmt\_Init

Attributes	
Name	Value
Workspace File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



#### Test Case 1: Boundary Test

Specification Performance Metrics:

(With "None" Instrumentation and WithPS Environment)

Environment

CPU Cycles:

TS1.1 25.00 Cycles TS1.2 25.00 Cycles

**Description** Vector Description:

TS1.1 SteeringAsstDefeat1\_Cnt\_lgc = True TS1.2 SteeringAsstDefeat1\_Cnt\_lgc = False

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
tgt_Pim_SteerAsstDefeat	1		
tgt_Rte_Inst_Ap_AstLmt.Pim_SteerAsstDefeat	tgt_Pim_SteerAsstDefeat		
Name	Actual Value	Expected Value	Result
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	1	✓

Test Step 1.2 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
tgt_Pim_SteerAsstDefeat	0		
tgt_Rte_Inst_Ap_AstLmt.Pim_SteerAsstDefeat	tgt_Pim_SteerAsstDefeat		
Name	Actual Value	Expected Value	Result
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0	0	~