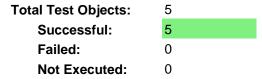
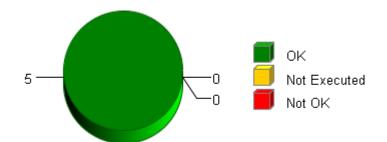


Summary

Overall Test Object Results (including Coverage)



Date: 2014-09-05 **Time:** 18:13:34+0530



Selected Project Items

Test Object "CBD_UnitTest/AstLmt/AstLmt_Init"

Test Object "CBD_UnitTest/AstLmt/AstLmt_Per1"

Test Object "CBD_UnitTest/AstLmt/AstLmt_Scom_GetSteeringAssistDefeat"

Test Object "CBD_UnitTest/AstLmt/AstLmt_Scom_ManualTrqCmd"

Test Object "CBD_UnitTest/AstLmt/AstLmt_Scom_SetSteeringAssistDefeat"

Used Test Environments

TI TMS 570 PLS UDE (Default)

Batch Operation Settings

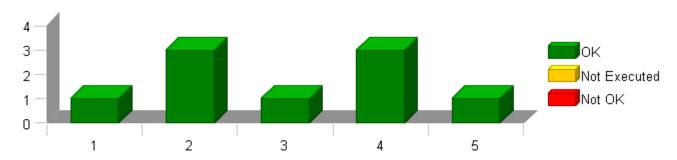
Check Interface: No
Generate Driver: Yes
Execute Test: Yes
Create New Test Run: No

Instrumentation: Test Object Only

Coverage: Statement Coverage, Branch Coverage, Decision Coverage, Modified Condition /

Decision Coverage, Multiple Condition Coverage

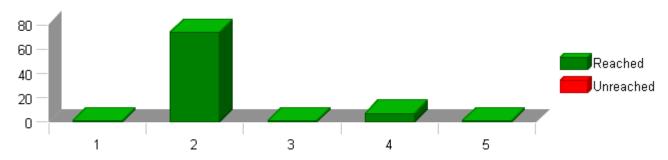
Test Case Results for Each Test Object (without Coverage)



The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

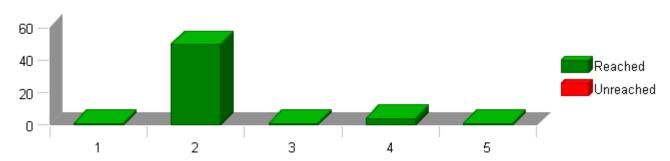


Statement (C0) Coverage: Total Statements for Each Test Object



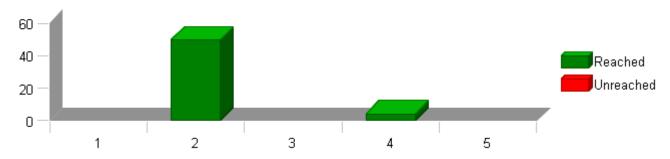
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

Decision Coverage: Total Decision Outcomes for Each Test Object

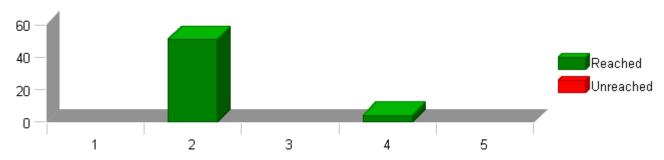


The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.



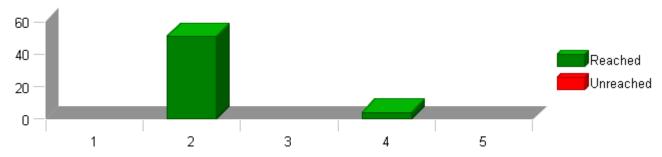
MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

TEST OVERVIEW REPORT

2014-09-05, 18:13:34+0530



Project AstLmt_CM

Test Object List

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	DC	MC/DC	МСС	Test Cases Re	esult
	AstLmt_CM	100 %	100 %	100 %	100 %	100 %	9 of 9 passed	•
	CBD_UnitTest	100 %	100 %	100 %	100 %	100 %	9 of 9 passed	~
	AstLmt	100 %	100 %	100 %	100 %	100 %	9 of 9 passed	~
1	AstLmt_Init	100 %	100 %	-	-	-	1 of 1 passed	•
2	AstLmt Per1	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	•
3	AstLmt Scom GetSteeringAssistDefeat	100 %	100 %	-	-	-	1 of 1 passed	•
4	AstLmt Scom ManualTrqCmd	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	•
5	AstLmt Scom SetSteeringAssistDefeat	100 %	100 %	-	-	-	1 of 1 passed	•

© Report created by TESSY V3.1.7, report template V2.0

2014-09-05, 18:08:50+0530



AstLmt_Scom_GetSteeringAssistDefeat

 Project
 AstLmt_CM

 Module
 AstLmt

Test Object AstLmt_Scom_GetSteeringAssistDefeat

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1	
Successful	1	~
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\AstLmt_CM
Configuration File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\AstLmt_CM\src\Ap_AstLmt.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AstLmt_CM\utp\contract -I\$(PROJECTROOT)\AstLmt_CM\utp\contract\Ap_AstLmt -I\$ (PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler \tms470\include

Name	Text
Module 'AstLmt'	**************************************
	Name of Tester:Ajit Shrivastava Code File(s) Under Test:Ap_AstLmt.c Code File(s) Version:9 Module Design Document:Assist_Sum_Limit_CurrentMode_MDD.docx Module Design Document Version:13 Data Dictionary Version:9 Unit Test Plan Version:6 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):942 Total RAM Used (Bytes):6 Total CALS Used (Bytes):6 Special Test Requirements: Test Date:09-05-2014 Comments:"NOTE 1: Inline functions defined in ""GlobalMacro.h"" are not Unit Tested. NOTE 2 : ""CBD_Sandbox_dbg.map"" map file is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg

TEST DETAILS REPORT AstLmt_Scom_GetSteeringAssistDefeat

2014-09-05, 18:08:50+0530



Attributes

Name

Value

Workspace File

D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



Test Case 1: Boundary Test

Specification Performance Metrics:

(With "None" Instrumentation and WithPS Environment)

CPU Cycles:

TS1.1 20.00 Cycles TS1.2 20.00 Cycles

Description Vector Description:

TS1.1 Rte_Pim_SteerAsstDefeat = True TS1.2 Rte_Pim_SteerAsstDefeat = False

Test Step 1.1 (Repeat Count = 1)		✓
Name	Input Value	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
SteeringAsstDefeat_Cnt_lgc	tgt_SteeringAsstDefeat_Cnt_lgc	
tgt_Pim_SteerAsstDefeat	1	
tgt_Rte_Inst_Ap_AstLmt.Pim_SteerAsstDefeat	tgt_Pim_SteerAsstDefeat	
Name	Actual Value Expected Value	Result
tgt_SteeringAsstDefeat_Cnt_lgc	1	✓

Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
SteeringAsstDefeat_Cnt_lgc	tgt_SteeringAsstDefeat_Cnt_lgc		
tgt_Pim_SteerAsstDefeat	0		
tgt_Rte_Inst_Ap_AstLmt.Pim_SteerAsstDefeat	tgt_Pim_SteerAsstDefeat		
Name	Actual Value	Expected Value	Result
tgt_SteeringAsstDefeat_Cnt_lgc	0	0	~

2014-09-05, 18:03:45+0530





 Project
 AstLmt_CM

 Module
 AstLmt

 Test Object
 AstLmt_Init

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1
Successful	1
Failed	0
Not Executed	0

Module Properties

Project Root Directory	D:\Synergy_Work_Area\AstLmt_CM
Configuration File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\AstLmt_CM\src\Ap_AstLmt.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AstLmt_CM\utp\contract -I\$(PROJECTROOT)\AstLmt_CM\utp\contract\Ap_AstLmt -I\$ (PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler \tms470\include

Name	Text
Module 'AstLmt'	**************************************
	Name of Tester:Ajit Shrivastava Code File(s) Under Test:Ap_AstLmt.c Code File(s) Version:9 Module Design Document:Assist_Sum_Limit_CurrentMode_MDD.docx Module Design Document Version:3 Data Dictionary Version:9 Unit Test Plan Version:6 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):942 Total RAM Used (Bytes):6 Total CALS Used (Bytes):8 Special Test Requirements: Test Date:09-05-2014 Comments:"NOTE 1: Inline functions defined in ""GlobalMacro.h"" are not Unit Tested. NOTE 2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference."

Attributes		
Name	Value	
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5	
Float Precision	9	
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj	
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src	
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>	
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl	
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2	
Time Unit	Cycles	
Timer Enabled	false	
Timer Prescale	0	
Timer Resolution		
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg	

2014-09-05, 18:03:45+0530



AstLmt_Init

Attributes	
Name	Value
Workspace File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



Test Case 1: Boundary Test

Specification Performance Metrics:

(With "None" Instrumentation and WithPS Environment)

CPU Cycles:

TS1.1 25.00 Cycles TS1.2 25.00 Cycles

Vector Description: Description

TS1.1 SteeringAsstDefeat1_Cnt_lgc = True TS1.2 SteeringAsstDefeat1_Cnt_lgc = False

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
tgt_Pim_SteerAsstDefeat	1		
tgt_Rte_Inst_Ap_AstLmt.Pim_SteerAsstDefeat	tgt_Pim_SteerAsstDefeat		
Name	Actual Value	Expected Value	Result
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	1	~

Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
tgt_Pim_SteerAsstDefeat	0		
tgt_Rte_Inst_Ap_AstLmt.Pim_SteerAsstDefeat	tgt_Pim_SteerAsstDefeat		
Name	Actual Value	Expected Value	Result
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0	0	~

2014-09-05, 18:10:13+0530



AstLmt_Scom_ManualTrqCmd

 Project
 AstLmt_CM

 Module
 AstLmt

Test Object AstLmt_Scom_ManualTrqCmd

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\AstLmt_CM
Configuration File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\AstLmt_CM\src\Ap_AstLmt.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AstLmt_CM\utp\contract -I\$(PROJECTROOT)\AstLmt_CM\utp\contract\Ap_AstLmt -I\$ (PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler \tms470\include

Name	Text
Module 'AstLmt'	**************************************
	Name of Tester:Ajit Shrivastava
	Code File(s) Under Test:Ap_AstLmt.c
	Code File(s) Version:9
	Module Design Document:Assist_Sum_Limit_CurrentMode_MDD.docx Module Design Document Version:13
	Data Dictionary Version: 9
	Unit Test Plan Version:6
	Optimization Level:Level 2
	Compiler (CodeGen) Version:TMS470_4.9.5
	Model Type:Excel Macro
	Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):942
	Total RAM Used (Bytes):6
	Total CALS Used (Bytes):8
	Special Test Requirements:
	Test Date:09-05-2014
	Comments:"NOTE 1: Inline functions defined in ""GlobalMacro.h"" are not Unit Tested. NOTE 2 : ""CBD Sandbox dbg.map"" map file is embedded for reference."

Attributes		
Name	Value	
Compiler Install Path	<pre>\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5</pre>	
Float Precision	9	
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj	
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src	
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd	
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl	
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2	
Time Unit	Cycles	
Timer Enabled	false	

2014-09-05, 18:10:13+0530



Attributes		
Name	Value	
Timer Prescale	0	
Timer Resolution	1	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg	
Workspace File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP	



AstLmt_Scom_ManualTrqCmd

Test Case 1: Matrix Test

Specification Performance Metrics:

(With "None" Instrumentation and WithPS

Environment)

CPU Cycles:

TS1.1 269.00 Cycles Shortest Execution Path TS1.2 274.00 Cycles Longest Execution Path

Description Vector Description:

TS1.1 "Shortest Execution Path:
(TRUE == EnableManualCtrl) =False"
TS1.2 "Longest Execution Path:
(TRUE == EnableManualCtrl) = True
(VehSpd_Kph_T_f32 < FLT_EPSILON) = True"

Test Step 1.1 (Repeat Count = 1)				
Name	Input Value			
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0			
AstLmt_ManualTrqCmd_MtrNm_M_f32	1			
EnableManualCtrl	0			
MtrTrqCmd_MtrNm_f32	3			
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Ve	tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data		
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	54	54		
Name	Actual Value	Expected Value	Result	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	0	~	
AstLmt_ManualTrqCmd_MtrNm_M_f32	0	0 ± 0.0009765625	✓	
AstLmt Scom ManualTrgCmd()	1	1	•	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~

Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	2		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	-6		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Ve	ehSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	0		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	✓
AstLmt_ManualTrqCmd_MtrNm_M_f32	-6	-6 ± 0.0009765625	✓
AstLmt_Scom_ManualTrqCmd()	1	1	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~



AstLmt_Scom_ManualTrqCmd

Test Case 2: Boundary Test

Specification

Performance Metrics:

(With "None" Instrumentation and WithPS

Environment)

CPU Cycles:

TS2.1 TS2.2 TS2.3 TS2.4

269.00 Cycles 272.00 Cycles 275.00 Cycles 275.00 Cycles 274.00 Cycles 274.00 Cycles 274.00 Cycles 274.00 Cycles 272.00 Cycles 272.00 Cycles 269.00 Cycles 272.00 Cycles TS2.4 TS2.5 TS2.6 TS2.7 TS2.8 TS2.9 TS2.10 TS2.11

Vector Description: Description

TS2.1 EnableManualCtrl = min TS2.2 EnableManualCtrl = max

TS2.2 EnableManualCtrl = max
TS2.3 MtrTrqCmd_MtrNm_f32 = min
TS2.4 MtrTrqCmd_MtrNm_f32 = zero
TS2.5 MtrTrqCmd_MtrNm_f32 = zero
TS2.6 MtrTrqCmd_MtrNm_f32 = neg
TS2.7 MtrTrqCmd_MtrNm_f32 = pos
TS2.8 VehSpd_Kph_f32 = min
TS2.9 VehSpd_Kph_f32 = max
TS2.10 VehSpd_Kph_f32 = pos
TS2.11 All min
TS2.12 All max

Test Step 2.1 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-15.9949999		
EnableManualCtrl	0		
MtrTrqCmd_MtrNm_f32	3.4000001		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Ve	hSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	54.6699982		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	0	~
AstLmt_ManualTrqCmd_MtrNm_M_f32	0	0 ± 0.0009765625	✓
AstLmt Scom ManualTrgCmd()	1	1	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~

Test Step 2.2 (Repeat Count = 1)			V
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-12.9949999		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	-6.19999981		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Ve	ehSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	29.9200001		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	~
AstLmt_ManualTrqCmd_MtrNm_M_f32	-12.9949999	-12.9949999 ± 0.0009765625	✓
AstLmt_Scom_ManualTrqCmd()	34	34	~

Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~

Test Step 2.3 (Repeat Count = 1)	✓
Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0
AstLmt_ManualTrqCmd_MtrNm_M_f32	-9.99499989
EnableManualCtrl	1

2014-09-05, 18:10:13+0530



Name	Input Value			
MtrTrqCmd_MtrNm_f32	-16	-16		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data			
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	3.4599993e-008			
Name	Actual Value	Expected Value	Result	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	~	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-16	-16 ± 0.0009765625	✓	
AstLmt_Scom_ManualTrqCmd()	1	1	~	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~

Test Step 2.4 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-6.99499989		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	15.9995003		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Ve	hSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	6.38000017e-008		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	~
AstLmt_ManualTrqCmd_MtrNm_M_f32	15.9995003	15.9995003 ± 0.0009765625	~
AstLmt_Scom_ManualTrqCmd()	1	1	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~

Test Step 2.5 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-3.99499989		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	0		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Ve	ehSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	7.27999989e-008		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	~
AstLmt_ManualTrqCmd_MtrNm_M_f32	0	0 ± 0.0009765625	✓
AstLmt_Scom_ManualTrqCmd()	1	1	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte Read Ap AstLmt VehSpd Kph f32	1	Rte Read Ap AstLmt VehSpd Kph f32	1	~

Test Step 2.6 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-0.995000005		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	-2.5		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Ve	ehSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	5.28000008e-008		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	~
AstLmt_ManualTrqCmd_MtrNm_M_f32	-2.5	-2.5 ± 0.0009765625	✓
AstLmt_Scom_ManualTrqCmd()	1	1	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~



Test Step 2.7 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	2.00500011		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	4.55999994		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Ve	hSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	8.38000034e-008		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	~
AstLmt_ManualTrqCmd_MtrNm_M_f32	4.55999994	4.55999994 ± 0.0009765625	✓
AstLmt_Scom_ManualTrqCmd()	1	1	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~

Test Step 2.8 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	5.00500011		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	15.1199999		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_V	ehSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	0		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	✓
AstLmt_ManualTrqCmd_MtrNm_M_f32	15.1199999	15.1199999 ± 0.0009765625	~
AstLmt_Scom_ManualTrqCmd()	1	1	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~

Test Step 2.9 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	8.00500011		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	-3.67000008		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Ve	ehSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	512		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	0	~
AstLmt_ManualTrqCmd_MtrNm_M_f32	8.00500011	8.00500011 ± 0.0009765625	~
AstLmt_Scom_ManualTrqCmd()	34	34	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~



Test Step 2.10 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	11.0050001		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	9.82999992		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Vel	nSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	123.260002		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	~
AstLmt_ManualTrqCmd_MtrNm_M_f32	11.0050001	11.0050001 ± 0.0009765625	✓
Astlimt Scom ManualTraCmd()	34	34	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~

Test Step 2.11 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-16		
EnableManualCtrl	0		
MtrTrqCmd_MtrNm_f32	-16		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Ve	ehSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	0		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	0	~
AstLmt_ManualTrqCmd_MtrNm_M_f32	0	0 ± 0.0009765625	✓
AstLmt_Scom_ManualTrqCmd()	1	1	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~

Name	Input Value		
AstLmt ManualTrqCmdEn Cnt M lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	15.9995003		
EnableManualCtrl	1		
MtrTrqCmd_MtrNm_f32	15.9995003	15.9995003	
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_V	ehSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	512		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	•
AstLmt_ManualTrqCmd_MtrNm_M_f32	15.9995003	15.9995003 ± 0.0009765625	•
AstLmt Scom ManualTrqCmd()	34	34	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~





Test Case 3: Path Coverage Test

Specification Performance Metrics:

(With "None" Instrumentation and WithPS

Environment)

CPU Cycles:

TS3.1 269.00 Cycles TS3.2 272.00 Cycles TS3.3 275.00 Cycles

Description Vector Description:

TS3.1 (TRUE == EnableManualCtrl) = False
TS3.2 "(TRUE == EnableManualCtrl) = True
and(VehSpd_Kph_T_f32 < FLT_EPSILON)=False"
TS3.3 (VehSpd_Kph_T_f32 < FLT_EPSILON)=True

Test Step 3.1 (Repeat Count = 1)			<u> </u>
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1		
EnableManualCtrl	0		
MtrTrqCmd_MtrNm_f32	3		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Ve	hSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	54		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0	0	✓
AstLmt_ManualTrqCmd_MtrNm_M_f32	0	0 ± 0.0009765625	✓
AstLmt_Scom_ManualTrqCmd()	1	1	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~

Test Step 3.2 (Repeat Count = 1)			✓	
Name	Input Value			
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1			
AstLmt_ManualTrqCmd_MtrNm_M_f32	2			
EnableManualCtrl	1	1		
MtrTrqCmd_MtrNm_f32	-6	-6		
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Ve	hSpd_Kph_f32_data		
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	29			
Name	Actual Value	Expected Value	Result	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	✓	
AstLmt_ManualTrqCmd_MtrNm_M_f32	2	2 ± 0.0009765625	✓	
AstLmt_Scom_ManualTrqCmd()	34	34	<u> </u>	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~

Test Step 3.3 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	3		
EnableManualCtrl	1	1	
MtrTrqCmd_MtrNm_f32	-16	-16	
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32(data)	tgt_Rte_Read_Ap_AstLmt_Ve	ehSpd_Kph_f32_data	
tgt_Rte_Read_Ap_AstLmt_VehSpd_Kph_f32_data	3.45999993e-008		
Name	Actual Value	Expected Value	Result
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	1	~
AstLmt_ManualTrqCmd_MtrNm_M_f32	-16	-16 ± 0.0009765625	~
AstLmt_Scom_ManualTrqCmd()	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	Rte_Read_Ap_AstLmt_VehSpd_Kph_f32	1	~

AstLmt_Scom_ManualTrqCmd

2014-09-05, 18:10:13+0530



2014-09-05, 18:07:58+0530





Project	AstLmt_CM
Module	AstLmt
Test Object	AstLmt_Per1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\AstLmt_CM
Configuration File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\AstLmt_CM\src\Ap_AstLmt.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AstLmt_CM\utp\contract -I\$(PROJECTROOT)\AstLmt_CM\utp\contract\Ap_AstLmt -I\$ (PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler \tms470\include

Comments/Descrip	otion/Specification
Name	Text
Module 'AstLmt'	**************************************
	Name of Tester:Ajit Shrivastava Code File(s) Under Test:Ap_AstLmt.c Code File(s) Version:9 Module Design Document:Assist_Sum_Limit_CurrentMode_MDD.docx Module Design Document Version:13 Data Dictionary Version:9 Unit Test Plan Version:6 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):942 Total RAM Used (Bytes):6 Total CALS Used (Bytes):6 Special Test Requirements: Test Date:09-05-2014 Comments:"NOTE 1: Inline functions defined in ""GlobalMacro.h"" are not Unit Tested. NOTE 2 : ""CBD_Sandbox_dbg.map"" map file is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	<pre>\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5</pre>
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false

2014-09-05, 18:07:58+0530



AstLmt_Per1

Attributes			
Name	Value		
Timer Prescale	0		
Timer Resolution	1		
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg		
Workspace File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP		



Test Case 1: Matrix Test

Specification

Performance Metrics:

(With "None" Instrumentation and WithPS

Environment)

TS1.1 1848.00 cycles Shortest Execution Path TS1.2 1948.00 cycles Longest Execution Path

Description Vector Description:

TS1.1:"Shortest execution Path:

(PosServCCDisable_Cnt_T_lgc = True) = False

(LmPnCtrCCDisable_Cnt_T_lgc = True = False

((AstLmt_ManualTrqCmdEn_Cnt_M_lgc = True) And (VehSpd_Kph_T_f32 < FLT_EPSILON)) = True

(DefeatLimitService_Cnt_T_lgc = True) = True

TS1.2"Longest Execution Path:

(PosServCCDisable_Cnt_T_lgc = True) = True

(LmPnCtrCCDisable_Cnt_T_lgc = True) = True

((AstLmt_ManualTrqCmdEn_Cnt_M_lgc = True) And (VehSpd_Kph_T_f32 < FLT_EPSILON)) = False

(LmPnCtrEnable_Cnt_T_lgc = True) = False

(AstLmt_SteeringAsstDefeat_Cnt_M_lgc = True) = False

(DefeatLimitService_Cnt_T_lgc = True) = False"

Test Step 1.1 (Repeat Count = 1)	
Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	8.80000019
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPICmpLimit_MtrNm_f32	0
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-3
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	1
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-8.80000019
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	1
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	1
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	0
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-8.80000019
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32

2014-09-05, 18:07:58+0530



AstLmt_Per1

Name	Input Value				
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32				
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32				
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32				
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32				
Name	Actual Value	Expected Value	Result		
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	1	1 ± 0.000030517578125	~		
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✓		
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✓		
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✓		
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	~		

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✓

Test Step 4.2 (Papert Count = 4)	
Test Step 1.2 (Repeat Count = 1)	·
Name	Input Value
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1
AstLmt_ManualTrqCmd_MtrNm_M_f32	8.80000019
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt
k_SumLimPlCmpLimit_MtrNm_f32	1
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	3
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-2.5999999
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-7
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	1
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1.20000005
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-7
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	0.5
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5
tgt AstLmt Per1 ThermalLimit MtrNm f32.value	8
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	512
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-6
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrEnable Cnt Igc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_UIs_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt AstLmt Per1 PowerLimitPerc Uls f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrNm_f32

2014-09-05, 18:07:58+0530



AstLmt_Per1

Name	Input Value		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f:	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3.	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32		
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	~
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5.73999977	5.73999977 ± 0.0009765625	✓
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-5.73999977	-5.73999977 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-5.73999977	-5.73999977 ± 0.0009765625	✓
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	8	8 ± 0.0009765625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	•

AstLmt_Per1

2014-09-05, 18:07:58+0530



Test Case 2: Boundary Test

2014-09-05, 18:07:58+0530

AstLmt_Per1



Specification

Performance Metrics:

(With "None" Instrumentation and WithPS

CPU Cycles:

TS2.1 1985.00 cycles TS2.2 1879.00 cycles TS2.3 1821.00 cycles TS2.2 1879.00 cycles
TS2.3 1821.00 cycles
TS2.3 1816.00 cycles
TS2.4 1816.00 cycles
TS2.5 1747.00 cycles
TS2.6 1777.00 cycles
TS2.6 1777.00 cycles
TS2.7 1747.00 cycles
TS2.8 1723.00 cycles
TS2.9 1747.00 cycles
TS2.10 1723.00 cycles
TS2.11 1747.00 cycles
TS2.12 1723.00 cycles
TS2.13 1747.00 cycles
TS2.14 1723.00 cycles
TS2.15 1747.00 cycles
TS2.16 1723.00 cycles
TS2.17 1723.00 cycles
TS2.18 1747.00 cycles
TS2.19 1723.00 cycles
TS2.19 1723.00 cycles
TS2.21 1723.00 cycles
TS2.21 1723.00 cycles
TS2.22 1747.00 cycles
TS2.23 1747.00 cycles
TS2.23 1747.00 cycles
TS2.24 1747.00 cycles
TS2.25 1723.00 cycles
TS2.26 1747.00 cycles
TS2.27 1723.00 cycles
TS2.28 1747.00 cycles
TS2.29 1723.00 cycles
TS2.29 1747.00 cycles
TS2.29 1723.00 cycles
TS2.29 1747.00 cycles
TS2.21 1747.00 cycles
TS2.23 1742.00 cycles
TS2.31 1742.00 cycles
TS2.31 1742.00 cycles
TS2.33 1742.00 cycles
TS2.33 1742.00 cycles
TS2.34 1742.00 cycles
TS2.35 1742.00 cycles
TS2.35 1742.00 cycles TS2.34 1742.00 cycles TS2.35 1742.00 cycles TS2.36 1742.00 cycles TS2.37 1742.00 cycles TS2.38 1742.00 cycles TS2.39 1742.00 cycles TS2.40 1742.00 cycles TS2.41 1742.00 cycles TS2.42 1742.00 cycles TS2.40 1742.00 cycles TS2.40 1742.00 cycles TS2.41 1742.00 cycles TS2.42 1742.00 cycles TS2.43 1758.00 cycles TS2.44 1758.00 cycles TS2.45 1760.00 cycles TS2.46 1767.00 cycles TS2.47 1742.00 cycles TS2.48 1742.00 cycles TS2.49 1742.00 cycles TS2.50 1742.00 cycles TS2.50 1742.00 cycles TS2.49 1742.00 cycles TS2.50 1742.00 cycles TS2.50 1742.00 cycles TS2.51 1742.00 cycles TS2.51 1742.00 cycles TS2.53 1742.00 cycles TS2.55 1742.00 cycles TS2.55 1758.00 cycles TS2.55 1758.00 cycles TS2.56 1758.00 cycles TS2.60 1758.00 cycles TS2.61 1723.00 cycles TS2.63 1723.00 cycles TS2.66 1767.00 cycles TS2.66 1767.00 cycles TS2.66 1767.00 cycles TS2.67 1723.00 cycles TS2.68 1767.00 cycles TS2.69 1723.00 cycles TS2.71 1723.00 cycles TS2.71 1723.00 cycles TS2.73 1723.00 cycles TS2.74 1729.00 cycles TS2.75 1729.00 cycles TS2.76 1729.00 cycles TS2.77 1723.00 cycles TS2.78 1723.00 cycles TS2.78 1723.00 cycles TS2.79 1723.00 cycles TS2.81 1729.00 cycles TS2.81 1758.00 cycles TS2.82 1723.00 cycles TS2.83 1729.00 cycles TS2.84 1758.00 cycles TS2.85 1767.00 cycles TS2.86 1757.00 cycles TS2.87 1829.00 cycles TS2.88 1719.00 cycles TS2.89 1712.00 cycles TS2.90 1728.00 cycles TS2.91 1714.00 cycles TS2.92 1729.00 cycles TS2.93 1719.00 cycles TS2.93 1719.00 cycles TS2.93 1719.00 cycles TS2.94 1712.00 cycles TS2.94 1712.00 cycles TS2.95 1731.00 cycles TS2.96 1731.00 cycles TS2.97 1729.00 cycles TS2.97 1729.00 cycles TS2.98 1790.00 cycles TS2.99 1758.00 cycles





Description Vector Description :

TS2.1AssistCmd MtrNm f32 = Min TS2.2AssistCmd_MtrNm_f32 = Max TS2.3AssistCmd_MtrNm_f32 = Mid TS2.4AssistCmd_MtrNm_f32 = Pos TS2.5AssistCmd_MtrNm_f32 = Neg TS2.6AssistEOTDamping MtrNm f32 = Min TS2.7AssistEOTDamping MtrNm f32 = Max TS2.8AssistEOTDamping MtrNm f32 = Mid TS2.9AssistEOTDamping_MtrNm_f32 = Mid
TS2.9AssistEOTDamping_MtrNm_f32 = Pos
TS2.10AssistEOTDamping_MtrNm_f32 = Neg
TS2.11AssistEOTGain_Uls_f32 = Min
TS2.12AssistEOTGain_Uls_f32 = Mid
TS2.14AssistEOTLimit_MtrNm_f32 = Min
TS2.14AssistEOTLimit_MtrNm_f32 = Min IS2.14AssistEOTLimit_MtrNm_f32 = Min TS2.15AssistEOTLimit_MtrNm_f32 = Max TS2.16AssistEOTLimit_MtrNm_f32 = Mid TS2.17AssistStallLimit_MtrNm_f32 = Min TS2.18AssistStallLimit_MtrNm_f32 = Max TS2.19AssistStallLimit_MtrNm_f32 = Mid TS2.20AssistVehSpdLimit_MtrNm_f32 = Min TS2.21AssistVehSpdLimit_MtrNm_f32 = Max TS2.22AssistVehSpdLimit_MtrNm_f32 = Mid TS2.23CombinedDamping_MtrNm_f32 = Min TS2.24CombinedDamping_MtrNm_f32 = Max TS2.25CombinedDamping_MtrNm_f32 = Mid TS2.26CombinedDamping_MtrNm_f32 = Pos TS2.27CombinedDamping_MtrNm_f32 = Neg TS2.28DefeatLimitService_Cnt_lgc = Min TS2.29DefeatLimitService_Cnt_lgc = Max TS2.30LimitedReturn_MtrNm_f32 = Min TS2.31LimitedReturn_MtrNm_f32 = Max TS2.32LimitedReturn_MtrNm_f32 = Mid TS2.33LimitedReturn_MtrNm_f32 = Pos TS2.34LimitedReturn_MtrNm_f32 = Neg TS2.35LmPnCtrCCDisable Cnt_lgc = Min
TS2.36LmPnCtrCCDisable_Cnt_lgc = Min
TS2.36LmPnCtrCDisable_Cnt_lgc = Max
TS2.38LmPnCtrEnable_Cnt_lgc = Min
TS2.38LmPnCtrEnable_Cnt_lgc = Max
TS2.39LmPnCtrTCmd_MtrNm_f32 = Min
TS2.40LmPnCtrTCmd_MtrNm_f32 = Max TS2.41LrnPnCtrTCmd_MtrNm_f32 = Mid TS2.42LrnPnCtrTCmd_MtrNm_f32 = Pos TS2.43LrnPnCtrTCmd_MtrNm_f32 = Neg $TS2.44OpTrqOvr_MtrNm_f32 = Min$ TS2.44OpTrqOvr_MtrNm_f32 = Min
TS2.45OpTrqOvr_MtrNm_f32 = Max
TS2.46OpTrqOvr_MtrNm_f32 = Mid
TS2.47OpTrqOvr_MtrNm_f32 = Pos
TS2.48OpTrqOvr_MtrNm_f32 = Neg
TS2.49OutputRampMult_Uls_f32 = Min
TS2.50OutputRampMult_Uls_f32 = Mia
TS2.51OutputRampMult_Uls_f32 = Mid
TS2.52PosServCCDisable_Cnt_lgc = Min
TS2.53PosServCCDisable_Cnt_lgc = Max
TS2.54Powerl_imitPerr_Uls_f32 = Min TS2.54PowerLimitPerc_UIs_f32 = Min TS2.55PowerLimitPerc_UIs_f32 = Max TS2.56PowerLimitPerc_UIs_f32 = Mid TS2.57PrkAssistCmd_MtrNm_f32 = Min TS2.58PrkAssistCmd_MtrNm_f32 = Max TS2.59PrkAssistCmd_MtrNm_f32 = Mid TS2.60PrkAssistCmd_MtrNm_f32 = Pos TS2.61PrkAssistCmd_MtrNm_f32 = Neg TS2.62PullCompCmd_MtrNm_f32 = Min TS2.63PullCompCmd_MtrNm_f32 = Max TS2.64PullCompCmd_MtrNm_f32 = Mid TS2.65PullCompCmd_MtrNm_f32 = Pos TS2.66PullCompCmd_MtrNm_f32 = Neg TS2.67ThermalLimitPerc_Uls_f32 = Min TS2.68ThermalLimitPerc_Uls_f32 = Max TS2.69ThermalLimitPerc_Uls_f32 = Mid TS2.70ThermalLimit_MtrNm_f32 = Min TS2.71ThermalLimit_MtrNm_f32 = Max TS2.72ThermalLimit_MtrNm_f32 = Mid TS2.73WheellmbalanceCmd_MtrNm_f32 = Min TS2.74WheellmbalanceCmd_MtrNm_f32 = Max TS2.75WheellmbalanceCmd_MtrNm_f32 = Mid TS2.76WheelImbalanceCmd_MtrNm_f32 = Pos TS2.77WheelImbalanceCmd_MtrNm_f32 = Neg TS2.778k_SumLimPlCmpLimit_MtrNm_f32 = Min TS2.78k_SumLimPlCmpLimit_MtrNm_f32 = Min
TS2.79k_SumLimPlCmpLimit_MtrNm_f32 = Max
TS2.80k_SumLimPlCmpLimit_MtrNm_f32 = Min
TS2.81VehSpd_Kph_f32 = Min
TS2.82VehSpd_Kph_f32 = Max
TS2.83VehSpd_Kph_f32 = Pos
TS2.84ManualTrqCmdEn_Cnt_M_lgc = Min
TS2.85ManualTrqCmdEn_Cnt_M_lgc = Max
TS2.85ManualTrqCmd_MtrNm_M_f32 = Max
TS2.86ManualTrqCmd_MtrNm_M_f32 = Max
TS2.88ManualTrqCmd_MtrNm_M_f32 = Neg
TS2.89ManualTrqCmd_MtrNm_M_f32 = Neg
TS2.99ManualTrqCmd_MtrNm_M_f32 = Pos
TS2.91SteeringAsstDefeat_Cnt_M_lgc = Min
TS2.92SteeringAsstDefeat_Cnt_M_lgc = Max
TS2.93TSMitCommand_MtrNm_f32=Min
TS2.94TSMitCommand_MtrNm_f32=Min
TS2.94TSMitCommand_MtrNm_f32=Max





TS2.95TSMitCommand_MtrNm_f32=Zero TS2.96TSMitCommand_MtrNm_f32=Neg TS2.97TSMitCommand_MtrNm_f32=Pos TS2.98All Min TS2.99All max

Fest Step 2.1 (Repeat Count = 1)	Input Value		
stLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-7.36000013		
AstLmt SteeringAsstDefeat Cnt M Igc	0		
Rte_Inst_Ap_AstLmt	tgt Rte Inst Ap AstLmt		
_SumLimPlCmpLimit_MtrNm_f32	8.10000038		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-3		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-3		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.100000001		
pt AstLmt Per1 AssistStallLimit MtrNm f32.value	1.79999995		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	1.5		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-5		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	3		
pt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
pt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LmPnCtrEnable_Cnt_tgc.value	4		
gt_AstLmt_Per1_ChrPriCthTofnid_withNin_i32.value	5		
pt_AstLmt_Per1_Op1rqOvt_MtNnin_i32.value	0.100000001		
pt_AstLmt_Per1_OutputRampMult_Ois_is2.value	1		
gt_AstLmt_Per1_PosservCcDisable_Cnt_lgc.value	0.100000001		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-3		
	2		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	0.100000001		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value pt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.0999999		
	42.0999985		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	-4.5		
gt_AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32.value		22	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_f	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_t		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_N	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_C	_ - -	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_0		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3:		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_0	·	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNn	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNn		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mtr1	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls	_	
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd		
lame	Actual Value	Expected Value	Res
	0.10000001	0.100000001 ± 0.000030517578125	
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value			
	8.80000019	8.80000019 ± 0.0009765625	
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value		8.80000019 ± 0.0009765625 4.5 ± 0.0009765625	





Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.2 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt ManualTrqCmd MtrNm M f32	5.36999989		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt Rte Inst Ap AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	7.19999981		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.9000001		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.200000003		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	1.8999998		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	1.60000002		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	4		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	6		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.20000005		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	38.2000008		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt AstLmt Per1 LimitPercentFiltered U		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrCCDisable C		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_tmPnCtrEnable_C		
	tgt AstLmt Per1 LrnPnCtrTCmd MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32	1_132	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32		£2.2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cr	_ ·	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_t	732	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resi
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125	
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	5	5 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	1	1 ± 0.0009765625	
-9	0.200000003	0.200000003 ± 0.0009765625	



Test Step Call Trace							
Actual Function	Count	Expected Function	Count	Result			
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~			
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~			

Test Step 2.3 (Repeat Count = 1)	Input Value		
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-4.3499999		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	6.30000019		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.79999995		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	0		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.300000012		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	1.70000005		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	7		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.30000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt AstLmt Per1 PowerLimitPerc Uls f32.value	0.30000012		
tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value	-5.5999999		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012		
tgt AstLmt Per1 ThermalLimit MtrNm f32.value	2.29999995		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	26.7999992		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-6		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mi		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U	s_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Ci	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lo	gc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_t	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cr	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3.	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.8000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.090000036	0.0900000036 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.300000012	0.300000012 ± 0.0009765625	٠,



Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~		
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓		

Test Step 2.4 (Repeat Count = 1) Name	Input Value			
	1			
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32	3.47000003			
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt			
S_SumLimPlCmpLimit_MtrNm_f32	5.4000001			
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.70000005			
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	1.5			
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-6			
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.300000012			
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.40000006			
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.0999999			
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	1.79999995			
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8			
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1			
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	6			
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0			
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1			
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	7			
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	8			
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.40000006			
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0			
pt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.40000006			
pt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-6			
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5			
yt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.40000006			
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.4000001			
yt_AstLmt_Per1_VehSpd_Kph_f32.value	14.5			
t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-7.19999981			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_	f32		
	tgt AstLmt Per1 AssistEOTDamping			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32				
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_Mtr	_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_Mtrl			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_	_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrN	m_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable	_Cnt_lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cn	t_lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_Mtrl	Nm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f	32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_U	ls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_	_Cnt_lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls	_f32		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_Mt			
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_Mtr	Nm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrN			
gt Rte Inst Ap AstLmt.AstLmt Per1 PullCompCmd MtrNm f32	tgt_AstLmt_Per1_PullCompCmd_MtrN	_		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mt			
at Rte Inst Ap AstLmt.AstLmt Per1 TSMitCommand MtrNm f32	target_AstLmt_Per1_TSMitCommand	_		
pt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimitPerc UIs f32	tgt_AstLmt_Per1_ThermalLimitPerc_U			
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNr	_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt AstLmt Per1 TrqLimitMin MtrNm			
		_102		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	d MtcNm f22		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCm			
Name	Actual Value	Expected Value	Res	
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.40000006	0.400000006 ± 0.000030517578125		
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625		
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	7	7 ± 0.0009765625		
	2 70000005	2.79999995 ± 0.0009765625		
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	2.79999995	2.79999993 1 0.0009703023		



Test Step Call Trace							
Actual Function	Count	Expected Function	Count	Result			
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~			
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~			

Innut Value		
·		
1		
0		
8		
-1.20000005		
0.5		
1		
0.5		
-7		
6		
0.5		
2.5		
38.2999992		
-8		
	m f32	
	<u>.</u>	
	32	
	_	
0		
	lgc	
	2	
	gc	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt AstLmt Per1 PreLimitForStall MtrNm f32		
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f		
0	32	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f	32 2	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3:	32 2 2	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3	32 2 2 32	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_	32 2 2 2 332 m_f32	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN	32 2 2 2 332 m_f32	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3:	32 2 2 2 332 m_f32	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3: tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	32 2 2 2 332 m_f32	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	32 2 2 (32 m_f32	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3: tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr	32 2 2 332 m_f32 ? Nm_f32	Rec
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3: tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr Actual Value	32 2 2 332 m_f32 2 Nm_f32 Expected Value	Resi
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3: tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr Actual Value 0.5	Nm_f32 Expected Value 0.5 ± 0.000030517578125	Resi
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3: tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr Actual Value 0.5 8.80000019	Nm_f32 Expected Value 0.5 ± 0.00030517578125 8.80000019 ± 0.0009765625	Resi
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3: tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr Actual Value 0.5	Nm_f32 Expected Value 0.5 ± 0.000030517578125	Resi
	0 8 -1.20000005 0.5 1 0.5 1 0.5 -7 6 0.5 2.5 38.2999992 -8 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrN tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f3 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3 tgt_AstLmt_Per1_CombinedDamping_MtrNr tgt_AstLmt_Per1_LimitPercentFiltered_Uls_igt_AstLmt_Per1_LimitPercentFiltered_Uls_igt_AstLmt_Per1_LimitPercentFiltered_Uls_igt_AstLmt_Per1_LimitPerCtrEcolisable_Cnt_igt_AstLmt_Per1_LimPnCtrEcolisable_Cnt_igt_AstLmt_Per1_LimPnCtrTCmd_MtrNm_f3 tgt_AstLmt_Per1_LimPnCtrTCmd_MtrNm_f3 tgt_AstLmt_Per1_OptrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OutputRampMult_Uls_f32 tgt_AstLmt_Per1_OptsevrCCDisable_Cnt_igt_AstLmt_Per1_OutputRampMult_Uls_f32 tgt_AstLmt_Per1_OptsevrCCDisable_Cnt_igt_AstLmt_Per1_OptsevrCCDisable_Cnt_igt_AstLmt_Per1_OptsevrCCDisable_Cnt_igt_AstLmt_Per1_OutputRampMult_Uls_f32	0 2.25999999 0 tgt Rte_Inst_Ap_AstLmt 4.5 -2.5999999 -2.5999999 -7 0.400000006 0.5 2.2000005 1.89999998 1 0 0 7 1 0 0 8 -1.20000005 0.5 1 1 0.5 -7 6 0.5 2.5 38.2999992 -8 tgt_AstLmt_Per1_AssistComd_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPerCortCord_MtrNm_f32 tgt_AstLmt_Per1_LimitPerCortCord_Igc tgt_AstLmt_Per1_LimitPerCortCord_Igc tgt_AstLmt_Per1_LimitPerCortCord_Igc tgt_AstLmt_Per1_LimitPerCortCord_Igc tgt_AstLmt_Per1_LimitPerCortTord_MtrNm_f32



Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~		
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✓		

Test Step 2.6 (Repeat Count = 1)	Input Value		
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-1.76999998		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	3.5999999		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.5		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-3		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5		
gt AstLmt Per1 AssistEOTLimit MtrNm f32.value	0.600000024		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.2999995		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	2		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	8		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1.2000005		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-8		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	7		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.5999999		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	36.4000015		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	1		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_N	ltrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNr	n_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNn	n_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mt	rNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M	trNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cr	nt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_L	lls_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C	:nt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_l	gc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNn	1_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_C		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	A # A # A # A # A # A # A # A # A # A #	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_		
Name	Actual Value	Expected Value	Resu
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3	3 ± 0.0009765625	•
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1.20000005	-1.20000005 ± 0.0009765625	•
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.720000029	-0.720000029 ± 0.0009765625	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✓	

Test Step 2.7 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1.75999999		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPlCmpLimit_MtrNm_f32	2.70000005		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.4000001		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-4		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	8.80000019		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.69999988		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.4000001		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.0999999		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	3		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	1.20000005		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.70000005		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	26.6000004		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	2		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	rNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt	:_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul	s_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr	nt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg	ıc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
gt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt AstLmt Per1 PrkAssistCmd MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target AstLmt Per1 TSMitCommand Mt	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt AstLmt Per1 WheelImbalanceCmd N	MtrNm f32	
Name	Actual Value	Expected Value	Res
		· ·	Kesi
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.699999988 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-8.80000019	-8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.48999998	-0.49000001 ± 0.0009765625	

0.699999988

tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value

0.699999988 ± 0.0009765625



Test Step Call Trace							
Actual Function	Count	Expected Function	Count	Result			
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~			
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~			

Test Step 2.8 (Repeat Count = 1) Name	Input Value			
AstLmt ManualTrqCmdEn Cnt M lgc	1			
AstLmt_ManualTrqCmd_MtrNm_M_f32				
AstLmt SteeringAsstDefeat Cnt M lgc	1	-8.5		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt			
k_SumLimPlCmpLimit_MtrNm_f32	1.79999995			
	-2.2999995			
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value				
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-5			
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	0			
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988			
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value		0.800000012		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.5			
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.20000005			
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value		4.19999981		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1			
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2.0999999			
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1			
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4.5			
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6.5			
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012			
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012	0.800000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	2			
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-1.20000005			
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012			
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.79999995			
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	34.7000008			
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm	f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32		tgt AstLmt Per1 AssistEOTDamping MtrNm f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTGain_Ois_132 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32				
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32		tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltere			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32			
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc			
tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc		tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_l			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32		tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_Mtr	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand	target_AstLmt_Per1_TSMitCommand_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_	tgt_AstLmt_Per1_ThermalLimitPerc_UIs_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrN	lm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNn			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32			
tgt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32		tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32		
Name	Actual Value	Expected Value	Resu	
	0.800000012		Resul	
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value		0.800000012 ± 0.000030517578125		
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625		
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-4.5	-4.5 ± 0.0009765625		
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-3.60000014	-3.5999999 ± 0.0009765625		
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.800000012	0.800000012 ± 0.0009765625		



Test Step Call Trace							
Actual Function	Count	Expected Function	Count	Result			
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~			
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✓			

Test Step 2.9 (Repeat Count = 1) Name	Input Value			
	0			
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt ManualTrqCmd MtrNm M f32	8.5			
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0			
Rte_Inst_Ap_AstLmt				
k_SumLimPlCmpLimit_MtrNm_f32	tgt_Rte_Inst_Ap_AstLmt			
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0.89999976			
		-2.20000005		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6.19999981			
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	1.5			
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.80000012			
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.89999976			
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.5999999			
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.29999995			
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5			
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0			
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value		-4.5		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1			
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0			
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-6.5			
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	3.2999995			
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.899999976			
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1			
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.89999976			
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	3			
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2.0999999			
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.89999976			
pt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.9000001			
gt_AstLmt_Per1_VehSpd_Kph_f32.value	65.9000015			
gt AstLmt Per1 WheelImbalanceCmd MtrNm f32.value	4.0999999			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mti			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32				
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32			
		_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc		tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f			
gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc		tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3:	2		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_t	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn	n_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr	Nm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_t	32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3	32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32			
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	AtrNm f32		
Name	Actual Value	Expected Value	Res	
		· ·	Kest	
gt_AstLmt_Per1_LimitPercentFiltered_UIs_f32.value	0.899999976	0.899999976 ± 0.000030517578125		
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	6.19999981	6.19999981 ± 0.0009765625		
	-2.66000032	-2.66000009 ± 0.0009765625		
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value				
gt_AstLmt_Per1_PreLimit1 orque_MtrNm_r32.value gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	-0.809999943 0.899999976	-0.810000002 ± 0.0009765625 0.899999976 ± 0.0009765625		



Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~		
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	•		

Innut Value		
·		
	400	
	m_f32	
	20	
	_	
	lgc	
	00	
	32	
	gc	
	_	
	2	
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	·Nm_f32	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32	Nm_f32 Expected Value	Resul
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr		Resul
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr Actual Value 0.100000001	Expected Value 0.100000001 ± 0.000030517578125	Resul
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr Actual Value 0.100000001 7	Expected Value 0.100000001 ± 0.000030517578125 7 ± 0.0009765625	•
	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f3 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f3 tgt_AstLmt_Per1_CombinedDamping_MtrNi tgt_AstLmt_Per1_DefeatLimitService_Cnt_li tgt_AstLmt_Per1_LimitPercentFiltered_Uls_ tgt_AstLmt_Per1_LimitPercentFiltered_Uls_ tgt_AstLmt_Per1_LrmPnCtrCDisable_Cnt_ligt_AstLmt_Per1_LrmPnCtrCDisable_Cnt_ligt_AstLmt_Per1_LrmPnCtrTCmd_MtrNm_f3 tgt_AstLmt_Per1_DrTrQOv_MtrNm_f32 tgt_AstLmt_Per1_OutputRampMult_Uls_f32 tgt_AstLmt_Per1_PosServCCDisable_Cnt_ligt_AstLmt_Per1_PowerLimitForCuls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f3 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f3 tgt_AstLmt_Per1_PreLimitForGal_MtrNm_f3 tgt_AstLmt_Per1_PreLimitForGal_MtrNm_f3 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_target_AstLmt_Per1_SumLimTrqCmd_MtrNm_target_AstLmt_Per1_ThermalLimitPerc_Uls_f32	1 . 7.5 1



Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~		
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓		

Test Step 2.11 (Repeat Count = 1) Name	Input Value		
	·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	7.5		
AstLmt_ManualTrqCmd_MtrNm_M_f32	0		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt 6.23999977		
C_SumLimPICmpLimit_MtrNm_f32			
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-8		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	2		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	1.10000002		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0.800000012		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.5		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	7.5999999		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	3.29999995		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
st_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-6.5		
t_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	3.0999999		
t_AstLmt_Per1_VehSpd_Kph_f32.value	75.8000031		
pt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	6		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt AstLmt Per1 AssistEOTDamping M		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNi		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_M		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_C		
at Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32	tgt_AstLmt_Per1_LimitPercentFiltered_I		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt AstLmt Per1 LimitedReturn MtrNm		
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_0	_	
yt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt AstLmt Per1 LrnPnCtrTCmd MtrNi		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_EmriletrCind_within tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_C	·	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_t		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNi		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	_	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_N		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls	<u>_</u> f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_	<u>f</u> 32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd	_MtrNm_f32	
lame	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125	
gt_AstEmt_reri_Emit creent intered_dis_los_value	8.80000019	8.80000019 ± 0.0009765625	
gt_AstLmt_Per1_PreLimit or3tan_withth_f32.value	-8.80000019	-8.80000019 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.160000011	-0.159999996 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.12 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32	-6.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
S_SumLimPlCmpLimit_MtrNm_f32	7.4499981		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.8999998		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	1.60000002		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	1		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	1.20000005		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0.89999976		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.5999999		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	8		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012		
pt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
pt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012		
pt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3.29999995		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	3.20000005		
st_AstLmt_Per1_VehSpd_Kph_f32.value	28.2000008		
yt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3	32	
	tgt AstLmt Per1 AssistEOTDamping		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32		-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_N	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_I		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_C		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNn	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt	_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrN	lm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls	s_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_0	Cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_Mtrl	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN	lm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNr		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNi	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mtr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_I	_	
at Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimitPerc Uls f32	tgt_AstLmt_Per1_ThermalLimitPerc_UI		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt AstLmt Per1 TrqLimitMin MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	, 	
		1 MtrNm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmc		
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.30000012	0.300000012 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	1.60000002	1.60000002 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1	-1 ± 0.0009765625	
	0.000000010	-0.300000012 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.300000012	-0.300000012 ± 0.0009703023	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✓

Test Step 2.13 (Repeat Count = 1)			•
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	6.55999994		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
s_SumLimPlCmpLimit_MtrNm_f32	1.24000001		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.79999995		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	2		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	4		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	1.29999995		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0.40000006		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	0.69999988		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-3		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.40000006		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.40000006		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.40000006		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	3.29999995		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	85.4000015		
gt AstLmt Per1 WheelImbalanceCmd MtrNm f32.value	8		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mti	Nm f32	
gt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTGain Uls f32	tgt AstLmt Per1 AssistEOTGain Uls f32	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrI		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
gt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul	- -	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt AstLmt Per1 OutputRampMult Uls f:	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt		
gt Rte Inst Ap AstLmt.AstLmt Per1 PowerLimitPerc Uls f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3:		
gt Rte Inst Ap AstLmt.AstLmt Per1 PreLimitForStall MtrNm f32	tgt AstLmt Per1 PreLimitForStall MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	-	
gt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt AstLmt Per1 PrkAssistCmd MtrNm 1		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt AstLmt Per1 SumLimTrqCmd MtrNn		
	target AstLmt Per1 TSMitCommand Mtr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32		_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_UIs_t		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	Mark Inc. 422	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N		1_
Name	Actual Value	Expected Value	Resu
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.40000006	0.400000006 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3.64000058	3.6400001 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	3.64000058	3.6400001 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.160000011	0.159999996 ± 0.0009765625	•



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✓

M	Inner A Males		
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-5.55999994 1		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt k_SumLimPlCmpLimit_MtrNm_f32	tgt_Rte_Inst_Ap_AstLmt 6.38000011		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.70000005		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	3.099999		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0		
gt AstLmt Per1 AssistStallLimit MtrNm f32.value	0.5		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	0.800000012		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2		
gt_AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LmPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-3		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4		
gt AstLmt Per1 OutputRampMult Uls f32.value	0.5		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt AstLmt Per1 PowerLimitPerc Uls f32.value	0.5		
gt AstLmt Per1 PrkAssistCmd MtrNm f32.value	8		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-1		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	3.4000001		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	76.5999985		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-1.20000005		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mi		
tgt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTGain Uls f32	tgt AstLmt Per1 AssistEOTGain Uls f3	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	ı_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	 :Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn	t_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U	ls_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	<u>f</u> 32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Ci	nt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lc	gc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_t	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cr	t_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt	rNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	<u>f</u> 32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3.	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resi
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3.099999	3.0999999 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-3	-3 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-1.5	-1.5 ± 0.0009765625	
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0	0 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓

Test Step 2.15 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	5.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
s_SumLimPlCmpLimit_MtrNm_f32	0.23999995		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.60000002		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	4		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	6		
pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.80000019		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0.600000024		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	0.899999976		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-3		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-1.20000005		
pt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
r	3.5		
pt_AstLmt_Per1_VehSpd_Kph_f32.value	425.299988		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-2.0999999		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtrl	Nm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	VIII_102	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_	f2.2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN		
		_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtrl		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_	•	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtrl	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3:	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_M	trNm f32	
lame	Actual Value	Expected Value	Res
			IVE2
gt_AstLmt_Per1_LimitPercentFiltered_UIs_f32.value	0.600000024	0.600000024 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	6.63999939 -6.63999939	6.63999987 ± 0.0009765625	
wh Anti-mat Dougl Dunck ImpleTourness Markley 100 1		-6.63999987 ± 0.0009765625	1
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value			
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	-0.360000014 0.600000024	-0.360000014 ± 0.0009765625 0.600000024 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.16 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-4.5		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt 1.36000001		
k_SumLimPlCmpLimit_MtrNm_f32			
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.5		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	7		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.30000012		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.5		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0.69999988		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	1.79999995		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt AstLmt Per1 PowerLimitPerc Uls f32.value	0.69999988		
tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value	-2.0999999		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	3.5999999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	153.100006		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-4.5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_	f32	
	tgt AstLmt Per1 AssistEOTDamping		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered	I_UIs_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable	:_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cr	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_t	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_U	lls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable	_Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls	s_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_Mtr	-	
tgt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_Mtrl	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_U		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNr	_	
tgt Rte Inst Ap AstLmt.AstLmt Per1 TrqLimitMin MtrNm f32	tgt AstLmt Per1 TrqLimitMin MtrNm		
		_102	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	ad Mirklim f22	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCm		
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.699999988 ± 0.000030517578125	•
	5	5 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value			
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-5	-5 ± 0.0009765625	
		-5 ± 0.0009765625 -3.5 ± 0.0009765625	•



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✓

Test Step 2.17 (Repeat Count = 1)	In and Male		
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-1.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt 2.48000002		
k_SumLimPICmpLimit_MtrNm_f32	-1.3999998		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_AssistCmd_withMin_isz.value	-4.5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	1.70000005		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.20000005		
tgt AstLmt Per1 CombinedDamping MtrNm f32.value	-4.5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt AstLmt Per1 LrnPnCtrEnable Cnt lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	12.5		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-1		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	!	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M	trNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	·Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cn	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U	ls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_l		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_		
tgt_Rte_Inst_Ap_AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNi	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mi		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	4	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	MtrNm f32	
	tgt_AstLmt_Per1_WheelImbalanceCmd_		Bee
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	1.20000005	1.20000005 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1.20000005	-1.20000005 ± 0.0009765625	<u> </u>
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.24000001	-0.239999995 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓

Test Step 2.18 (Repeat Count = 1) Name	Input Value		
	0		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32	1.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt 3.5999999		
S_SumLimPlCmpLimit_MtrNm_f32			
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.29999995		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	1.79999995		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.80000019		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.2999995		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012		
pt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
pt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012		
pt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-1		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-8		
pt AstLmt Per1 ThermalLimitPerc Uls f32.value	0.300000012		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4.0999999		
pt AstLmt Per1 VehSpd Kph f32.value	510.899994		
t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-1.20000005		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3	2	
	tgt AstLmt Per1 AssistEOTDamping M		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32		_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_M	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_C		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_l		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_0	Cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_	lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNr	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_C	nt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_t	32	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNi	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNn	_	
gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_N	_	
pt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimitPerc Uls f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_etc_ois_i32	tgt_AstLmt_Per1_ThermalLimit_etc_ois	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt AstLmt Per1 TrqLimitMin MtrNm f		
gt_Rte_Inst_Ap_AstLint.AstLint_Per1_InqLinitt\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	tgt_AstLmt_Per1_TrqLimitwiii1_MtrNiii_i tgt_AstLmt_Per1_VehSpd_Kph_f32	<u> </u>	
		Mtrhlm f22	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd		-
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.30000012	0.300000012 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2.0999999	2.0999999 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	1.92000008	1.91999996 ± 0.0009765625	
		0.540000004 + 0.0000705005	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.540000021	0.540000021 ± 0.0009765625	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.19 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32	-7		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
S_SumLimPlCmpLimit_MtrNm_f32	4.71999979		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3.2999995		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.89999976		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	1.8999998		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.5		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.4000001		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	3.2999995		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.40000006		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
yt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.40000006		
pt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-2		
pt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	1.20000005		
pt AstLmt Per1 ThermalLimitPerc Uls f32.value	0.400000006		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4.19999981		
t_AstLmt_Per1_VehSpd_Kph_f32.value	425.700012		
t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-2.0999999		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3	22	
	tgt AstLmt Per1 AssistEOTDamping M		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32		-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNi		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_M	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_C	·	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_l	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm	1_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_0	Cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_	_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNi	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_C	Cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_	f32	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN	m_f32	
gt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm		
at Rte Inst Ap AstLmt.AstLmt Per1 PullCompCmd MtrNm f32	tgt AstLmt Per1 PullCompCmd MtrNn	_	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_N	_	
pt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimitPerc Uls f32	tgt AstLmt Per1 ThermalLimitPerc Uls		
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_	_	
pt Rte Inst Ap AstLmt.AstLmt Per1 TrqLimitMin MtrNm f32	tgt AstLmt Per1 TrqLimitMin MtrNm f		
gt_Rte_Inst_Ap_AstLint.AstLint_Fer1_TrqLinittviii_witrviii_is2 gt_Rte_Inst_Ap_AstLint.AstLint_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_TrqLimitwiii1_MtrNff1_1 tgt_AstLmt_Per1_VehSpd_Kph_f32	<u></u>	
		Mirklim #22	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd		-
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.40000006	0.400000006 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	4.5	4.5 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-4.5	-4.5 ± 0.0009765625	
	4.00000007	-1.79999995 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-1.80000007	-1.79999993 I 0.0009703023	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	•

Test Step 2.20 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	7		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
_SumLimPlCmpLimit_MtrNm_f32	5.84000015		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.10000002		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5.5999999		
pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001		
pt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	1.3999998		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	0		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
pt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	3.29999995		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
pt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5		
pt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-3		
at AstLmt Per1 PullCompCmd MtrNm f32.value	2.2999995		
at AstLmt Per1 ThermalLimitPerc Uls f32.value	0.5		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4.30000019		
t_AstLmt_Per1_VehSpd_Kph_f32.value	373.299988		
yt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-4		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_N		
		_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mt	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_L		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_l	gc	
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNn	n_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_	<u>f</u> 32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_C	nt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNr	n_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm	_f32	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	m_f32	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M		
pt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimitPerc Uls f32	tgt AstLmt Per1 ThermalLimitPerc Uls		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt AstLmt Per1 ThermalLimit MtrNm		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3		
pt Rte Inst Ap AstLmt.AstLmt Per1 VehSpd Kph f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
pt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt AstLmt Per1 WheelImbalanceCmd	MtrNm f32	
			De-
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	6.8499999	6.8499999 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-6.8499999	-6.8499999 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0	0 ± 0.0009765625	
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0	0 ± 0.0009765625	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓

Fest Step 2.21 (Repeat Count = 1)	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-6		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
_SumLimPlCmpLimit_MtrNm_f32	6.96000004		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	3.2999995		
pt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-1		
pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.200000003		
pt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2.0999999		
pt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	1.5		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.80000019		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-1		
pt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	3.29999995		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-4		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3.4000001		
t_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4.4000001		
t_AstLmt_Per1_VehSpd_Kph_f32.value	324.600006		
t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-5		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_N		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNi	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_M		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_C		
pt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32	tgt_AstLmt_Per1_LimitPercentFiltered_I		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrCCDisable (_	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_		
	tgt_AstLmt_Per1_tmPnCtrTCmd_MtrNi	•	
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32		_	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_C		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_UIs_f32	tgt_AstLmt_Per1_PowerLimitPerc_UIs_		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNn	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	_	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_N		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls	_	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd	_MtrNm_f32	
lame	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	
A ASILITI FELL LITTILFEICENTFILETEU DIS 132.VAIDE			
	3.29999995	3,29999995 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3.29999995 3.29999995	3.29999995 ± 0.0009765625 3.29999995 ± 0.0009765625	
	3.2999995 3.2999995 1.9800002	3.29999995 ± 0.0009765625 3.29999995 ± 0.0009765625 1.98000002 ± 0.0009765625	



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓

Name	
Section Sect	
Astlum! Scientinp-Assillation Display Disp	
Ris_Inst_Ap_AstLmt	
SumLimPCropLimi Mirkin, 132 gat. Ast.mir, Pert, AssistErOT Mirkin, 132 value gat. Ast.mir, Pert, AssistSulatini, Mirkin, 132 value gat. Ast.mir, Pert, AssistSulatini, Mirkin, 132 value gat. Ast.mir, Pert, Defeat. Irmited Review (and in the second seco	
got Ast.ml Pert _ TSMICOmmand_MitNim_152 value	
g. AstLm. Pert _AssistCOT (MYNn_ 152 value 5.599999 g. AstLm. Pert _AssistCOT (Comp. MYNn_ 152 value 2 2 2 2 2 2 2 2 2	
gl. Aslt.mt Pert AssistEOTDamping MRNm, 152 value 2 2 2 2 2 2 2 2 2	
Q. AstLim, Perl. AssistEOTGain, Us. 132 value 2 20000005	
Ast.ml. Perl	
Jastimil Pert AssistiveNpdit imil, Mrkhm, 132 value 5.5	
Ast.mt Peri AssistVehSpdLimit MtrNm 52 value 2	
AstLmt_Per1_CombinedDamping_MirNm_132 value 2 2 3,2999995 3 3,2999995 3 3,2999995 3 3,2999995 3 3,2999995 3 3,2999995 3 3,2999995 3 3,2999995 3 3,2999995 3 3,2999995 3 3,2999995 3 3,2999995 3 3,2999995 3 3,2999995 3 3,2999995 3 3,2999995 3 3,2999999 3 3,2999999 3 3,2999999 3 3,2999999 3 3,2999999 3 3,2999999 3 3,2999999 3 3,2999999 3 3,2999999 3 3,2999999 3 3,2999999 3 3,2999999 3 3,2999999 3 3,2999999 3 3,29999999 3 3,29999999 3 3,29999999 3 3,29999999 3 3,29999999 3 3,29999999 3 3,299999999 3 3,2999999999 3 3,299999999999 3,2999999999999999999999999999999999999	
gl AstLIM_Pert_LimitedRetum_MrtNin_f32_value 0 gl AstLIM_Pert_LimitedRetum_MrtNin_f32_value 3 gl AstLIM_Pert_LimitedRetum_MrtNin_f32_value 1 gl AstLIM_Pert_LimitedRetum_MrtNin_f32_value 0 gl AstLIM_Pert_ImitedToChosable_Cnt_ligo_value 5 gl AstLIM_Pert_OptTroOv_MitNim_f32_value 5 gl AstLIM_Pert_OptTroOv_MitNim_f32_value 1 gl AstLIM_Pert_PosSexCCDSable_Cnt_ligo_value 1 gl AstLIM_Pert_PowerLiminPerc_Uis_f32_value 0 gl AstLIM_Pert_PrixAssistCom_MitNim_f32_value 5 gl AstLIM_Pert_PrixAssistCom_MitNim_f32_value 45 gl AstLIM_Pert_ThermalLimitPerc_Uis_f32_value 0 gl AstLIM_Pert_ThermalLimitPerc_Uis_f32_value 45 gl AstLIM_Pert_VerbSpx_fxb, f32_value 45 gl AstLIM_Pert_NetibilitanceCom_MitNim_f32_value 45 gl Rie_Inst_Ap_AstLIM_AstLIM_Pert_AssistCom_MitNim_f32 tgt_AstLIM_Pert_AssistCom_MitNim_f32_value gl Rie_Inst_Ap_AstLIM_AstLIM_Pert_AssistCom_MitNim_f32_ tgt_AstLIM_Pert_AssistCom_MitNim_f32_value gl Rie_Inst_Ap_AstLIM_AstLIM_Pert_AssistCom_MitNim_f32_ tgt_AstLIM_Pert_AssistCom_MitNim_f32_ gl Rie_Inst_Ap_AstLIM_AstLIM_Pert_AssistCom_MitNim_f32_ tg	
J. AstLmt. Perl _ LimitedRetum_MtrNm_f32.value 3.29999995 J. AstLmt. Perl _ LimPriCtrCDisable _ Cnt. gevalue 1 J. AstLmt. Perl _ LimPriCtrCDisable _ Cnt. gevalue 0 J. AstLmt. Perl _ LimPriCtrCrind_ MtrNm_f32.value 5.599999 J. AstLmt. Perl _ LimPriCtrCrind_ MtrNm_f32.value 1 J. AstLmt. Perl _ Dovignov_MtrNm_f32.value 0.69999988 J. AstLmt. Perl _ PosServ. CCDisable _ Cnt. gevalue 1 J. AstLmt. Perl _ PosServ. CCDisable _ Cnt. gevalue 1 J. AstLmt. Perl _ PosServ. CCDisable _ Cnt. gevalue 1 J. AstLmt. Perl _ PosServ. CCDisable _ Cnt. gevalue 1 J. AstLmt. Perl _ PrkAssistCornd_MtrNm_f32.value 5 J. AstLmt. Perl _ PrkAssistCornd_MtrNm_f32.value 4.5 J. AstLmt. Perl _ Posterv. Development _ J. AstLmt. Perl _ Townshilm. MtrNm_f32.value 4.5 J. AstLmt. Perl _ ThermalLimiRtryc. Uls _ f32.value 4.5 J. AstLmt. Perl _ ThermalLimiRtryc. Uls _ f32.value 4.5 J. AstLmt. Perl _ NeighbalanceCord_MtrNm_f32.value 4.5 J. AstLmt. Perl _ NeighbalanceCord_MtrNm_f32.value 4.5 J. AstLmt. Perl _ Vhospl _ Kph. T. AssistCord_MtrNm_f32 J. Rie. Inst. Ap. AstLmt. AstLmt. Perl _ AssistEOTGam_Uls _ f32 J. Rie. Inst. Ap. AstLmt. AstLmt. Perl _ AssistEOTGam_Uls _ f32 J. Rie. Inst. Ap. AstLmt. AstLmt. Perl _ AssistEOTGam_Uls _ f32 J. Rie. Inst. Ap. AstLmt. AstLmt. Perl _ AssistEOTGam_Uls _ f32 J. Rie. Inst. Ap. AstLmt. AstLmt. Perl _ AssistEOTGam_Uls _ f32 J. Rie. Inst. Ap. AstLmt. AstLmt. Perl _ AssistEOTGam_Uls _ f32 J. Rie. Inst. Ap. AstLmt. AstLmt. Perl _ AssistEOTGam_Uls _ f32 J. Rie. Inst. Ap. AstLmt. AstLmt. Perl _ AssistEOTGam_Uls _ f32 J. Rie. Inst. Ap. AstLmt. AstLmt. Perl _ AssistEOTGam_Uls _ f32 J. Rie. Inst. Ap. AstLmt. AstLmt. Perl _ AssistEOTGam_Uls _ f32 J. Rie. Inst. Ap. AstLmt. AstLmt. Perl _ Development _ MtrNm_ f32 J. Rie. Inst. Ap. AstLmt. AstLmt. Perl _ LombiceCobable_ Cnt. g0 J. Rie. Inst. Ap. AstLmt. AstLmt. Perl _ LombiceCobable_ Cnt. g0 J. Rie. Inst.	
1, AstLmt_Perl_LmPnCtrCoDisable_Cnt_lgcvalue 1 0 0 0 0 0 0 0 0 0	
J. AstLmT. Pert LmPhCritTcnde Cnt Igc. value 5.599999 J. AstLmT. Pert LmPhCritTcnde Mixhm 132 value 5.5999999 J. AstLmt. Pert OutpurRampMilt, Uls 132 value 1	
AstLmt Per1 LmPnCtrTCmd MtrNm_f32 value	
J. AstLmt. Pert OptrqOv_MtrNm_f32 value 0.699999988 1. J. AstLmt. Pert OrtputRampMult_Uis_182 value 0.699999988 1. J. AstLmt. Pert PosservcCDisable_Cnt_Ug. value 1. J. AstLmt. Pert PosservcCDisable_Cnt_Ug. value 1. J. AstLmt. Pert PowerLmmtPerc_Uis_182 value 0.699999988 1. J. AstLmt. Pert PowerLmmtPerc_Uis_182 value 0.699999988 1. J. AstLmt. Pert PowerLmmtPerc_Uis_182 value 0.699999988 1. J. AstLmt. Pert ThermalLimitPerc_Uis_182 value 0.699999988 1. J. AstLmt. Pert ThermalLimit MrNm_f32 value 0.699999988 1. J. AstLmt. Pert ThermalLimit MrNm_f32 value 4.5 0.699999988 1. J. AstLmt. Pert ThermalLimit MrNm_f32 value 4.5 0.699999988 1. J. AstLmt. Pert VenSpd_Kpn_F32 value 4.5 0.699999988 1. J. AstLmt. Pert ThermalLimit MrNm_f32 value 4.5 0.699999988 1. J. AstLmt. Pert VenSpd_Kpn_F32 value 4.5 0.699999988 1. J. AstLmt. Pert ThermalLimit MrNm_f32 value 4.5 0.699999988 1. J. AstLmt. Pert ThermalLimit MrNm_f32 value 4.5 0.699999988 1. J. Rel. Inst. Ap_AstLmt. AstLmt. Pert AssistEOTIO MrNm_f32 0.699999988 1. J. Rel. Inst. Ap_AstLmt. AstLmt. Pert AssistEOTIO MrNm_f32 0.6999999988 1. J. Rel. Inst. Ap_AstLmt. AstLmt. Pert_ AssistEOTIO MrNm_f32 0.699999999988 1. J. Rel. Inst. Ap_AstLmt. AstLmt. Pert_ AssistEOTIO MrNm_f32 0.69999999988 1. J. Rel. Inst. Ap_AstLmt. AstLmt. Pert_ AssistEOTIO MrNm_f32 0.6999999988 1. J. Rel. Inst. Ap_AstLmt. AstLmt. Pert_ AssistEOTIO MrNm_f32 0.6999999999999999999999999999999999999	
31, AstLmt, Pert PosSenvCCDisable, Cnt. Jgc. value 1	
1, AstLmt_Per1 PosservCCDisable_Cnt_lgc_value 1	
AstLmt Perf PowerLimitPerc Uis	
AstLmt_Pert_PowerLimitPerc_Uis_r32.value 0.699999988 AstLmt_Pert_PrkAssistComd_MirNm_r32.value 5	
31, AstLmt_Per1_ThermalLimitPerc_Uls_f32.value 0.689999988	
AstLmt Per1_ThermalLimitPerc_UIs_f32.value	
AstLmt_Per1_ThermalLimitPerc_Uis_f32.value 4.5 AstLmt_Per1_ThermalLimit_MtrNm_f32.value 4.5 AstLmt_Per1_VehSpd_Kph_f32.value 4.5 AstLmt_Per1_WehSpd_Kph_f32.value 4.5 AstLmt_Per1_WheelimbalanceCmd_MtrNm_f32 4.5 AstLmt_Per1_WheelimbalanceCmd_MtrNm_f32 4.5 AstLmt_Per1_WheelimbalanceCmd_MtrNm_f32 4.5 AstLmt_Per1_AssistComd_MtrNm_f32 4.5 AstLmt_Ap_AstLmt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 4.5 AstLmt_Ap_AstLmt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 4.5 AstLmt_Ap_AstLmt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 4.5 AstLmt_Ap_AstLmt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 4.5 AstLmt_Ap_AstLmt_AstLmt_Per1_AssistSotLimit_MtrNm_f32 4.5 AstLmt_Ap_AstLmt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 4.5 AstLmt_Ap_AstLmt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 4.5 AstLmt_Ap_AstLmt_AstLmt_Per1_CombinedDamping_MtrNm_f32 4.5 AstLmt_Ap_AstLmt_AstLmt_Per1_DefeatLimitService_Cnt_Igo 4.5 AstLmt_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uis_f32 4.5 AstLmt_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uis_f32 4.5 AstLmt_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uis_f32 4.5 AstLmt_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uis_f32 4.5 AstLmt_Ap_AstLmt_AstLmt_Per1_LimPnCtrCCDisable_Cnt_Igo 4.5 AstLmt_Ap_AstLmt_AstLmt_Per1_LimPnCtrCDisable_Cnt_Igo 4.5 AstLmt_Ap_AstLmt_AstLmt_Per1_LimPnCtrCDisable_Cnt_Igo 4.5 AstLmt_Ap_AstLmt_AstLmt_Per1_UirPnCirtCmd_MtrNm_f32 4.5 AstLmt_Ap_AstLmt_AstLmt_Per1_Dirpovr_MtrNm_f32 4.5 AstLmt_Ap_AstLmt_AstLmt_Per1_Dirpovr_MtrNm_f32 4.5 AstLmt_Ap_AstLmt_AstLmt_Per1_Dirpovr_MtrNm_f32 4.5 AstLmt_Per1_Dirpovr_MtrNm_f32 4.5 AstLmt_Per1_Dirpovr_MtrNm_f32 4.5 AstLmt_Per1_Dirpovr_MtrNm_f32 4.5 AstLmt_Per1_Dirpovr_MtrNm_f32 4.5 AstLmt_Per1_Dirpovr_MtrNm_f32 4.5 AstLmt_Per1_PreLimitTorque_MtrNm_f32 4.5 AstLmt_Per1_PreLimitTorque_MtrNm_f32 4.5 AstLmt_Per1_PreLimitTorque_MtrNm_f32 4.5 AstLmt_Per1_PreLimitTorque_Mt	
11. AstLmt_Perl_VehSpd_Kph_f32.value 12. AstLmt_Perl_VehSpd_Kph_f32.value 13. AstLmt_Perl_WheelImbalanceCmd_MtrNm_f32.value 13. Rte_Inst_Ap_AstLmtAptLmt_Perl_AssistComd_MtrNm_f32 14. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistComd_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistComd_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistEOTDamping_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistEOTDamping_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistStOTGain_Uls_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistStoTlLimit_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistStallLimit_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistStallLimit_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistStallLimit_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_AssistStallLimit_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_CombinedDamping_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_DefeatLimitService_Cnt_Igc 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_DefeatLimitService_Cnt_Igc 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_LimitedRetur_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_LimitedRetur_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_LimPrCtrCoDisable_Cnt_Igc 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_LimPrCtrCoDisable_Cnt_Igc 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_LimPrCtrCoDisable_Cnt_Igc 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_LimPrCtrTCmd_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_LimPrCtrTCmd_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_Coptro_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_Optrqov_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_Optrqov_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_Optrqov_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_Optrqov_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_PowerLimitForcStall_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_PowerLimitForcStall_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_PowerLimitForcStall_MtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_PreLimitForgMtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_PreLimitForgMtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_PreLimitForgMtrNm_f32 15. Rte_Inst_Ap_AstLmtAstLmt_Perl_PreLimitForgMtrNm_f32 15. Rte_	
ti, AstLmt, Perl_VhRopd, Kph_f32.value ti, AstLmt, Perl_VhRoellmbalanceCmd_MtrNm_f32.value ti, CastLmt, Perl_VhRoellmbalanceCmd_MtrNm_f32.value ti, CastLmt, Ap. AstLmt, Perl_AssistEOTDamping_MtrNm_f32 tip, Rte_Inst_Ap_AstLmt, Perl_AssistStallLimit, MtrNm_f32 tip, Rte_Inst_Ap_AstLmt, Perl_AssistStallLimit, MtrNm_f32 tip, Rte_Inst_Ap_AstLmt, Perl_AssistStallLimit, MtrNm_f32 tip, Rte_Inst_Ap_AstLmt, Perl_CombinedDamping_MtrNm_f32 tip, Rte_Inst_Ap_AstLmt, Perl_DefeatLimitService_Cnt_Igc tip, Rte_Inst_Ap_AstLmt, Perl_DefeatLimitService_Us_f32 tip, Rte_Inst_Ap_AstLmt, Perl_LimitedRetur_MtrNm_f32 tip, Rte_Inst_Ap_AstLmt, Perl_LimPnCtrCObiable_Cnt_Igc tip, Rte_Inst_Ap_AstLmt, Perl_LimPnCtrCobiable_Cnt_Igc tip, Rte_Inst_Ap_AstLmt, Perl_LimPnCtrTCmd_MtrNm_f32 tip, Rte_Inst_Ap_AstLmt, Perl_DefeatLimitForcut, tip, f32 tip, Rte_Inst_Ap_AstLmt, Perl_PosservCObiasble_Cnt_Igc tip, Rte_Inst_Ap_AstLmt, Perl_PosservCObiasble_Cnt_Igc tip, Rte_Inst_Ap_AstLmt, Perl_PreLimitForcut, tip, f32 tip, Rte_Inst_Ap_AstLmt, Perl_PreLimitForcut, tip, f32 tip, Rte_Inst_Ap_AstLmt, Perl_PreLimitForcut, tim, f32 tip, Rte_Inst_Ap_AstLmt, Perl_PreLimitForcut, tip,	
It AstLmt Perl WheelinbalanceCmd MtrNm f32 value It Rie Inst. Ap. AstLmt. AstLmt. Perl _AssistComd_MtrNm_f32 It Rie Inst. Ap. AstLmt. AstLmt. Perl _AssistEOTDamping_MtrNm_f32 It Rie Inst. Ap. AstLmt. AstLmt. Perl _AssistEOTDamping_MtrNm_f32 It Rie Inst. Ap. AstLmt. AstLmt. Perl _AssistEOTDamping_MtrNm_f32 It Rie Inst. Ap. AstLmt. AstLmt. Perl _AssistEOTDaminum, MtrNm_f32 It Rie Inst. Ap. AstLmt. AstLmt. Perl _AssistEOTDaminum, MtrNm_f32 It Rie Inst. Ap. AstLmt. AstLmt. Perl _AssistEOTLimit_MtrNm_f32 It Rie Inst. Ap. AstLmt. AstLmt. Perl _AssistEOTLimit_MtrNm_f32 It Rie Inst. Ap. AstLmt. AstLmt. Perl _AssistEOTLimit_MtrNm_f32 It Rie Inst. Ap. AstLmt. AstLmt. Perl _AssistSidall_init_MtrNm_f32 It Rie Inst. Ap. AstLmt. AstLmt. Perl _DefeatLimitService_Cnt. Igc It Rie Inst. Ap. AstLmt. AstLmt. Perl _DefeatLimitService_Cnt. Igc It Rie Inst. Ap. AstLmt. AstLmt. Perl _LimitePrecentFiltered_UIs_f32 It Rie Inst. Ap. AstLmt. AstLmt. Perl _LimiteRecture_UIs_f32 It Rie Inst. Ap. AstLmt. AstLmt. Perl _DeftcTCotlable_Cnt. Igc It Rie Inst. Ap. AstLmt. AstLmt. Perl _DeftcTCotlable_Cnt. Igc It Rie Inst. Ap. AstLmt. AstLmt. Perl _DeftcTCotlable_Cnt. Igc It Rie Inst. Ap. AstLmt. AstLmt. Perl _DeftcTCotlable_Cnt. Igc It Rie Inst. Ap. AstLmt. AstLmt. Perl _DeftcTCotlable_Cnt. Igc It Rie Inst. Ap. AstLmt. AstLmt. Perl _PoservCotlable_Cnt. Igc It Rie Inst. Ap. AstLmt. AstLmt. Perl _PreLimitForStall_MtrNm_f32 It Rie Inst. Ap. AstLmt. AstLmt. Perl _PreLimitForStall_MtrNm_f32 It Rie Inst. Ap. AstLmt. AstLmt. Perl _PreLimitForGotlall_MtrNm_f32 It Rie Inst. Ap.	
tgt. Rite_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_AstLmt_Per1_AssistOmd_MtrNm_f32 tgt_AstLmt_Per1_AssistOmd_MtrNm_f32 tgt_AstLmt_Per1_AssistOmd_MtrNm_f32 tgt_AstLmt_Per1_AssistOmd_MtrNm_f32 tgt_AstLmt_Per1_AssistOmd_MtrNm_f32 tgt_AstLmt_Per1_AssistOmd_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc tgt_AstLmt_Per1_LimitPercentFiltered_Uis_f32 tgt_AstLmt_Per1_DoTTcCODisable_Cnt_lgc tgt_AstLmt_Per1_DoTTcCCDisable_Cnt_lgc tgt_AstLmt_Per1_OptTqOv_MtrNm_f32 tgt_AstLmt_Per1_OptTqOv_MtrNm_f32 tgt_AstLmt_Per1_OptTqOv_MtrNm_f32 tgt_AstLmt_Per1_PosservCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosservCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosservCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosservCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosservCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosservCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosservCCDisable_Cnt_lgc tgt_	
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistEOTCain_Uts_f32 tgt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc tgt_AstLmt_Per1_LimitPerceutFiltered_Uts_f32 tgt_AstLmt_Per1_DeftTCmd_MtrNm_f32 tgt_AstLmt_Per1_DeftTCmd_MtrNm_f32 tgt_AstLmt_Per1_DeftTCmd_MtrNm_f32 tgt_AstLmt_Per1_DeftTCmd_MtrNm_f32 tgt_AstLmt_Per1_DeftTCmd_MtrNm_f32 tgt_AstLmt_Per1_DeftTCmd_MtrNm_f32 tgt_AstLmt_Per1_DeftTCmd_MtrNm_f32 tgt_AstLmt_Per1_PoservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PoservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PoservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PoservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PoservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PoservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PoservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PoservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PoservCCDisable_Cnt_Igc tgt_AstLmt_Per1_PoservCD	
gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistEOTGain_UIs_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistEOTLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistEOTLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistStallLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistStallLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistStallLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_AssistStallLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_DefeatLimitService_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_DefeatLimitService_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimitPercentFiltered_UIs_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimitedReturn_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimitedReturn_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimitedReturn_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimPnCtrCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimPnCtrCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_LimPnCtrCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_DeftrTCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_OpTrqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_OpTrqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_OpTrqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_DescrotCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_DescrotCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_DescrotCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PoservCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PoservCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PoservCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForcStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForcMill_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForcMill_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForcMill_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForcMill_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForcMill_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForcMill_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForcMill_MtrNm_f32 gt_Rte_Inst_Ap_AstLmtAstLmt_Per1_PreLimitForcMill_MtrNm_f32 gt_Rte_Inst_Ap_AstL	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPerc_Uls_f32 tgt_AstLmt_Per1_LimitPercEort_Igc tgt_AstLmt_Per1_LimitPerc_Uls_f32 tgt_AstLmt_Per1_LimitPerc_Uls_f32 tgt_AstLmt_Per1_LimitPerc_Uls_f32 tgt_AstLmt_Per1_LimitPerc_Uls_f32 tgt_AstLmt_Per1_LimitPerc_Uls_f32 tgt_AstLmt_Per1_DopTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_DopTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_DopTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_PoservLimitPerc_Uls_f32 tgt_AstLmt_Per1_PoservLimitForsLall_MtrNm_f32 tgt_AstLmt_Per1_PoservLimitForsLall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForsque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForsque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForsque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForsque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForsque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForsque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForsdue_MtrNm_f32 tgt_AstLmt_Per1_SumLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TsmallLimitPerc_Uls_f32 tgt_AstLmt_Per1_TsmallLimitPero_Uls_f	
tgt_AstLmt_Ap_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Ap_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt_AstLmt_Ap_AstLmt_Ap_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_DefeatLimitService_Cnt_Igc tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_DirtrCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCCDisable_Cnt_Igc tgt_AstLmt_Per1_PosServCCDisable_Cnt_Igc tgt_AstLmt_Per1_PreLimitForCall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForCall_MtrNm_f	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_DefeatLimitService_Cnt_Igc tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_DefeatLimitService_Cnt_Igc tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_Derfection_MtrNm_f32 tgt_AstLmt_Per1_Derfectio	
tgt_AstLmt_AstLmt_Per1_CombinedDamping_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimiteReturn_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimiteReturn_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimiteReturn_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCTDisable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCTDisable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCTDisable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DpTrqCvv_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqCvv_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqCvv_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OptrqCvb_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitForStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_Per0_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_Mtr	
tgt_Rte_Inst_Ap_AstLmt_Per1_DefeatLimitService_Cnt_Igc gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCcDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_Igc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTcmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_Optqovr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PoservCCDisable_Cnt_Igc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitForStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitForStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstL	
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DopTrqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OptrqOvr_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosservCCDisable_Cnt_lgc gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_M	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrCCDisable_Cnt_lgc tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrEnable_Cnt_lgc tgt_AstLmt_Per1_LrmPnCtrEnable_Cnt_lgc tgt_AstLmt_Per1_LrmPnCtrEnable_Cnt_lgc tgt_AstLmt_Per1_LrmPnCtrEnable_Cnt_lgc tgt_AstLmt_Per1_LrmPnCtrEnable_Cnt_lgc tgt_AstLmt_Per1_LrmPnCtrTCmd_MtrNm_f32 tgt_AstLmt_Per1_LrmPnCtrTCmd_MtrNm_f32 tgt_AstLmt_Per1_DpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OptrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OutputRampMult_Uls_f32 tgt_AstLmt_Per1_OutputRampMult_Uls_f32 tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_TormalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLm	
tgt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_DpTrqOvr_MtrNm_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitMtrNm_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitMtrNm_f32 gt_Rtte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitMtrNm_f32 gt_Rtte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitMtrNm_f32 gt_Rtte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitMtrNm_f32 gt_Rtte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitMtrNm_f32 gt_Rtte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitMtrNm_f32 gt_Rtte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitMtrNm_f32 gt_Rtte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitMtrNm_f32 gt_Rtte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitMtrNm_f32 gt_Rtte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitMtrNm_f32 gt_Rtte_Inst_Ap_AstLmt_AstLmt_Per1_T	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 tgt_AstLmt_Per1_DpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OutputRampMult_Uls_f32 tgt_AstLmt_Per1_OutputRampMult_Uls_f32 tgt_AstLmt_Per1_PosServCCDisable_Cnt_Igc tgt_AstLmt_Per1_PosServCCDisable_Cnt_Igc tgt_AstLmt_Per1_PosServCCDisable_Cnt_Igc tgt_AstLmt_Per1_PosServCCDisable_Cnt_Igc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TormalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrmPnCtrTCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OutputRampMult_Uls_f32 tgt_AstLmt_Per1_OutputRampMult_Uls_f32 tgt_AstLmt_Per1_OutputRampMult_Uls_f32 tgt_AstLmt_Per1_DosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TosMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TosMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TosMi	
tgt_Rte_Inst_Ap_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OutputRampMult_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TormalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OutputRampMult_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimi	
target_AstLmt_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimit_MtrNm_f32 target_AstLmt_Per1_ThermalLimitMtrNm_f32 target_AstLmt_Per1_ThermalLimitMtrNm_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimitMtrNm_f32 target_AstLmt_Per1_ThermalLimitMtrNm_f32 target_AstLmt_Per1_ThermalLimitMtrNm_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimitMtrNm_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimit_MtrNm_f32 target_AstLmt_Per1_ThermalLimitPerc_Uls_f32 target_AstLmt_Per1_ThermalLimit_MtrNm_f32 tar	
gt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_	
gt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32	
gt_Rte_Inst_Ap_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	
	Per
Name Actual Value Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 5.5999999 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value -1.01999986 -1.01999998 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value -0.713999867 -0.713999867 -0.713999987 ± 0.0009765625	
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value 1.60000002 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.23 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt ManualTrqCmd MtrNm M f32	-5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.0599999987		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.800000012		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-1		
tqt AstLmt Per1 AssistEOTDamping MtrNm f32.value	-3		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2.29999995		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	1.7000005		
tgt AstLmt Per1 AssistVehSpdLimit MtrNm f32.value	2.0999999		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.80000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4.5999999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	345.100006		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-7		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	?	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M	trNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNn	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M	trNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cr	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U	ls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_l	gc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cr	_ ·	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_		
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	1	1 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1	-1 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.800000012	-0.800000012 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓

Test Step 2.24 (Repeat Count = 1) Name	Input Value		
	0		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt ManualTrqCmd MtrNm M f32	5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.899999976		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.699999988		
	-0.099999966		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-Z -4		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value			
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2.4000001		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	1.79999995		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.20000005		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	8.80000019		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-3		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.899999976		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.899999976		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-7		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.899999976		
pt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4.69999981		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	234.199997		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-8		
gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	Nm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN		
		_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	<u>f</u> 32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	ltrNm f32	
Name	Actual Value	Expected Value	Res
			Nest
gt_AstLmt_Per1_LimitPercentFiltered_UIs_f32.value	0.89999976	0.899999976 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
		-8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	-1.61999989 1.79999995	-1.62 ± 0.0009765625 1.79999995 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.25 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-4		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
s_SumLimPlCmpLimit_MtrNm_f32	1.74000001		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.600000024		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-3		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-5		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2.5		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	1.8999998		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.2999995		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	0		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-3		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.100000001		
gt AstLmt Per1 PrkAssistCmd MtrNm f32.value	-8		
gt AstLmt Per1 PullCompCmd MtrNm f32.value	7		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.100000001		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4.80000019		
t_AstLmt_Per1_VehSpd_Kph_f32.value	123.800003		
st_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	1.20000005		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr1	ulm f32	
		NIII_132	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	ra a	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_1		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt	- ×	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_t	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	<u>lgc</u>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_	<u>f</u> 32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_Venopd_rpn_i32	trNm f32	
			Pos
Name	Actual Value	Expected Value	Resi
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.100000001	0.100000001 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3	3 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-3	-3 ± 0.0009765625	
	0.000000040	0.000000046 : 0.000075	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	-0.300000012 1.8999998	-0.300000012 ± 0.0009765625 1.89999998 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.26 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt ManualTrqCmd MtrNm M f32	0 4		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	2.57999992		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.5		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-4		
tqt AstLmt Per1 AssistEOTDamping MtrNm f32.value	-6		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2.599999		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.4000001		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	1.5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	1		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	4.9000001		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	500.700012		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	2		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	N. FOO	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	-	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_UIs_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_ tgt_AstLmt_Per1_AssistStallLimit_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLlllll_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr1		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f	732	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	/ltrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	4	4 ± 0.0009765625	•
	3.77999973	3.77999997 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	*********		



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓

Test Step 2.27 (Repeat Count = 1) Name	Input Value	
	•	
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1	
AstLmt_ManualTrqCmd_MtrNm_M_f32	-3	
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1	
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt	
s_SumLimPlCmpLimit_MtrNm_f32	3.42000008	
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.400000006	
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-5	
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-7	
pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012	
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2.70000005	
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.0999999	
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.5	
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-2.5999999	
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1	
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4	
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0	
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1	
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-5	
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6	
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.30000012	
pt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0	
pt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.30000012	
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	2	
at AstLmt Per1 PullCompCmd MtrNm f32.value	-1.20000005	
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.30000012	
yt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	5	
t_AstLmt_Per1_VehSpd_Kph_f32.value	400.899994	
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	
gt_rtte_mst_Ap_Astentt.Astentt_ren_relimit orotan_withtin_isz	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Actual Value Expected Value	Resi
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Actual Value 0.300000012 0.300000012 Expected Value	Resi
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Actual Value 0.300000012 0.300000012 5 \$ ± 0.00009765625	Resi
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheellmbalanceCmd_MtrNm_f32 Actual Value 0.300000012 0.300000012 0.300000012 5 5 ± 0.00009765625 -5 ± 0.0009765625	Resi
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Actual Value 0.300000012 0.300000012 5 \$ ± 0.00009765625	Res



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~	

Test Step 2.28 (Repeat Count = 1) Name	Input Value		
	0		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	3		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	4.26000023		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.300000012		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_AssistEOTGain_UIs_f32.value	0.899999976		
tgt AstLmt Per1 AssistEOTLimit MtrNm f32.value	2.79999995		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.20000005		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.5999999		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt AstLmt Per1 PowerLimitPerc Uls f32.value	0.40000006		
tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value	3		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.40000006		
tgt AstLmt Per1 ThermalLimit MtrNm f32.value	5.0999999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	300.5		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	4		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mi		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U	ls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	<u>f</u> 32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Ci	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lo	gc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_t	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cr	ıt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	ı_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	<u>f</u> 32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	<u>f</u> 32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3.	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.40000006	0.400000006 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7.2000029	7.19999981 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	7.20000029	7.19999981 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.880000055	0.879999995 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2.20000005	2.20000005 ± 0.0009765625	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓	

Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-2		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPICmpLimit_MtrNm_f32	5.0999999		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.200000003		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	1		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2.9000001		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.29999995		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.70000005		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-7 8		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	-8 0.5		
tgt_AstLmt_Per1_OutputRampMuit_Ois_132.value tgt_AstLmt_Per1_PosServCCDisable_Cnt_Igc.value	0.5		
tgt_AstLmt_Per1_Posservocdbisable_Crit_igc.value	0.5		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	5.19999981		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	200.300003		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	rNm f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_UI	s_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg	IC .	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
tgt_Rte_Inst_Ap_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	target_AstLmt_Per1_TSMitCommand_Mt tgt AstLmt Per1 ThermalLimitPerc Uls		
tgt_Rte_inst_Ap_AstLmt.AstLmt_Peri_mermail.imitPerc_ois_is2 tgt_Rte_inst_Ap_AstLmt.AstLmt_Peri_mermail.imit_MtrNm_f32	tgt AstLmt Per1 ThermalLimit MtrNm f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrefmalLimit_MtrNm_f32	tgt AstLmt Per1 TrqLimitMin MtrNm f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitwiin_witrviin_is2 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_TrqLimitwiin_withmi_isz		
tgt_Rte_inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	MtrNm f32	
Name	Actual Value	Expected Value	Resu
	0.5	0.5 ± 0.000030517578125	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitForStail_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-7	7 ± 0.0009765625 -7 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-/ -3.5	-7 ± 0.0009765625 -3.5 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2.29999995	2.29999995 ± 0.0009765625	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓	

Test Step 2.30 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc			
AstLmt_ManualTrqCmd_MtrNm_M_f32	2.25999999		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	O		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPlCmpLimit_MtrNm_f32	5.94000006		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.100000001		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	5		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	7		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	3.5		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.9000001		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	3.2999995		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	4		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.20000003		
at AstLmt Per1 PosServCCDisable Cnt Igc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.20000003		
gt AstLmt Per1 PrkAssistCmd MtrNm f32.value	-2.0999999		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-3		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003		
st_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	5.80000019		
at AstLmt Per1 VehSpd Kph f32.value	58.2000008		
: - : -	-4.5		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value		422	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping	, - -	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_UI		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_Mi	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_Mt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService	_Cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltere	d_Uls_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_Mtrl	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable	e_Cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_C	nt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_Mt	rNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_l	Jls_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_U		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_M		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_Mi	-	
gt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt_AstLmt_Per1_PrkAssistCmd_Mtr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_Mtr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_N		
pt Rte Inst Ap AstLmt.AstLmt Per1 TSMitCommand MtrNm f32	target_AstLmt_Per1_TSMitCommand	_	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_		
t Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimit MtrNm f32	tgt_AstLmt_Per1_ThermalLimit_MtrN	_	
	tgt_AstLmt_Per1_mermaiLimit_ivitiv		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32		11_102	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	and Marklan 600	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCi		
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1.20000005	-1.20000005 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.24000001	-0.239999995 ± 0.0009765625	
gt_AstEmt_Ferr_SumEliminqCmd_withVm_i32.value	0.2400001		



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓

Test Step 2.31 (Repeat Count = 1) Name	Input Value		
	0		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-1.76999998		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt k_SumLimPlCmpLimit_MtrNm_f32	tgt_Rte_Inst_Ap_AstLmt 6.78000021		
	0		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value			
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	8		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	3.5999999		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	3.4000001		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.30000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	5.9000001		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	42.5999985		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-6.5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_	MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_Mtr1		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_I		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_0		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNi		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrCCDisable Cnt Igc	tgt AstLmt Per1 LrnPnCtrCCDisable	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt AstLmt Per1 LrnPnCtrEnable Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrN		
tgt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32	tgt AstLmt Per1 OpTrgOvr MtrNm f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Ul		
	tgt_AstLmt_Per1_PosServCCDisable_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls	_ 	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_Mtr1		
tgt_Rte_Inst_Ap_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrN		
tgt_Rte_Inst_Ap_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_U	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_	<u>†</u> 32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCm	d_MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	6	6 ± 0.0009765625	,
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3	3 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓

Test Step 2.32 (Repeat Count = 1) Name	Input Value		
	· ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1.75999999		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
s_SumLimPlCmpLimit_MtrNm_f32	7.61999989		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0.100000001		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	7		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-1.20000005		
pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.89999976		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	3.70000005		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.0999999		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	3.5		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	6		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	0		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.40000006		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.40000006		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-6.5		
pt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-5		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.40000006		
yt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6		
pt_AstLmt_Per1_VehSpd_Kph_f32.value	38.099985		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3.2999995		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mti	Nm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrI		
		_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f:		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3:		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_t	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	AtrNm f32	
lame	Actual Value	Expected Value	Res
	0.400000006	· ·	IVE2
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value		0.400000006 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	
AL ASILINI PARI PRALIMITIONNIA INTINIM 137 VOLIA	-4.5	-4.5 ± 0.0009765625	
	1.0000007	4 7000000E + 0 0000705005	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	-1.80000007 3.0999999	-1.79999995 ± 0.0009765625 3.0999999 ± 0.0009765625	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~	

Test Step 2.33 (Repeat Count = 1) Name	Input Value		
	0		
AstLmt_ManualTrqCmdEn_Cnt_M_Igc AstLmt_ManualTrqCmd_MtrNm_M_32	-8.5		
AstLmt SteeringAsstDefeat Cnt M Igc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	8.46000004		
	0.20000003		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value			
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	8		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.10000001		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	3.7999995		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.20000005		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	3.5999999		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	7		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	1.5		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	3.2999995		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	3.29999995		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.0999999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	26.7000008		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	5.5999999		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm	1 f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt AstLmt Per1 AssistEOTDampin		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_U	·	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_N		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_M	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimi		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDampin		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt AstLmt Per1 LimitPercentFiltere	:	
	tgt AstLmt Per1 LimitedReturn Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32		_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisab		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_0		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_M	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_		
tgt_Rte_Inst_Ap_AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisabl	,	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_UIs_f32	tgt_AstLmt_Per1_PowerLimitPerc_U		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_N	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_N	ltrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitComman	d_MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_	_Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_Mtrl	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrN	m_f32	
	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32		Cmd MtrNm f32	
	tgt_AstLmt_Per1_WheelImbalanceC		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name			Resul
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name	Actual Value	Expected Value	Resul
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	Actual Value 0.5	Expected Value 0.5 ± 0.000030517578125	Resul
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	Actual Value 0.5 8	Expected Value 0.5 ± 0.000030517578125 8 ± 0.0009765625	Resul
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	Actual Value 0.5	Expected Value 0.5 ± 0.000030517578125	Resul



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Innut Value			
•			
·			
0			
1			
3.2999995			
5.5999999			
0.600000024			
0			
0.600000024			
5.5999999			
-7			
0.600000024			
6.19999981			
3.5			
-1			
tat AstLmt Per1 AssistCmd MtrNm f32			
tgt_AstLmt_Per1_OutputRampMult_Uls_f32			
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc			
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32			
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32			
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32			
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32			
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32			
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32			
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32			
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32			
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32			
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32			
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32			
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32			
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	Rec		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Actual Value Expected Value			
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TydLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Actual Value 0.6000000024 0.6000000024			
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TvehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Actual Value 0.600000024 3.29999995 3.29999995 2.20000000000000000000000000000000000	Resi		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TydLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Actual Value 0.6000000024 0.6000000024			
	1 3.29999995 5.5999999 0.600000024 0 0.600000024 5.5999999 -7 0.600000024 6.19999981 3.5 -1 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_AstLmt_Per1_LimPnCtrCCDisable_Cnt_lgc tgt_AstLmt_Per1_LimPnCtrEnable_Cnt_lgc tgt_AstLmt_Per1_LimPnCtrTCmd_MtrNm_f32 tgt_AstLmt_Per1_LimPnCtrTCmd_MtrNm_f32 tgt_AstLmt_Per1_LimPnCtrTCmd_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.35 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt ManualTrqCmd MtrNm M f32	-7.5		
AstLmt SteeringAsstDefeat Cnt M Igc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.959999979		
	0.40000006		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value			
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.4000001		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	3.7999995		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	4		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-8.10000038		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.30000019		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	253.600006		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-2		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3.	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrEnable Cnt Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm		
tgt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32	_102	
	tgt_AstLmt_Per1_OutputRampMult_Uls_f	22	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OutputRampMult_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc			
	tgt_AstLmt_Per1_PosServCCDisable_Cn		
tgt_Rte_Inst_Ap_AstLmt_Per1_PowerLimitPerc_UIs_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3:	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_I	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.69999988 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2.099999	2.0999999 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	
,	3.4000001	3.4000001 ± 0.0009765625	



Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~		
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~		

Test Step 2.36 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	7.5		
AstLmt_ManualTrqCmd_MtrNm_M_f32	0		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPICmpLimit_MtrNm_f32	1.32000005		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0.5		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3.2999995		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.0999999		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.5		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	3.9000001		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-2		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	1		
at AstLmt Per1 ThermalLimitPerc Uls f32.value	0.800000012		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.400001		
at AstLmt Per1 VehSpd Kph f32.value	342.100006		
	-3		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3:		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cni		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr	nt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg	jc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cn	t_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_f32	
gt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
gt Rte Inst Ap AstLmt.AstLmt Per1 SumLimTrgCmd MtrNm f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr		
gt Rte Inst Ap AstLmt.AstLmt Per1 TSMitCommand MtrNm f32	target AstLmt Per1 TSMitCommand Mt	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
pt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimit MtrNm f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
gt_Rte_Inst_Ap_AstLint.AstLint_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_Trefmat.imit_withvim_s		
		_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	Mechine \$22	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_I		
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	4.5	4.5 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
	0 0 3.5	0 ± 0.0009765625 0 ± 0.0009765625 3.5 ± 0.0009765625	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓	

Test Step 2.37 (Repeat Count = 1)	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-6.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
x_SumLimPlCmpLimit_MtrNm_f32	1.67999995		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0.600000024		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.19999981		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.5999999		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	6		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.899999976		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.89999976		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-3		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	2		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.899999976		
yt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.5		
pt_AstLmt_Per1_VehSpd_Kph_f32.value	484.799988		
gt AstLmt Per1 WheellmbalanceCmd MtrNm f32.value	-4.5		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	m f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	···_·-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_l		
		-	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_	igc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	20	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_l	gc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_t		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3:	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
	tgt_AstLmt_Per1_WheelImbalanceCmd_Mti	rNm_f32	
		T	12
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	Actual Value	Expected value	Resi
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name	Actual Value	Expected Value 0.899999976 + 0.000030517578125	Res
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32 Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.899999976	0.899999976 ± 0.000030517578125	Resi
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32 Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	0.899999976 6.5	0.899999976 ± 0.000030517578125 6.5 ± 0.0009765625	Res
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32 Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.899999976	0.899999976 ± 0.000030517578125	Res



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	~

Test Step 2.38 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	6.55999994		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPICmpLimit_MtrNm_f32	2.03999996		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0.69999988		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	3.2999995		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-1		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.60000024		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.30000019		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.70000005		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.0999999		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-5		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	3.2999995		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.100000001		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-4		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3.20000005		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.5999999		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	155.899994		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	rNm f32	
tgt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTGain Uls f32	tgt AstLmt Per1 AssistEOTGain Uls f32	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtri		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt AstLmt Per1 LimitPercentFiltered UI		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_t	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm		
gt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32		_102	
	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.100000001	0.100000001 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-5	-5 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.5	-0.5 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3.70000005	3.70000005 ± 0.0009765625	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓	

Fest Step 2.39 (Repeat Count = 1)	Input Value			
	· ·			
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0			
AstLmt_ManualTrqCmd_MtrNm_M_f32	-5.55999994			
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt			
S_SumLimPlCmpLimit_MtrNm_f32	2.4000001			
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0.800000012			
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	5.5999999			
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-2			
pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988			
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.4000001			
pt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.79999995			
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.19999981			
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	3.2999995			
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0			
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	8			
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1			
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0			
gt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32.value	-8.80000019			
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5.5999999			
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003			
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1			
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003			
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-5			
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	4			
t_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003			
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.69999981			
t_AstLmt_Per1_VehSpd_Kph_f32.value	278.600006			
pt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-6			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3	32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_l			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_	_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrN	_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_N			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N	_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_0			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNn			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrCCDisable	_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt AstLmt Per1 LrnPnCtrEnable Cnt			
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrN			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_0	— -		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNn	_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNr	_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mtrl	_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_I			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Ul	_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_	f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd	I_MtrNm_f32		
Name	Actual Value	Expected Value	Res	
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125		
gt AstLmt Per1 PreLimitForStall MtrNm f32.value	5.5999999	5.5999999 ± 0.0009765625		
	0	0 ± 0.0009765625		
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625 0 ± 0.0009765625		



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓

Test Step 2.40 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	5.5		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt SteeringAsstDefeat Cnt M lgc	0		
Rte_Inst_Ap_AstLmt	tgt Rte Inst Ap AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	2.75999999		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0.89999976		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-1		
tgt AstLmt Per1 AssistEOTDamping MtrNm f32.value	-3		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
tgt AstLmt Per1 AssistEOTLimit MtrNm f32.value	4.5		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.9000001		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.30000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.30000012		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.80000019		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	312.399994		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3.2999995		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn	— -	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Ci		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	_132	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OpTrqOVI_MtNMI_IS2	R2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt AstLmt Per1 PreLimitForStall MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForGtail_within1_i32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitPorStail_MtrNm	_	
tgt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt_AstLmt_Per1_PreLimitrorque_withtin	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
tgt Rte Inst Ap AstLmt.AstLmt Per1 SumLimTrqCmd MtrNm f32	tgt AstLmt Per1 SumLimTrqCmd MtrNi		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mi	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	2.6400001	2.6400001 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3.900001	3.9000001 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓

Test Step 2.41 (Repeat Count = 1) Name	Input Value		
	0		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-4.5		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
	tgt Rte Inst Ap AstLmt		
Rte_Inst_Ap_AstLmt k_SumLimPlCmpLimit_MtrNm_f32	3.11999989		
	1		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value			
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.89999976		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.5999999		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.4000001		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	0		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.9000001		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	328.299988		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	5.5999999		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_	MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrN	Im f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_N		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_I	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_0		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_		
tgt_Rte_Inst_Ap_AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNr		
tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt AstLmt Per1 LrnPnCtrEnable Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrN		
tgt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls		
	tgt_AstLmt_Per1_PosServCCDisable_0		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_Mtrl	-	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN		
tgt_Rte_Inst_Ap_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNi		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_		
tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimitPerc_UIs_f32	tgt_AstLmt_Per1_ThermalLimitPerc_UI	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_	†32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmo	J_MtrNm_f32	
Name	Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.40000006	0.400000006 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2	2 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4	4 ± 0.0009765625	•



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✓

Test Step 2.42 (Repeat Count = 1)	1 (2)		
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	4.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
<_SumLimPlCmpLimit_MtrNm_f32	3.48000002		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.10000002		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-3		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-5		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.6999981		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.0999999		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.5		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	1.5		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-3		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-8		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	7		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	429.399994		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	1		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	Nm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	'	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr1	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt AstLmt Per1 LimitPercentFiltered Uls	•	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LmPnCtrCCDisable_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	132	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	0	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtrl	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f:		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_M	trNm_f32	
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3	3 ± 0.0009765625	
lgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	1.5	1.5 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.75	0.75 ± 0.0009765625	
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.0999999	4.0999999 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓

Test Step 2.43 (Repeat Count = 1)	Input Value		
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-3.5		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	3.83999991		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.20000005		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_AssistEOTGain_UIs_f32.value	0.200000003		
tgt AstLmt Per1 AssistEOTLimit MtrNm f32.value	4.80000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.19999981		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.5999999		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2.5999999		
tgt AstLmt Per1 OpTrqOvr MtrNm f32.value	-4		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.60000024		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt AstLmt Per1 PowerLimitPerc Uls f32.value	0.600000024		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	1		
tgt AstLmt Per1 PullCompCmd MtrNm f32.value	8		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
tgt AstLmt Per1 ThermalLimit MtrNm f32.value	7.0999999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	52.7999992		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	2		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	1_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	·Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U	ls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	<u>f</u> 32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lo	gc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cr	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	1_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNi	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mi	trNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.60000024	0.600000024 ± 0.000030517578125	
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	2.51999998	2.51999998 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.1999981	4.19999981 ± 0.0009765625	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.44 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt ManualTrqCmd MtrNm M f32	-7		
AstLmt SteeringAsstDefeat Cnt M Igc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	4.19999981		
	1.29999995		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value			
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.4000001		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.80000019		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.19999981		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	1		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.30000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.6999981		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	37.5		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mi		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Ci		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrEnable Cnt Igc	tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	_132	
		200	
tgt_Rte_Inst_Ap_AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_t		
tgt_Rte_Inst_Ap_AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt_Per1_PowerLimitPerc_UIs_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3.	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.30000012	0.300000012 ± 0.000030517578125	
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	5.29999924	5.30000019 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	5.29999924	5.30000019 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	1.44000006	1.4400006 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.80000019	4.80000019 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✓

Test Step 2.45 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	7		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPlCmpLimit_MtrNm_f32	4.55999994		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.3999998		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	3		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.89999976		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.5		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.900001		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.30000019		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	2		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	-5		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value			
gt_AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	1 -5		
	8.80000019		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.400000019		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_Posservoodisable_Grit_igt.value	0.40000006		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	8		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-1		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.40000006		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.80000019		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	83.900015		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-1.20000005		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	lm f32	
gt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTGain Uls f32	tgt AstLmt Per1 AssistEOTGain Uls f32	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNi	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_	gc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_	_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_Mt		
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.400000006	0.400000006 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-5	-5 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-2	-2 ± 0.0009765625	
	I consider the contract of the	1	

4.9000001

tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value

4.9000001 ± 0.0009765625



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✓

Test Step 2.46 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-6		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
<_SumLimPICmpLimit_MtrNm_f32 <_SumLimPICmpLimit_MtrNm_f32	4.92000008		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.5		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	4		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	6		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.4000001		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	3		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-6		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-6		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	0		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.9000001		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	73.6999969		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-2.0999999		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_N	ltrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNi	n_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNr	n_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_M	trNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N	trNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_C	nt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_L	Jls_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_0	cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_	lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNr	n_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_C	nt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_t	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNi	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	lm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_N	ltrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f:		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd	MtrNm f32	
Name	Actual Value	Expected Value	Resi
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	i i i
gt_AstLmt_Per1_LimitPercentFiltered_dis_isz.value	4	4 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitForStall_mtrNm_t32.value gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
	0		
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5	0 ± 0.0009765625 5 ± 0.0009765625	



Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~		
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~		

Test Step 2.47 (Repeat Count = 1) Name	Input Value		
	· ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	6		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt 5.28000021		
k_SumLimPlCmpLimit_MtrNm_f32			
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.60000002		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	7		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.6999981		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.0999999		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.5		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	4		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	1.5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	500.200012		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-4.5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt AstLmt Per1 AssistEOTDamping		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_	-	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_N		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_l	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_0		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_		
tgt_Rte_Inst_Ap_AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNr	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrN	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_	MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_U	ls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmo	d MtrNm f32	
Name	Actual Value	Expected Value	Resu
	0.600000024	· ·	i vesui
tgt_AstLmt_Per1_LimitPercentFiltered_UIs_f32.value		0.600000024 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-7 -4.20000029	-7 ± 0.0009765625 -4.19999981 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5.0999999	5.0999999 ± 0.0009765625	



Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~		
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓		

Test Step 2.48 (Repeat Count = 1) Name	Input Value		
	0		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-5		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt Rte Inst Ap AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	5.63999987		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.70000005		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	6		
tgt AstLmt Per1 AssistEOTDamping MtrNm f32.value	8		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.300000012		
gt AstLmt Per1 AssistEOTLimit MtrNm f32.value	5.80000019		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.19999981		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-8		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-8		
gt AstLmt Per1 OpTrqOvr MtrNm f32.value	-2.5999999		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt AstLmt Per1 PowerLimitPerc Uls f32.value	0.69999988		
gt AstLmt Per1 PrkAssistCmd MtrNm f32.value	-4.5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-4		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.69999988		
gt AstLmt Per1 ThermalLimit MtrNm f32.value	8.10000038		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	58.200008		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-6.5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mi		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3.	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt AstLmt Per1 LimitPercentFiltered Ul	 -	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm		
gt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt AstLmt Per1 OutputRampMult Uls 1	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt AstLmt Per1 ThermalLimitPerc Uls	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3:		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_I	MtrNm_f32	
Name	Actual Value	Expected Value	Resi
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.699999988 ± 0.000030517578125	1,030
tgt_AstLmt_Per1_LimitPercentPiliereu_ois_isz.value	6	6 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitrorStail_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5.19999981	5.19999981 ± 0.0009765625	



Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~		
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓		

Test Step 2.49 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt ManualTrqCmd MtrNm M f32	5		
·	0		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc Rte Inst Ap AstLmt			
	tgt_Rte_Inst_Ap_AstLmt		
<_SumLimPlCmpLimit_MtrNm_f32 Instant_Apt mt_Part_TSMitCommand_MtrNm_f32 value			
rarget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.79999995		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.9000001		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.30000019		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.69999981		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	6		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	1		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	1		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-5		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012		
pt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.19999981		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	42.2999992		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3.2999995		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	Jm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_t	132	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN		
		_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3:		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	<u>lgc</u>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_t	732	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN	lm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_Mi	trNm f32	
lame	Actual Value	Expected Value	Res
			Kest
gt_AstLmt_Per1_LimitPercentFiltered_UIs_f32.value	0.800000012	0.800000012 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	1	1 ± 0.0009765625	
		0 . 0 0000707070	
gt_AstLmt_Perl_Fret.mintTorque_Murkin_r32.value gt_AstLmt_Perl_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Perl_TrqLimitMin_MtrNm_f32.value	0 5.30000019	0 ± 0.0009765625 5.30000019 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓

Test Step 2.50 (Repeat Count = 1) Name	Input Value		
	0		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32	-4		
AstLmt SteeringAsstDefeat Cnt M lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	6.36000013		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.8999998		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	8		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.4000001		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.80000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	7		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	2		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	2		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt AstLmt Per1 PowerLimitPerc Uls f32.value	0.899999976		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	3.2999995		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.89999976		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.30000019		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	38.4000015		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	5.5999999		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	?	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M	trNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNn	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	1_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	trNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U	ls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	<u>f</u> 32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_l	gc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	ı_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNi	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M	-	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_		
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.899999976	0.899999976 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8	8 ± 0.0009765625	,
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5.4000001	5.4000001 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.51 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32	4		
AstLmt SteeringAsstDefeat Cnt M Igc	0		
Rte_Inst_Ap_AstLmt			
k_SumLimPlCmpLimit_MtrNm_f32	tgt_Rte_Inst_Ap_AstLmt 6.71999979		
	2		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value			
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.0999999		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.5		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.9000001		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	8		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	3		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	3		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.100000001		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.100000001		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.39999962		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	26.5		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-1		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mi	rNm f32	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Ci		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm		
	tgt AstLmt Per1 OpTrqOvr MtrNm f32	_132	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32		32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_t		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cr	_ -	
tgt_Rte_Inst_Ap_AstLmt_Per1_PowerLimitPerc_UIs_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3.	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.100000001	0.100000001 ± 0.000030517578125	
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	3	3 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	3	3 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	1.5	1.5 ± 0.0009765625	
	5.5	5.5 ± 0.0009765625	



Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~		
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~		

Test Step 2.52 (Repeat Count = 1) Name	Input Value		
	· ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-3		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPICmpLimit_MtrNm_f32	7.07999992		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2.0999999		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.19999981		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	4		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	4		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	3.2999995		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
at AstLmt Per1 PosServCCDisable Cnt Igc.value	0		
pt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003		
gt AstLmt Per1 PrkAssistCmd MtrNm f32.value	-1		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-8		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.5		
at AstLmt Per1 VehSpd Kph f32.value	14.6000004		
	-2		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	N. 622	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtrl		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt	t_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgd		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt	_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_		
gt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f		
gt Rte Inst Ap AstLmt.AstLmt Per1 PullCompCmd MtrNm f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_t		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm		
gt Rte Inst Ap AstLmt.AstLmt Per1 TSMitCommand MtrNm f32	target_AstLmt_Per1_TSMitCommand_Mtr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f		
pt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimit AtrNm f32	tgt_AstLmt_Per1_ThermalLimit_erc_ois_r		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt AstLmt Per1 TrqLimitMin MtrNm f32		
gt_Rte_inst_Ap_AstLint.AstLint_Peri_inqLinitwiii_withti_is2 gt_Rte_inst_Ap_AstLint.AstLint_Peri_inqLinitwiii_withti_is2			
	tgt_AstLmt_Per1_VehSpd_Kph_f32	Itrhim f22	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N		1-
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2.0999999	2.0999999 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	



Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~		
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓		

Test Step 2.53 (Repeat Count = 1) Name	Input Value		
	· ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	3		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPICmpLimit_MtrNm_f32	7.4400006		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2.20000005		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3.2999995		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.30000019		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.69999981		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.0999999		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.30000012		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
at AstLmt Per1 PowerLimitPerc Uls f32.value	0.300000012		
gt AstLmt Per1 PrkAssistCmd MtrNm f32.value	-2		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	1		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.6000038		
yt_AstLmt_Per1_VehSpd_Kph_f32.value	38.7000008		
	-3		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value		22	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_I	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_t		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_N	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_C		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm	1_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_0	Cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_	_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrN	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_0	Cnt_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	lm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN	m_f32	
gt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt AstLmt Per1 PullCompCmd MtrNn	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrI	_	
gt Rte Inst Ap AstLmt.AstLmt Per1 TSMitCommand MtrNm f32	target_AstLmt_Per1_TSMitCommand_N	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt AstLmt Per1 ThermalLimitPerc Uls		
pt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimit AtrNm f32	tgt_AstLmt_Per1_ThermalLimit_erc_ois	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt AstLmt Per1 TrqLimitMin MtrNm f		
gt_Rte_inst_Ap_AstEmt.AstEmt_Fer1_inqEinitiviiin_withViii_is2 gt_Rte_inst_Ap_AstEmt.AstEmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	<u></u>	
		Mirklim #22	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd		1-
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.30000012	0.300000012 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	5	5 ± 0.0009765625	
		4 5 + 0 0000705005	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	1.5	1.5 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓

Test Step 2.54 (Repeat Count = 1) Name	Input Value		
	0		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-2		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt Rte Inst Ap AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	7.80000019		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2.2999995		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.899999976		
tgt AstLmt Per1 AssistEOTLimit MtrNm f32.value	6.4000001		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.80000019		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.19999981		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	6		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt AstLmt Per1 PowerLimitPerc Uls f32.value	0		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	2.2999995		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.6999981		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	36.7999992		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-4.19999981		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M	trNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mt	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U	ls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_l	дс	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cr	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNi	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_UIs_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_		
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.400000006	0.400000006 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	,
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-8.80000019	-8.80000019 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-2.32000017	-2.31999993 ± 0.0009765625	•
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5.80000019	5.80000019 ± 0.0009765625	- -



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓

Test Step 2.55 (Repeat Count = 1) Name	Input Value		
	· · · · · · · · · · · · · · · · · · ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	2		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
_SumLimPlCmpLimit_MtrNm_f32	8.15999985		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2.4000001		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	3.29999995		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-1		
pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001		
pt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.5		
pt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.9000001		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.30000019		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
pt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7		
st_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	7		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2		
pt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
pt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
t_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	1		
pt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-4		
gt AstLmt Per1 PullCompCmd MtrNm f32.value	3.4000001		
t_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.0999999		
t_AstLmt_Per1_VehSpd_Kph_f32.value	26.8999996		
t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-5		
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	Im f32	
		III_132	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	22	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_1		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f	32	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	2	
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	<u>lgc</u>	
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_t	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3	2	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_	_f32	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt AstLmt Per1 ThermalLimitPerc Uls f3		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt AstLmt Per1 ThermalLimit MtrNm f32		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
t Rte Inst Ap AstLmt.AstLmt Per1 VehSpd Kph f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
t Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt AstLmt Per1 WheelImbalanceCmd Mi	rNm f32	
lame	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	1	1 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	7	7 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	3.5	3.5 ± 0.0009765625	
	5.9000001	5.9000001 ± 0.0009765625	
t_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5.9000001	3.9000001 ± 0.0009703023	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.56 (Repeat Count = 1)	Input Value		
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	7.36000013		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-7.36000013		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	8.52000046		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2.5		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.20000003		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.5999999		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.4000001		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	3.29999995		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	8		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	4.5		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.19999981		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	34.5		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3.	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_N		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f:	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Ci		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LimitedReturn MtrNm f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LmPnCtrCCDisable_0		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrEnable Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNr	•	
tgt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_ tgt_AstLmt_Per1_PosServCCDisable_C		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	_MtrNm_f32	
Name	Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5.5999999	5.5999999 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	2.81999969	2.81999993 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	1.69199991	1.69200003 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6	6 ± 0.0009765625	٠,



Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~		
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~		

Test Step 2.57 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt ManualTrqCmd MtrNm M f32	5.36999989		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.40000006		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2.5999999		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-1		
tgt AstLmt Per1 AssistEOTDamping MtrNm f32.value	-3		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.30000012		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.6999981		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.0999999		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.5		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.699999988 7.30000019		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value tgt_AstLmt_Per1_VehSpd_Kph_f32.value	65.3000031		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_N		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNi		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_C	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_l	Jls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_0	Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_	<u>lgc</u>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNr	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_C	_ -	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_t		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_N		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_ tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_r32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Peri_irqLimitiviin_witrNm_r tgt_AstLmt_Peri_VehSpd_Kph_f32	OL .	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_venSpd_Rpn_t32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd	MtrNm f32	
			Dage
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.699999988 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	1.20000005	1.20000005 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-1.20000005 -0.840000033	-1.20000005 ± 0.0009765625 -0.839999974 ± 0.0009765625	
tgt_AstLmt_Per1_TqLimitMin_MtrNm_f32.value	6.0999999	-0.839999974 ± 0.0009765625 6.0999999 ± 0.0009765625	



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	~

Test Step 2.58 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-4.3499999		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.829999983		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2.70000005		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.80000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.19999981		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.5999999		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2.0999999		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6.69999981		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.4000001		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	25.1000004		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-1.20000005		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	rNm f32	
tgt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTGain Uls f32	tgt AstLmt Per1 AssistEOTGain Uls f32	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtrl		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_t		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_		
tgt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32		_132	
gt_Rte_inst_Ap_AstLint.AstLint_Per1_Op1rqOvi_MtiNin_is2 gt_Rte_Inst_Ap_AstLint.AstLint_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OutputRampMult_Uls_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3.		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	/ltrNm_f32	
Name	Actual Value	Expected Value	Resi
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3.62999988	3.63000011 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	3.62999988	3.63000011 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	2.90400004	2.90400004 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6.19999981	6.19999981 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.59 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	3.47000003		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
s_SumLimPlCmpLimit_MtrNm_f32	1.25999999		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2.79999995		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-3		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-5		
pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.9000001		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.30000019		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.6999981		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-2		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.899999976		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	0		
pt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	7		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.899999976		
yt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.5		
pt_AstLmt_Per1_VehSpd_Kph_f32.value	75.6999969		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-2.0999999		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	Nm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrlMtrl tgt_AstLmt_Per1_AssistVehSpdLimit_Mtrl		
		_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_UI		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_t		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn	n_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt AstLmt Per1 WheelImbalanceCmd M	AtrNm f32	
lame	Actual Value	Expected Value	Res
	0.899999976	· ·	IVE2
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value		0.899999976 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	4.5	4.5 ± 0.0009765625	
	-4.5	-4.5 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value		4.05000040 + 0.000705005	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	-4.04999971 6.30000019	-4.05000019 ± 0.0009765625 6.30000019 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓

Test Step 2.60 (Repeat Count = 1) Name	Input Value		
	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	3 35000000		
AstLmt_ManualTrqCmd_MtrNm_M_f32	2.25999999		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	-		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
<_SumLimPlCmpLimit_MtrNm_f32 Instant_Apt mt_Part_TSMitCommand_MtrNm_f32 value	1.69000006		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2.9000001		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-4		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-6		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.4000001		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.80000019		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-3		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-7		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	1.5		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.5999999		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	28.8999996		
pt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-4.5		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt AstLmt Per1 AssistEOTDamping M		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mti		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mi	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt AstLmt Per1 LimitedReturn MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt AstLmt Per1 LrnPnCtrEnable Cnt le		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt AstLmt Per1 LrnPnCtrTCmd MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	1 132	
		f2.2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cr	_ -	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNn		
gt_Rte_Inst_Ap_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	<u>f</u> 32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.699999988 ± 0.000030517578125	
gt_AstEmt_For_Emitt erecht intereu_ois_ios_value	6.19000006	6.19000006 ± 0.0009765625	
gt_AstLmt_Per1_PreLimit orotali_wtrNm_f32.value	6.19000006	6.19000006 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.619000018	0.619000018 ± 0.0009765625	
	3.010000010	5.5 100000 TO ± 0.00007 00020	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.61 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-1.76999998		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	2.11999989		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
tgt AstLmt Per1 AssistEOTLimit MtrNm f32.value	7.0999999		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.5		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.9000001		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	3.29999995		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	3.2999995		
tgt AstLmt Per1 OpTrqOvr MtrNm f32.value	-8		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.20000003		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt AstLmt Per1 PowerLimitPerc Uls f32.value	0.800000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-2.5999999		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.69999981		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	85.1999969		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_I	MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_t	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrN	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_N	ltrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N	/ltrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_C	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_	Uls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNn	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_0	Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrN	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_0	·	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mtrl	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_I	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Ul	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_t	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd	_MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	3.2999995	3.29999995 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.660000026	0.660000026 ± 0.0009765625	•
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6.5	6.5 ± 0.0009765625	- -



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.62 (Repeat Count = 1)			•
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1.75999999		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	2.54999995		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.19999981		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.5999999		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	1		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.899999976		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	3		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.80000019		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	76.5999985		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	4		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_	lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrCCDisable Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3:	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt AstLmt Per1 PosServCCDisable Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_		
tgt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_		
tgt Rte Inst Ap AstLmt.AstLmt Per1 TSMitCommand MtrNm f32	target_AstLmt_Per1_TSMitCommand_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_UIs_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_etc_ois_i32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt Rte Inst Ap AstLmt.AstLmt Per1 TrqLimitMin MtrNm f32	tgt AstLmt Per1 TrqLimitMin MtrNm f32		
tgt Rte Inst Ap AstLmt.AstLmt Per1 VehSpd Kph f32	tgt AstLmt Per1 VehSpd Kph f32		
		trNm f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_M		Per
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.89999976	0.899999976 ± 0.000030517578125	,
tot Anti mt Dord Drol imitEnrCtall MtrNm 522 value	6	6 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value			
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	2.05000019	2.04999995 ± 0.0009765625	•
		2.04999995 ± 0.0009765625 0.61500001 ± 0.0009765625 6.5999999 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✓

Test Step 2.63 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-8.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPICmpLimit_MtrNm_f32	2.98000002		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.9000001		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	1		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.89999976		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.30000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.6999981		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.0999999		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	2		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.100000001		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.40000006		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.9000001		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	425.399994		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	5.19999981		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mi		
tgt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTGain Uls f32	tgt AstLmt Per1 AssistEOTGain Uls f3.	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt AstLmt Per1 LimitPercentFiltered Ul		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr		
gt_Rte_inst_Ap_AstLmt.AstLmt_Fer1_tmFnCtrCobisable_Cnt_gc			
gt_Rte_inst_Ap_AstLmt.AstLmt_Fer1_tmFnCtrEnable_Cnt_igt gt_Rte_inst_Ap_AstLmt.AstLmt_Per1_tmFnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_132	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	200	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_1		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3:	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_I	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.40000006	0.400000006 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1	-1 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.400000006	-0.400000006 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6.69999981	6.69999981 ± 0.0009765625	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓	

Test Step 2.64 (Repeat Count = 1)	Input Value		
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	8.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPICmpLimit_MtrNm_f32	3.41000009		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.79999995		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-8		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	2		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.4000001		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.80000019		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.19999981		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-7		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	3		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.20000003		
pt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5		
at AstLmt Per1 PullCompCmd MtrNm f32.value	0		
yt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8		
t_AstLmt_Per1_VehSpd_Kph_f32.value	363.799988		
t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	6		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	Nm f32	
		_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrI		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgd		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt	_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	<u> </u>	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtrl		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt AstLmt Per1 ThermalLimit MtrNm f3.		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
at Rte Inst Ap AstLmt.AstLmt Per1 VehSpd Kph f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_M	ltrNm f32	
			Des
Name	Actual Value	Expected Value	Resu
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	3.4000001	3.4000001 ± 0.0009765625	
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6.80000019	6.80000019 ± 0.0009765625	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓	

Test Step 2.65 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32	-7.5		
AstLmt_ManualTrqCind_MtNni_M_i32 AstLmt_SteeringAsstDefeat Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	3.83999991		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.70000005		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	1		
tgt_nstEm_ eri_ testerma_wirkin_joz.value	3		
tgt AstLmt Per1 AssistEOTGain Uls f32.value	0.200000003		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.5		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6.9000001		
tgt AstLmt Per1 AssistVehSpdLimit MtrNm f32.value	7.30000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	4		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	1.5		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.10000038		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	246.100006		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mt	-	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mi		
tgt_Rte_Inst_Ap_AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn	_ -	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_ tgt_AstLmt_Per1_LmPnCtrCCDisable_C		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LmPnCtrCobisable_C		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32	1 132	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt AstLmt Per1 PowerLimitPerc Uls f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt Rte Inst Ap AstLmt.AstLmt Per1 PullCompCmd MtrNm f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt AstLmt Per1 SumLimTrqCmd MtrNi		
tgt Rte Inst Ap AstLmt.AstLmt Per1 TSMitCommand MtrNm f32	target_AstLmt_Per1_TSMitCommand_M	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	3	3 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-3	-3 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-1.8000007	-1.79999995 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6.9000001	6.9000001 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.66 (Repeat Count = 1)			•
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	7.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	4.26999998		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.5999999		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	2		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.5999999		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.4000001		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	1		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.400000006		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2.5999999		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.19999981		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	153.300003		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtrl	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_t	T32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_	lgc .	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt	lgc	
tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrEnable Cnt Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_		
tgt Rte Inst Ap AstLmt.AstLmt Per1 PowerLimitPerc UIs f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt Rte Inst Ap AstLmt.AstLmt Per1 PreLimitForStall MtrNm f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3		
tgt Rte Inst Ap AstLmt.AstLmt Per1 PullCompCmd MtrNm f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr1		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_UIs_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_etc_ois_i32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt Rte Inst Ap AstLmt.AstLmt Per1 TrqLimitMin MtrNm f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt Rte Inst Ap AstLmt.AstLmt Per1 VehSpd Kph f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_venSpd_Rpn_is2 tgt_AstLmt_Per1_WheelImbalanceCmd_M	trNm f32	
			Per
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.69999988 ± 0.000030517578125	,
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2	2 ± 0.0009765625	,
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	•
		0 . 0 0000705005	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	7	0 ± 0.0009765625 7 ± 0.0009765625	



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓

Test Step 2.67 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-6.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
_SumLimPlCmpLimit_MtrNm_f32	4.69999981		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.5		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	3		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5		
pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
pt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.6999981		
pt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7.0999999		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.5		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	2		
pt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-5		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-5		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	6		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5		
pt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	8		
t_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	4		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0		
r	8.30000019		
t_AstLmt_Per1_VehSpd_Kph_f32.value	84.5		
t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-1.20000005		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr1	Jm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	4III_132	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_	หว	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_t		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistOtalLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN		
		_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt	- *	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_t	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_	_f32	
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	2	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
at Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_M	trNm f32	
lame	Actual Value	Expected Value	Res
		0.5 ± 0.000030517578125	1163
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5		
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5 -5	5 ± 0.0009765625	
wh Anti-mat Dougl Dunck ImpleTourness Advanture \$000 1		-5 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	-4 7.099999	-4 ± 0.0009765625 7.0999999 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✓

Name	Input Value		
	· ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32	0 6.55999994		
AstLmt SteeringAsstDefeat Cnt M Igc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	5.13000011		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.4000001		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	4		
tgt AstLmt Per1 AssistEOTDamping MtrNm f32.value	6		
tgt AstLmt Per1 AssistEOTGain Uls f32.value	0.5		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.80000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7.19999981		
tgt AstLmt Per1 AssistVehSpdLimit MtrNm f32.value	7.5999999		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	3		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	7		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.899999976		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	1		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.39999962		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	27.7000008		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-2.0999999		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNi	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_I	gc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3.	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_Mt	rNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	1	1 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	4	4 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	7.19999981	7.19999981 ± 0.0009765625	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~	

Test Step 2.69 (Repeat Count = 1) Name	Input Value			
	1			
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32	-5.55999994			
AstLmt SteeringAsstDefeat Cnt M Igc	0			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt 5.55999994			
k_SumLimPlCmpLimit_MtrNm_f32				
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2.29999995			
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	5			
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	7			
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.60000024			
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.9000001			
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7.30000019			
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.69999981			
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	4			
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1			
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-7			
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0			
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1			
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-7			
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	8			
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.100000001			
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0			
tgt AstLmt Per1 PowerLimitPerc Uls f32.value	0.69999988			
tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value	-2.0999999			
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6			
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5			
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.5			
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	2.9000001			
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-4.5			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_	f32		
	tgt AstLmt Per1 AssistEOTDamping			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32		-		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_Mtr	_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_Mtrl			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_	_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrN	m_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable	_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cn	t_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_Mtrl	Nm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f	32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_U	ls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_	_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls	_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_Mt	rNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_Mtr	Nm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrN	_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrN	_		
tgt Rte Inst Ap AstLmt.AstLmt Per1 SumLimTrgCmd MtrNm f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mt			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target AstLmt Per1 TSMitCommand	_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_UIs_f32	tgt_AstLmt_Per1_ThermalLimitPerc_U	-		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_HtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNr	_		
tgt_Rte_Inst_Ap_AstLint.AstLint_Peri_ThermalLinit_WithNin_isz	tgt AstLmt Per1 TrqLimitMin MtrNm			
tgt_Rte_inst_Ap_AstLmt.AstLmt_Peri_TrqLimitwiin_wtiNm_is2 tgt_Rte_inst_Ap_AstLmt.AstLmt_Peri_VehSpd_Kph_f32	tgt_AstLmt_Per1_TrqLimitwiin_withmi	_102		
		d MtcNm f22		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCm			
Name	Actual Value	Expected Value	Resu	
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.699999988 ± 0.000030517578125		
	7	7 ± 0.0009765625	•	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value				
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-7	-7 ± 0.0009765625	•	
		-7 ± 0.0009765625 -0.699999988 ± 0.0009765625	•	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.70 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt ManualTrqCmd MtrNm M f32	5.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	5.98999977		
	-2.20000005		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value			
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	8		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7.400001		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.80000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.100000001		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	0		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	85.1999969		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-6.5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M	trNm f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	-	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mi	-	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrCCDisable C		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_l		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm		
	tgt AstLmt Per1 OpTrqOvr MtrNm f32	1_132	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32		f2.2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cr	_ -	
tgt_Rte_Inst_Ap_AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNn		
tgt_Rte_Inst_Ap_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	6	6 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0	0 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.71 (Repeat Count = 1) Name	Input Value			
	1			
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32	-4.5			
AstLmt SteeringAsstDefeat Cnt M lgc	0			
Rte_Inst_Ap_AstLmt	tgt Rte Inst Ap AstLmt			
k_SumLimPlCmpLimit_MtrNm_f32	6.42000008			
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value		-2.0999999		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	7			
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-1.20000005			
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012			
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.10000038			
	7.5			
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.9000001			
·	6			
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	1			
tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value				
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	1			
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0			
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1			
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	1			
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2.0999999			
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012			
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0			
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.89999976			
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-6.5			
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8			
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.20000003			
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.8000019			
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	53.4000015			
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3.29999995			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNi	_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNr			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_M	-		
tgt_Rte_Inst_Ap_AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N	ltrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_C			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_l	Jls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm	_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_0	Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_	lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNr	n_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls	_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_C	nt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_t	32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	m_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNi	m_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm	_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm	n_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	lm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M	ltrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls	_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_	f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd	MtrNm f32		
Name	Actual Value	Expected Value	Resu	
	0.899999976	0.899999976 ± 0.000030517578125	Resul	
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value				
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625		
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	1	1 ± 0.0009765625		
tot Act mt Dord Cuml imTraCond Minhlon \$20 uplus	0.200000042	0.200000012 + 0.0000705005		
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.300000012 7.5	0.300000012 ± 0.0009765625 7.5 ± 0.0009765625		



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

### ### ### ### ### ### ### ### ### ##	Rte_Inst_Ap_AstLmt 199999 999999 9999976 1999991 100000006 100000001 1999995 10000005 100000012 100000012 100000012 100000014 1999999 AstLmt_Per1_AssistCmd_MtrNm_f32 AstLmt_Per1_AssistEOTDamping_MtrNm_f32 AstLmt_Per1_AssistEOTGain_UIs_f32 AstLmt_Per1_AssistEOTLimit_MtrNm_f32 AstLmt_Per1_AssistEOTLimit_MtrNm_f32 AstLmt_Per1_AssistEOTLimit_MtrNm_f32 AstLmt_Per1_AssistStallLimit_MtrNm_f32 AstLmt_Per1_AssistStallLimit_MtrNm_f32 AstLmt_Per1_CombinedDamping_MtrNm_f32 AstLmt_Per1_DefeatLimitDervice_Cnt_Igc AstLmt_Per1_LimitPercentFiltered_UIs_f32	
### ### ### ### ### ### ### ### ### ##	999999 9999996 9999996 9999999 9999999 999999	
### ### ### ### ### ### ### ### ### ##	999999 9999996 9999996 9999999 9999999 999999	
E_Inst_Ap_AstLmt	999999 9999996 9999996 9999999 9999999 999999	
SumLimPiCmpLimit_MtrNm_f32 6.8	999999 9999996 9999996 9999999 9999999 999999	
AstLmt_Per1_AssistCmd_MtrNm_f32.value	9999999 999999 999999 999999 999999 9999	
LASILMT_Per1_AssistEOTDamping_MtrNm_f32.value	99999999999999999999999999999999999999	
LASTLMT_Per1_AssistEOTDamping_MtrNm_f32.value	99999999999999999999999999999999999999	
LAStLmt_Per1_AssistEOTGain_Uls_f32.value 0.8 t_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value 8.1* t_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value 7.5* t_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value 7.5* t_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value 0 t_AstLmt_Per1_LimitedReturn_MtrNm_f32.value 2 t_AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc.value 1 t_AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc.value 0 t_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32.value 2 t_AstLmt_Per1_DotpTqOvr_MtrNm_f32.value 4.5* t_AstLmt_Per1_OutputRampMult_Uls_f32.value 0.4* t_AstLmt_Per1_PosservCCDisable_Cnt_lgc.value 1 t_AstLmt_Per1_PosservCCDisable_Cnt_lgc.value 1 t_AstLmt_Per1_Powert.imitPerc_Uls_f32.value 0.1* t_AstLmt_Per1_Powert.imitPerc_Uls_f32.value 1.2* t_AstLmt_Per1_Powert.imitPerc_Uls_f32.value 1.2* t_AstLmt_Per1_Thermall.imit_MtrNm_f32.value 1.2* t_AstLmt_Per1_Thermall.imit_MtrNm_f32.value 1.2* t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value 1.2* t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value 1.2*	99999999999999999999999999999999999999	
LAStLmt_Per1_AssistStallLimit_MtrNm_f32.value	99999999999999999999999999999999999999	
LAStLmt_Per1_AssistStallLimit_MtrNm_f32.value 7.5 t_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value 7 t_AstLmt_Per1_CombinedDamping_MtrNm_f32.value 7 t_AstLmt_Per1_LimitedReturm_MtrNm_f32.value 0 t_AstLmt_Per1_LimitedReturm_MtrNm_f32.value 1 t_AstLmt_Per1_LrnPnCtrCDDisable_Cnt_lgc.value 1 t_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value 2 t_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value 2 t_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value 2 t_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value 2 t_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value 2 t_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value 2 t_AstLmt_Per1_PowerLimitPerc_Uls_f32.value 0.4 t_AstLmt_Per1_PowerLimitPerc_Uls_f32.value 1 t_AstLmt_Per1_PullCompCmd_MtrNm_f32.value 1.2 t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value 1.2 t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value 1.5 t_AstLmt_Per1_WehSpd_Kph_f32.value 1.2 t_AstLmt_Per1_WehSpd_Kph_f32.value 1.2 t_AstLmt_Per1_WehSpd_Kph_f32.value 1.2 t_Rel_Inst_Ap_AstLmt.AstLmt_Per1_AssistComd_MtrNm_f32 1gt_t	00000006 00000001 00000001 0000005 00000012 00000012 0000004 0000004 0000004 0000009 AstLmt_Per1_AssistCmd_MtrNm_f32 AstLmt_Per1_AssistEOTDamping_MtrNm_f32 AstLmt_Per1_AssistEOTGain_Uls_f32 AstLmt_Per1_AssistEOTLimit_MtrNm_f32 AstLmt_Per1_AssistStallLimit_MtrNm_f32 AstLmt_Per1_AssistStallLimit_MtrNm_f32 AstLmt_Per1_AssistStallLimit_MtrNm_f32 AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 AstLmt_Per1_CombinedDamping_MtrNm_f32 AstLmt_Per1_CombinedDamping_MtrNm_f32 AstLmt_Per1_CombinedDamping_MtrNm_f32 AstLmt_Per1_DefeatLimitService_Cnt_lgc AstLmt_Per1_LimitPercentFiltered_Uls_f32	
LAStLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value t_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value t_AstLmt_Per1_LimitedReturn_MtrNm_f32.value t_AstLmt_Per1_LimitedReturn_MtrNm_f32.value t_AstLmt_Per1_LrmPnCtrCCDisable_Cnt_lgc.value t_AstLmt_Per1_LrmPnCtrEnable_Cnt_lgc.value t_AstLmt_Per1_LrmPnCtrTcmd_MtrNm_f32.value t_AstLmt_Per1_LrmPnCtrTcmd_MtrNm_f32.value t_AstLmt_Per1_DoptrqOvr_MtrNm_f32.value t_AstLmt_Per1_OptrqOvr_MtrNm_f32.value t_AstLmt_Per1_OptrqOvr_MtrNm_f32.value t_AstLmt_Per1_PosservCCDisable_Cnt_lgc.value t_AstLmt_Per1_PosservCCDisable_Cnt_lgc.value t_AstLmt_Per1_PowerlimitPerc_Uls_f32.value t_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value t_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value t_AstLmt_Per1_Preff_ThermalLimitPerc_Uls_f32.value t_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value t_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistCOTDamping_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DopTrqOvr_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DopTrqOvr_MtrNm_f32 t	00000006 00000001 0999995 0000005 00000012 00000012 0000004 099999 AstLmt_Per1_AssistCmd_MtrNm_f32 AstLmt_Per1_AssistEOTDamping_MtrNm_f32 AstLmt_Per1_AssistEOTGain_Uls_f32 AstLmt_Per1_AssistEOTLimit_MtrNm_f32 AstLmt_Per1_AssistStallLimit_MtrNm_f32 AstLmt_Per1_AssistStallLimit_MtrNm_f32 AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 AstLmt_Per1_CombinedDamping_MtrNm_f32 AstLmt_Per1_CombinedDamping_MtrNm_f32 AstLmt_Per1_DefeatLimitService_Cnt_lgc AstLmt_Per1_LimitPercentFiltered_Uls_f32	
LAStLmt_Per1_DefeatLimitService_Cnt_lgc.value t_AstLmt_Per1_LimitedReturn_MtrNm_f32.value t_AstLmt_Per1_LimitedReturn_MtrNm_f32.value t_AstLmt_Per1_LmPnCtrCcDisable_Cnt_lgc.value t_AstLmt_Per1_LmPnCtrEnable_Cnt_lgc.value t_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32.value t_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32.value t_AstLmt_Per1_DopTrqOvr_MtrNm_f32.value t_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value t_AstLmt_Per1_OptutRampMult_Uls_f32.value t_AstLmt_Per1_PosServCcDisable_Cnt_lgc.value t_AstLmt_Per1_PosServCcDisable_Cnt_lgc.value t_AstLmt_Per1_PowerLimitPerc_Uls_f32.value t_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value t_AstLmt_Per1_PreflCompCmd_MtrNm_f32.value t_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value t_AstLmt_Per1_VehSpd_Kph_f32.value t_AstLmt_NetImSper1_AssistCompling_MtrNm_f32 tgt_t_Rel_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_t_Rel_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_t_Rel_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_t_Rel_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_t_Rel_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc tgt_t_Rel_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_t_Rel_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_t_Rel_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_t_Rel_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_t_Rel_Inst_Ap_AstLmt.AstLmt_Per1_DopTrCrCDisable	00000006 00000001 0999995 0000005 00000012 00000012 0000004 099999 0000004 0000004 0000004 00000000	
LAStLmt_Per1_DefeatLimitService_Cnt_Igc.value LAStLmt_Per1_LimitedReturn_MtrNm_f32.value LAStLmt_Per1_LmPnCtrCCDisable_Cnt_Igc.value LAStLmt_Per1_LmPnCtrEnable_Cnt_Igc.value LAStLmt_Per1_LmPnCtrTCmd_MtrNm_f32.value LAStLmt_Per1_LmPnCtrTCmd_MtrNm_f32.value LAStLmt_Per1_OpTrqOvr_MtrNm_f32.value LAStLmt_Per1_OpTrqOvr_MtrNm_f32.value LAStLmt_Per1_PosServCCDisable_Cnt_Igc.value LAStLmt_Per1_PosServCCDisable_Cnt_Igc.value LAStLmt_Per1_PowerLimitPerc_Uls_f32.value LAStLmt_Per1_PowerLimitPerc_Uls_f32.value LAStLmt_Per1_PowerLimitPerc_Uls_f32.value LAStLmt_Per1_PrkAssistCmd_MtrNm_f32.value LAStLmt_Per1_PlulCompCmd_MtrNm_f32.value LAStLmt_Per1_ThermalLimitPerc_Uls_f32.value LAStLmt_Per1_ThermalLimit_MtrNm_f32.value LAStLmt_Per1_ThermalLimit_MtrNm_f32.value LAStLmt_Per1_VehSpd_Kph_f32.value LAStLmt_Per1_VehSpd_Kph_f32.value LAStLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value LAStLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value LAStLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value LAStLmt_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 LTRe_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 LTRe_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 LTRe_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 LTRe_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 LTRe_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 LTRe_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 LTRe_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 LTRe_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 LTRe_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 LTRe_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 LTRe_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 LTRe_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 LTRe_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 LTRe_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 LTRe_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 LTRe_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 LTRe_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 LTRe_Inst_Ap_AstLmt.AstLmt_Per1_PerLimitForStall_	00000006 00000001 0999995 0000005 00000012 00000012 0000004 099999 0000004 0000004 0000004 00000000	
LASILMT_Per1_LimitedReturn_MtrNm_i32.value LASILMT_Per1_LmPnCtrCCDisable_Cnt_lgc.value LASILMT_Per1_LmPnCtrEnable_Cnt_lgc.value LASILMT_Per1_LmPnCtrTCmd_MtrNm_i32.value LASILMT_Per1_LmPnCtrTCmd_MtrNm_i32.value LASILMT_Per1_OpTrqOvr_MtrNm_i32.value LASILMT_Per1_OpTrqOvr_MtrNm_i32.value LASILMT_Per1_PosServCcDisable_Cnt_lgc.value LASILMT_Per1_PowerLimitPerc_Uls_i32.value LASILMT_Per1_PowerLimitPerc_Uls_i32.value LASILMT_Per1_PowerLimitPerc_Uls_i32.value LASILMT_Per1_PricompCmd_MtrNm_i32.value LASILMT_Per1_PricompCmd_MtrNm_i32.value LASILMT_Per1_PricompCmd_MtrNm_i32.value LASILMT_Per1_ThermalLimit_MtrNm_i32.value LASILMT_Per1_ThermalLimit_MtrNm_i32.value LASILMT_Per1_ThermalLimit_MtrNm_i32.value LASILMT_Per1_ThermalLimit_MtrNm_i32.value LASILMT_Per1_VehSpd_Kph_i32.value LASILMT_Per1_AssistComd_MtrNm_i32 LETE_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTDamping_MtrNm_i32 LETE_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTDamping_MtrNm_i32 LETE_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTDamping_MtrNm_i32 LETE_Inst_Ap_AstLmt_AstLmt_Per1_DefeatLimitService_Cnt_lgc LETE_Inst_Ap_AstLmt_AstLmt_Per1_DefeatLimitService_Cnt_lgc LETE_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_i32 LETE_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_i32 LETE_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_i32 LETE_Inst_Ap_AstLmt_AstLmt_Per1_OpTrqOvr_MtrNm_i32 LETE_Inst_Ap_AstLmt_AstLmt_Per1_OpTrqOvr_MtrNm_i32 LETE_Inst_Ap_AstLmt_AstLmt_Per1_OpTrqOvr_MtrNm_i32 LETE_Inst_Ap_AstLmt_AstLmt_Per1_OpTrqOvr_MtrNm_i32 LETE_Inst_Ap_AstLmt_AstLmt_Per	00000006 00000001 0999995 0000005 00000012 00000012 0000004 099999 0000004 0000004 0000004 00000000	
LASILmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value LASILmt_Per1_LrnPnCtrEnable_Cnt_lgc.value LASILmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value LASILmt_Per1_DopTrqOvr_MtrNm_f32.value LASILmt_Per1_OpTrqOvr_MtrNm_f32.value LASILmt_Per1_OpTrqOvr_MtrNm_f32.value LASILmt_Per1_PowerLimitPerc_Uls_f32.value LASILmt_Per1_PowerLimitPerc_Uls_f32.value LASILmt_Per1_PowerLimitPerc_Uls_f32.value LASILmt_Per1_PrkAssistCmd_MtrNm_f32.value LASILmt_Per1_PullCompCmd_MtrNm_f32.value LASILmt_Per1_PullCompCmd_MtrNm_f32.value LASILmt_Per1_PullCompCmd_MtrNm_f32.value LASILmt_Per1_ThermalLimitPerc_Uls_f32.value LASILmt_Per1_ThermalLimit_MtrNm_f32.value LASILmt_Per1_ThermalLimit_MtrNm_f32.value LASILmt_Per1_VehSpd_Kph_f32.value LASILmt_Per1_VehSpd_Kph_f32.value LASILmt_Per1_WheelImbalanceCmd_MtrNm_f32.value LASILmt_Per1_WheelImbalanceCmd_MtrNm_f32.value LRIE_Inst_Ap_AsILmt_AsILmt_Per1_AssistEOTDamping_MtrNm_f32 LRIE_Inst_Ap_AsILmt_AsILmt_Per1_AssistEOTDamping_MtrNm_f32 LRIE_Inst_Ap_AsILmt_AsILmt_Per1_AssistEOTGain_Uls_f32 LRIE_Inst_Ap_AsILmt_AsILmt_Per1_AssistEOTLimit_MtrNm_f32 LRIE_Inst_Ap_AsILmt_AsILmt_Per1_AssistEOTLimit_MtrNm_f32 LRIE_Inst_Ap_AsILmt_AsILmt_Per1_AssistVehSpdLimit_MtrNm_f32 LRIE_Inst_Ap_AsILmt_AsILmt_Per1_DefaelLimitService_Cnt_lgc LRIE_Inst_Ap_AsILmt_AsILmt_Per1_DefaelLimitService_Cnt_lgc LRIE_Inst_Ap_AsILmt_AsILmt_Per1_LimitPercentFiltered_Uls_f32 LRIE_Inst_Ap_AsILmt_AsILmt_Per1_LimitPercentFiltered_Uls_f32 LRIE_Inst_Ap_AsILmt_AsILmt_Per1_LimitPercentFiltered_Uls_f32 LRIE_Inst_Ap_AsILmt_AsILmt_Per1_LimitPercentFiltered_Uls_f32 LRIE_Inst_Ap_AsILmt_AsILmt_Per1_LimitPercentFiltered_Uls_f32 LRIE_Inst_Ap_AsILmt_AsILmt_Per1_LimitPercentFiltered_Uls_f32 LRIE_Inst_Ap_AsILmt_AsILmt_Per1_LimitPercentFiltered_Uls_f32 LRIE_Inst_Ap_AsILmt_AsILmt_Per1_LimitPercentFiltered_Uls_f32 LRIE_Inst_Ap_AsILmt_AsILmt_Per1_LimitPercentFiltered_Uls_f32 LRIE_Inst_Ap_AsILmt_AsILmt_Per1_OpTrqOvr_MtrNm_f32 LRIE_Inst_Ap_AsILmt_AsILmt_Per1_OpTrqOvr_MtrNm_f32 LRIE_Inst_Ap_AsILmt_AsILmt_Per1_OpTrqOvr_MtrNm_f32 LRIE_Inst_Ap_AsILmt_AsILmt_Per1_Per1_mitForStall_MtrNm_	00000006 00000001 0999995 0000005 00000012 00000012 0000004 099999 0000004 0000004 0000004 00000000	
t_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value t_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value t_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value t_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value t_AstLmt_Per1_OutputRampMult_Uls_f32.value t_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value t_AstLmt_Per1_PowerLimitPerc_Uls_f32.value t_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value t_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value t_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value t_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value t_AstLmt_Per1_VehSpd_Kph_f32.value t_AstLmt_Per1_VehSpd_Kph_f32.value t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LofficeDamping_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTcmd_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTcmd_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTcmd_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTcmd_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DorfiqOvr_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DorfiqOvr_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_Per2_UntpRampMult_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_l	00000006 00000001 0999995 0000005 00000012 00000012 0000004 099999 0000004 0000004 0000004 00000000	
t_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value t_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value t_AstLmt_Per1_OutputRampMult_Uls_f32.value t_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value t_AstLmt_Per1_PowerLimitPerc_Uls_f32.value t_AstLmt_Per1_PowerLimitPerc_Uls_f32.value t_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value t_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value t_AstLmt_Per1_PullCompCmd_MtrNm_f32.value t_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value t_AstLmt_Per1_VehSpd_Kph_f32.value t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value t_AstLmt_Ap_AstLmt_AstLmt_Per1_AssistCmd_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimiteReturn_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimiteReturn_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimiteReturn_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimiteReturn_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimiteReturn_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimiteReturn_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimiteReturn_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimiteReturn_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimiteRimtPer0_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_Der1_CrtCODisable_Cnt_lgc t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_Per0_UntrRampMult_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_Per0_UntrRampMult_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_Per0_UntrRampMult_Uls_f32 t_Rte_Inst_Ap_As	00000006 00000001 0999995 0000005 00000012 00000012 0000004 099999 0000004 0000004 0000004 00000000	
t_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value t_AstLmt_Per1_OutputRampMult_Uls_f32.value t_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value t_AstLmt_Per1_PowerLimitPerc_Uls_f32.value t_AstLmt_Per1_PowerLimitPerc_Uls_f32.value t_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value t_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value t_AstLmt_Per1_ThermallLimitPerc_Uls_f32.value t_AstLmt_Per1_ThermallLimitPerc_Uls_f32.value t_AstLmt_Per1_ThermallLimit_MtrNm_f32.value t_AstLmt_Per1_ThermallLimit_MtrNm_f32.value t_AstLmt_Per1_VehSpd_Kph_f32.value t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value t_AstLmt_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimiteReturn_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimiteReturn_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimiteReturn_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimiteReturn_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimiteReturn_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimiteReturn_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimiteReturn_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimiteReturn_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimiteReturn_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimiteRimtPer1_LimiteRimtPer1_LimiteRimtPer1_LimiteRimtPer1_LimiteRimtPer1_LimiteRimtPer1_LimiteRimtPer1_LimiteRimtPer1_LimiteRimtPer1_LimiteRimtPer1_LimiteRimtPer1_LimiteRimtRimtRimtRimtRimtRimtRimtRimtRimtRimt	00000006 00000001 0999995 0000005 00000012 00000012 0000004 099999 0000004 0000004 0000004 00000000	
t_AstLmt_Per1_OutputRampMult_Uls_f32.value t_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value t_AstLmt_Per1_PowerLimitPerc_Uls_f32.value t_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value t_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value t_AstLmt_Per1_PullCompCmd_MtrNm_f32.value t_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value t_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value t_AstLmt_Per1_VehSpd_Kph_f32.value t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimitMtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatCrt_Cobisable_Cnt_lgc t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DeverLimitPerc_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PerLimitForStall_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PerLimi	00000006 00000001 0999995 0000005 00000012 00000012 0000004 099999 0000004 099999 0000004 0000004 0000004 0000004 0000004 000000	
LASILmt_Per1_PosServCCDisable_Ont_Igc.value LASILmt_Per1_PowerLimitPerc_Uls_f32.value LASILmt_Per1_PrkAssistCmd_MtrNm_f32.value LASILmt_Per1_PullCompCmd_MtrNm_f32.value LASILmt_Per1_ThermalLimitPerc_Uls_f32.value LASILmt_Per1_ThermalLimitPerc_Uls_f32.value LASILmt_Per1_ThermalLimit_MtrNm_f32.value LASILmt_Per1_ThermalLimit_MtrNm_f32.value LASILmt_Per1_VehSpd_Kph_f32.value LASILmt_Per1_WheelImbalanceCmd_MtrNm_f32.value LASILmt_Per1_WheelImbalanceCmd_MtrNm_f32.value LASILmt_Per1_WheelImbalanceCmd_MtrNm_f32.value LASILmt_Ap_AsILmt_AsILmt_Per1_AssistEOTDamping_MtrNm_f32 LASILmt_Ap_AsILmt_AsILmt_Per1_AssistEOTDamping_MtrNm_f32 LASIL_Inst_Ap_AsILmt_AsILmt_Per1_AssistEOTGain_Uls_f32 LASIL_Inst_Ap_AsILmt_AsILmt_Per1_AssistEOTLimit_MtrNm_f32 LASIL_Inst_Ap_AsILmt_AsILmt_Per1_AssistEOTLimit_MtrNm_f32 LASIL_Inst_Ap_AsILmt_AsILmt_Per1_AssistStallLimit_MtrNm_f32 LASIL_Inst_Ap_AsILmt_AsILmt_Per1_AssistStallLimit_MtrNm_f32 LASIL_Inst_Ap_AsILmt_AsILmt_Per1_AssistStallLimit_MtrNm_f32 LASIL_Inst_Ap_AsILmt_AsILmt_Per1_DefeatLimitService_Cnt_lgc LASIL_Inst_Ap_AsILmt_AsILmt_Per1_DefeatLimitService_Cnt_lgc LASIL_Inst_Ap_AsILmt_AsILmt_Per1_LimitPercentFiltered_Uls_f32 LASIL_Inst_Ap_AsILmt_AsILmt_Per1_LimitPercentFiltered_Uls_f32 LASIL_Inst_Ap_AsILmt_AsILmt_Per1_LimitPercentFiltered_Uls_f32 LASIL_Inst_Ap_AsILmt_AsILmt_Per1_LimitPercentFiltered_Uls_f32 LASIL_Inst_Ap_AsILmt_AsILmt_Per1_LimitPercentFiltered_Uls_f32 LASIL LASILMT_ASILmt_Per1_LimitPercentFiltered_Uls_f32 LASILMT_ASILMT_ASILMT_Per1_LimitPercentFiltered_Uls_f32 LASILMT_ASILMT_ASILMT_Per1_LimitPercentFiltered_Uls_f32 LASILMT_ASILMT_ASILMT_Per1_LimitPercentFiltered_Uls_f32 LASILMT_ASILMT_ASILMT_Per1_LimitPercentFiltered_Uls_f32 LASILMT_ASILMT_ASILMT_Per1_LimitPercentFiltered_Uls_f32 LASILMT_ASILMT_ASILMT_Per1_LimitPercentFiltered_Uls_f32 LASILMT_ASILMT_ASILMT_Per1_DottrCCDisable_Cnt_lgc LASILMT_ASILMT_ASILMT_Per1_DottrCCDIsable_Cnt_lgc LASILMT_ASILMT_ASILMT_Per1_PerLimitForStall_MtrNm_f32 LASILMT_ASILMT_ASILMT_Per1_PerLimitForStall_MtrNm_f32	00000001 0999995 0000005 00000012 6000004 099999 AstLmt_Per1_AssistCmd_MtrNm_f32 AstLmt_Per1_AssistEOTDamping_MtrNm_f32 AstLmt_Per1_AssistEOTGain_Uls_f32 AstLmt_Per1_AssistEOTLimit_MtrNm_f32 AstLmt_Per1_AssistStallLimit_MtrNm_f32 AstLmt_Per1_AssistStallLimit_MtrNm_f32 AstLmt_Per1_AssistStallLimit_MtrNm_f32 AstLmt_Per1_CombinedDamping_MtrNm_f32 AstLmt_Per1_CombinedDamping_MtrNm_f32 AstLmt_Per1_DefeatLimitService_Cnt_lgc AstLmt_Per1_LimitPercentFiltered_Uls_f32	
LASILmt_Perl_PrkAssistCmd_MtrNm_f32.value t_ASILmt_Perl_PullCompCmd_MtrNm_f32.value t_ASILmt_Perl_ThermalLimitPerc_Uls_f32.value t_ASILmt_Perl_ThermalLimit_MtrNm_f32.value t_ASILmt_Perl_ThermalLimit_MtrNm_f32.value t_ASILmt_Perl_VehSpd_Kph_f32.value t_ASILmt_Perl_WheelImbalanceCmd_MtrNm_f32.value t_ASILmt_Perl_WheelImbalanceCmd_MtrNm_f32.value t_ASILmt_Ap_ASILmt_ASILmt_Perl_AssistCmd_MtrNm_f32 t_Rie_Inst_Ap_ASILmt_ASILmt_Perl_AssistEOTDamping_MtrNm_f32 t_Rie_Inst_Ap_ASILmt_ASILmt_Perl_AssistEOTGain_Uls_f32 t_Rie_Inst_Ap_ASILmt_ASILmt_Perl_AssistSillIlimit_MtrNm_f32 t_Rie_Inst_Ap_ASILmt_ASILmt_Perl_AssistSillIlimit_MtrNm_f32 t_Rie_Inst_Ap_ASILmt_ASILmt_Perl_AssistSillIlimit_MtrNm_f32 t_Rie_Inst_Ap_ASILmt_ASILmt_Perl_AssistVehSpalimi_MtrNm_f32 t_Rie_Inst_Ap_AsILmt_ASILmt_Perl_DefaatLimitService_Cnt_lgc t_Rie_Inst_Ap_AsILmt_ASILmt_Perl_LimiteReturn_MtrNm_f32 t_Rie_Inst_Ap_AsILmt_ASILmt_Perl_LimiteReturn_MtrNm_f32 t_Rie_Inst_Ap_AsILmt_ASILmt_Perl_LimiteReturn_MtrNm_f32 t_Rie_Inst_Ap_AsILmt_ASILmt_Perl_LimiteReturn_MtrNm_f32 t_Rie_Inst_Ap_AsILmt_ASILmt_Perl_LmPnCtrCcDisable_Cnt_lgc t_Rie_Inst_Ap_AsILmt_AsILmt_Perl_LmPnCtrTcmd_MtrNm_f32 t_Rie_Inst_Ap_AsILmt_AsILmt_Perl_LmPnCtrTcmd_MtrNm_f32 t_Rie_Inst_Ap_AsILmt_AsILmt_Perl_LmPnCtrTcmd_MtrNm_f32 t_Rie_Inst_Ap_AsILmt_AsILmt_Perl_OpTrqOvr_MtrNm_f32 t_Rie_Inst_Ap_AsILmt_AsILmt_Perl_OpTrqOvr_MtrNm_f32 t_Rie_Inst_Ap_AsILmt_AsILmt_Perl_OpTrqOvr_MtrNm_f32 t_Rie_Inst_Ap_AsILmt_AsILmt_Perl_OpTrqOvr_MtrNm_f32 t_Rie_Inst_Ap_AsILmt_AsILmt_Perl_PosServCCDisable_Cnt_lgc t_Rie_Inst_Ap_AsILmt_AsILmt_Perl_PosServCCDisable_Cnt_lgc t_Rie_Inst_Ap_AsILmt_AsILmt_Perl_PosServCCDisable_Cnt_lgc t_Rie_Inst_Ap_AsILmt_AsILmt_Perl_PosServCCDisable_Cnt_lgc t_Rie_Inst_Ap_AsILmt_AsILmt_Perl_PosServCCDisable_Cnt_lgc t_Rie_Inst_Ap_AsILmt_AsILmt_Perl_PosServCCDisable_Cnt_lgc t_Rie_Inst_Ap_AsILmt_AsILmt_Perl_PosServCCDisable_Cnt_lgc t_Rie_Inst_Ap_AsILmt_AsILmt_Perl_PosServCCDisable_Cnt_lgc t_Rie_Inst_Ap_AsILmt_AsILmt_Perl_PosServCCDisable_Cnt_lgc t_Rie_Inst_Ap_AsILmt	9999995 0000005 00000012 6000004 999999 AstLmt_Per1_AssistCmd_MtrNm_f32 AstLmt_Per1_AssistEOTDamping_MtrNm_f32 AstLmt_Per1_AssistEOTGain_Uls_f32 AstLmt_Per1_AssistEOTLimit_MtrNm_f32 AstLmt_Per1_AssistStallLimit_MtrNm_f32 AstLmt_Per1_AssistStallLimit_MtrNm_f32 AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 AstLmt_Per1_CombinedDamping_MtrNm_f32 AstLmt_Per1_CombinedDamping_MtrNm_f32 AstLmt_Per1_DefeatLimitService_Cnt_lgc AstLmt_Per1_LimitPercentFiltered_Uls_f32	
LASILmt_Per1_PullCompCmd_MtrNm_f32.value t_ASILmt_Per1_ThermalLimitPerc_Uls_f32.value t_ASILmt_Per1_ThermalLimit_MtrNm_f32.value t_ASILmt_Per1_ThermalLimit_MtrNm_f32.value t_ASILmt_Per1_VehSpd_Kph_f32.value t_ASILmt_Per1_WheelImbalanceCmd_MtrNm_f32.value t_ASILmt_Per1_WheelImbalanceCmd_MtrNm_f32.value t_Ret_Inst_Ap_ASILmt_ASILmt_Per1_AssistCmd_MtrNm_f32 t_Ret_Inst_Ap_ASILmt_ASILmt_Per1_AssistEOTDamping_MtrNm_f32 t_Ret_Inst_Ap_ASILmt_ASILmt_Per1_AssistEOTGain_Uls_f32 t_Ret_Inst_Ap_ASILmt_ASILmt_Per1_AssistStallLimit_MtrNm_f32 t_Ret_Inst_Ap_ASILmt_ASILmt_Per1_AssistStallLimit_MtrNm_f32 t_Ret_Inst_Ap_ASILmt_ASILmt_Per1_AssistVehSpdLimit_MtrNm_f32 t_Ret_Inst_Ap_ASILmt_ASILmt_Per1_CombinedDamping_MtrNm_f32 t_Ret_Inst_Ap_ASILmt_ASILmt_Per1_DefeatLimitService_Cnt_lgc t_Ret_Inst_Ap_ASILmt_ASILmt_Per1_LimitPercentFiltered_Uls_f32 t_Ret_Inst_Ap_ASILmt_ASILmt_Per1_LimitPerCentFiltered_Uls_f32 t_Ret_Inst_Ap_ASILmt_ASILmt_Per1_LimitPerCentFiltered_Uls_f32 t_Ret_Inst_Ap_ASILmt_ASILmt_Per1_LmPnCtrCCDisable_Cnt_lgc t_Ret_Inst_Ap_ASILmt_ASILmt_Per1_LmPnCtrTcmd_MtrNm_f32 t_Ret_Inst_Ap_ASILmt_ASILmt_Per1_LmPnCtrTcmd_MtrNm_f32 t_Ret_Inst_Ap_ASILmt_ASILmt_Per1_LmPnCtrTcmd_MtrNm_f32 t_Ret_Inst_Ap_ASILmt_ASILmt_Per1_OpTrqOvr_MtrNm_f32 t_Ret_Inst_Ap_ASILmt_ASILmt_Per1_OpTrqOvr_MtrNm_f32 t_Ret_Inst_Ap_ASILmt_ASILmt_Per1_OpTrqOvr_MtrNm_f32 t_Ret_Inst_Ap_ASILmt_ASILmt_Per1_PosServCCDisable_Cnt_lgc t_Ret_Inst_Ap_ASILmt_ASILmt_Per1_Pos	0000005 00000012 00000014 0000004 0000004 0000004 0000004 AstLmt_Per1_AssistCmd_MtrNm_f32 AstLmt_Per1_AssistEOTDamping_MtrNm_f32 AstLmt_Per1_AssistEOTGain_Uls_f32 AstLmt_Per1_AssistEOTLimit_MtrNm_f32 AstLmt_Per1_AssistStallLimit_MtrNm_f32 AstLmt_Per1_AssistStallLimit_MtrNm_f32 AstLmt_Per1_CombinedDamping_MtrNm_f32 AstLmt_Per1_CombinedDamping_MtrNm_f32 AstLmt_Per1_DefeatLimitService_Cnt_lgc AstLmt_Per1_LimitPercentFiltered_Uls_f32	
LAStLmt_Per1_ThermalLimitPerc_Uls_f32.value t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value t_AstLmt_Per1_VehSpd_Kph_f32.value t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPerCoDisable_Cnt_lgc t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrrCnd_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrrCnd_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrrCnd_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosCrrCnd_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosCrrCnd_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosCrrCnd_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosCrrCnd_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosCrrCnd_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosCrrCnd_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosCrrCnd_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosCrrCnd_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosCrrCnd_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPorc_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPorc_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPorc_Uls_f32 t_Rte_Inst_A	20000012 20000012 20000004 2099999 20000014 2099999 20000015 20000017 20000017 200000017 200000017 200000017 200000017 200000017 2000000017 2000000017 2000000017 20000000000	
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value t_AstLmt_Per1_VehSpd_Kph_f32.value t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 t_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPerCoDisable_Cnt_lgc t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrrCnd_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrrCnd_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_UnPnCtrrCnd_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitForStall_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Pe	6000004 6099999 AstLmt_Per1_AssistCmd_MtrNm_f32 AstLmt_Per1_AssistEOTDamping_MtrNm_f32 AstLmt_Per1_AssistEOTGain_Uls_f32 AstLmt_Per1_AssistEOTLimit_MtrNm_f32 AstLmt_Per1_AssistStallLimit_MtrNm_f32 AstLmt_Per1_AssistStallLimit_MtrNm_f32 AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 AstLmt_Per1_CombinedDamping_MtrNm_f32 AstLmt_Per1_DefeatLimitService_Cnt_lgc AstLmt_Per1_LimitPercentFiltered_Uls_f32	
LAStLmt_Per1_VehSpd_Kph_f32.value 12. LAStLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value 5.5. LRte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPrCtrCCDisable_Cnt_lgc tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPrOttrEnable_Cnt_lgc tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPrOttrTCmd_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_ t_Rte_In	AstLmt_Per1_AssistCmd_MtrNm_f32 AstLmt_Per1_AssistEOTDamping_MtrNm_f32 AstLmt_Per1_AssistEOTGain_Uls_f32 AstLmt_Per1_AssistEOTLimit_MtrNm_f32 AstLmt_Per1_AssistStallLimit_MtrNm_f32 AstLmt_Per1_AssistStallLimit_MtrNm_f32 AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 AstLmt_Per1_CombinedDamping_MtrNm_f32 AstLmt_Per1_DefeatLimitService_Cnt_lgc AstLmt_Per1_LimitPercentFiltered_Uls_f32	
LASILMT_Per1_WheelImbalanceCmd_MtrNm_f32.value LRte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPrCtrCCDisable_Cnt_lgc t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPrCtrGdd_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPrCtrGdd_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitForStall_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForque_MtrNm_f32	AstLmt_Per1_AssistCmd_MtrNm_f32 AstLmt_Per1_AssistEOTDamping_MtrNm_f32 AstLmt_Per1_AssistEOTGain_Uls_f32 AstLmt_Per1_AssistEOTLimit_MtrNm_f32 AstLmt_Per1_AssistStallLimit_MtrNm_f32 AstLmt_Per1_AssistStallLimit_MtrNm_f32 AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 AstLmt_Per1_CombinedDamping_MtrNm_f32 AstLmt_Per1_DefeatLimitService_Cnt_lgc AstLmt_Per1_LimitPercentFiltered_Uls_f32	
LRte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimiteReturn_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimiteReturn_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPrCtrCCDisable_Cnt_lgc tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPrCtrTCmd_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OptrqOvr_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OptrqOvr_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForque_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForque_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForque_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForque_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForque_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForque_Mt	AstLmt_Per1_AssistCmd_MtrNm_f32 AstLmt_Per1_AssistEOTDamping_MtrNm_f32 AstLmt_Per1_AssistEOTGain_Uls_f32 AstLmt_Per1_AssistEOTLimit_MtrNm_f32 AstLmt_Per1_AssistStallLimit_MtrNm_f32 AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 AstLmt_Per1_CombinedDamping_MtrNm_f32 AstLmt_Per1_DefeatLimitService_Cnt_lgc AstLmt_Per1_LimitPercentFiltered_Uls_f32	
LRte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCTOmd_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OptrqOvr_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForque_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForque_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForque_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForque_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForque_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForque_MtrNm_f32 tgt_t_Rte	AstLmt_Per1_AssistEOTDamping_MtrNm_f32 AstLmt_Per1_AssistEOTGain_Uls_f32 AstLmt_Per1_AssistEOTLimit_MtrNm_f32 AstLmt_Per1_AssistStallLimit_MtrNm_f32 AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 AstLmt_Per1_CombinedDamping_MtrNm_f32 AstLmt_Per1_DefeatLimitService_Cnt_lgc AstLmt_Per1_LimitPercentFiltered_Uls_f32	
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 tgt t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc tgt t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCcDisable_Cnt_lgc tgt t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTcmd_MtrNm_f32 tgt t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 tgt t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCcDisable_Cnt_lgc tgt t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForGtall_MtrNm_f32	AstLmt_Per1_AssistEOTGain_Uls_f32 AstLmt_Per1_AssistEOTLimit_MtrNm_f32 AstLmt_Per1_AssistStallLimit_MtrNm_f32 AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 AstLmt_Per1_CombinedDamping_MtrNm_f32 AstLmt_Per1_DefeatLimitService_Cnt_lgc AstLmt_Per1_LimitPercentFiltered_Uls_f32	
LRte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCcDisable_Cnt_lgc tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_lgc tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCcDisable_Cnt_lgc tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForGtall_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForGtal	AstLmt_Per1_AssistEOTLimit_MtrNm_f32 AstLmt_Per1_AssistStallLimit_MtrNm_f32 AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 AstLmt_Per1_CombinedDamping_MtrNm_f32 AstLmt_Per1_DefeatLimitService_Cnt_lgc AstLmt_Per1_LimitPercentFiltered_Uls_f32	
LRte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTcmd_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_	AstLmt_Per1_AssistStallLimit_MtrNm_f32 AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 AstLmt_Per1_CombinedDamping_MtrNm_f32 AstLmt_Per1_DefeatLimitService_Cnt_lgc AstLmt_Per1_LimitPercentFiltered_Uls_f32	
LRte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCcDisable_Cnt_Igc tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTcmd_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCcDisable_Cnt_Igc tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_	AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 AstLmt_Per1_CombinedDamping_MtrNm_f32 AstLmt_Per1_DefeatLimitService_Cnt_Igc AstLmt_Per1_LimitPercentFiltered_Uls_f32	
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCcDisable_Cnt_Igc tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTcmd_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCcDisable_Cnt_Igc tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_	AstLmt_Per1_CombinedDamping_MtrNm_f32 AstLmt_Per1_DefeatLimitService_Cnt_lgc AstLmt_Per1_LimitPercentFiltered_Uls_f32	
LRte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCcDisable_Cnt_Igc tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTcmd_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DpTrqOvr_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCcDisable_Cnt_Igc tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_	AstLmt_Per1_DefeatLimitService_Cnt_Igc AstLmt_Per1_LimitPercentFiltered_Uls_f32	
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCnDisable_Cnt_Igc tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTcmd_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCcDisable_Cnt_Igc tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt.As	AstLmt_Per1_LimitPercentFiltered_Uls_f32	
LRte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_tRte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_Igc tgt_tRte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_Igc tgt_trte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 tgt_trte_Inst_Ap_AstLmt.AstLmt_Per1_DpTrqOvr_MtrNm_f32 tgt_trte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_trte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 tgt_trte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc tgt_trte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_trte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_trte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_trte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_trte_Inst_Ap_A		
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_Igc tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_Igc tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_	AstLmt_Per1_LimitedReturn_MtrNm_f32	
L Rte Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_Igc tgt. t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 tgt. t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt. t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 tgt. t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc tgt. t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt. t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt. t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt.		
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 tgt t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 tgt t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt	AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	
L Rte Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt. t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 tgt. t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc tgt. t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt. t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt. t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt.	AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32 tgt. t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc tgt. t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt. t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt. t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt.	AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_	AstLmt_Per1_OpTrqOvr_MtrNm_f32	
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	AstLmt_Per1_OutputRampMult_Uls_f32	
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_ t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_	AstLmt_Per1_PosServCCDisable_Cnt_lgc	
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_	AstLmt_Per1_PowerLimitPerc_Uls_f32	
	AstLmt_Per1_PreLimitForStall_MtrNm_f32	
Pte Inst An Astl mt Astl mt Per1 PrkAssistCmd MtrNm f22	AstLmt_Per1_PreLimitTorque_MtrNm_f32	
	AstLmt_Per1_PrkAssistCmd_MtrNm_f32	
	AstLmt_Per1_PullCompCmd_MtrNm_f32	
	AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	
	et_AstLmt_Per1_TSMitCommand_MtrNm_f32	
	AstLmt_Per1_ThermalLimitPerc_UIs_f32	
	AstLmt_Per1_ThermalLimit_MtrNm_f32	
	AstLmt_Per1_TrqLimitMin_MtrNm_f32	
	AstLmt_Per1_VehSpd_Kph_f32	
	AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	
	ual Value Expected Value	Resu
	0.30000012 0.300000012 ± 0.000030517578125	,
t_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 8	8 ± 0.0009765625	•
t_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value 0	0 ± 0.0009765625	•
t_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value 0 t_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value 5.5	0 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✓

Test Step 2.73 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmd_MtrNm_M_f22	-3.5		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	7.28000021		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.8999998		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-1.20000005		
tgt AstLmt Per1 AssistEOTDamping MtrNm f32.value	-4.5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001		
tgt AstLmt Per1 AssistEOTLimit MtrNm f32.value	8.30000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7.69999981		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.10000038		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	8		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	3		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	3		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt AstLmt Per1 PowerLimitPerc Uls f32.value	0.200000003		
tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value	5.599999		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.40000006		
tgt AstLmt Per1 ThermalLimit MtrNm f32.value	2.0999999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	510.799988		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-8.80000019		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mti		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U	— -	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lc	gc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNi	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mi	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	<u>f</u> 32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.400000006	0.40000006 ± 0.000030517578125	
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	3	3 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	3	3 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	1.5	1.5 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2.0999999	2.0999999 ± 0.0009765625	٠,



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.74 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt ManualTrqCmd MtrNm M f32	3.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt Rte Inst Ap AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	7.71000004		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.79999995		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.39999962		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7.80000019		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.19999981		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	4		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	3.29999995		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.20000005		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	425.5		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8.80000019		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mti	rNm f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtrl	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul	s_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg	С	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f:		
tgt_Rte_Inst_Ap_AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt	— -	
tgt_Rte_Inst_Ap_AstLmt_Per1_PowerLimitPerc_UIs_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3:		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_t tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_FullCompCind_MtrNm_i32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt AstLmt Per1 SumLimTrqCmd MtrNn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target AstLmt Per1 TSMitCommand Mtr	_	
tgt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimitPerc UIs f32	tgt AstLmt Per1 ThermalLimitPerc Uls 1	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt AstLmt Per1 ThermalLimit MtrNm f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	/ltrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2.0999999	2.0999999 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2.20000005	2.20000005 ± 0.0009765625	



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓

Test Step 2.75 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt ManualTrqCmd MtrNm M f32	-2.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	8.14000034		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.70000005		
tgt_AstLmt_Per1_13/intcommand_winvin_i3z.value	-4.5		
	3.29999995		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value			
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.5		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7.9000001		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.30000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.29999995		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	373.100006		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	0		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M	trNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mi	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrCCDisable C		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrEnable Cnt le	_ :	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt AstLmt Per1 LrnPnCtrTCmd MtrNm		
tgt Rte Inst Ap AstLmt.AstLmt Per1 OpTrgOvr MtrNm f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32	1_102	
		f2.2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cr	_ -	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNi	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Res
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	5	5 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	3.5	3.5 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2.29999995	2.29999995 ± 0.0009765625	





Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	2.5		
AstLmt SteeringAsstDefeat Cnt M Igc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	8.56999969		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.60000002		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.6000038		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8		
tgt AstLmt Per1 AssistVehSpdLimit MtrNm f32.value	8.39999962		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	6		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_tmPnCtrTCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_cmr-notine_within_isz.value	4		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_OutputRampwint_Ois_isz.value	1		
tqt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5		
tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value	6		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.4000001		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	324.200012		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	1.5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt AstLmt Per1 AssistEOTDamping Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3:	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_UI		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LimitedReturn MtrNm f32	tgt AstLmt Per1 LimitedReturn MtrNm		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrCCDisable Cnt Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt AstLmt Per1 LrnPnCtrTCmd MtrNm		
tgt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
tgt Rte Inst Ap AstLmt.AstLmt Per1 PowerLimitPerc Uls f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tqt Rte Inst Ap AstLmt.AstLmt Per1 PreLimitForStall MtrNm f32	tgt AstLmt Per1 PreLimitForStall MtrNn		
tgt_Rte_Inst_Ap_AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt AstLmt Per1 SumLimTrqCmd MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
tgt Rte Inst Ap AstLmt.AstLmt Per1 TrqLimitMin MtrNm f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt Rte Inst Ap AstLmt.AstLmt Per1 VehSpd Kph f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt AstLmt Per1 WheelImbalanceCmd I	MtrNm f32	
			Post
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_UIs_f32.value	0.69999988	0.699999988 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	6.5	6.5 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0 2.4000001	0 ± 0.0009765625 2.4000001 ± 0.0009765625	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓	

Test Step 2.77 (Repeat Count = 1) Name	Input Value		
	· ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-1.5		
AstLmt_ManualTrqCmd_MtrNm_M_f32	0		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPICmpLimit_MtrNm_f32	0.600000024		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.5		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	3.29999995		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	4		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.6999981		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.10000038		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.5		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	7		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.89999976		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
st_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
pt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2.0999999		
t_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.5		
t_AstLmt_Per1_VehSpd_Kph_f32.value	456.299988		
t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-2.5999999		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt AstLmt Per1 AssistEOTDamping M		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mti		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt AstLmt Per1 LimitedReturn MtrNm		
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Ci		
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_k		
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt AstLmt Per1 LrnPnCtrTCmd MtrNm		
gt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	1 132	
		f2.2	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cr	_ -	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNi	_	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
lame	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	7	7 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	6.29999971	6.30000019 ± 0.0009765625	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~	

Test Step 2.78 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt ManualTrqCmd MtrNm M f32	1.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.3999998		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024		
	7.0999999		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.1999981		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value			
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.60000038		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	3.29999995		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	8		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	6		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.100000001		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	8		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.899999976		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.5999999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	345.399994		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-1.20000005		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mi	trNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt AstLmt Per1 LrnPnCtrCCDisable Ci		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrEnable Cnt le		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32	92	
tgt_Rte_Inst_Ap_AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_t	132	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_Posservecbisable_cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PowerLimitFerC_ois_is		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStail_MtiNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStail_MtrNm	_	
	tgt_AstLmt_Per1_PreLimit10rque_MitNin tgt_AstLmt_Per1_PrkAssistCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	V		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mi		
tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimitPerc_UIs_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.899999976	0.899999976 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5.5999999	5.5999999 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2.5999999	2.5999999 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Fest Step 2.79 (Repeat Count = 1)	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-7		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
s_SumLimPlCmpLimit_MtrNm_f32	8.80000019		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.29999995		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	4		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	6		
pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7.19999981		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.30000019		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.6999981		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	7		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.10000001		
pt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.70000005		
pt_AstLmt_Per1_VehSpd_Kph_f32.value	234.5		
pt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-2.0999999		
gt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	m f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	··· <u>-</u> - •-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNi	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_l		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_UIs_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_	igc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	•	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f3	§2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_I	gc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f		
gne_me_p_net_me_ren_introduc_muninicoz	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3	32 2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f	32 2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3	32 2 2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3	32 2 2 732	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3; tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3; tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_	32 2 2 2 m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN	32 2 2 2 m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3:	32 2 2 2 m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32	32 2 2 2 m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	32 2 2 (32 m_{32 2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr	32 2 2 32 m_f32 2 Nm_f32	Resi
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3: tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr Actual Value	32 2 2 2 32 m_f32 2 Nm_f32 Expected Value	Resi
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3: tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr Actual Value 0.8000000012	22 22 23 22 32 m_f32 2 Nm_f32 Expected Value 0.800000012 ± 0.000030517578125	Resi
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3; tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr Actual Value 0.800000012 4	Nm_f32 Expected Value 0.800000012 ± 0.000030517578125 4 ± 0.0009765625	Resi
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 Name gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f3: tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3: tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3: tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_Mtr Actual Value 0.8000000012	22 22 23 22 32 m_f32 2 Nm_f32 Expected Value 0.800000012 ± 0.000030517578125	Resi



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓

Test Step 2.80 (Repeat Count = 1) Name	Input Value		
	0		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32	7		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	5.5		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.20000005		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	5		
tgt AstLmt Per1 AssistEOTDamping MtrNm f32.value	7		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.80000012		
tgt AstLmt Per1 AssistEOTLimit MtrNm f32.value	7.3000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.39999962		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7.0999999		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	4		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	8		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.89999976		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3.29999995		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	2.79999995		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	123.599998		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3.29999995		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	trNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_132	
tgt_Rte_Inst_Ap_AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	roo	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PreLimit1orque_MtrNm_ tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrikAssistCmd_MtiNm_132 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLint.AstLint_FeT1_FullCompCind_MtrNin_i32 tgt_Rte_Inst_Ap_AstLint.AstLint_FeT1_SumLimTrqCmd_MtrNin_i32	tgt_AstLmt_Per1_Pullcomporting_wirNrin_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target AstLmt Per1 TSMitCommand Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	_	
tgt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimit HtrNm f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3;		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt AstLmt Per1 VehSpd Kph f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_I	MtrNm f32	
Name	Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.899999976	· ·	Iveaul
tgt_AstLmt_Peri_LimitPercentFiltered_Uis_f32.value tgt_AstLmt_Peri_LimitPorStall_MtrNm_f32.value	5	0.899999976 ± 0.000030517578125 5 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitForStail_MtrNm_t32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2.79999995	2.79999995 ± 0.0009765625	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓	

Test Step 2.81 (Repeat Count = 1) Name	Input Value		
	·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0 4		
AstLmt_ManualTrqCmd_MtrNm_M_f32	0		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
<_SumLimPICmpLimit_MtrNm_f32 specific-street Add mt. Part. TCMitCommand. MtrNm. f32 value.	0.300000012		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.10000002		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-1		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-3		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.5		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3.9000001		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.30000019		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.30000012		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-6		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.30000012		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.80000019		
t_AstLmt_Per1_VehSpd_Kph_f32.value	0		
pt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-7		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt AstLmt Per1 AssistEOTDamping Mt	rNm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3:	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mti	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_UI		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt AstLmt Per1 LimitedReturn MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt AstLmt Per1 LrnPnCtrTCmd MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	_132	
pt_Rte_Inst_Ap_AsiLint.AsiLint_Per1_OptriqOvi_withVin_i32 pt_Rte_Inst_Ap_AsiLint.AsiLint_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cn	_ -	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_UIs_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	_	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_I	MtrNm_f32	
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	
gt AstLmt Per1 PreLimitForStall MtrNm f32.value	8.80000019	8.80000019 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	2.6400001	2.6400001 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.82 (Repeat Count = 1) Name	Input Value		
	·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-3		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat Cnt M lgc	0		
	-		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt 0.579999983		
C_SumLimPICmpLimit_MtrNm_f32			
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-2		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-4		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.89999976		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.5999999		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.4000001		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-1		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2.0999999		
pt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.40000006		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
st_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.40000006		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-7		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.40000006		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6.900001		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	512		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-8		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M	trNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mi	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt AstLmt Per1 LimitedReturn MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C		
t_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_le		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt AstLmt Per1 LrnPnCtrTCmd MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	1_132	
pt_Rte_Inst_Ap_AsiLint_NstEnit_Feri_OptriqOvi_withViii_i32 pt_Rte_Inst_Ap_AsiLmt.AsiLmt_Peri_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_	f2.2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cr	_ ·	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_UIs_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	_	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNi	_	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.40000006	0.400000006 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5.11999989	5.11999989 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-5.11999989	-5.11999989 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-1.6000002	-1.60000002 ± 0.0009765625	



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	~

Test Step 2.83 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	3		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt <_SumLimPlCmpLimit_MtrNm_f32	tgt_Rte_Inst_Ap_AstLmt 0.860000014		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.899999976		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-3		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-5		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001		
	4.6999981		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.0999999		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value			
	4.5		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	1		
gt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value			
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4.5		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	1		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-3		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.5		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-8		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	7		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	125.260002		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	1		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_	<u>f</u> 32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	lm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt	_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls	s_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn	t_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	<u>f</u> 32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt	_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_t		
gt Rte Inst Ap AstLmt.AstLmt Per1 SumLimTrqCmd MtrNm f32	tgt AstLmt Per1 SumLimTrqCmd MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt AstLmt Per1 ThermalLimitPerc Uls f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt AstLmt Per1 ThermalLimit MtrNm f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt AstLmt Per1 VehSpd Kph f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	ltrNm f32	
			Dest
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.5	0.5 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3	3 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	1	1 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.5	0.5 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓

Test Step 2.84 (Repeat Count = 1)			
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-2		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPICmpLimit_MtrNm_f32	1.13999999		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.800000012		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-4		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-6		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.20000003		
gt AstLmt Per1 AssistEOTLimit MtrNm f32.value	4.80000019		
gt AstLmt Per1 AssistStallLimit MtrNm f32.value	4.19999981		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.5999999		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-3		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-6.5		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value			
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-4		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	1		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.0999999		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	75.3000031		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	2.29999995		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTGain Uls f32	tgt AstLmt Per1 AssistEOTGain Uls f32	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_I		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt AstLmt Per1 LimitPercentFiltered Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3:		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_		
gt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrEnable Cnt Igc	tgt AstLmt Per1 LrnPnCtrEnable Cnt Igc	<u>.</u> 190	
		22	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f3	52	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	gc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3.	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_Mt	rNm f32	
Name	Actual Value	Expected Value	Res
	0.600000024	0.600000024 ± 0.000030517578125	IXES!
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value			
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	2.51999998	2.51999998 ± 0.0009765625	

4.19999981

tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value

4.19999981 ± 0.0009765625



Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~		
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓		

Test Step 2.85 (Repeat Count = 1) Name	Input Value		
	· · · · ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1 2		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
C_SumLimPICmpLimit_MtrNm_f32	1.41999996		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.699999988		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-5		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-7		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.30000012		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4.9000001		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.30000019		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.69999981		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-4.19999981		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	3.29999995		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	3.2999995		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	2.29999995		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-1.20000005		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.69999988		
pt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.1999981		
pt_AstLmt_Per1_VehSpd_Kph_f32.value	37.4000015		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3.4000001		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3:	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt AstLmt Per1 AssistEOTDamping M		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNn	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mi		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Ci		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_L		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNn	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_C		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNr	n_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	m_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_N	ltrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm f32	
Name	Actual Value	Expected Value	Res
	0.69999988	0.699999988 ± 0.000030517578125	14631
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value			
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	
at Aptl mt Port Prol imitTorque MtrNm f22 value			
	3.29999995	3.29999995 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3.29999995 2.30999994 4.3000019	2.30999994 ± 0.0009765625 4.30000019 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓

Test Step 2.86 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt ManualTrqCmd MtrNm M f32	-16		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	1.70000005		
	-0.6000003		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value			
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.400000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.400001		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.80000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-5.30000019		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	3.4000001		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.30000019		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	5.37e-009		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	4.5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3:	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_N		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	-	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mi		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M	-	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_L		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrEnable Cnt Igc	tgt_AstLmt_Per1_LmPnCtrEnable_C		
	tgt AstLmt Per1 LrnPnCtrTCmd MtrNn	•	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32		_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_C	_ -	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_N	ltrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	_MtrNm_f32	
Name	Actual Value	Expected Value	Resi
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-8.80000019	-8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-3.52000022	-3.51999998 ± 0.0009765625	
-9	4.400001	4.400001 ± 0.0009765625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓

Test Step 2.87 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt ManualTrqCmd MtrNm M f32	15.9995003		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt Rte Inst Ap AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	1.98000002		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.5		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	1		
	0.5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.0999999		
	4.5		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.9000001		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6 1		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value			
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.89999976		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.89999976		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	4.5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.899999976		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.4000001		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	5.38000009e-008		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	5.5999999		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_	<u>f</u> 32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_	<u>f</u> 32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtrl	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt AstLmt Per1 LrnPnCtrCCDisable Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt AstLmt Per1 LrnPnCtrEnable Cnt Ige		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt AstLmt Per1 LrnPnCtrTCmd MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimit 0/3tail_withtin_52	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PirAssistCmtd_withMin_i32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_1		
tgt Rte Inst Ap AstLmt.AstLmt Peri SumLimTrqCmd MtrNm f32	tgt_AstLmt_Per1_FullCompCmd_MtrNm_i		
tgt_Rte_inst_Ap_AstLmt.AstLmt_Peri_SumLimTrqCmd_wtrNm_r32 tgt_Rte_inst_Ap_AstLmt.AstLmt Peri_SumLimTrqCmd_wtrNm_r32	target_AstLmt_Per1_TSMitCommand_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	W. A.L. (600	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N		
Name	Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.89999976	0.899999976 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	•
@C			



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~	

Test Step 2.88 (Repeat Count = 1) Name	Input Value		
	·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	0		
AstLmt_SteeringAsstDefeat_Cnt_M_Igc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	2.25999999		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.40000006		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	2		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.19999981		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.5999999		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-7		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.100000001		
tgt AstLmt Per1 PosServCCDisable Cnt Igc.value	1		
tgt AstLmt Per1 PowerLimitPerc Uls f32.value	0.100000001		
tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value	5.5999999		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.100000001		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.5		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	3.77999996e-008		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	6	0	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_N	-	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNi	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_M	-	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_C		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_L	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_0	Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_	lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNr	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_C	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_UIs_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNi	m_f32	
tgt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_N	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Peri_ThermalLimit_within_		
		JL .	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	Mtchim f22	
	tgt_AstLmt_Per1_WheelImbalanceCmd		1_
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.100000001	0.100000001 ± 0.000030517578125	
		0 . 0 0000705005	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8	8 ± 0.0009765625	•
	8 0	0 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value			



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.89 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt ManualTrqCmd MtrNm M f32	-1.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	2.53999996		
	-0.300000012		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value			
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	1		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.30000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.6999981		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.0999999		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3.29999995		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.5999999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	6.38000008e-009		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3:	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_N		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	-	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mi		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_L		
tgt_Rte_Inst_Ap_AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrCCDisable C		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt AstLmt Per1 LrnPnCtrEnable Cnt	_ :	
		•	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNn tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_C		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_UIs_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	-	
tgt_Rte_Inst_Ap_AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_N		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	_MtrNm_f32	
Name	Actual Value	Expected Value	Resi
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125	
tgt AstLmt Per1 PreLimitForStall MtrNm f32.value	1.5	1.5 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1.5	-1.5 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.300000012	-0.300000012 ± 0.0009765625	
	4.69999981	4.69999981 ± 0.0009765625	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~	

Test Step 2.90 (Repeat Count = 1) Name	Input Value		
	1		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc AstLmt_ManualTrqCmd_MtrNm_M_f32	1.60000002		
AstLmt SteeringAsstDefeat Cnt M Igc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	2.81999993		
	-0.20000003		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value			
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.80000012		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.4000001		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.80000019		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.19999981		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	1		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.30000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.30000012		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.69999981		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	7.27999989e-008		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_l	MtrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_N		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_0		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_		
gt Rte Inst Ap AstLmt.AstLmt Per1 LimitedReturn MtrNm f32	tgt AstLmt Per1 LimitedReturn MtrNn		
gt Rte Inst Ap AstLmt.AstLmt Per1 LmPnCtrCCDisable Cnt Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt AstLmt Per1 LrnPnCtrTCmd MtrN	- ·	
tgt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_0		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNn	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mtrl	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_I	MtrNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Ul	s_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_1	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd	J_MtrNm_f32	
Name	Actual Value	Expected Value	Resu
	0.300000012	0.300000012 ± 0.000030517578125	
tot AstLmt Per1 LimitPercentFiltered Uls f32.value			
		2 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2	2 ± 0.0009765625 1.60000002 ± 0.0009765625	
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value		2 ± 0.0009765625 1.60000002 ± 0.0009765625 0.479999989 ± 0.0009765625	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓	

Test Step 2.91 (Repeat Count = 1) Name	Input Value		
	· ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
S_SumLimPlCmpLimit_MtrNm_f32	3.0999999		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-0.100000001		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	1		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3		
pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.30000019		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.69999981		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.0999999		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-3		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-3		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
pt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003		
pt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6		
gt AstLmt Per1 PullCompCmd MtrNm f32.value	3.2999995		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.20000003		
t_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.5999999		
t_AstLmt_Per1_VehSpd_Kph_f32.value	503.5		
yt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	Im f32	
		MII_132	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	22	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_1		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt	- •	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_M	rNm f32	
		1	Pac
Name	Actual Value	Expected Value	Res
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.200000003	0.200000003 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3	3 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-3	-3 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.600000024	-0.600000024 ± 0.0009765625	
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.69999981	4.69999981 ± 0.0009765625	
,			



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓

Test Step 2.92 (Repeat Count = 1)	Input Value		
Name	0		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	-7		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat Cnt M lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	3.38000011		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	2		
tgt AstLmt Per1 AssistEOTDamping MtrNm f32.value	4		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.4000001		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.80000019		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.19999981		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	1		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.30000012		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt AstLmt Per1 PowerLimitPerc Uls f32.value	0.300000012		
gt AstLmt Per1 PrkAssistCmd MtrNm f32.value	7		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5.599999		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012		
gt AstLmt Per1 ThermalLimit MtrNm f32.value	7.6999981		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	37.5999985		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3:	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_N		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNn	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mi		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_L	_ ·	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_0		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNn		
gt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_		
gt Rte Inst Ap AstLmt.AstLmt Per1 PosServCCDisable Cnt Igc	tgt AstLmt Per1 PosServCCDisable C		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt AstLmt Per1 PreLimitForStall MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt AstLmt Per1 PreLimitTorque MtrNr	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm	_	
gt Rte Inst Ap AstLmt.AstLmt Per1 SumLimTrqCmd MtrNm f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target AstLmt Per1 TSMitCommand M	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt AstLmt Per1 ThermalLimitPerc Uls	_	
gt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimit MtrNm f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt AstLmt Per1 VehSpd Kph f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm f32	
Name	Actual Value	Expected Value	Resu
	0.300000012	·	Kest
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value		0.300000012 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	0	2 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625 0 ± 0.0009765625	
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.80000019	4.80000019 ± 0.0009765625	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓	

Test Step 2.93 (Repeat Count = 1) Name	Input Value		
	· ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc			
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt 2.25999999		
C_SumLimPlCmpLimit_MtrNm_f32			
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-3		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-8		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	2		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.19999981		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.5999999		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-7		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-2		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.100000001		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-6.5		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.100000001		
pt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.5		
pt_AstLmt_Per1_VehSpd_Kph_f32.value	3.77999996e-008		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	6		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt AstLmt Per1 AssistEOTDamping Mt	rNm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3.	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_t	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cn	t_lgc	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	n_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	<u>f</u> 32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	n_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt	_	
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
at Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimit MtrNm f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt AstLmt Per1 TrqLimitMin MtrNm f3:		
pt Rte Inst Ap AstLmt.AstLmt Per1 VehSpd Kph f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_l	MtrNm f32	
			Pa-
Name	Actual Value	Expected Value	Resi
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.100000001	0.100000001 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8	8 ± 0.0009765625	
	a		
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0 0 4.5999999	0 ± 0.0009765625 0 ± 0.0009765625 4.5999999 ± 0.0009765625	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~

Test Step 2.94 (Repeat Count = 1)			•
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-1.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	2.53999996		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	1		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.30000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.69999981		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.0999999		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3.2999995		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.5999999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	6.38000008e-009		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr1	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_	lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls	:_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt AstLmt Per1 LmPnCtrEnable Cnt Igo		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt AstLmt Per1 PowerLimitPerc Uls f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt AstLmt Per1 PreLimitTorque MtrNm		
tgt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt AstLmt Per1 PrkAssistCmd MtrNm f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target AstLmt Per1 TSMitCommand Mtrl	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f	_	
tgt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimit MtrNm f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3.		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt AstLmt Per1 TrqLimitMin MtrNm f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_M	ltrNm f32	
Name	Actual Value	Expected Value	Resu
		· ·	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_UIs_f32.value	0.200000003	0.200000003 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	1.5	1.5 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-1.5	-1.5 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0.30000012	-0.300000012 ± 0.0009765625	

4.69999981

tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value

4.69999981 ± 0.0009765625



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓

Test Step 2.95 (Repeat Count = 1)	Immut Value		
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt SteeringAsstDefeat Cnt M lgc	1.60000002		
	1 tot Dto Inst An Astlet		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt 2.81999993		
k_SumLimPlCmpLimit_MtrNm_f32	0		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	4		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value			
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.80000012		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.4000001		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.80000019 5.19999981		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	0		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value			
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.300000012		
gt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5.5999999		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.30000012		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.69999981		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	7.27999989e-008		
gt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNn	_	
ggt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mt	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_M		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_I		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cr	_ ·	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNn	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_t		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	22	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_		
Name	Actual Value	Expected Value	Resi
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.30000012	0.300000012 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2	2 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	1.60000002	1.60000002 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0.480000019	0.479999989 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.80000019	4.80000019 ± 0.0009765625	



Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~		
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✓		

Test Step 2.96 (Repeat Count = 1) Name	Input Value		
	· ·		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
s_SumLimPlCmpLimit_MtrNm_f32	3.0999999		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1.5		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	1		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	3		
pt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.30000019		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.6999981		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.0999999		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-3		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-3		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-5		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.200000003		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.20000003		
pt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6		
at AstLmt Per1 PullCompCmd MtrNm f32.value	3.2999995		
pt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.200000003		
yt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.5999999		
yt_AstLmt_Per1_VehSpd_Kph_f32.value	503.5		
t_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	Nm f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	NIII_132	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_	f22	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN		
		_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtrl		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cni		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgd		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_	f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_t	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr	Nm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
pt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	ltrNm f32	
lame	Actual Value	Expected Value	Res
	0.200000003	· ·	I/C2
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value		0.200000003 ± 0.000030517578125	
gt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3	3 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-3	-3 ± 0.0009765625	
	0.000000004	0.600000004 + 0.0000705005	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	-0.600000024 4.69999981	-0.600000024 ± 0.0009765625 4.69999981 ± 0.0009765625	



Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~		
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~		

Test Step 2.97 (Repeat Count = 1) Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-7		
AstLmt SteeringAsstDefeat Cnt M Igc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	3.38000011		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1.5		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value			
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5.4000001		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.80000019		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5.19999981		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	1		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.30000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.300000012		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.6999981		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	37.5999985		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	rNm f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtri		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LimitPercentFiltered Uls f32	tgt_AstLmt_Per1_LimitPercentFiltered_UI		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_t		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrEnable Cnt Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32	_102	
		22	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.300000012	0.300000012 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2	2 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.80000019	4.80000019 ± 0.0009765625	



Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~		
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✓		

Test Step 2.98 (Repeat Count = 1)			3
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-16		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
x_SumLimPlCmpLimit_MtrNm_f32	0		
arget_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-3		
gt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0		
gt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0		
gt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0		
gt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	0		
gt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
gt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
gt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0		
gt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
gt AstLmt Per1 PowerLimitPerc Uls f32.value	0		
gt AstLmt Per1 PrkAssistCmd MtrNm f32.value	-8.80000019		
gt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-8.80000019		
gt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0		
gt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	0		
gt_AstLmt_Per1_VehSpd_Kph_f32.value	0		
gt AstLmt Per1 WheellmbalanceCmd MtrNm f32.value	-8.80000019		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt		
gt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTGain Uls f32	tgt AstLmt Per1 AssistEOTGain Uls f3.	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr		
	tgt_AstLmt_Per1_CombinedDamping_Mt	_	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32			
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_t		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	-	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	n_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt	rNm_f32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	<u>f</u> 32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f	32	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
gt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_I	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
gt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0	0 ± 0.000030517578125	
gt AstLmt Per1 PreLimitForStall MtrNm f32.value	8.80000019	8.80000019 ± 0.0009765625	
gt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-3	-3 ± 0.0009765625	
gt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0	0 ± 0.0009765625	
gt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0	0 ± 0.0009765625	



Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~		
Rte Call AstLmt Per1 CP1 CheckpointReached	1	Rte Call AstLmt Per1 CP1 CheckpointReached	1	✓		

Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	15.9995003		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPICmpLimit_MtrNm_f32	8.80000019		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	1		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	1		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	512		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8.80000019		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	Nm f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtrl	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgd		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtrl		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3.		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Peri_InermalLimit_intrivm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt Peri_InermalLimit_Min MtrNm f32			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_IrqLimitMin_MtrNm_r32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_venSpd_Rpn_t32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_VenSpd_Kpn_t32 tgt_AstLmt_Per1_WheelImbalanceCmd_M	ltrNm f32	
Name			Dagui
	Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_UIs_f32.value	1	1 ± 0.000030517578125	
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625 0 ± 0.0009765625	
gr_nateme_ren_aumemmqomu_withviii_laz.value	8.80000019	8.80000019 ± 0.0009765625	

TEST DETAILS REPORT

2014-09-05, 18:07:58+0530



AstLmt_Per1

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~



Test Case 3: Path Coverage Test

```
Performance Metrics:
Specification
                           (With "None" Instrumentation and WithPS
                           Environment)
                          CPU Cycles:
TS3.1 1985.00 cycles
TS3.2 1867.00 cycles
TS3.3 1821.00 cycles
TS3.4 1745.00 cycles
TS3.5 1747.00 cycles
                          TS3.5 1747.00 cycles
TS3.6 1723.00 cycles
TS3.7 1747.00 cycles
TS3.7 1747.00 cycles
TS3.8 1723.00 cycles
TS3.10 1723.00 cycles
TS3.11 1747.00 cycles
TS3.12 1723.00 cycles
TS3.13 1770.00 cycles
TS3.14 1804.00 cycles
TS3.15 1851.00 cycles
TS3.16 1790.00 cycles
TS3.17 1757.00 cycles
                          TS3.18 1702.00 cycles
TS3.19 1718.00 cycles
TS3.20 1745.00 cycles
Description
                          Vector Description:
                          TS3.1"( PosServCCDisable_Cnt_T_lgc == TRUE )=True
                          and
                          ( LrnPnCtrCCDisable_Cnt_T_lgc == TRUE )=True
                          and
                          (PullCompCmd_MtrNm_T_f32 >= k_SumLimPlCmpLimit_MtrNm_f32) = True
                          ( (ManualTrqCmdEn_Cnt_M_lgc == TRUE) =False && (VehSpd_Kph_T_f32 < FLT_EPSILON) )
                          and
                          ( LrnPnCtrEnable_Cnt_T_lgc == TRUE )=True
                          and
                          (PreLimitTorque_MtrNm_T_f32 >= 0)=True
                          (AbsPreLimitTorque_MtrNm_T_f32<= AssistEOTLimit_MtrNm_T_f32) = True
                          and
                          (PreLimitForStall_MtrNm_T_f32 <= AssistVehSpdLimit_MtrNm_T_f32)=True
                          ( DefeatLimitService_Cnt_T_lgc == TRUE )=True
                          and
                          TS3.2"(PullCompCmd_MtrNm_T_f32 >= k_SumLimPlCmpLimit_MtrNm_f32) = False
                          (PullCompCmd_MtrNm_T_f32 <= -k_SumLimPlCmpLimit_MtrNm_f32) = false
                          and
                          ((ManualTrqCmdEn_Cnt_M_lgc == TRUE) =True && (VehSpd_Kph_T_f32 < FLT_EPSILON))=False
                          and
                          (Sign_f32_m(AssistPowerLimit_MtrNm_T_f32) >= D_ZERO_CNT_S8)=False and(ThermalLimitPerc_Uls_T_f32 >= PowerLimitPerc_Uls_T_f32) = True" TS3.3"((HwTrqPolarity_Cnt_T_f32 * SignMRFMtrVel_Cnt_T_f32 * SignPreLimitForPower_Cnt_T_f32) >= D_ZERO_ULS_F32)=False
                          and
                          (AssistPowerLimit_MtrNm_T_f32 >= Abs_f32_m(k_SumLimOvrSpdMtrTrq2QLmt_MtrNm_f32))=False
                          (AbsPreLimitForPower_MtrNm_T_f32 <= ForPowerLimit_MtrNm_T_f32 )=True" TS3.4"( DefeatLimitService_Cnt_T_lgc == TRUE )= False
                          and
                          (PreLimitForStall_MtrNm_T_f32 <= AssistStallLimit_MtrNm_T_f32) = True
                          and
                          (AbsLimitedTorque_MtrNm_T_f32 <= ThermalLimit_MtrNm_T_f32) = True"
                           TS3.5"( LrnPnCtrEnable_Cnt_T_lgc == TRUE )=False
                          and
                          (AbsPreLimitTorque_MtrNm_T_f32<= AssistEOTLimit_MtrNm_T_f32) = False
                          ànd
                          (PreLimitForStall_MtrNm_T_f32 <= 0)=False
                          and
                          (PreLimitForPower_MtrNm_T_f32 <= 0)=False"
TS3.6"( LrnPnCtrCCDisable_Cnt_T_lgc == TRUE )=False
                          and
                          (AbsPreLimitForPower_MtrNm_T_f32 <= AssistPowerLimit_MtrNm_T_f32 )=False" TS3.7( PosServCCDisable_Cnt_T_lgc == TRUE )=False TS3.8"(PreLimitTorque_MtrNm_T_f32 >= 8.8) = True
                          and
                          (PreLimitForStall_MtrNm_T_f32 >= 8.8)=True"
                           TS3.9"(PreLimitForStall_MtrNm_T_f32 <= AssistVehSpdLimit_MtrNm_T_f32)=False
                          TS3.10(PostLimitForAssistSumCC_MtrNm_T_f32 >= 8.8)=True
                          TS3.11"(PreLimitTorque_MtrNm_T_f32 >= 0)=False
                          and
                          and(PreLimitTorque_MtrNm_T_f32 <= -8.8) = True
                          TS3.12(PullCompCmd_MtrNm_T_f32 <= -k_SumLimPlCmpLimit_MtrNm_f32) = True
                          TS3.12(PullCompCmd_MtrNm_T_f32 <= -k_SumLimPlCmpLimit_MtrNm_f32) = True
TS3.13(PreLimitForStall_MtrNm_T_f32 <= AssistStallLimit_MtrNm_T_f32) = False
TS3.14(LimitPercentFiltered_Uls_T_f32 >= 1)=True
TS3.15(AbsLimitedTorque_MtrNm_T_f32 <= ThermalLimit_MtrNm_T_f32) = False
TS3.16(LimitPercentFiltered_Uls_T_f32 <= 0) = True
TS3.17"( (ManualTrqCmdEn_Cnt_M_lgc == TRUE)=True && (VehSpd_Kph_T_f32 < FLT_EPSILON) )=True
(SteeringAsstDefeat_Cnt_M_lgc == TRUE)=False"
TS3.18(SteeringAsstDefeat_Cnt_M_lgc == TRUE)=True
TS3.19(TrqLimitMin_MtrNm_T_f32 >= 8.8)=True
TS3.20(PreLimitForStall_MtrNm_T_f32 <= 0)=True
```





Test Step 3.1 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	1		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	5		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	4		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_LmPnCtr1Cmd_MtrNm_f32.value tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	7		
tgt AstLmt Per1 OutputRampMult Uls f32.value	0.800000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_rer1_rosservccbisable_cnt_tgt.value	0.69999988		
tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value	6		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
tgt AstLmt Per1 ThermalLimit MtrNm f32.value	7		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	1		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNi	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_I	gc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_	<u>f</u> 32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	igc	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32	ran	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TSMitCommand_MtrNm1_32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSmitCommand_MtrNm1_32	tgt_AstLmt_Per1_TSWittCommand_with tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit_etc_ois_is2 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_Mt	rNm f32	
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.699999988 ± 0.000030517578125	- Locale
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	V
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	6	6 ± 0.0009765625	
		1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	

Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~		
Rte Call Astlimt Per1 CP1 ChecknointReached	1	Rte Call Astlimt Per1 CP1 ChecknointReached	1	-		





Test Step 3.2 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt ManualTrqCmdEn Cnt M lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-1		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	6		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	6		
tgt AstLmt Per1 LrnPnCtrCCDisable Cnt lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt AstLmt Per1 LrnPnCtrTCmd MtrNm f32.value	6		
tgt AstLmt Per1 OpTrqOvr MtrNm f32.value	6		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	5		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	6		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	22	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f3 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_I		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3:		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_	.lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	target_AstLmt_Per1_TSMitCommand_MtrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3.		
tgt Rte Inst Ap AstLmt.AstLmt Per1 ThermalLimit MtrNm f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt AstLmt Per1 TrqLimitMin MtrNm f32		
tgt Rte Inst Ap AstLmt.AstLmt Per1 VehSpd Kph f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_Mt	rNm_f32	
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	_
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	6	6 ± 0.0009765625	~
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	~
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	~
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5	5 ± 0.0009765625	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	•	





Test Step 3.3 (Repeat Count = 1)			•
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	2		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	7		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	7		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	3		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7		
tgt AstLmt Per1 LrnPnCtrCCDisable Cnt lgc.value	1		
	1		
tgt_AstLmt_Per1_LmPnCtrEnable_Cnt_lgc.value	7		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.400000006		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	8		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt	:_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_UI	s_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_t	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg	ıc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3	— ·	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	n f32	
tgt Rte Inst Ap AstLmt.AstLmt Per1 PreLimitTorque MtrNm f32	tgt AstLmt Per1 PreLimitTorque MtrNm	_	
tgt Rte Inst Ap AstLmt.AstLmt Per1 PrkAssistCmd MtrNm f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn		
tgt Rte Inst Ap AstLmt.AstLmt Per1 TSMitCommand MtrNm f32	target_AstLmt_Per1_TSMitCommand_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_UIs_f32	tgt AstLmt Per1 ThermalLimitPerc Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt AstLmt Per1 VehSpd Kph f32		
		Marklan 500	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N		-
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.69999988 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	4	4 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3	3 ± 0.0009765625	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	-





Test Step 3.4 (Repeat Count = 1)			~
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-2		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	6		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	2		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	6		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5		
tgt AstLmt Per1 LmPnCtrCCDisable Cnt lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	3		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5		
tgt AstLmt Per1 OutputRampMult Uls f32.value	0.30000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt AstLmt Per1 PowerLimitPerc Uls f32.value	0.30000012		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5		
tgt AstLmt Per1 PullCompCmd MtrNm f32.value	4		
tgt AstLmt Per1 ThermalLimitPerc Uls f32.value	0.600000024		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	348		
	5		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_l	gc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cr	ıt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	<u>f</u> 32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNi	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mi	_	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt AstLmt Per1 WheelImbalanceCmd	MtrNm f32	
Name	Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	. tosui
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	3	3 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	2	2 ± 0.0009765625	

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	•	





Test Step 3.5 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt ManualTrqCmdEn Cnt M lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	3		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	7		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	7		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	4		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	34		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	6 tot Acti mt Bort AssistCmd MtrNm f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f3 tgt_AstLmt_Per1_AssistEOTDamping_N		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f	-	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTGain_ois_i		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNi	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_N	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_C		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_l		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt AstLmt Per1 LrnPnCtrCCDisable (_	
tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrEnable Cnt Igc	tgt AstLmt Per1 LrnPnCtrEnable Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNi	•	
tgt Rte Inst Ap AstLmt.AstLmt Per1 OpTrgOvr MtrNm f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_C	Cnt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_	f32	
tgt_Rte_Inst_Ap_AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm	1_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNn	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrN	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M	/ltrNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls	s_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd	_MtrNm_f32	
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	5.5	5.5 ± 0.0009765625	~
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	2.75	2.75 ± 0.0009765625	~
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3	3 ± 0.0009765625	~

Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~		
Rte Call Astlimt Per1 CP1 ChecknointReached	1	Rte Call Astlimt Per1 CP1 ChecknointReached	1	-		





Test Step 3.6 (Repeat Count = 1)			~
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-3		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	5		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	6		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	6		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	245		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mi	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt		
tgt_Rte_Inst_Ap_AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U		
tgt_Rte_Inst_Ap_AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Ci		
tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	200	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_t		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	target_AstLmt_Per1_TSMitCommand_Mi		
	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3 tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32		MtrNm f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_		D
Name	Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.699999988 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	7	7 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	4.9000001	4.9000001 ± 0.0009765625	•
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4	4 ± 0.0009765625	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	•





Test Step 3.7 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt ManualTrqCmdEn Cnt M lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	4		
AstLmt SteeringAsstDefeat Cnt M Igc	0		
Rte Inst Ap AstLmt	tgt Rte Inst Ap AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	5		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	2		
tgt AstLmt Per1 AssistEOTDamping MtrNm f32.value	3		
tgt AstLmt Per1 AssistEOTGain Uls f32.value	0.40000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5		
tgt AstLmt Per1 AssistStallLimit MtrNm f32.value	7		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	7		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	6		
tgt AstLmt Per1 OutputRampMult Uls f32.value	0.5		
tgt AstLmt Per1 PosServCCDisable Cnt Igc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.200000003		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	3		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	166		
tgt_AstLmt_Per1_WheelimbalanceCmd_MtrNm_f32.value	4		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_t	f2.2	
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrI	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrN tgt_AstLmt_Per1_AssistVehSpdLimit_l		
tgt_Rte_Inst_Ap_AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt AstLmt Per1 LimitedReturn MtN		
		_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_		
	tgt_AstLmt_Per1_LrnPnCtrEnable_Cn	— -	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrN	-	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Ul tgt_AstLmt_Per1_PosServCCDisable	_	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDIsable_ tgt_AstLmt_Per1_PosServCCDIsable_		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PowerLimitPerc_Uls_f32		_	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_U	_	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_	_132	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	d Machine 622	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCm		
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.40000006	0.400000006 ± 0.000030517578125	~
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	2	2 ± 0.0009765625	~
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	~
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	~
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	3	3 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✓





Test Step 3.8 (Repeat Count = 1)			V
Name	Input Value		
AstLmt ManualTrqCmdEn Cnt M lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-4		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	5		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.5		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.80000019 4		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	1		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5		
tgt AstLmt Per1 LrnPnCtrCCDisable Cnt Igc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	7		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	437		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	-	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_ tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr1	_	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LmPnCtrCCDisable Cnt Igc	tgt AstLmt Per1 LrnPnCtrCCDisable Cnf		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgd	;	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtrl	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f: tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3.		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_veriSpd_Rpin_i32 tgt_AstLmt_Per1_WheelImbalanceCmd_M	ltrNm f32	
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.699999988 ± 0.000030517578125	- tosuit
tgt_AstLmt_Per1_PreLimitFer0enti ilieled_Ois_J2.value	8.80000019	8.80000019 ± 0.0009765625	-
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	7.04000044	7.03999996 ± 0.0009765625	~
	6	6 ± 0.0009765625	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call Astlimt Per1 CP1 ChecknointReached	1	Pte Call Astlimt Per1 CP1 ChecknointReached	1	





Test Step 3.9 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt ManualTrqCmdEn Cnt M lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	6		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	7		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	1 6		
tgt AstLmt Per1 LrnPnCtrCCDisable Cnt lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019		
tgt AstLmt Per1 OpTrqOvr MtrNm f32.value	6		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	367		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	6		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	lm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	22	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_fi tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNi		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrN	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_I		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3.		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_	.lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_ target AstLmt Per1 TSMitCommand MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_TswittCommand_intrN tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt AstLmt Per1 ThermalLimit MtrNm f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_Mt	rNm_f32	
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.600000024	0.600000024 ± 0.000030517578125	~
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✓
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	~
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	✓
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5	5 ± 0.0009765625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	-





Test Step 3.10 (Repeat Count = 1)			~
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	5		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	4		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	6		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	6		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.600000024		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	5		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	4		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	6		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	75		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	7		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_M	trNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	<u>1_</u> f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cn	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_U	ls_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	<u>f</u> 32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_C	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_l	дс	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNi	m_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_M		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_	MtrNm_f32	
Name	Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.69999988	0.699999988 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4	4 ± 0.0009765625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call Astl mt Per1 CP1 ChecknointReached	1	Rte Call Astl mt Per1 CP1 CheckpointReached	1	





k_SumLimPlCmpLimit_MtrNm_f32 7 target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value 0 tgt_AstLmt_Per1_AssistComd_MtrNm_f32.value 4 tgt_AstLmt_Per1_AssistEoTDamping_MtrNm_f32.value 6 tgt_AstLmt_Per1_AssistEoTGain_Uls_f32.value 0.60000000 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value 7 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value 3 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value 3 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value 5 tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value 1 tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value 7 tgt_AstLmt_Per1_LimPnCtrCDisable_Cnt_lgc.value 0 tgt_AstLmt_Per1_LmPnCtrCtDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32.value -8.8000001 tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value 7 tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value 1 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value 7 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value 7	t_Ap_AstLmt 24 9
AstLmt_ManualTrqCmdEn_Cnt_M_lgc 0 AstLmt_SteeringAsstDefeat_Cnt_M_lgc 0 Rte_Inst_Ap_AstLmt tgt_Rte_Inst_K_SumLimPlCmpLimit_MtrNm_f32 k_SumLimPlCmpLimit_MtrNm_f32 7 target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value 0 tgt_AstLmt_Per1_AssistComd_MtrNm_f32.value 4 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value 6 tgt_AstLmt_Per1_AssistEOTCain_Uls_f32.value 6 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value 7 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value 3 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value 3 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value 5 tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value 1 tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value 7 tgt_AstLmt_Per1_LimPnCtrCtDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_LmPnCtrCtoBable_Cnt_lgc.value 1 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value 7 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value 1 tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value 7 tgt_AstLmt_Per1_PirkAssistCmd_MtrNm_f32.value 7 t	t_Ap_AstLmt 24 9
AstLmt_ManualTrqCmd_MtrNm_M_f32 6 AstLmt_SteeringAsstDefeat_Cnt_M_lgc 0 Rte_Inst_Ap_AstLmt tgt_Rte_Inst_Ap_AstLmt k_SumLimPlCmpLimit_MtrNm_f32 7 target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value 0 tgt_AstLmt_Per1_AssistComd_MtrNm_f32.value 4 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value 6 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value 6 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value 7 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value 3 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value 5 tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value 1 tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value 7 tgt_AstLmt_Per1_LimPnCtrCtDisable_Cnt_lgc.value 0 tgt_AstLmt_Per1_LmPnCtrCtDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_DpTrqOvr_MtrNm_f32.value 7 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value 1 tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value 7 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_PirkAssistCmd_MtrNm_f32.val	9
Rte_Inst_Ap_AstLmt tgt_Rte_Inst k_SumLimPlCmpLimit_MtrNm_f32 7 target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value 0 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value 4 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value 6 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value 6 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value 7 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value 3 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value 5 tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value 1 tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value 7 tgt_AstLmt_Per1_LimPnCtrCDisable_Cnt_lgc.value 0 tgt_AstLmt_Per1_LmPnCtrCDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32.value -8.8000001 tgt_AstLmt_Per1_Optrqvr_MtrNm_f32.value 7 tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value 1 tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value 7 tgt_AstLmt_Per1_PorkAssistCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_PirkAssistCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_PirkA	9
k_SumLimPlCmpLimit_MtrNm_f32 7 target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value 0 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value 4 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value 6 tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value 0.60000000 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value 6 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value 7 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value 3 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value 5 tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value 1 tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value 7 tgt_AstLmt_Per1_LmrPnCtrCDisable_Cnt_lgc.value 0 tgt_AstLmt_Per1_LmrPnCtrCDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_LmrPnCtrTCmd_MtrNm_f32.value -8.8000001 tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value 7 tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value 1 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value 7 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value 7 tgt_AstLmt_Per1_PirkAssistCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value 7	9
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value 0 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value 4 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value 6 tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value 0.60000002 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value 6 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value 7 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value 3 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value 5 tgt_AstLmt_Per1_DefeatLimitService_Ont_lgc.value 1 tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value 7 tgt_AstLmt_Per1_LmPnCtrCDisable_Cnt_lgc.value 0 tgt_AstLmt_Per1_LmPnCtrCDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_OpTrqOv_MtrNm_f32.value 7 tgt_AstLmt_Per1_OpTrqOv_MtrNm_f32.value 1 tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value 7 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value 0.69999998	9
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value 4 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value 6 tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value 0.600000000000000000000000000000000000	9
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value 6 tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value 0.600000000000000000000000000000000000	9
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value 0.600000000000000000000000000000000000	9
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value 6 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value 7 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value 3 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value 5 tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value 1 tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value 7 tgt_AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc.value 0 tgt_AstLmt_Per1_LmPnCtrTcdd_MtrNm_f32.value 1 tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value 7 tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value 1 tgt_AstLmt_Per1_PosServCcDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value 0.40000000 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value 0.69999998	9
tgl_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value 7 tgl_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value 3 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value 5 tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value 1 tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value 7 tgt_AstLmt_Per1_LmPnCtrCcDisable_Cnt_lgc.value 0 tgt_AstLmt_Per1_LmPnCtrTcmd_MtrNm_f32.value 1 tgt_AstLmt_Per1_LmPnCtrTcmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value 7 tgt_AstLmt_Per1_OptrqOvr_MtrNm_f32.value 1 tgt_AstLmt_Per1_PosServCcDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value 0.40000000 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value 0.69999998	06
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value 3 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value 5 tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value 1 tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value 7 tgt_AstLmt_Per1_LrmPnCtrCcDisable_Cnt_lgc.value 0 tgt_AstLmt_Per1_LrmPnCtrTcmd_MtrNm_f32.value -8.8000001 tgt_AstLmt_Per1_DopTrqOvr_MtrNm_f32.value 7 tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value 1 tgt_AstLmt_Per1_PosServCcDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value 0.40000000 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value 0.69999998	06
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value 5 tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value 1 tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value 7 tgt_AstLmt_Per1_LmPnCtrCcDisable_Cnt_lgc.value 0 tgt_AstLmt_Per1_LmPnCtrTcmd_MtrNm_f32.value -8.8000001 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value 7 tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value 1 tgt_AstLmt_Per1_PosServCcDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value 0.40000000 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value 0.69999998	06
tgl_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value 1 tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value 7 tgt_AstLmt_Per1_LrnPnCtrCcDisable_Cnt_lgc.value 0 tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value 1 tgt_AstLmt_Per1_LrnPnCtrTcmd_MtrNm_f32.value -8.8000001 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value 7 tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value 1 tgt_AstLmt_Per1_PosServCcDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value 0.40000000 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value 0.69999998	06
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value 7 tgt_AstLmt_Per1_LrmPnCtrCCDisable_Cnt_lgc.value 0 tgt_AstLmt_Per1_LrmPnCtrEnable_Cnt_lgc.value 1 tgt_AstLmt_Per1_LrmPnCtrTCmd_MtrNm_f32.value -8.8000001 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value 7 tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value 1 tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value 0.40000000 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value 0.69999998	06
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value 0 tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value 1 tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value -8.8000001 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value 7 tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value 1 tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value 0.40000000 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value 0.69999998	06
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value 1 tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value -8.8000001 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value 7 tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value 1 tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value 0.40000000 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value 0.69999998	06
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value -8.8000001 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value 7 tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value 1 tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value 0.40000000 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value 0.69999998	06
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value 7 tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value 1 tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value 0.40000000 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value 0.69999998	
tgl_AstLmt_Per1_OutputRampMult_Uls_f32.value 1 tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value 0.40000000 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value 0.69999998	
tgt_AstLmt_Per1_PowerLimitPerc_UIs_f32.value 0.40000000 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_ThermalLimitPerc_UIs_f32.value 0.69999998	
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value 0.69999998	
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value 7 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value 0.69999998	8
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value 0.699999998	88
v	18
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value 7	
tgt_AstLmt_Per1_VehSpd_Kph_f32.value 24	
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value 7	D 4 A 1 10 1 ANN N 700
	Per1_AssistCmd_MtrNm_f32
	Per1_AssistEOTDamping_MtrNm_f32
	Per1_AssistEOTGain_Uls_f32 Per1_AssistEOTLimit_MtrNm_f32
	Per1_AssistStallLimit_MtrNm_f32
	Per1_AssistVehSpdLimit_MtrNm_f32
	Per1_CombinedDamping_MtrNm_f32
	Per1_DefeatLimitService_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_	Per1_LimitPercentFiltered_Uls_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_AstLmt_	Per1_LimitedReturn_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc tgt_AstLmt_	_Per1_LrnPnCtrCCDisable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc tgt_AstLmt_	Per1_LrnPnCtrEnable_Cnt_lgc
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 tgt_AstLmt_	Per1_LrnPnCtrTCmd_MtrNm_f32
	Per1_OpTrqOvr_MtrNm_f32
	Per1_OutputRampMult_Uls_f32
	Per1_PosServCCDisable_Cnt_lgc
	Per1_PowerLimitPerc_Uls_f32
	Per1_PreLimitForStall_MtrNm_f32 Per1_PreLimitTorque_MtrNm_f32
	Per1_PrkAssistCmd_MtrNm_f32
	Per1_PullCompCmd_MtrNm_f32
	Per1_SumLimTrqCmd_MtrNm_f32
	mt Per1 TSMitCommand MtrNm f32
	Per1_ThermalLimitPerc_Uls_f32
	Per1_ThermalLimit_MtrNm_f32
	Per1_TrqLimitMin_MtrNm_f32
tgt_Rte_Inst_Ap_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_	Per1_VehSpd_Kph_f32
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_	Per1_WheelImbalanceCmd_MtrNm_f32
Name Actual Va	lue Expected Value Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value 0.69999998	8 0.699999988 ± 0.000030517578125
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value 8.80000019	8.80000019 ± 0.0009765625
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value -8.8000001	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value -8.8000001	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value 3	3 ± 0.0009765625

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	-





Test Step 3.12 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-8.5		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.800000012		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.69999988		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0.800000012		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	2.5		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	2.20000005		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	4		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-4.5		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value			
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	0.800000012		
	-1.20000005		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012		
tgt AstLmt Per1 ThermalLimit MtrNm f32.value	2.7999995		
tgt AstLmt Per1 VehSpd Kph f32.value	34		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mtr	Nm f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	_	
tgt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTLimit MtrNm f32	tgt AstLmt Per1 AssistEOTLimit MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr1		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt	:_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgd	:	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtrl		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f:		
tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3.		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32	had an ECC	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_W		-
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	~
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	5	5 ± 0.0009765625	*
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-4.5	-4.5 ± 0.0009765625	V
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-3.60000014	-3.5999999 ± 0.0009765625	~
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0.800000012	0.800000012 ± 0.0009765625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call Astl mt Per1 CP1 ChecknointReached	1	Rte Call Astl mt Per1 CP1 CheckpointReached	1	





Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_gc AstLmt_ManualTrqCmd_MtrNm_M_g2 AstLmt_SteeringAsstDefeat_Cnt_M_gc Lig_AstLmt_SteeringAsstDefeat_Cnt_M_gc Lig_AstLmt_Perl_AssistCmd_MtrNm_g32 Larget_AstLmt_Perl_TSMitCommand_MtrNm_g32.value Lig_AstLmt_Perl_AssistCmd_MtrNm_g32.value Lig_AstLmt_Perl_AssistCortCain_Uls_ga2.value Lig_AstLmt_Perl_AssistEOTGain_Uls_ga2.value Lig_AstLmt_Perl_AssistEOTGain_Uls_ga2.value Lig_AstLmt_Perl_AssistEOTGain_Uls_ga2.value Lig_AstLmt_Perl_AssistEOTGain_Uls_ga2.value Lig_AstLmt_Perl_AssistEOTGain_Uls_ga2.value Lig_AstLmt_Perl_AssistStallLimit_MtrNm_ga2.value Lig_AstLmt_Perl_AssistStallLimit_MtrNm_ga2.value Lig_AstLmt_Perl_AssistVehSpdLimit_MtrNm_ga2.value Lig_AstLmt_Perl_AssistVehSpdLimit_MtrNm_ga2.value Lig_AstLmt_Perl_DefeatLimitService_Cnt_lgc_value Lig_AstLmt_Perl_LimitedReturn_MtrNm_ga2.value Lig_AstLmt_Perl_LimitedReturn_MtrNm_ga2.value Lig_AstLmt_Perl_LimitedReturn_MtrNm_ga2.value Lig_AstLmt_Perl_LimitedReturn_MtrNm_ga2.value Lig_AstLmt_Perl_LimitedReturn_MtrNm_ga2.value Lig_AstLmt_Perl_LimitedReturn_MtrNm_ga2.value Lig_AstLmt_Perl_LimitedReturn_MtrNm_ga2.value Lig_AstLmt_Perl_LimitedReturn_MtrNm_ga2.value Lig_AstLmt_Perl_LimitedReturn_MtrNm_ga2.value Lig_AstLmt_Perl_DoftrCodliasable_Cnt_lgc_value Lig_AstLmt_Perl_DoftrCodliasable_Cnt_lgc_value Lig_AstLmt_Perl_PoservCodliasable_Cnt_lgc_value Lig_AstLmt_Perl_PoservCodliasable_Cnt_lgc_value Lig_AstLmt_Perl_Powerl.mitPerc_Uls_ga2.value Lig_AstLmt_Perl_Powerl.mitPerc_Uls_ga2.value Lig_AstLmt_Perl_Powerl.mitPerc_Uls_ga2.value Lig_AstLmt_Perl_Powerl.mitPerc_Uls_ga2.value Lig_AstLmt_Perl_Defemd_MtrNm_ga2.value Lig_AstLmt_Perl_Defemd_MtrNm_ga2.value Lig_AstLmt_Perl_ThermalLimitPerl_Uls_ga2.value Lig_AstLmt_Perl_ThermalLimitPerl_AssistCod_MtrNm_ga2 Lig_AstLmt_Perl_AssistCod_MtrNm_ga2.value Lig_AstLmt_Perl_Defemd_MtrNm_ga2.value Lig_AstLmt_Perl_Defemd_MtrNm_ga2.value Lig_AstLmt_Perl_AssistCod_MtrNm_ga2.value Lig_AstLmt_Perl_AssistCod_MtrNm_ga2.value Lig_AstLmt_Perl_AssistCod_MtrNm_ga2.value Lig_AstLmt_Perl_AssistCod_MtrNm_ga2.value Lig_AstLmt_Perl_Assist		
AstLmt_ManualTrqCmd_MtrNm_M_f32 AstLmt_SteeringAsstDefeat_Cnt_M_lgc Rte_Inst_Ap_AstLmt k_sumLimplCmpLimit_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value tgt_AstLmt_Per1_AssistCntd_MtrNm_f32.value dgt_AstLmt_Per1_AssistCoTDamping_MtrNm_f32.value tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value dgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value dgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value dgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value dgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value dgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value dgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value dgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value dgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value dgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value dgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value dgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value dgt_AstLmt_Per1_LimPnCtrCCDisable_Cnt_lgc.value dgt_AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc.value dgt_AstLmt_Per1_DefractOff_MtrNm_f32.value dgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value dgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value dgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value dgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value dgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value dgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value dgt_AstLmt_Per1_ProwerLimitPerc_Uls_f32.value dgt_AstLmt_Per1_DefrompCmd_MtrNm_f32.value dgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value dgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value dgt_AstLmt_Per1_ThermalLimitMtrNm_f32.value dgt_AstLmt_Per1_ThermalLimitMtrNm_f32.value dgt_AstLmt_Per1_ThermalLimitMtrNm_f32.value dgt_AstLmt_Per1_ThermalLimitMtrNm_f32.value dgt_AstLmt_Per1_ThermalLimitMtrNm_f32.value dgt_AstLmt_Per1_ThermalLimitMtrNm_f32.value dgt_AstLmt_Per1_ThermalLimitMtrNm_f32.value dgt_AstLmt_Per1_AssistCond_MtrNm_f32.value dgt_AstLmt_Per1_AssistCond_MtrNm_f32.value dgt_AstLmt_Per1_AssistCond_MtrNm_f32.value dgt_AstLmt_Per1_AssistCond_MtrNm_f32 dgt_AstLmt_Per1_AssistEond_MtrNm_f32 dgt_AstLmt_Per1_AssistEond_MtrNm_f32 dgt_AstLmt_Per1_AssistEond_MtrNm_f32 dgt_AstLmt_Per1_AssistEond_MtrNm_f32 dgt_AstLmt_Per1_Assist		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc 1 Rte_Inst_Ap_AstLmt tgt_Rte_Inst_Ap_AstLmt k_SumLimPlCmpLimit_MtrNm_f32 0.200000003 target_AstLmt_Per1_AssistComd_MtrNm_f32.value 2 tgt_AstLmt_Per1_AssistComd_MtrNm_f32.value -8 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value 2 tgt_AstLmt_Per1_AssistEOTCain_Uls_f32.value 0 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value 1.10000002 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value 0.800000012 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value 2.5 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value 7 tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value 0 tgt_AstLmt_Per1_LmPnCtrCcDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_LmPnCtrCcDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32.value 5.5999999 tgt_AstLmt_Per1_Ortprqovr_MtrNm_f32.value -1 tgt_AstLmt_Per1_PorservCCDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value 0.200000003 tgt_AstLmt_Per1_PrrAssistCmd_MtrNm_f32.value 5 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value 6		
k_SumLimPlCmpLimit_MtrNm_f32 0.200000003 target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value 2 tgt_AstLmt_Per1_AssistComd_MtrNm_f32.value -8 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value 2 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value 0 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value 1.10000002 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value 0.800000012 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value 7 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value 7 tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value 0 tgt_AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_LmPnCtrCaptle_Cnt_lgc.value 1 tgt_AstLmt_Per1_LmPnCtrCaptle_MtrNm_f32.value 5.999999 tgt_AstLmt_Per1_Optory_MtrNm_f32.value 0.200000003 tgt_AstLmt_Per1_OptoryTown_Mmm_f32.value 0.200000003 tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value 0.200000003 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value 0.200000003 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value 6.5 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_		
target_AstLmt_Per1_TSMitCommand_MtrNm_r32.value		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value -8 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value 2 tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value 0 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value 1.10000002 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value 0.800000012 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value 2.5 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value 7 tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value 0 tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value 3.29999995 tgt_AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_LmPnCtrCTCmd_MtrNm_f32.value 0 tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32.value 5.5999999 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value -1 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value 1 tgt_AstLmt_Per1_PoservCCDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_PoservCCDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value 0.200000003 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value 5 tgt_AstLmt_Per1_ThermalLimit_Per0_MtrNm_f32.value 0.200000003 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value 2 tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value 0 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value 1.10000002 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value 0.800000012 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value 2.5 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value 7 tgt_AstLmt_Per1_DefeatLimitGervice_Cnt_Igc.value 0 tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value 3.29999995 tgt_AstLmt_Per1_LmPnCtrCCDisable_Cnt_Igc.value 1 tgt_AstLmt_Per1_LmPnCtrTCad_MtrNm_f32.value 0 tgt_AstLmt_Per1_DoTrqOvr_MtrNm_f32.value 5.5999999 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value -1 tgt_AstLmt_Per1_OptrqOvr_MtrNm_f32.value 0.200000003 tgt_AstLmt_Per1_PosServCcDisable_Cnt_Igc.value 1 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value 5 tgt_AstLmt_Per1_PosServCcDisable_Cnt_Igc.value 0.200000003 tgt_AstLmt_Per1_TrermalLimitPerc_Uls_f32.value 0.200000003 tgt_AstLmt_Per1_TrermalLimitPerc_Uls_f32.value 5 tgt_AstLmt_Per1_TrermalLimit_MtrNm_f32.value 6 tgt_AstLmt_Per1_TrermalLimit_MtrNm_		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value 0 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value 1.10000002 tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value 0.800000012 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value 2.5 tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value 7 tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value 3.29999995 tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value 1 tgt_AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc.value 0 tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32.value 5.5999999 tgt_AstLmt_Per1_DpTrqOvr_MtrNm_f32.value -1 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value 0.200000003 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value 1 tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value 0.200000003 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value 5 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value 6.5 tgt_AstLmt_Per1_ThermalLimit MtrNm_f32.value 0.200000003 tgt_AstLmt_Per1_VehSpd_Kph_f32.value 3.0999999 tgt_AstLmt_Per1_AssistComd_MtrNm_f32.value 6 tgt_AstLmt_Per1_AssistComd_MtrNm_f32.value		
tgl_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value 1.10000002 tgl_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value 0.800000012 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value 2.5 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value 7 tgt_AstLmt_Per1_LimitGReturn_MtrNm_f32.value 0 tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value 3.29999995 tgt_AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc.value 0 tgt_AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc.value 0 tgt_AstLmt_Per1_pnCtrCCDM_MtrNm_f32.value 5.5999999 tgt_AstLmt_Per1_OptrqOvr_MtrNm_f32.value -1 tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value 0.200000003 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value 1 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value 5 tgt_AstLmt_Per1_PitCompCmd_MtrNm_f32.value 5 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value 0.200000003 tgt_AstLmt_Per1_ThermalLimitMtrNm_f32.value 0.200000003 tgt_AstLmt_Per1_WebSpd_Kph_f32.value 75 tgt_AstLmt_Per1_AssistComd_MtrNm_f32 tgt_AstLmt_Per1_AssistComd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLm		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value tgt_AstLmt_Per1_LmPnCtrCcDisable_Cnt_lgc.value tgt_AstLmt_Per1_LmPnCtrTcmd_MtrNm_f32.value tgt_AstLmt_Per1_LmPnCtrTcmd_MtrNm_f32.value tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value tgt_AstLmt_Per1_DosServCcDisable_Cnt_lgc.value tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value tgt_AstLmt_Per1_PrincmalLimitPerc_Uls_f32.value tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCod_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value tgt_AstLmt_Per1_LrmPnCtrCCDisable_Cnt_lgc.value tgt_AstLmt_Per1_LrmPnCtrEnable_Cnt_lgc.value tgt_AstLmt_Per1_LrmPnCtrTCmd_MtrNm_f32.value tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value tgt_AstLmt_Per1_OptrqOvr_MtrNm_f32.value tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value tgt_AstLmt_Per1_VehSpd_Kph_f32.value tgt_AstLmt_Per1_VehSpd_Kph_f32.value tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value tgt_AstLmt_Per1_LrnPnCtrCmd_MtrNm_f32.value tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value tgt_AstLmt_Per1_PreflCompCmd_MtrNm_f32.value tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value tgt_AstLmt_Per1_VehSpd_Kph_f32.value tgt_AstLmt_Per1_VehSpd_Kph_f32.value tgt_AstLmt_Per1_VehSpd_Kph_f32.value tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value tgt_AstLmt_Per1_LrmPnCtrCCDisable_Cnt_lgc.value tgt_AstLmt_Per1_LrmPnCtrCCDisable_Cnt_lgc.value tgt_AstLmt_Per1_LrmPnCtrCmd_MtrNm_f32.value tgt_AstLmt_Per1_LrmPnCtrTcmd_MtrNm_f32.value tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value tgt_AstLmt_Per1_OptrqOvr_MtrNm_f32.value tgt_AstLmt_Per1_PosservCcDisable_Cnt_lgc.value tgt_AstLmt_Per1_PosservCcDisable_Cnt_lgc.value tgt_AstLmt_Per1_PosservCcDisable_Cnt_lgc.value tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value tgt_AstLmt_Per1_PreflCompCmd_MtrNm_f32.value tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value tgt_AstLmt_Per1_VehSpd_Kph_f32.value tgt_AstLmt_Per1_VehSpd_Kph_f32.value tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCod_MtrNm_f32 tgt_AstLmt_Per1_AssistCod_MtrNm_f32 tgt_AstLmt_Per1_AssistCod_mtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value tgt_AstLmt_Per1_LrmPnCtrCcDisable_Cnt_lgc.value tgt_AstLmt_Per1_LrmPnCtrTcmd_MtrNm_f32.value tgt_AstLmt_Per1_LrmPnCtrTcmd_MtrNm_f32.value tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value tgt_AstLmt_Per1_PosServCcDisable_Cnt_lgc.value tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value tgt_AstLmt_Per1_VehSpd_Kph_f32.value tgt_AstLmt_Per1_VehSpd_Kph_f32.value tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value tgt_Re_Inst_Ap_AstLmt.AstLmt_Per1_AssistCTDamping_MtrNm_f32 tgt_Re_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_Re_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_Re_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_AstLmt_Per1_LrnPnCtrCcDisable_Cnt_lgc.value tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value tgt_AstLmt_Per1_PosServCcDisable_Cnt_lgc.value tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value tgt_AstLmt_Per1_VehSpd_Kph_f32.value tgt_AstLmt_Per1_VehSpd_Kph_f32.value tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_AstLmt_Per1_LrnPnCtrEnable_ont_igc.value tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value tgt_AstLmt_Per1_PosServCCDisable_Cnt_igc.value tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value tgt_AstLmt_Per1_VehSpd_Kph_f32.value tgt_AstLmt_Per1_VehSpd_Kph_f32.value tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_Ret_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_Ret_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_Ret_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32.value 5.5999999 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value -1 tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value 0.200000003 tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value 0.200000003 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value 5 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value -6.5 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value 0.200000003 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value 3.0999999 tgt_AstLmt_Per1_VehSpd_Kph_f32.value 75 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value 6 tgt_Re_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_Re_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_Re_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_Re_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value -1 tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value 0.200000003 tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value 1 tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value 0.200000003 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value 5 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value -6.5 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value 0.200000003 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value 3.0999999 tgt_AstLmt_Per1_VehSpd_Kph_f32.value 75 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value 6 tgt_Re_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_Re_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_Re_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_Re_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value tgt_AstLmt_Per1_VehSpd_Kph_f32.value tgt_AstLmt_Per1_VehSpd_Kph_f32.value tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value tgt_Re_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_Re_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_Re_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_Re_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_AstLmt_Per1_PosServCDisable_Cnt_lgc.value tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value tgt_AstLmt_Per1_VehSpd_Kph_f32.value tgt_AstLmt_Per1_VehSpd_Kph_f32.value tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value 0.200000003 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value 5 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value -6.5 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value 0.200000003 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value 3.0999999 tgt_AstLmt_Per1_VehSpd_Kph_f32.value 75 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value 6 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value tgt_AstLmt_Per1_VehSpd_Kph_f32.value tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value tgt_AstLmt_Per1_VehSpd_Kph_f32.value tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value tgt_AstLmt_Per1_VehSpd_Kph_f32.value tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value tgt_Rte_Inst_Ap_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value fgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value 6 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_igt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_Per1_AssistCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_AstLmt_Per1_AssistEOTGain_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	f32	
tot Rte Inst An Astl mt Astl mt Per1 AssistStalll imit MtrNm f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32 tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32 tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32	132	
tgt_Rte_Inst_Ap_AstLmt_Per1_DefeatLimitService_Cnt_Igc tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc tgt_AstLmt_Per1_DefeatLimitService_Cnt_Igc		
tgt_Rte_Inst_Ap_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32 tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 tgt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32 tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_PosServCCDisable_Cnt_Igc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_UIs_f32 tgt_AstLmt_Per1_PowerLimitPerc_UIs_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32 tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32 tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_PrkAssistCmd_MtrNm_f32 tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_PullCompCmd_MtrNm_f32 tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32 tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 target_AstLmt_Per1_TSMitCommand_MtrNm_f32	f32	
tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_ThermalLimit_MtrNm_f32 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_TrqLimitMin_MtrNm_f32 tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_VehSpd_Kph_f32 tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32 tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm	n_f32	
	Expected Value	Result
	0.200000003 ± 0.000030517578125	~
*	3 ± 0.0009765625	~
) ± 0.0009765625	~
) ± 0.0009765625	~
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value 0.800000012 0	0.800000012 ± 0.0009765625	_

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte Call Astlimt Per1 CP1 ChecknointReached	1	Rte Call Astlimt Per1 CP1 ChecknointReached	1	-





Test Step 3.14 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	2		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.899999976		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	3		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	3.29999995		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.10000001		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.5		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.9000001		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.30000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	1		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	-4		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	3		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	7.0999999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	26		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mt	rNm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cn	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul	s_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_	<u>f</u> 32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr	nt_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg	gc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_t	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cn	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_UIs_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3	2	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNn	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_	_f32	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNr	n_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_	<u>f</u> 32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f3:	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_I	MtrNm_f32	
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	1	1 ± 0.000030517578125	-
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	7	7 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	7	7 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	3.5	3.5 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5.9000001	5.9000001 ± 0.0009765625	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✓





Test Step 3.15 (Repeat Count = 1)			· ·
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	8.80000019		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	1		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-3		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	1		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	1		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	1		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	512		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8.80000019		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
tgt_Rte_Inst_Ap_AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_UI		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	/ltrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	1	1 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	8	8 ± 0.0009765625	•
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	8	8 ± 0.0009765625	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✓





Test Step 3.16 (Repeat Count = 1)			•
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-8.80000019		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	0		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	0		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	0		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-8.80000019		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0		
tgt AstLmt Per1 PrkAssistCmd MtrNm f32.value	-8.80000019		
tgt AstLmt Per1 PullCompCmd MtrNm f32.value	-8.80000019		
tgt AstLmt Per1 ThermalLimitPerc Uls f32.value	0		
tgt AstLmt Per1 ThermalLimit MtrNm f32.value	0		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	0		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-8.80000019		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt AstLmt Per1 AssistEOTDamping Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	_	
tgt Rte Inst Ap AstLmt.AstLmt Per1 AssistEOTLimit MtrNm f32	tgt AstLmt Per1 AssistEOTLimit MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_UI		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_t		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LmPnCtrTCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrgOvr_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	_132	
	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32	22	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_Igc	tgt_AstLmt_Per1_PosServCCDisable_Cn	— ·	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheeIImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_I	MtrNm_f32	
Name	Actual Value	Expected Value	Resu
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0	0 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-2	-2 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-0	0 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	0	0 ± 0.0009765625	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✓





Test Step 3.17 (Repeat Count = 1)			•
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-8.80000019		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	0		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.800000012		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.400000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.400001		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.80000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	5 5000000		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	0.800000012 3		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	-2.0999999 0.800000012		
	7.30000012		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	5.37e-009		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	4		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt Rte Inst Ap AstLmt.AstLmt Peri AssistEOTDamping MtrNm f32	tgt AstLmt Per1 Assistentumining Mtr	Nm f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	_	
tgt Rte Inst Ap AstLmt.AstLmt Peri AssistEOTLimit MtrNm f32	tgt AstLmt Per1 AssistEOTLimit MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtrl	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_Igc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgr		
tgt Rte Inst Ap AstLmt.AstLmt Per1 LrnPnCtrTCmd MtrNm f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_		
tgt Rte Inst Ap AstLmt.AstLmt Per1 OpTrqOvr MtrNm f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt Rte Inst Ap AstLmt.AstLmt Per1 OutputRampMult Uls f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3	32	
tgt Rte Inst Ap AstLmt.AstLmt Per1 PosServCCDisable Cnt Igc	tgt_AstLmt_Per1_PosServCCDisable_Cnt		
tgt Rte Inst Ap AstLmt.AstLmt Per1 PowerLimitPerc Uls f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
tgt Rte Inst Ap AstLmt.AstLmt Per1 PreLimitTorque MtrNm f32	tgt AstLmt Per1 PreLimitTorque MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt AstLmt Per1 PullCompCmd MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target AstLmt Per1 TSMitCommand Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt AstLmt Per1 ThermalLimit MtrNm f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt Rte Inst Ap AstLmt.AstLmt Per1 WheelImbalanceCmd MtrNm f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	ltrNm f32	
Name	Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	. tosui
tgt_AstLmt_Per1_PreLimitFerCentFileFer0_Uis_i32.value	8.80000012	8.80000012 ± 0.00030317376123	
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	-8.80000019	-8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-3.52000022	-3.51999998 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.400001	4.400001 ± 0.0009765625	
SC SCORE OF LINGERHOUSENLING VIOLENCE	1.7000001	000001 ± 0.0000100020	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	✓





Test Step 3.18 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	-8.80000019		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.800000012		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	0		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	-6		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-8		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.40000006		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	5		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	4.4000001		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	4.80000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	5.5999999		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.800000012		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	0.800000012 3		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	-2.0999999		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.800000012		
tgt AstLmt Per1 ThermalLimit MtrNm f32.value	7.30000019		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	5.37e-009		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	4		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mti	Nm f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtrl	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Ul	s_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cn	t_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lg	С	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_	<u>f</u> 32	
tgt_Rte_Inst_Ap_AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt	— -	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3:		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_t		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn	_	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_t tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TreImatLimit_MitNim_132 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_TrqLimitwiin_witrNm_i32		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_WheelImbalanceCmd_N	MrNm f32	
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	0.800000012	0.800000012 ± 0.000030517578125	Nesult
tgt_AstLmt_Per1_cimitPercentPillered_Ois_132.value	8.80000019	8.80000019 ± 0.0009765625	
tgt AstLmt Per1 PreLimitForstall_MitNin_i32.value	-8.80000019	-8.80000019 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	-3.52000022	-3.51999998 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	4.400001	4.400001 ± 0.0009765625	
27 ·	1		

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte Call Astlimt Per1 CP1 ChecknointReached	1	Rte Call Astlimt Per1 CP1 ChecknointReached	1	-	





Test Step 3.19 (Repeat Count = 1)			~
Name	Input Value		
AstLmt_ManualTrqCmdEn_Cnt_M_lgc	1		
AstLmt_ManualTrqCmd_MtrNm_M_f32	15.9995003		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	8.80000019		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	1		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	1		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_LmPnCtrCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	1		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	1		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value	1		
tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_PullCompCmd_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	1		
tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32.value	8.80000019		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	512		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	8.80000019		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32	N. 700	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_Mt	_	
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_Mtr	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_Mtr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_UI		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc	tgt_AstLmt_Per1_LimitedReturn_MtrNm_tgt AstLmt Per1 LrnPnCtrCCDisable Cr		
	tgt_AstLmt_Per1_tmPnCtrCcbisable_Cr		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_tmPnCtrPnable_Cnt_ig		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt AstLmt Per1 OpTrqOvr MtrNm f32	_132	
		32	
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_OutputRampMult_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_OutputRampMult_Uls_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosservCcDisable_Cnt_igc tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PosServCCDisable_Cn tgt_AstLmt_Per1_PowerLimitPerc_Uls_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Ois_I32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm		
	tgt_AstLmt_Per1_PreLimitForStall_MtrNm tgt_AstLmt_Per1_PreLimitTorque_MtrNm	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32			
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNn target AstLmt Per1 TSMitCommand Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32	target_AstLmt_Peri_ISMitCommand_Mt	_	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimit_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Peri_Thermail.imit_MtrNm_132 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Peri_TrqLimitMin_MtrNm_132	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_FrqLimitMin_MthMin_is2	tgt_AstLmt_Per1_frqLimitwiin_withm_isz		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VeriSpd_Rpn_is2 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_venspd_kpn_i32 tgt_AstLmt_Per1_WheelImbalanceCmd_N	AtrNm f32	
			D
Name	Actual Value	Expected Value	Resul
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	1	1 ± 0.000030517578125	•
tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	•
tgt_AstLmt_Per1_PreLimitTorque_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	•
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	8.80000019	8.80000019 ± 0.0009765625	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~
Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP1_CheckpointReached	1	~





Test Step 3.20 (Repeat Count = 1)			✓
Name	Input Value		
AstLmt ManualTrqCmdEn Cnt M lgc	0		
AstLmt_ManualTrqCmd_MtrNm_M_f32	2		
AstLmt_SteeringAsstDefeat_Cnt_M_lgc	1		
Rte_Inst_Ap_AstLmt	tgt_Rte_Inst_Ap_AstLmt		
k_SumLimPlCmpLimit_MtrNm_f32	0.899999976		
target_AstLmt_Per1_TSMitCommand_MtrNm_f32.value	2		
tgt_AstLmt_Per1_AssistCmd_MtrNm_f32.value	0		
tgt_AstLmt_Per1_AssistEOTDamping_MtrNm_f32.value	-1		
tgt_AstLmt_Per1_AssistEOTGain_Uls_f32.value	0.100000001		
tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_f32.value	6.5		
tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f32.value	5.9000001		
tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32.value	6.30000019		
tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32.value	-6.5		
tgt_AstLmt_Per1_DefeatLimitService_Cnt_lgc.value	1		
tgt_AstLmt_Per1_LimitedReturn_MtrNm_f32.value	7		
tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32.value	7		
tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32.value	-2		
tgt_AstLmt_Per1_OutputRampMult_Uls_f32.value	0.5		
tgt_AstLmt_Per1_PosServCCDisable_Cnt_lgc.value	0		
tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32.value tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f32.value	1 -4		
tgt_AstLmt_Per1_PtiAssistCmt_within_i32.value	3		
tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f32.value	0.5		
tgt AstLmt Per1 ThermalLimit MtrNm f32.value	7.0999999		
tgt_AstLmt_Per1_VehSpd_Kph_f32.value	26		
tgt_AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32.value	-5		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistCmd_MtrNm_f32	tgt_AstLmt_Per1_AssistCmd_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTDamping_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTDamping_MtrN	Vm f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTGain_Uls_f32	tgt_AstLmt_Per1_AssistEOTGain_Uls_f32	-	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistEOTLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistEOTLimit_MtrNm_	f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistStallLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistStallLimit_MtrNm_f		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32	tgt_AstLmt_Per1_AssistVehSpdLimit_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_CombinedDamping_MtrNm_f32	tgt_AstLmt_Per1_CombinedDamping_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_DefeatLimitService_Cnt_lgc	tgt_AstLmt_Per1_DefeatLimitService_Cnt_	lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitPercentFiltered_Uls_f32	tgt_AstLmt_Per1_LimitPercentFiltered_Uls	_f32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LimitedReturn_MtrNm_f32	tgt_AstLmt_Per1_LimitedReturn_MtrNm_f3	2	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrCCDisable_Cnt_Igc	tgt_AstLmt_Per1_LrnPnCtrCCDisable_Cnt	_lgc	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc	tgt_AstLmt_Per1_LrnPnCtrEnable_Cnt_lgc		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f32	tgt_AstLmt_Per1_LrnPnCtrTCmd_MtrNm_f	32	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OpTrqOvr_MtrNm_f32	tgt_AstLmt_Per1_OpTrqOvr_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_OutputRampMult_Uls_f32	tgt_AstLmt_Per1_OutputRampMult_Uls_f3:		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PosServCCDisable_Cnt_lgc	tgt_AstLmt_Per1_PosServCCDisable_Cnt_	<u>lgc</u>	
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PowerLimitPerc_Uls_f32	tgt_AstLmt_Per1_PowerLimitPerc_Uls_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitForStall_MtrNm_f32	tgt_AstLmt_Per1_PreLimitForStall_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PreLimitTorque_MtrNm_f32	tgt_AstLmt_Per1_PreLimitTorque_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PrkAssistCmd_MtrNm_f32	tgt_AstLmt_Per1_PrkAssistCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_PullCompCmd_MtrNm_f32	tgt_AstLmt_Per1_PullCompCmd_MtrNm_f3		
tgt_Rte_Inst_Ap_AstLmt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32	tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_TSMitCommand_MtrNm_f32	target_AstLmt_Per1_TSMitCommand_MtrN		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimitPerc_Uls_f32 tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_ThermalLimit_MtrNm_f32	tgt_AstLmt_Per1_ThermalLimitPerc_Uls_f3 tgt_AstLmt_Per1_ThermalLimit_MtrNm_f32		
tgt_Rte_inst_Ap_AstLmt.AstLmt_Per1_InermaiLimit_MtrNm_f32 tgt_Rte_inst_Ap_AstLmt.AstLmt_Per1_TrqLimitMin_MtrNm_f32	tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_VehSpd_Kph_f32	tgt_AstLmt_Per1_VehSpd_Kph_f32		
tgt_Rte_Inst_Ap_AstLmt.AstLmt_Per1_WheelImbalanceCmd_MtrNm_f32	tgt_AstLmt_Per1_veriSpd_kpir_i32 tgt_AstLmt_Per1_WheelImbalanceCmd_M	trNm f32	
Name	Actual Value	Expected Value	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uls_f32.value	1	1 ± 0.000030517578125	Result
tgt_AstLmt_Per1_LimitPercentFiltered_Uis_132.value tgt_AstLmt_Per1_PreLimitForStall_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_PreLimitForstail_withtin_isz.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_SumLimTrqCmd_MtrNm_f32.value	0	0 ± 0.0009765625	
tgt_AstLmt_Per1_TrqLimitMin_MtrNm_f32.value	5.9000001	5.9000001 ± 0.0009765625	
	1.00000	1.130001. 2 0.0300100020	

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	Rte_Call_AstLmt_Per1_CP0_CheckpointReached	1	~	
Rte Call Astlimt Per1 CP1 ChecknointReached	1	Rte Call Astlimt Per1 CP1 ChecknointReached	1	-	

TEST DETAILS REPORT

2014-09-05, 18:11:04+0530



AstLmt_Scom_SetSteeringAssistDefeat

 Project
 AstLmt_CM

 Module
 AstLmt

Test Object AstLmt_Scom_SetSteeringAssistDefeat

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1	
Successful	1	✓
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\AstLmt_CM
Configuration File	D:\Synergy_Work_Area\AstLmt_CM\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\AstLmt_CM\src\Ap_AstLmt.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AstLmt_CM\utp\contract -I\$(PROJECTROOT)\AstLmt_CM\utp\contract\Ap_AstLmt -I\$ (PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler \tms470\include

Name	Text
Module 'AstLmt'	**************************************
	Name of Tester:Ajit Shrivastava Code File(s) Under Test:Ap_AstLmt.c Code File(s) Version:9 Module Design Document:Assist_Sum_Limit_CurrentMode_MDD.docx Module Design Document Version:3 Data Dictionary Version:9 Unit Test Plan Version:6 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.30 Total FLASH Used (Bytes):942 Total RAM Used (Bytes):6 Total CALS Used (Bytes):8 Special Test Requirements: Test Date:09-05-2014 Comments:"NOTE 1: Inline functions defined in ""GlobalMacro.h"" are not Unit Tested. NOTE 2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference."

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 3.2
Time Unit	Cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg

TEST DETAILS REPORT AstLmt_Scom_SetSteeringAssistDefeat

2014-09-05, 18:11:04+0530



Attributes	
Name	Value
Workspage File	D'\Cymergy Work Area\Agtimt CM\ImitTegtEny\gonfig\IDE TMC570 DEDIG WCD



Test Case 1: Boundary Test

Specification Performance Metrics:

(With "None" Instrumentation and WithPS Environment)

CPU Cycles:

TS1.1 241.00 Cycles TS1.2 241.00 Cycles

Description Vector Description:

TS1.1 SteeringAsstDefeat1_Cnt_lgc = True TS1.2 SteeringAsstDefeat1_Cnt_lgc = False

Test Step 1.1 (Repeat Count = 1)			
Name	Input Value		
Rte_Inst_Ap_AstLmt	target_Rte_Inst_Ap_AstLmt		
SteeringAsstDefeat_Cnt_lgc 1			
target_Rte_Inst_Ap_AstLmt.Pim_SteerAsstDefeat target_Pim_SteerAsstDefeat			
Name	Actual Value	Expected Value	Result
target_Pim_SteerAsstDefeat	1	1	~

Test Step Call Trace							
Actual Function	Count	Expected Function	Count	Result			
Rte Call Ap AstLmt SteeringAsstDefeat WriteBlock	1	Rte Call Ap AstLmt SteeringAsstDefeat WriteBlock	1				

Test Step 1.2 (Repeat Count = 1)				
Name	Input Value			
Rte_Inst_Ap_AstLmt	target_Rte_Inst_Ap_AstLmt			
SteeringAsstDefeat_Cnt_lgc	0			
target_Rte_Inst_Ap_AstLmt.Pim_SteerAsstDefeat	target_Pim_SteerAsstDefeat			
Name	Actual Value	Expected Value	Result	
target Pim SteerAsstDefeat	0	0	✓	

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_Ap_AstLmt_SteeringAsstDefeat_WriteBlock	1	Rte_Call_Ap_AstLmt_SteeringAsstDefeat_WriteBlock	1	~	