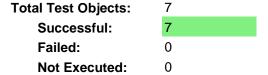
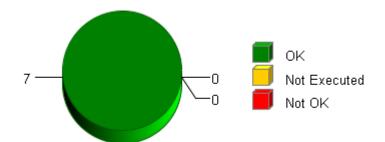


Summary

Overall Test Object Results (including Coverage)



Date: 2016-09-15 **Time:** 18:24:58+0530



Selected Project Items

Test Object "CBD_UnitTest/PICurrCntrl/CalLowPassFiltBilinearOut"

Test Object "CBD UnitTest/PICurrCntrl/CalLowPassFiltBilinearTerm"

Test Object "CBD_UnitTest/PICurrCntrl/CalLowPassFiltVecuOut"

Test Object "CBD UnitTest/PICurrCntrl/IntegralStateVarNonOperState"

Test Object "CBD_UnitTest/PICurrCntrl/LoaMtgtnSclFac"

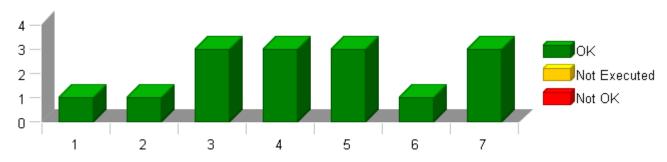
Test Object "CBD_UnitTest/PICurrCntrl/PICurrCntrl_Init"

Test Object "CBD_UnitTest/PICurrCntrl/PICurrCntrl_Per1"

Used Test Environments

TI TMS 570 PLS UDE (Default)

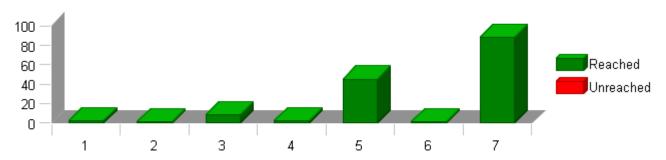
Test Case Results for Each Test Object (without Coverage)



The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

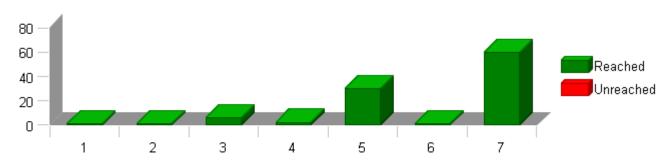


Statement (C0) Coverage: Total Statements for Each Test Object



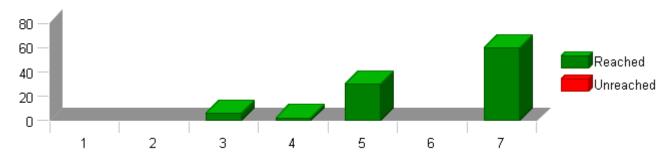
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

Decision Coverage: Total Decision Outcomes for Each Test Object

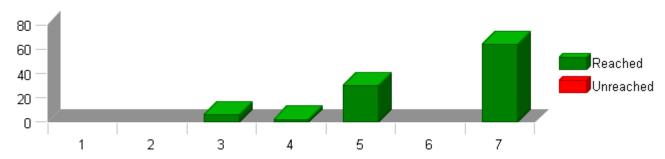


The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.



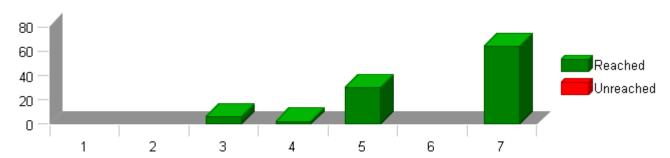
MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

TEST OVERVIEW REPORT

2016-09-15, 18:24:58+0530



Project MtrCtrl_CM_SF99B

Test Object List

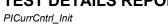
The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	DC	MC/DC	MCC	Test Cases Resu	ult
	MtrCtrl_CM_SF99B	100 %	100 %	100 %	100 %	100 %	15 of 15 passed	~
	CBD_UnitTest	100 %	100 %	100 %	100 %	100 %	15 of 15 passed	~
	PICurrCntrl	100 %	100 %	100 %	100 %	100 %	15 of 15 passed	~
1	<u>CalLowPassFiltBilinearOut</u>	100 %	100 %	-	-	-	1 of 1 passed	~
2	<u>CalLowPassFiltBilinearTerm</u>	100 %	100 %	-	-	-	1 of 1 passed	~
3	<u>CalLowPassFiltVecuOut</u>	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	•
4	<u>IntegralStateVarNonOperState</u>	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	~
5	<u>LoaMtgtnSclFac</u>	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	•
6	PICurrCntrl Init	100 %	100 %	-	-	-	1 of 1 passed	~
7	PICurrCntrl_Per1	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	•

© Report created by TESSY V3.1.13, report template V2.0

2016-09-15, 18:06:18+0530





Project MtrCtrl_CM_SF99B

Module PICurrCntrl
Test Object PICurrCntrl_Init

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1	
Successful	1	✓
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\MtrCtrl_CM_SF99B
Configuration File	D:\Synergy_Work_Area\MtrCtrl_CM_SF99B\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\MtrCtrl_CM\src\Ap_PlCurrCntrl.c
Compiler Options	-D_DATA_ACCESS= -Dsqrtf=sqrtf -Dconst= -I\$(PROJECTROOT)\MtrCtrl_CM\utp\contract\Ap_PlCurrCntrl -I\$(PROJECTROOT) \MtrCtrl_CM\utp\contract -I\$(PROJECTROOT)\MtrCtrl_CM\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef \include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dsqrtf=sqrtf -Dconst= -I\$(PROJECTROOT)\MtrCtrl_CM\utp\contract\Ap_PICurrCntrl -I\$(PROJECTROOT) \MtrCtrl_CM\utp\contract -I\$(PROJECTROOT)\MtrCtrl_CM\\include -I\$(PROJECTROOT)\NxtrLib\\include -I\$(PROJECTROOT)\StdDef \include -I\$(Compiler Install Path)\include

Comments/Description/Sp	ecification
Name	Text
Module 'PICurrCntrl'	Name of Tester:Komal Sharma Code File(s) Under Test:App PlCurrCntrl.c Code File(s) Version:16 Module Design Document:PlCurrentContrl.doc Module Design Document:PlCurrentContrl.doc Module Design Document Version:12 Data Dictionary Version:15 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS570_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):2834 Total FLASH Used (Bytes):2834 Total FLASH Used (Bytes):2865 Special Test Requirements:NA Test Date:Sy15/2016 Comments:"Note 1: INLINE functions defined in globalmacro.h are not unit tested. Note 2: ""CBD_Sandbox_dbg.map""map file is embedded for reference. Note 3: Out of range value is given in function ""LoaMtgtnSclFac" for variables ""k_CLOAFdbackSignalSclFacSlew_UlspS_f32,k_ILOAFdbackSignalSclFacSlew_UlspS_f32,k_DualEcuSignalSclFacSlew_UlspS_f32, PlCurrCntrl_DualEcuFailSclFac_Uls_M_f32" to achieve 100% path coverage in Path sheet. Note 4: In function PlCurrCntrl_Per1 PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 and PlCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 variables are going out of range. Note 5: In function PlCurrCntrl_Per1, the range of MtrPosComputationDelay_Rad_M_f32[2] is considered as -3.14 to 3.14"

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

2016-09-15, 18:06:18+0530

PICurrCntrl_Init



Attributes	
Name	Value
InitObjDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj</pre>
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
Timer Unit	Cycles
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\MtrCtrl_CM_SF99B\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



Test Case 1: BoundaryTest

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles:

TS 1.1 51.00 Cycles
TS 1.2 51.00 Cycles
TS 1.3 51.00 Cycles
TS 1.4 51.00 Cycles
TS 1.5 51.00 Cycles
TS 1.6 51.00 Cycles
TS 1.6 51.00 Cycles
TS 1.7 51.00 Cycles
TS 1.8 51.00 Cycles
TS 1.9 51.00 Cycles
TS 1.10 51.00 Cycles
TS 1.11 51.00 Cycles
TS 1.12 51.00 Cycles
TS 1.13 51.00 Cycles
TS 1.12 51.00 Cycles

Vector Description: Description

TS1.1All Min

TS1.2All Max
TS1.3k_MtrVoltVecuFiltKn_Hz_f32==>Min
TS1.4k_MtrVoltVecuFiltKn_Hz_f32==>Max
TS1.5k_MtrVoltVecuFiltKn_Hz_f32==>Pos
TS1.6k_MtrVoltVecuFiltKn_Hz_f32==>Def
TS1.7k_PiSamplingTs_Sec_f32==>Min
TS1.8k_PiSamplingTs_Sec_f32==>Max
TS1.9k_PiSamplingTs_Sec_f32==>Pos/Def
TS1.10k_MtrVoltQaxFiltFFKn_Hz_f32==>Min
TS1.11k_MtrVoltQaxFiltFFKn_Hz_f32==>Max
TS1.12k_MtrVoltQaxFiltFFKn_Hz_f32==>Pos
TS1.13k_MtrVoltQaxFiltFFKn_Hz_f32==>Def TS1.2All Max

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
k_MtrVoltQaxFiltFFKn_Hz_f32	0.10000001		
k_MtrVoltVecuFiltKn_Hz_f32	0.100000001		
k_PiSamplingTs_Sec_f32	6.2500003e-005		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	~
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	50928.5781	50928.582	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.963457e-005	1.96345682e-005	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	50928.5781	50928.582	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	1.963457e-005	1.96345682e-005	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	•

Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
k_MtrVoltQaxFiltFFKn_Hz_f32	1000		
k_MtrVoltVecuFiltKn_Hz_f32	1000		
k_PiSamplingTs_Sec_f32	0.10000001		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	~
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-0.996816874	-0.996816874	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.996826947	0.996827006	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	-0.996816874	-0.996816874	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.996826947	0.996827006	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	~	



PICurrCntrl_Init

Test Step 1.3 (Repeat Count = 1)			✓
Name	Input Value		
k_MtrVoltQaxFiltFFKn_Hz_f32	709.776001		
k_MtrVoltVecuFiltKn_Hz_f32	0.10000001		
k_PiSamplingTs_Sec_f32	0.10000001		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	~
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	30.8309879	30.8309879	~
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0304590296	0.0304590277	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	-0.995515347	-0.995515347	~
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.995535374	0.995535374	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	~

Test Step 1.4 (Repeat Count = 1)			
Name	Input Value		
k_MtrVoltQaxFiltFFKn_Hz_f32	45.4935989		
k_MtrVoltVecuFiltKn_Hz_f32	1000		
k_PiSamplingTs_Sec_f32	0.10000001		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	~
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-0.996816874	-0.996816874	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.996826947	0.996827006	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	-0.930031955	-0.930031955	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.934607327	0.934607327	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	~

Test Step 1.5 (Repeat Count = 1)			✓
Name	Input Value		
k_MtrVoltQaxFiltFFKn_Hz_f32	414.50531		
k_MtrVoltVecuFiltKn_Hz_f32	59.2360001		
k_PiSamplingTs_Sec_f32	0.100000001		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	~
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-0.946264148	-0.946264088	~
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.949004412	0.949004412	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	-0.992320716	-0.992320716	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.992379308	0.992379248	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	~

Test Step 1.6 (Repeat Count = 1)			✓
Name	Input Value		
k_MtrVoltQaxFiltFFKn_Hz_f32	863.495728		
k_MtrVoltVecuFiltKn_Hz_f32	200		
k_PiSamplingTs_Sec_f32	0.10000001		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125 1 ± 0.00048828125	~
	1 1 -0.984084487		· ·
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1 1 -0.984084487 0.984333813	1 ± 0.00048828125	•

PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32

CalLowPassFiltBilinearTerm

2016-09-15, 18:06:18+0530



0.996327221

FIGUICIUI_IIII		
Name	Actual Value	Expected Value

2

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result

CalLowPassFiltBilinearTerm

0.996327221

Test Step 1.7 (Repeat Count = 1)			✓
Name	Input Value		
k_MtrVoltQaxFiltFFKn_Hz_f32	791.291382		
k_MtrVoltVecuFiltKn_Hz_f32	289.923096		
k_PiSamplingTs_Sec_f32	6.2500003e-005		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	~
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	~
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	16.5665817	16.5665836	~
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0538602099	0.0538602062	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	5.4362607	5.43626118	~
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.134476185	0.134476185	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	~

Test Step 1.8 (Repeat Count = 1)			~
Name	Input Value		
k_MtrVoltQaxFiltFFKn_Hz_f32	801.490417		
k_MtrVoltVecuFiltKn_Hz_f32	302.319794		
k_PiSamplingTs_Sec_f32	0.100000001		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	•
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	~
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-0.989471078	-0.989471078	•
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.98958081	0.98958081	~
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	-0.996028543	-0.996028543	~
PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.996044278	0.996044219	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	~

Test Step 1.9 (Repeat Count = 1)			✓
Name	Input Value		
k_MtrVoltQaxFiltFFKn_Hz_f32	325.598114		
k_MtrVoltVecuFiltKn_Hz_f32	775.537415		
k_PiSamplingTs_Sec_f32	0.000125000006		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	~
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.28350234	2.28350258	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.23345381	0.23345381	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	6.82092714	6.82092714	~
PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.113366775	0.113366768	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	~





Test Step 1.10 (Repeat Count = 1)			✓
Name	Input Value		
k_MtrVoltQaxFiltFFKn_Hz_f32	0.10000001		
k_MtrVoltVecuFiltKn_Hz_f32	14.1302996		
k_PiSamplingTs_Sec_f32	0.0988999978		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	~
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-0.77222687	-0.77222687	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.814482749	0.814482749	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	31.1850204	31.1850243	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0301340781	0.0301340744	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	~

Test Step 1.11 (Repeat Count = 1)			✓
Name	Input Value		
k_MtrVoltQaxFiltFFKn_Hz_f32	1000		
k_MtrVoltVecuFiltKn_Hz_f32	761.508179		
k_PiSamplingTs_Sec_f32	0.0340999998		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	~
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-0.987741947	-0.987741947	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.987890363	0.987890422	✓
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	-0.990665376	-0.990665376	~
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.990751743	0.990751743	✓

Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	~

Test Step 1.12 (Repeat Count = 1)			✓
Name	Input Value		
k_MtrVoltQaxFiltFFKn_Hz_f32	213.235001		
k_MtrVoltVecuFiltKn_Hz_f32	815.32312		
k_PiSamplingTs_Sec_f32	0.034099998		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	~
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	~
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-0.988551021	-0.988551021	~
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.988680601	0.988680661	~
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	-0.956223905	-0.956223905	✓
PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.958059847	0.958059847	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	~

Test Step 1.13 (Repeat Count = 1)			✓	
Name	Input Value			
k_MtrVoltQaxFiltFFKn_Hz_f32	500			
k_MtrVoltVecuFiltKn_Hz_f32	815.32312	815.32312		
k_PiSamplingTs_Sec_f32	0.0302000009			
Name	Actual Value	Expected Value	Result	
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	~	
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1	1 ± 0.00048828125	✓	
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-0.987072527	-0.987072527	✓	
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.987237573	0.987237513	✓	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	-0.978919864	-0.978919864		

PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32

2016-09-15, 18:06:18+0530



0.979355097

PICurrCntrl_Init

Name Actual Value Expected Value

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearTerm	2	CalLowPassFiltBilinearTerm	2	~

0.979355037

2016-09-15, 18:16:08+0530



IntegralStateVarNonOperState

Project MtrCtrl_CM_SF99B

Module PICurrCntrl

Test Object IntegralStateVarNonOperState

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3	
Successful	3	✓
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\MtrCtrl_CM_SF99B
Configuration File	D:\Synergy_Work_Area\MtrCtrl_CM_SF99B\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\MtrCtrl_CM\src\Ap_PICurrCntrl.c
Compiler Options	-D_DATA_ACCESS= -Dsqrtf=sqrtf -Dconst= -I\$(PROJECTROOT)\MtrCtrl_CM\utp\contract\Ap_PlCurrCntrl -I\$(PROJECTROOT) \MtrCtrl_CM\utp\contract -I\$(PROJECTROOT)\MtrCtrl_CM\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef \include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dsqrtf=sqrtf -Dconst= -I\$(PROJECTROOT)\MtrCtrl_CM\utp\contract\Ap_PlCurrCntrl -I\$(PROJECTROOT) \MtrCtrl_CM\utp\contract -I\$(PROJECTROOT)\MtrCtrl_CM\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef \include -I\$(Compiler Install Path)\include

Comments/Description/Spe	ecification
Name	Text

2016-09-15, 18:16:08+0530





Module 'PlCurrCntrl'

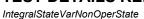
Name of Tester:Komal Sharma
Code File(s) Under Test:Ap_PlCurrCntrl.c
Code File(s) Under Test:Ap_PlCurrentContrl.doc
Module Design Document.PlCurrentContrl.doc
Module Design Document Version:12
Data Dictionary Version:15
Unit Test Plan Version:4
Optimization Level:Level 2
Compiler (CodeGen) Version:TMS570_4.9.5
Model Type:Excel Macro
Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32
Total FLASH Used (Bytes):2834
Total FLASH Used (Bytes):2834
Total RAM Used (Bytes):2865
Special Test Requirements:NA
Test Date:9/15/2016
Comments:"Note 1: INLINE functions defined in globalmacro.h are not unit tested.
Note 2: ""CBD_Sandbox_dbg.map""map file is embedded for reference.

Note 3: Out of range value is given in function ""LoaMtgnSclFac" for variables
""K_CLOAFdbackSignalSclFacSlew_UlspS_f32,k_ILOAFdbackSignalSclFacSlew_UlspS_f32,PlCurrCntrl_DualEcuFailSclFac_Uls_M_f32 and PlCurrCntrl_InverterFailSclFac_Uls_M_f32 variables are going out of range.

Note 4: In function PlCurrCntrl_Per1 PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 and PlCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 variables are going out of range.

Note 5: In function PlCurrCntrl_Per1, the range of MtrPosComputationDelay_Rad_M_f32[2] is considered as -3.14 to 3.14"

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
Timer Unit	Cycles
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\MtrCtrl_CM_SF99B\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP





Test Case 1: Metric Test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles:

TS 1.1 46.00 Cycles TS 1.2 63.00 Cycles

Description Vector Description:

TS 1.1Longest Path==>(SysState_Cnt_T_Enum != RTE_MODE_StaMd_Mode_OPERATE)=True TS 1.2Shortest Path==>(SysState_Cnt_T_Enum != RTE_MODE_StaMd_Mode_OPERATE)=False

Test Step 1.1 (Repeat Count = 1)			
Name	Input Value		
MtrCurrDaxPrevIntg_Volt_M_f32	-31		
MtrCurrQaxPrevIntg_Volt_M_f32	-31		
SysState_Cnt_T_Enum	0		
Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	✓
MtrCurrQaxPrevIntg_Volt_M_f32	0	0	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrDaxPrevIntg_Volt_M_f32	24.8391		
MtrCurrQaxPrevIntg_Volt_M_f32	-6.723		
SysState_Cnt_T_Enum	2		
Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	24.8390999	24.8390999	✓
MtrCurrQaxPrevIntg_Volt_M_f32	-6.72300005	-6.72300005	✓

Test Step Call Trace				✓	
	Actual Function	Count	Expected Function	Count	Result
	none	0	*** No Call Expected ***	0	~

Test Case 2: Boundary test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles:

TS 2.1 46.00 Cycles TS 2.2 46.00 Cycles TS 2.3 46.00 Cycles TS 2.4 46.00 Cycles TS 2.5 63.00 Cycles TS 2.6 46.00 Cycles TS 2.7 46.00 Cycles

Description Vector Description:

TS 2.1All Min TS 2.2All Max

TS 2.3SysState_Cnt_T_Enum =>RTE_MODE_StaMd_Mode_DISABLE
TS 2.4SysState_Cnt_T_Enum =>RTE_MODE_StaMd_Mode_OFF
TS 2.5SysState_Cnt_T_Enum =>RTE_MODE_StaMd_Mode_OPERATE
TS 2.6SysState_Cnt_T_Enum=>RTE_MODE_StaMd_Mode_WARMINIT
TS 2.7SysState_Cnt_T_Enum=>RTE_TRANSITION_StaMd_Mode

Test Step 2.1 (Repeat Count = 1)			
Name	Input Value		
MtrCurrDaxPrevIntg_Volt_M_f32	-31		
MtrCurrQaxPrevIntg_Volt_M_f32	-31		
SysState_Cnt_T_Enum	0		
Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
MtrCurrQaxPrevIntg_Volt_M_f32	0	0	✓

IntegralStateVarNonOperState



Test Step Call Trace						
	Actual Function	Count	Expected Function	Count	Resu	lt
	none	0	*** No Call Expected ***	n		_

Test Step 2.2 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrDaxPrevIntg_Volt_M_f32	31		
MtrCurrQaxPrevIntg_Volt_M_f32	31		
SysState_Cnt_T_Enum	4		
Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
MtrCurrQaxPrevIntg_Volt_M_f32	0	0	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.3 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrDaxPrevIntg_Volt_M_f32	7.8214		
MtrCurrQaxPrevIntg_Volt_M_f32	19.751		
SysState_Cnt_T_Enum	0		
Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
MtrCurrQaxPrevIntg Volt M f32	0	0	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	

Test Step 2.4 (Repeat Count = 1)			V
Name	Input Value		
MtrCurrDaxPrevIntg_Volt_M_f32	8.089		
MtrCurrQaxPrevIntg_Volt_M_f32	-23.062		
SysState_Cnt_T_Enum	1		
Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
MtrCurrQaxPrevIntg_Volt_M_f32	0	0	✓

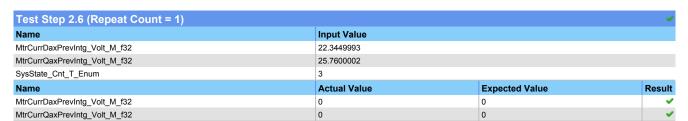
Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.5 (Repeat Count = 1)				
Name	Input Value			
MtrCurrDaxPrevIntg_Volt_M_f32	24.8391			
MtrCurrQaxPrevIntg_Volt_M_f32	-6.723			
SysState_Cnt_T_Enum	2			
Name	Actual Value	Expected Value	Result	
MtrCurrDaxPrevIntg_Volt_M_f32	24.8390999	24.8390999	✓	
MtrCurrQaxPrevIntg_Volt_M_f32	-6.72300005	-6.72300005	✓	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

IntegralStateVarNonOperState





Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	

Test Step 2.7 (Repeat Count = 1)				
Name	Input Value			
MtrCurrDaxPrevIntg_Volt_M_f32	12.4499998			
MtrCurrQaxPrevIntg_Volt_M_f32	11.3400002			
SysState_Cnt_T_Enum	4			
Name	Actual Value	Expected Value	Result	
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~	
MtrCurrQaxPrevIntg_Volt_M_f32	0	0	~	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Case 3: Path Test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cycles:

TS 3.1 46.00 Cycles TS 3.2 63.00 Cycles

Description Vector Description:

TS 3.1(SysState_Cnt_T_Enum != RTE_MODE_StaMd_Mode_OPERATE)=True TS 3.2(SysState_Cnt_T_Enum != RTE_MODE_StaMd_Mode_OPERATE)=False

Test Step 3.1 (Repeat Count = 1)			✓
Name	Input Value		
MtrCurrDaxPrevIntg_Volt_M_f32	-31		
MtrCurrQaxPrevIntg_Volt_M_f32	-31		
SysState_Cnt_T_Enum	0		
Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	·
MtrCurrQaxPrevIntg Volt M f32	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 3.2 (Repeat Count = 1)			
Name	Input Value		
MtrCurrDaxPrevIntg_Volt_M_f32	24.8391		
MtrCurrQaxPrevIntg_Volt_M_f32	-6.723		
SysState_Cnt_T_Enum	2		
Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	24.8390999	24.8390999	~
MtrCurrQaxPrevIntg_Volt_M_f32	-6.72300005	-6.72300005	✓

2016-09-15, 18:16:08+0530



IntegralStateVarNonOperState

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

2016-09-15, 17:59:40+0530



CalLowPassFiltBilinearTerm

Project MtrCtrl_CM_SF99B

Module PICurrCntrl

Test Object CalLowPassFiltBilinearTerm

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1	
Successful	1	✓
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\MtrCtrl_CM_SF99B
Configuration File	D:\Synergy_Work_Area\MtrCtrl_CM_SF99B\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\MtrCtrl_CM\src\Ap_PlCurrCntrl.c
Compiler Options	-D_DATA_ACCESS= -Dsqrtf=sqrtf -Dconst= -I\$(PROJECTROOT)\MtrCtrl_CM\utp\contract\Ap_PlCurrCntrl -I\$(PROJECTROOT) \MtrCtrl_CM\utp\contract -I\$(PROJECTROOT)\MtrCtrl_CM\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef \include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dsqrtf=sqrtf -Dconst= -I\$(PROJECTROOT)\MtrCtrl_CM\utp\contract\Ap_PICurrCntrl -I\$(PROJECTROOT) \MtrCtrl_CM\utp\contract -I\$(PROJECTROOT)\MtrCtrl_CM\\include -I\$(PROJECTROOT)\NxtrLib\\include -I\$(PROJECTROOT)\StdDef \include -I\$(Compiler Install Path)\include

Comments/Description	n/Specification
Name	Text
Module 'PICurrCntrl'	Name of Tester:Komal Sharma Code File(s) Under Test:Ap_PlCurrCntrl.c Code File(s) Version:16 Module Design Document.PlCurrentContrl.doc Module Design Document.PlCurrentContrl.doc Module Design Document.PlCurrentContrl.doc Module Design Document.PlCurrentContrl.doc Module Design Document Version:12 Data Dictionary Version:15 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS570_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):2834 Total RAM Used (Bytes):2834 Total RAM Used (Bytes):2855 Special Test Requirements:NA Test Date:9/15/2016 Comments:"Note 1: INLINE functions defined in globalmacro.h are not unit tested. Note 2: ""CBD_Sandbox_dbg.map""map file is embedded for reference. Note 3: Out of range value is given in function ""LoaMtgtnSclFac"* for variables ""k_CLOAFdbackSignalSclFacSlew_UIspS_132,k_ILOAFdbackSignalSclFacSlew_UIspS_132,k_DualEcuSignalSclFacSlew_UIspS_132,PlCurrCntrl_CurrSensFailSclFac_UIs_M_132 and PlCurrCntrl_InverterFailSclFac_UIs_M_132** to achieve 100% path coverage in Path sheet. Note 4: In function PlCurrCntrl_Per1 PlCurrCntrl_MtrCurrDaxSatFluxRatio_UIs_M_132 and PlCurrCntrl_MtrCurrQaxSatFluxRatio_UIs_M_132 variables are going out of range. Note 5: In function PlCurrCntrl_Per1, the range of MtrPosComputationDelay_Rad_M_132[2] is considered as -3.14 to 3.14**

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

2016-09-15, 17:59:40+0530





Attributes	
Name	Value
InitObjDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj</pre>
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
Timer Unit	Cycles
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\MtrCtrl_CM_SF99B\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



Test Case 1: BoundaryTest

Specification Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles:

TS 1.1 14.00 Cycles TS 1.2 14.00 Cycles TS 1.3 14.00 Cycles TS 1.4 14.00 Cycles TS 1.5 14.00 Cycles TS 1.5 14.00 Cycles TS 1.6 14.00 Cycles TS 1.7 14.00 Cycles TS 1.8 14.00 Cycles

Description Vector Description:

TS1.1All Min
TS1.2All Max
TS1.3Freq_Hz_T_f32=Min
TS1.4Freq_Hz_T_f32=Max
TS1.5Freq_Hz_T_f32=Pos
TS1.6TimeCons_Sec_T_f32=Min
TS1.7TimeCons_Sec_T_f32=Max
TS1.8TimeCons_Sec_T_f32=Pos

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
Freq_Hz_T_f32	0.100000001		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_S	Str	
TimeCons_Sec_T_f32	6.2500003e-005		
Name	Actual Value	Expected Value	Result
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	50928.5781	50928.582	~
target LowPassFiltBilinear T Str.TermD Uls f32	1.963457e-005	1.96345682e-005	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
Freq_Hz_T_f32	2.14748365e+009		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_	Str	
TimeCons_Sec_T_f32	0.10000001		
Name	Actual Value	Expected Value	Result
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	-1	-1	~
target LowPassFiltBilinear T Str.TermD Uls f32	1	1	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.3 (Repeat Count = 1)			✓
Name	Input Value		
Freq_Hz_T_f32	0.100000001		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_S	Str	
TimeCons_Sec_T_f32	0.0024000011		
Name	Actual Value	Expected Value	Result
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1325.29114	1325.29114	~
target LowPassFiltBilinear T Str.TermD Uls f32	0.000753414235	0.000753414177	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.4 (Repeat Count = 1)	✓
Name	Input Value
Freq_Hz_T_f32	2.14748365e+009

2016-09-15, 17:59:40+0530



CalLowPassFiltBilinearTerm

Name	Input Value					
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_5	target_LowPassFiltBilinear_T_Str				
TimeCons_Sec_T_f32	0.0214000009	0.0214000009				
Name	Actual Value	Expected Value	Result			
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	-1	-1	~			
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	1	1	✓			

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.5 (Repeat Count = 1)			
Name	Input Value		
Freq_Hz_T_f32	25348.7129		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_S	Str	
TimeCons_Sec_T_f32	0.0365200005		
Name	Actual Value	Expected Value	Result
target_LowPassFiltBilinear_T_Str.TermN_UIs_f32	-0.999656141	-0.999656141	~
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	0.99965632	0.99965626	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.6 (Repeat Count = 1)			V
Name	Input Value		
Freq_Hz_T_f32	250.412292		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_S	Str	
TimeCons_Sec_T_f32	6.2500003e-005		
Name	Actual Value	Expected Value	Result
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	19.3382893	19.3382912	~
target LowPassFiltBilinear T Str.TermD Uls f32	0.0468641147	0.046864111	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.7 (Repeat Count = 1)			✓
Name	Input Value		
Freq_Hz_T_f32	1050.32556		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_	Str	
TimeCons_Sec_T_f32	0.10000001		
Name	Actual Value	Expected Value	Result
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	-0.996969402	-0.996969402	~
target LowPassFiltBilinear T Str.TermD Uls f32	0.996978641	0.996978581	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	

Test Step 1.8 (Repeat Count = 1)			✓	
Name	Input Value			
Freq_Hz_T_f32	286.223602			
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_	Str		
TimeCons_Sec_T_f32	0.0030000003	0.00300000003		
Name	Actual Value	Expected Value	Result	
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	-0.629299283	-0.629299283	~	
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	0.729553878	0.729553878	✓	

CalLowPassFiltBilinearTerm

2016-09-15, 17:59:40+0530



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	

2016-09-15, 17:58:55+0530



CalLowPassFiltBilinearOut

Project MtrCtrl_CM_SF99B

Module PICurrCntrl

Test Object CalLowPassFiltBilinearOut

Instrumentation: Test Object Only

Statement (C0) Coverage 100 %
Branch (C1) Coverage 100 %

Statistics

Total Testcases	1	
Successful	1	✓
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\MtrCtrl_CM_SF99B
Configuration File	D:\Synergy_Work_Area\MtrCtrl_CM_SF99B\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\MtrCtrl_CM\src\Ap_PICurrCntrl.c
Compiler Options	-D_DATA_ACCESS= -Dsqrtf=sqrtf -Dconst= -I\$(PROJECTROOT)\MtrCtrl_CM\utp\contract\Ap_PlCurrCntrl -I\$(PROJECTROOT) \MtrCtrl_CM\utp\contract -I\$(PROJECTROOT)\MtrCtrl_CM\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef \include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dsqrtf=sqrtf -Dconst= -I\$(PROJECTROOT)\MtrCtrl_CM\utp\contract\Ap_PlCurrCntrl -I\$(PROJECTROOT) \MtrCtrl_CM\utp\contract -I\$(PROJECTROOT)\MtrCtrl_CM\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef \include -I\$(Compiler Install Path)\include

Comments/Descript	tion/Specification
Name	Text
Module 'PICurrCntri'	Name of Tester:Komal Sharma Code File(s) Under Test:Ap_PlCurrCntrl.c Code File(s) Version:16 Module Design Document:PlCurrentContrl.doc Module Design Document Version:12 Data Dictionary Version:15 Unit Test Plan Version:4 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS570_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):2834 Total RAM Used (Bytes):2834 Total RAM Used (Bytes):2855 Special Test Requirements:NA Test Date:9/15/2016 Comments:"Note 1: INLINE functions defined in globalmacro.h are not unit tested. Note 2: ""CBD_Sandbox_dbg.map""map file is embedded for reference. Note 3: Out of range value is given in function ""LoaMtgtnSclFac" for variables ""k_ CLOAFdbackSignalSclFacSlew_UlspS_f32,k_ILOAFdbackSignalSclFacSlew_UlspS_f32,k_DualEcuSignalSclFacSlew_UlspS_f32, PlCurrCntrl_DualEcuFailSclFac_Uls_M_f32 and PlCurrCntrl_InverterFailSclFac_Uls_M_f32 variables are going out of range. Note 4: In function PlCurrCntrl_Per1 PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 and PlCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 variables are going out of range. Note 5: In function PlCurrCntrl_Per1, the range of MtrPosComputationDelay_Rad_M_f32[2] is considered as -3.14 to 3.14"

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9

2016-09-15, 17:58:55+0530





Attributes	
Name	Value
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
Timer Unit	Cycles
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\MtrCtrl_CM_SF99B\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



Test Case 1: BoundaryTest

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles:

CPU Cycles:

TS 1.1 9.00 Cycles
TS 1.2 9.00 Cycles
TS 1.3 9.00 Cycles
TS 1.3 9.00 Cycles
TS 1.4 9.00 Cycles
TS 1.5 9.00 Cycles
TS 1.6 9.00 Cycles
TS 1.7 9.00 Cycles
TS 1.9 9.00 Cycles
TS 1.9 9.00 Cycles
TS 1.10 9.00 Cycles
TS 1.11 9.00 Cycles
TS 1.12 9.00 Cycles
TS 1.12 9.00 Cycles
TS 1.14 9.00 Cycles
TS 1.15 9.00 Cycles
TS 1.16 9.00 Cycles
TS 1.17 9.00 Cycles
TS 1.18 9.00 Cycles
TS 1.19 9.00 Cycles
TS 1.19 9.00 Cycles
TS 1.19 9.00 Cycles
TS 1.19 9.00 Cycles
TS 1.20 9.00 Cycles
TS 1.21 9.00 Cycles
TS 1.25 9.00 Cycles
TS 1.25 9.00 Cycles
TS 1.25 9.00 Cycles
TS 1.26 9.00 Cycles
TS 1.27 9.00 Cycles
TS 1.27 9.00 Cycles

Description

Vector Description:

TS1.1All Min TS1.2All Max

TS1.1All Min
TS1.2All Max
TS1.3Input_UIs_T_f32=Min
TS1.4Input_UIs_T_f32=Max
TS1.5Input_UIs_T_f32=Pos
TS1.6Input_UIs_T_f32=Pos
TS1.7Input_UIs_T_f32=Pos
TS1.7Input_UIs_T_f32=Neg
TS1.8LowPassFiltBilinear_T_Str.PrevInput_UIs_f32=Min
TS1.9LowPassFiltBilinear_T_Str.PrevInput_UIs_f32=Max
TS1.10LowPassFiltBilinear_T_Str.PrevInput_UIs_f32=Pos
TS1.11LowPassFiltBilinear_T_Str.PrevInput_UIs_f32=Neg
TS1.13LowPassFiltBilinear_T_Str.PrevOutput_UIs_f32=Neg
TS1.13LowPassFiltBilinear_T_Str.PrevOutput_UIs_f32=Max
TS1.15LowPassFiltBilinear_T_Str.PrevOutput_UIs_f32=Max
TS1.16LowPassFiltBilinear_T_Str.PrevOutput_UIs_f32=Pos
TS1.17LowPassFiltBilinear_T_Str.PrevOutput_UIs_f32=Pos
TS1.17LowPassFiltBilinear_T_Str.PrevOutput_UIs_f32=Neg
TS1.19LowPassFiltBilinear_T_Str.TermN_UIs_f32=Max
TS1.20LowPassFiltBilinear_T_Str.TermN_UIs_f32=Pos
TS1.21LowPassFiltBilinear_T_Str.TermN_UIs_f32=Pos
TS1.22LowPassFiltBilinear_T_Str.TermN_UIs_f32=Pos
TS1.23LowPassFiltBilinear_T_Str.TermD_UIs_f32=Neg
TS1.23LowPassFiltBilinear_T_Str.TermD_UIs_f32=Min
TS1.24LowPassFiltBilinear_T_Str.TermD_UIs_f32=Max
TS1.25LowPassFiltBilinear_T_Str.TermD_UIs_f32=Neg
TS1.26LowPassFiltBilinear_T_Str.TermD_UIs_f32=Pos
TS1.26LowPassFiltBilinear_T_Str.TermD_UIs_f32=Pos
TS1.26LowPassFiltBilinear_T_Str.TermD_UIs_f32=Pos
TS1.27LowPassFiltBilinear_T_Str.TermD_UIs_f32=Pos

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
Input_Uls_T_f32	-2.14748365e+009		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str	•	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-2.14748365e+009		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-2.14748365e+009		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	-2.14748365e+009		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	-2.14748365e+009		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	-9.90352031e+027	-9.90352031e+027	✓
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-2.14748365e+009	-2.14748365e+009	•
target LowPassFiltBilinear T Str PrevOutput Uls f32	-9 90352031e+027	-9 90352031e+027	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	_

Test Step 1.2 (Repeat Count = 1)		✓
Name	Input Value	
Input_Uls_T_f32	2.14748365e+009	
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	2.14748365e+009	

2016-09-15, 17:58:55+0530



Name	Input Value		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	2.14748365e+009		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	2.14748365e+009		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	2.14748365e+009		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	9.90352031e+027	9.90352031e+027	~
target_LowPassFiltBilinear_T_Str.PrevInput_UIs_f32	2.14748365e+009	2.14748365e+009	•
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	9.90352031e+027	9.90352031e+027	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.3 (Repeat Count = 1)			✓
Name	Input Value		
Input_Uls_T_f32	-2.14748365e+009		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_St	tr	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	153.836899		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	8286.36523		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1000.22357		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	5.5236001		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	-1.18160589e+010	-1.18160589e+010	~
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-2.14748365e+009	-2.14748365e+009	✓
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-1.18160589e+010	-1.18160589e+010	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.4 (Repeat Count = 1)			✓
Name	Input Value		
Input_Uls_T_f32	2.14748365e+009		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_S	tr	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	42.2523003		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	123.3657		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1050.87891		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	99.9235992		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	2.14597239e+011	2.14597255e+011	✓
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	2.14748365e+009	2.14748365e+009	✓
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	2.14597239e+011	2.14597255e+011	✓

Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	

Test Step 1.5 (Repeat Count = 1)				
Name	Input Value			
Input_Uls_T_f32	0			
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_	Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-895.362122			
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-89.7412033			
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	85.9630966			
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	-5.36210012			
Name	Actual Value	Expected Value	Result	
CalLowPassFiltBilinearOut()	46166.5781	46166.5742	✓	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	0	0	✓	
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	46166.5781	46166.5742	~	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~



Test Step 1.6 (Repeat Count = 1)			✓
Name	Input Value		
Input_Uls_T_f32	2546.15991		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	258.325989		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	423.574005		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1101.47412		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	1283.396		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	602375168	602375104	~
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	2546.15991	2546.15991	✓
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	602375168	602375104	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

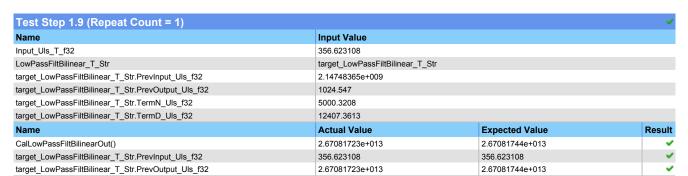
Test Step 1.7 (Repeat Count = 1)				
Name	Input Value			
Input_Uls_T_f32	-2546.15991			
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_	Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	58.3260002			
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	423.574005			
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1101.47412			
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	1283.396			
Name	Actual Value	Expected Value	Result	
CalLowPassFiltBilinearOut()	595582976	595582976	✓	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-2546.15991	-2546.15991	✓	
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	595582976	595582976	✓	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	•

Test Step 1.8 (Repeat Count = 1)			✓
Name	Input Value		
Input_Uls_T_f32	252.320999		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_S	tr	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-2.14748365e+009		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	724.321228		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1152.31238		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	6845.52148		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	-1.46949297e+013	-1.46949297e+013	✓
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	252.320999	252.320999	✓
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-1.46949297e+013	-1.46949297e+013	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~





Test Step Call Trace					✓
	Actual Function	Count	Expected Function	Count	Result
	none	0	*** No Call Expected ***	0	~

Test Step 1.10 (Repeat Count = 1)			✓
Name	Input Value		
Input_Uls_T_f32	52.3652		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_	Str	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	0		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-86.3150024		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	546.398682		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	-785.632019		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	37011156	37011152	~
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	52.3652	52.3652	✓
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	37011156	37011152	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.11 (Repeat Count = 1)				
Name	Input Value			
Input_Uls_T_f32	11423.2314			
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_S	tr		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	3582.41992			
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	1325.12305			
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1253.22302			
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	17969.7402			
Name	Actual Value	Expected Value	Result	
CalLowPassFiltBilinearOut()	3.01115412e+010	3.01115412e+010	~	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	11423.2314	11423.2314	✓	
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	3.01115412e+010	3.01115412e+010	✓	

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	

Test Step 1.12 (Repeat Count = 1)			
Name	Input Value		
Input_Uls_T_f32	11423.2314		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Si	tr	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-3582.41992		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	1325.12305		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1253.22302		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	17969.7402		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	2.99827896e+010	2.99827896e+010	✓
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	11423.2314	11423.2314	✓

2016-09-15, 17:58:55+0530



CalLowPassFiltBilinearOut		Razon	at
Name	Actual Value	Expected Value	Result
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	2.99827896e+010	2.99827896e+010	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.13 (Repeat Count = 1)			
Name	Input Value		
Input_Uls_T_f32	286.321014		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	12.5235996		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-2.14748365e+009		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1303.82361		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	23531.3203		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	-6.58862801e+016	-6.58862844e+016	~
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	286.321014	286.321014	~
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-6.58862801e+016	-6.58862844e+016	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	

Test Step 1.14 (Repeat Count = 1)				
Name	Input Value			
Input_Uls_T_f32	1123.36523			
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_S	tr		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	102.823601			
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	2.14748365e+009			
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1354.42297			
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	29093.4473			
Name	Actual Value	Expected Value	Result	
CalLowPassFiltBilinearOut()	8.46212324e+016	8.4621241e+016	~	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	1123.36523	1123.36523	✓	
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	8.46212324e+016	8.4621241e+016	✓	

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	

Test Step 1.15 (Repeat Count = 1)	Test Step 1.15 (Repeat Count = 1)				
Name	Input Value				
Input_Uls_T_f32	563.231018				
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_	Str			
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-5657.51416				
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	0				
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	-73.3619995				
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	-89.6539993				
Name	Actual Value	Expected Value	Result		
CalLowPassFiltBilinearOut()	456722.875	456722.875	~		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	563.231018	563.231018	~		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	456722.875	456722.875	✓		

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	





Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	

Test Step 1.17 (Repeat Count = 1)			✓
Name	Input Value		
Input_Uls_T_f32	968.314514		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Si	tr	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	193.112305		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-1525.82996		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	1405.12415		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	34655.3242		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	-7.42600868e+010	-7.4260095e+010	~
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	968.314514	968.314514	✓
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-7.42600868e+010	-7.4260095e+010	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.18 (Repeat Count = 1)			
Name	Input Value		
Input_Uls_T_f32	12.3512001		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_S	tr	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	283.423615		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	2.4230001		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	-2.14748365e+009		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	40217.2891		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	-2.09264732e+014	-2.09264732e+014	•
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	12.3512001	12.3512001	•
target LowPassFiltBilinear T Str.PrevOutput Uls f32	-2.09264732e+014	-2.09264732e+014	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.19 (Repeat Count = 1)			✓
Name	Input Value		
Input_Uls_T_f32	-88.9124527		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	373.723114		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	56.7743988		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	2.14748365e+009		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	45779.3203		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	5.58151079e+015	5.58151079e+015	~
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-88.9124527	-88.9124527	✓

CalLowPassFiltBilinearOut

2016-09-15, 17:58:55+0530



Name	Actual Value	Expected Value	Result
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	5.58151079e+015	5.58151079e+015	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.20 (Repeat Count = 1)			✓
Name	Input Value		
Input_Uls_T_f32	-12.3620005		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_	Str	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-78.9599991		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	56.1230011		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	0		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	-741.236511		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	67691.2031	67691.2031	✓
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-12.3620005	-12.3620005	✓
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	67691.2031	67691.2031	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	

Test Step 1.21 (Repeat Count = 1)			✓
Name	Input Value		
Input_Uls_T_f32	753.745605		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_St	tr	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	464.321014		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	111.321404		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	990.090027		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	51341.1641		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	5.72126822e+009	5.72126771e+009	~
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	753.745605	753.745605	~
target LowPassFiltBilinear T Str.PrevOutput Uls f32	5.72126822e+009	5.72126771e+009	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.22 (Repeat Count = 1)			✓
Name	Input Value		
Input_Uls_T_f32	753.745605		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_	Str	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	464.321014		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	111.321404		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	-89.6500015		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	51341.1641		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	-449846016	-449845984	~
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	753.745605	753.745605	✓
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-449846016	-449845984	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~





Test Step 1.23 (Repeat Count = 1)			✓
Name	Input Value		
Input_Uls_T_f32	186.523605		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_S	tr	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	554.330017		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	165.357407		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	86.6320038		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	-2.14748365e+009		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	-3.23541963e+013	-3.23541942e+013	~
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	186.523605	186.523605	✓
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-3.23541963e+013	-3.23541942e+013	✓

Test Step Call Trace					✓
	Actual Function	Count	Expected Function	Count	Result
	none	0	*** No Call Expected ***	0	~

Test Step 1.24 (Repeat Count = 1)			
Name	Input Value		
Input_Uls_T_f32	32.5564003		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_S	tr	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	644.63208		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	219.654694		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	3.32200003		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	2.14748365e+009		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	3.02125469e+012	3.02125495e+012	~
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	32.5564003	32.5564003	✓
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	3.02125469e+012	3.02125495e+012	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Name	Input Value		
Input UIs T f32	-89.6320038		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_	Str	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-78.9599991		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	-89.471199		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	-78.9599991		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	0		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	0	0	•
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-89.6320038	-89.6320038	•
target LowPassFiltBilinear T Str.PrevOutput Uls f32	0	0	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.26 (Repeat Count = 1)			✓
Name	Input Value		
Input_Uls_T_f32	236.350006		
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	99999.3203		
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	1000.32098		
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	78.4514008		
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	10250.0703		
Name	Actual Value	Expected Value	Result
CalLowPassFiltBilinearOut()	1.83181312e+009	1.83181312e+009	~
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	236.350006	236.350006	✓

CalLowPassFiltBilinearOut

2016-09-15, 17:58:55+0530



Name	Actual Value	Expected Value	Result
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	1.83181312e+009	1.83181312e+009	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.27 (Repeat Count = 1)				
Name	Input Value			
Input_Uls_T_f32	-78.9599991			
LowPassFiltBilinear_T_Str	target_LowPassFiltBilinear_T_	Str		
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	125.629997			
target_LowPassFiltBilinear_T_Str.PrevOutput_Uls_f32	86.7409973			
target_LowPassFiltBilinear_T_Str.TermN_Uls_f32	78.4514008			
target_LowPassFiltBilinear_T_Str.TermD_Uls_f32	-20.0699997			
Name	Actual Value	Expected Value	Result	
CalLowPassFiltBilinearOut()	-137512.063	-137512.078	~	
target_LowPassFiltBilinear_T_Str.PrevInput_Uls_f32	-78.9599991	-78.9599991	✓	
target LowPassFiltBilinear T Str.PrevOutput Uls f32	-137512.063	-137512.078	✓	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

2016-09-15, 18:23:31+0530



PICurrCntrl_Per1

Project MtrCtrl_CM_SF99B

Module PICurrCntrl

Test Object PICurrCntrl_Per1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\MtrCtrl_CM_SF99B
Configuration File	D:\Synergy_Work_Area\MtrCtrl_CM_SF99B\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\MtrCtrl_CM\src\Ap_PICurrCntrl.c
Compiler Options	-D_DATA_ACCESS= -D_sqrtf=sqrtf -Dconst= -I\$(PROJECTROOT)\MtrCtrl_CM\utp\contract\Ap_PlCurrCntrl -I\$(PROJECTROOT) \MtrCtrl_CM\utp\contract -I\$(PROJECTROOT)\\StdDef \include -I\$(Ompiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dsqrtf=sqrtf -Dconst= -I\$(PROJECTROOT)\MtrCtrl_CM\utp\contract\Ap_PlCurrCntrl -I\$(PROJECTROOT) \MtrCtrl_CM\utp\contract -I\$(PROJECTROOT)\MtrCtrl_CM\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef \include -I\$(Compiler Install Path)\include

Comments/Description/Spe	ecification
Name	Text



Module 'PICurrCntri'

Name of Tester:Komal Sharma
Code File(s) Under Test:Ap_PICurrCntrl.c
Code File(s) Version:16
Module Design Document:PICurrentContrl.doc
Module Design Document Version:12
Data Dictionary Version:15
Unit Test Plan Version:4
Optimization Level:Level 2
Compiler (CodeCen) Version:15
Unit Test Plan Version:4
Optimization Level:Level 2
Compiler (CodeCen) Version:TMS570_4.9.5
Model Type:Excel Macro
Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32
Total FLASH Used (Bytes):2834
Total RAM Used (Bytes):2845
Special Test Requirements:NA
Test Date:9/15/2016
Comments:"Note 1: INLINE functions defined in globalmacro.h are not unit tested.

Note 2: ""CBD_Sandbox_dbg.map""map file is embedded for reference.

Note 3: Out of range value is given in function ""LoaMtgtnSclFac"" for variables
""k_CLOAFdbackSignalSclFacSlew_UlspS_f32,k_ILOAFdbackSignalSclFacSlew_UlspS_f32,k_DualEcuSignalSclFacSlew_UlspS_f32,
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32, PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 and PICurrCntrl_InverterFailSclFac_Uls_M_f32
variables are going out of range.

Attributes		
Name	Value	
Compiler Install Path	<pre>\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5</pre>	
Float Precision	9	
InitObjDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj</pre>	
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src	
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>	
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl	
Target Install Path	<pre>\$(ProgramFiles)\pls\UDE 4.4</pre>	
Timer Enabled	false	
Timer Prescale	0	
Timer Resolution	1	
Timer Unit	Cycles	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg	
Workspace File	D:\Synergy_Work_Area\MtrCtrl_CM_SF99B\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP	

Note 5: In function PICurrCntrl_Per1, the range of MtrPosComputationDelay_Rad_M_f32[2] is considered as -3.14 to 3.14"



Test Case 1: Metric Test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles:

TS 1.1 7295 Cycles TS 1.2 7094 Cycles

Description Vector Description:

TS 1.1Longest Path==>(k_MtrCurrQaxRefModifRplEn_Cnt_lgc ==

TS 1.1Longest Path==>(k_MtrCurrQaxRefModifRplEn_Cnt_lgc == TRUE)=False&&(MtrCurrQaxRefModif_Amp_T_f32<-220)=False&&(MtrCurrQaxRefModif_Amp_T_f32>220)=False&&(MtrCurrQaxRefModif_Amp_T_f32<-220)=False&&(MtrCurrQaxRefModif_Amp_T_f32<-220)=False&&(MtrCurrQaxRefModif_Amp_T_f32<-220)=False&&(MtrCurrQaxRefModif_Amp_T_f32<-220)=False&&(MtrCurrQaxRefModif_Amp_T_f32<-220)=False&&(NtrCurrQaxRefModif_Amp_T_f32<-220)=False&&(VoltSatnRatio_Uls_T_f32<-220)=True&&(VoltSatnRatio_Uls_T_f32<-220)=True&&(ModifAxriCorrQaxRefModifDsb_Cnt_lgc == FALSE)=False
TS 1.2Shortest

Paths = MtrCurrQaxRefModif_Amp_T_f32>=7300=True&&(MtrCurrQaxRefModifDsb_Cnt_lgc == FALSE)=False
TS 1.2Shortest

 $Path = > (MtrCurrQaxRefModif_Amp_T_f32 > = 220) = True\&(MtrCurrQaxRefModif_Amp_T_f32 > = 220) = True\&(MtrCurrQaxRefModif_Amp_T_f32 > = k_MtrAuthor(MtrCurrQaxRefModif_Amp_T_f32 > k_MtrAuthor(MtrCurrQaxRefModif_Amp_T_f32 > = k_MtrAuthor(MtrCurrQaxRefModif_Amp_T_f32 > k_MtrAuthor(MtrCurrQaxRefModif_A$

 $TRUE) = True \& (k_MtrVoltQaxFiltFFEnable_Cnt_lgc == TRUE) = False \& (ModldxSrlComSvcDft_Cnt_T_lgc == TRUE) = False \& (k_MtrCurrQaxRefMcC$ == FALSE)=True

Name	Input Value	
	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
/trCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	
/trCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
/trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
/trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
ftrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
htrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	171.485992	
ltrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	163.787003	
ItrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.125650004	
ltrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.125650004	
htrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0370000005	
ltrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0379999988	
ltrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.80200005	
ltrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.740999997	
ltrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-489.436005	
ItrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	938.341003	
ltrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0199999996	
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0879999995	
ltrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0120000001	
ltrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0560000017	
ltrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.10899997	
ltrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.479999989	
htrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	916.997009	
ltrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	1002.97998	
NtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-26.5079994	
NtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	4.36100006	
htrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	15.1960001	
htrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-2.83699989	
ftrCtrl_Vecu_Volt_M_f32[0]	5.33099985	
ftrCtrl_Vecu_Volt_M_f32[1]	7.69099998	
ItrCurrDaxPrevIntg_Volt_M_f32	6.17600012	
htrCurrDaxRef_Amp_M_f32[0]	-146.173996	
ItrCurrDaxRef_Amp_M_f32[1]	-213.335007	
1trCurrQaxCog_Amp_M_f32	152.016006	
ItrCurrQaxPrevIntg_Volt_M_f32	1.08770001	
htrCurrQaxRef_Amp_M_f32[0]	-216.921997	
htrCurrQaxRef_Amp_M_f32[1]	-184.923996	
/trCurrQaxRpl Amp M f32	0	
htrPosComputationDelay_Rad_M_f32[0]	-3.13800001	
ItrPosComputationDelay_Rad_M_f32[1]	2.11599994	
CurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.432999998	
CurrCntrl DualEcuFailSclFac Uls M f32	0.100000001	
ICurrCntrl InverterFailSclFac Uls M f32	0.0109999999	
CurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.335599989	
CurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.851999998	
PlCurrCntrl MtrVecuFilt M str.PrevInput Uls f32	-43.1699982	
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	-10.21	
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	12079.9004	
PICurrCntrl MtrVecuFilt M str.TermD Uls f32	0.620700002	

PICurrCntrl_Per1

2016-09-15, 18:23:31+0530



Name	Input Value		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-10.21		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	12079.9004		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.620700002		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2475.81006		
k_DualEcuSignalSclFacSlew_UlspS_f32	10		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2645.06006		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.179000005		
k_MtrCtrlVirualResQax_Ohm_f32	0.0120000001		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	7.70650005		
k MtrVoltDaxIntegLoLim Volt f32	-4.0999999		
k MtrVoltQaxFiltFFEnable Cnt lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	0.614899993		
k MtrVoltQaxIntegLoLim Volt f32	-6.5		
k MtrVoltVecuFiltEnable Cnt lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-1.26999998		
k VoltSatQaxPolyCoeff Uls f32	16.9449997		
k deadtimeVScale Uls f32	0.962000012		
t_CommOffsetTblX_Uls_u3p13[0]	4809		
t_CommOffsetTblX_Uls_u3p13[1]	5553		
t CommOffsetTblY Cnt u16[0]	663		
t_CommOffsetTblY_Cnt_u16[1]	905		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr	0		
target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target MtrCntrl Read MtrCurrDax Amp f32 Val	114.946999		
target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	1956		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-198.285995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Resul
MtrCntrl Write CommOffset Cnt u16(val)	1956	1956	
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	
MtrCntrl Write MtrCurrQaxFinalRef Amp f32(val)	-220	-220 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0.0375825167	0.0375825092 ± 4.88E-04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.80985308	-4.80985308 ± 4.88E-04	
MtrCntrl Write PhaseAdvanceFinal Rev u0p16(val)	65492	65492 ± 1.52588E-05	
MtrCurrDaxPrevIntg Volt M f32	0	0	
	0.10125	0.10125 ± 0.0625	
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.10125	U. IU IZO ± U.U0ZO	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

🗸
out Value
ou





Name	Input Value
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	220
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	220
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.125650004 0.125650004
MtrCtrl_MtrDaxpTermQax_Ohm_M_f32[1] MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	2
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	2
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	1024
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	1024
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.125650004
MtrCtrl MtrlmpedDax Ohm M f32[1]	0.125650004
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	2
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	2
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	1024
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	1024
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	31
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	31
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	31
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	31
MtrCtrl_Vecu_Volt_M_f32[0]	31
MtrCtrl_Vecu_Volt_M_f32[1]	31
MtrCurrDaxPrevIntg_Volt_M_f32	31
MtrCurrDaxRef_Amp_M_f32[0]	220
MtrCurrDaxRef_Amp_M_f32[1]	220
MtrCurrQaxCog_Amp_M_f32	220
MtrCurrQaxPrevIntg_Volt_M_f32	31
MtrCurrQaxRef_Amp_M_f32[0]	220
MtrCurrQaxRef_Amp_M_f32[1]	220
MtrCurrQaxRpI_Amp_M_f32 MtrPosComputationDelay_Rad_M_f32[0]	3.1400001
MtrPosComputationDelay_Rad_M_f32[1]	3.1400001
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.019999996
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	1
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	1
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	1350
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1350
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	50928.6016
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.996827006
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	1350
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1350
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	50928.6016
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.996827006
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	8000
k_DualEcuSignalSclFacSlew_UlspS_f32	8000
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	8000
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.20000003
k_MtrCtrlVirualResQax_Ohm_f32	0.200000003
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	31
k_MtrVoltDaxIntegHiLim_Volt_f32	0
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	31
	0
k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltVecuFiltEnable_Cnt_loc	1
k_MtrVoltVecuFiltEnable_Cnt_lgc	1 25

2016-09-15, 18:23:31+0530



Name	Input Value		
t_CommOffsetTblX_Uls_u3p13[0]	8192		
t_CommOffsetTblX_Uls_u3p13[1]	8192		
t_CommOffsetTblY_Cnt_u16[0]	2000		
t_CommOffsetTblY_Cnt_u16[1]	2000		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	220		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	5000		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	220		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	5000	5000	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	0	0 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	21.9203072	21.9203072 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	21.9203072	21.9203072 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	40943	40943 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0	0 ± 0.0625	~

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	-

PICurrCntrl_Per1

2016-09-15, 18:23:31+0530



Test Case 2: Boundary Test

2016-09-15, 18:23:31+0530

PICurrCntrl_Per1



Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cycles: TS 2.1 7138 Cycles
TS 2.2 7055 Cycles
TS 2.2 7055 Cycles
TS 2.3 7197 Cycles
TS 2.4 7162 Cycles
TS 2.5 7093 Cycles
TS 2.6 7193 Cycles
TS 2.7 7156 Cycles
TS 2.8 7269 Cycles
TS 2.9 7112 Cycles
TS 2.10 7123 Cycles
TS 2.11 7110 Cycles
TS 2.12 7081 Cycles
TS 2.12 7081 Cycles
TS 2.13 7152 Cycles
TS 2.14 7054 Cycles \$ 2.12 7081 cycles \$ 2.13 7152 cycles \$ 2.13 7152 cycles \$ 2.14 7054 Cycles \$ 2.14 7054 Cycles \$ 2.15 7064 Cycles \$ 2.16 7106 Cycles \$ 2.17 7107 Cycles \$ 2.18 7103 Cycles \$ 2.19 7114 Cycles \$ 2.20 7050 Cycles \$ 2.22 7035 Cycles \$ 2.22 7035 Cycles \$ 2.23 7071 Cycles \$ 2.25 7213 Cycles \$ 2.25 7213 Cycles \$ 2.26 7111 Cycles \$ 2.26 7111 Cycles \$ 2.27 7063 Cycles \$ 2.28 7111 Cycles \$ 2.29 7063 Cycles \$ 2.30 7111 Cycles \$ 2.30 7111 Cycles \$ 2.30 7111 Cycles \$ 2.35 7041 Cycles \$ 2.35 7041 Cycles \$ 2.36 7082 Cycles \$ 2.37 7199 Cycles \$ 2.37 7199 Cycles \$ 2.38 7073 C TS TS TS TS TS 5 2.37 7199 Cycles 5 2.38 7073 Cycles 5 2.38 7073 Cycles 5 2.39 7105 Cycles 5 2.40 7064 Cycles 5 2.41 7094 Cycles 5 2.42 7125 Cycles 5 2.43 7005 Cycles 5 2.44 7056 Cycles 5 2.45 7118 Cycles 5 2.46 7172 Cycles 5 2.47 7121 Cycles 5 2.48 7050 Cycles 5 2.49 7190 Cycles 5 2.50 7096 Cycles 5 2.51 7074 Cycles 5 2.53 7106 Cycles 5 2.53 7106 Cycles 5 2.53 7106 Cycles 5 2.54 704 Cycles TS TS TS TS TS TS TS TS TS \$2.52 7116 Cycles
\$2.53 7106 Cycles
\$2.54 7147 Cycles
\$2.55 7093 Cycles
\$2.56 7105 Cycles
\$2.57 7110 Cycles
\$2.57 7110 Cycles
\$2.58 7085 Cycles
\$2.59 7249 Cycles
\$2.59 7249 Cycles
\$2.60 7131 Cycles
\$2.61 7106 Cycles
\$2.62 7099 Cycles
\$2.62 7099 Cycles
\$2.63 7032 Cycles
\$2.64 7059 Cycles
\$2.65 7159 Cycles
\$2.66 7091 Cycles
\$2.68 7167 Cycles
\$2.68 7167 Cycles
\$2.69 7107 Cycles
\$2.70 7130 Cycles
\$2.70 7130 Cycles
\$2.71 7054 Cycles
\$2.72 7073 Cycles
\$2.72 7073 Cycles
\$2.73 7148 Cycles
\$2.74 7077 Cycles
\$2.75 7008 Cycles
\$2.75 7008 Cycles
\$2.76 7134 Cycles
\$2.77 7130 Cycles
\$2.77 7068 Cycles
\$2.77 7068 Cycles TS 2.77 7130 Cycles 2.78 7068 Cycles 2.79 7083 Cycles 2.80 7005 Cycles 2.81 7094 Cycles 2.82 7099 Cycles 2.83 7080 Cycles 2.84 7073 Cycles TS TS TS TS TS TS TS 2.84 7073 Cycles 2.85 7043 Cycles 2.85 7043 Cycles 2.86 7156 Cycles 2.87 7140 Cycles 2.88 7115 Cycles 2.89 7019 Cycles 2.90 7099 Cycles 2.91 7019 Cycles 2.92 7116 Cycles 2.93 7049 Cycles 2.94 7093 Cycles 2.95 7071 Cycles 2.95 7071 Cycles 2.96 7091 Cycles 2.97 7083 Cycles 2.98 7053 Cycles 2.99 7081 Cycles 2.99 7081 Cycles 2.99 7081 Cycles 2.99 7081 Cycles TS 2.99 7081 Cycles 2.100 7058 Cycles 2.101 7014 Cycles 2.102 7092 Cycles 2.103 7083 Cycles 2.104 7067 Cycles 2.105 7185 Cycles 2.106 7149 Cycles 2.107 7083 Cycles 2.108 7131 Cycles TS TS

2.109 7088 Cycles 2.110 7029 Cycles

TS

2016-09-15, 18:23:31+0530

PICurrCntrl_Per1



TS 2.111 7194 Cycles
TS 2.112 7080 Cycles
TS 2.113 7093 Cycles
TS 2.113 7093 Cycles
TS 2.114 7140 Cycles
TS 2.115 7097 Cycles
TS 2.116 7165 Cycles
TS 2.117 7033 Cycles
TS 2.117 7033 Cycles
TS 2.118 7152 Cycles
TS 2.119 7111 Cycles
TS 2.120 7134 Cycles
TS 2.120 7134 Cycles
TS 2.121 7099 Cycles
TS 2.122 7300 Cycles
TS 2.122 7300 Cycles
TS 2.124 7201 Cycles
TS 2.125 7136 Cycles
TS 2.126 7122 Cycles
TS 2.127 7105 Cycles
TS 2.127 7105 Cycles
TS 2.128 7129 Cycles
TS 2.129 7130 Cycles
TS 2.131 7098 Cycles
TS 2.132 7219 Cycles
TS 2.132 7219 Cycles
TS 2.133 7087 Cycles
TS 2.134 7053 Cycles
TS 2.135 7196 Cycles
TS 2.136 7118 Cycles
TS 2.137 7098 Cycles
TS 2.137 7098 Cycles
TS 2.137 7098 Cycles
TS 2.131 7098 Cycles
TS 2.131 7098 Cycles
TS 2.131 7098 Cycles
TS 2.131 7098 Cycles
TS 2.137 7196 Cycles
TS 2.137 7196 Cycles
TS 2.137 7196 Cycles
TS 2.137 7098 Cycles IS 2.136 7118 Cycles
TS 2.137 7098 Cycles
TS 2.137 7098 Cycles
TS 2.138 7146 Cycles
TS 2.139 7138 Cycles
TS 2.140 7111 Cycles
TS 2.140 7111 Cycles
TS 2.141 7084 Cycles
TS 2.142 7234 Cycles
TS 2.142 7234 Cycles
TS 2.144 7180 Cycles
TS 2.145 7232 Cycles
TS 2.146 7119 Cycles
TS 2.147 7153 Cycles
TS 2.148 7080 Cycles
TS 2.149 7140 Cycles
TS 2.151 7153 Cycles
TS 2.152 7125 Cycles
TS 2.152 7125 Cycles
TS 2.155 7044 Cycles
TS 2.155 7044 Cycles
TS 2.155 7050 Cycles
TS 2.156 7126 Cycles
TS 2.157 7152 Cycles
TS 2.158 7169 Cycles
TS 2.158 7169 Cycles
TS 2.157 7152 Cycles
TS 2.158 7169 Cycles
TS 2.157 7152 Cycles
TS 2.158 7160 Cycles
TS 2.157 7152 Cycles
TS 2.158 7165 Cycles
TS 2.158 7165 Cycles
TS 2.158 7165 Cycles
TS 2.161 7175 Cycles
TS 2.162 7274 Cycles
TS 2.163 7066 Cycles
TS 2.164 7132 Cycles
TS 2.166 7153 Cycles
TS 2.167 7078 Cycles
TS 2.167 7078 Cycles
TS 2.168 7293 Cycles
TS 2.169 7115 Cycles
TS 2.169 7115 Cycles
TS 2.170 7078 Cycles
TS 2.171 7079 Cycles
TS 2.171 7079 Cycles
TS 2.172 7255 Cycles
TS 2.174 7105 Cycles
TS 2.175 7099 Cycles
TS 2.175 7099 Cycles
TS 2.177 7132 Cycles
TS 2.177 7132 Cycles
TS 2.178 7178 Cycles
TS 2.177 7132 Cycles
TS 2.178 7178 Cycles
TS 2.179 7134 Cycles
TS 2.179 7138 Cycles
TS 2.180 7067 Cycles
TS 2.181 7078 Cycles
TS 2.181 7078 Cycles
TS 2.182 7142 Cycles
TS 2.181 7078 Cycles
TS 2.183 7078 Cycles
TS 2.183 7078 Cycles
TS 2.183 7078 Cycles TS 2.183 7078 Cycles TS 2.184 7142 Cycles



Description Vector Description:

```
TS 2.1All Min
   TS 2.2AII_Max
  TS 2.3MtrCurrQaxRef_Amp_M_f32[2] = Min
TS 2.4MtrCurrQaxRef_Amp_M_f32[2] = Max
TS 2.5MtrCurrQaxRef_Amp_M_f32[2] = zero
TS 2.6MtrCurrQaxRef_Amp_M_f32[2] = Neg
TS 2.6MtrCurrQaxRef_Amp_M_f32[2] = Pos
TS 2.6MtrCurrQaxRef_Amp_M_f32[2] = Min
 TS 2.8MtrCurrDaxRef_Amp_M_f32[2]= Min
TS 2.9MtrCurrDaxRef_Amp_M_f32[2] = Max
TS 2.10MtrCurrDaxRef_Amp_M_f32[2] = zero
TS 2.11MtrCurrDaxRef_Amp_M_f32[2] = Neg
TS 2.12MtrCurrDaxRef_Amp_M_f32[2] = Neg
TS 2.12MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[2]= Min
TS 2.13MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[2] = Max
TS 2.15MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[2] = Pos
TS 2.16MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[2] = Min
TS 2.17MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[2] = Max
TS 2.18MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[2] = Max
     TS 2.18MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[2] = Zero
  TS 2.18MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[2] = Neg
TS 2.19MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[2] = Neg
TS 2.20MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[2] = Pos
TS 2.21MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[2] = Nin
TS 2.22MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[2] = Neg
TS 2.23MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[2] = Pos
TS 2.24MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[2] = Min
TS 2.24MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[2] = Min
TS 2.24MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[2] = Min
   TS 2.25MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[2] = Max TS 2.26MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[2] = zero TS 2.27MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[2] = Neg
  TS 2.28MtrCtrl_MtrQaxPropotionalGain_Ohm_M_52[2] = Neg
TS 2.28MtrCtrl_MtrQaxPropotionalGain_Ohm_M_52[2] = Pos
TS 2.29MtrPosComputationDelay_Rad_M_f32[2] = Min
TS 2.30MtrPosComputationDelay_Rad_M_f32[2] = Max
TS 2.31MtrPosComputationDelay_Rad_M_f32[2] = Zero
 TS 2.31MtrPosComputationDelay_Rad_M_f32[2] = Zero TS 2.32MtrPosComputationDelay_Rad_M_f32[2] = Neg TS 2.33MtrPosComputationDelay_Rad_M_f32[2] = Pos TS 2.34MtrCtrl_MtrVoltDaxFF_Volt_M_f32[2] = Min TS 2.35MtrCtrl_MtrVoltDaxFF_Volt_M_f32[2] = Max TS 2.36MtrCtrl_MtrVoltDaxFF_Volt_M_f32[2] = Zero TS 2.37MtrCtrl_MtrVoltDaxFF_Volt_M_f32[2] = Neg TS 2.38MtrCtrl_MtrVoltDaxFF_Volt_M_f32[2] = Neg TS 2.38MtrCtrl_MtrVoltDaxFF_Volt_M_f32[2] = Pos TS 2.39MtrCtrl_MtrVoltDaxFF_Volt_M_f32[2] = Min TS 2.40MtrCtrl_MtrVoltQaxFF_Volt_M_f32[2] = Max TS 2.41MtrCtrl_MtrVoltQaxFF_Volt_M_f32[2] = Zero TS 2.42MtrCtrl_MtrVoltQaxFF_Volt_M_f32[2] = Neg TS 2.43MtrCtrl_MtrVoltQaxFF_Volt_M_f32[2] = Pos TS 2.44MtrCtrl_MtrVoltQaxFF_Volt_M_f32[2] = Pos TS 2.44MtrCtrl_MtrVoltQaxFF_Volt_M_f32[2] = Neg TS 2.44MtrCtrl_MtrVoltQaxFF_Volt_M_f32[2] = Neg TS 2.44MtrCtrl_MtrVoltQaxFF_Volt_M_f32[2] = Neg TS 2.44MtrCtrl_MtrDampTermDax_Ohm_M_f32[2] = Min TS 2.44MtrCtrl_MtrDampTermDax_Ohm_f42[2] = Min TS 2.44MtrCtrl_MtrDampTermDax_Ohm_M_f32[2] = Min TS
  TS 2.43MtrCtrl_MtrDampTermDax_Ohm_M_f32[2] = Min
TS 2.44MtrCtrl_MtrDampTermDax_Ohm_M_f32[2] = Min
TS 2.45MtrCtrl_MtrDampTermDax_Ohm_M_f32[2] = Max
TS 2.46MtrCtrl_MtrDampTermDax_Ohm_M_f32[2] = Pos
TS 2.47MtrCtrl_MtrDampTermQax_Ohm_M_f32[2] = Min
TS 2.48MtrCtrl_MtrDampTermQax_Ohm_M_f32[2] = Max
TS 2.49MtrCtrl_MtrDampTermQax_Ohm_M_f32[2] = Pos
  1S 2.49MtrCtrl_MtrImpedDax_Ohm_M_f32[2] = Min
TS 2.50MtrCtrl_MtrImpedDax_Ohm_M_f32[2] = Min
TS 2.51MtrCtrl_MtrImpedDax_Ohm_M_f32[2] = Max
TS 2.52MtrCtrl_MtrImpedDax_Ohm_M_f32[2] = Pos
TS 2.53MtrCtrl_MtrImpedQax_Ohm_M_f32[2] = Min
   TS 2.54MtrCtrl_MtrImpedQax_Ohm_M_f32[2] = Max
TS 2.55MtrCtrl_MtrImpedQax_Ohm_M_f32[2] = Pos
TS 2.56MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[2]= Min
  TS 2.59MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[2] = Max
TS 2.58MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[2] = Zero
TS 2.59MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[2] = Neg
TS 2.69MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[2] = Pos
   TS 2.61MtrCurrQaxRpl_Amp_M_f32[2]= Min
TS 2.62MtrCurrQaxRpl_Amp_M_f32[2] = Max
TS 2.63MtrCurrQaxRpl_Amp_M_f32[2] = zero
   TS 2.64MtrCurrQaxRpl_Amp_M_f32[2] = Neg
TS 2.65MtrCurrQaxRpl_Amp_M_f32[2] = Pos
TS 2.66MtrCurrQaxCog_Amp_M_f32= Min
   TS 2.67MtrCurrQaxCog_Amp_M_f32 = Max
TS 2.68MtrCurrQaxCog_Amp_M_f32 = zero
TS 2.69MtrCurrQaxCog_Amp_M_f32 = Pos
   TS 2.70MtrCurrQaxCog_Amp_M_f32 = Neg
TS 2.71PlCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 = Min
TS 2.72PlCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 = Max
     TS 2.73PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 = mid
  TS 2.74PlCurrCntrl_MtrCurrDaxSatFluxRatio_UIs_M_f32 = Min
TS 2.75PlCurrCntrl_MtrCurrDaxSatFluxRatio_UIs_M_f32 = Max
     TS 2.76PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 = mid
   TS 2.77MtrCurrQaxPrevIntg_Volt_M_f32 = Min
TS 2.78MtrCurrQaxPrevIntg_Volt_M_f32 = Max
TS 2.78MtrCurrQaxPrevIntg_Volt_M_f32 = Max
TS 2.79MtrCurrQaxPrevIntg_Volt_M_f32 = zero
TS 2.80MtrCurrQaxPrevIntg_Volt_M_f32 = Pos
TS 2.81MtrCurrQaxPrevIntg_Volt_M_f32 = Neg
TS 2.81MtrCurrDaxPrevIntg_Volt_M_f32 = Min
TS 2.83MtrCurrDaxPrevIntg_Volt_M_f32 = Min
TS 2.83MtrCurrDaxPrevIntg_Volt_M_f32 = Max
TS 2.84MtrCurrDaxPrevIntg_Volt_M_f32 = zero
TS 2.85MtrCurrDaxPrevIntg_Volt_M_f32 = Pos
TS 2.86MtrCurrDaxPrevIntg_Volt_M_f32 = Neg
TS 2.87k_MtrCtrlFeedbackControlDisable_Cnt_lgc = Min/Def
TS 2.88k_MtrCtrlFeedbackControlDisable_Cnt_lgc = Max
TS 2.89k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc = 0/Def
TS 2.90k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc = 1
TS 2.91k_CLOAFdbackSignalSclFacSlew_UlspS_f32 = Min
TS 2.92k_CLOAFdbackSignalSclFacSlew_UlspS_f32 = Pos
   TS 2.93k_CLOAFdbackSignalSclFacSlew_UlspS_f32 = Pos
TS 2.94k_CLOAFdbackSignalSclFacSlew_UlspS_f32 = Def
```



```
TS 2.95PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 = Min
TS 2.96PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 = Max
TS 2.97PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 = mid
TS 2.98k_ILOAFdbackSignalSclFacSlew_UlspS_f32 = Min
 TS 2.99k_ILOAFdbackSignalSclFacSlew_UlspS_f32 = Max
TS 2.100k_ILOAFdbackSignalSclFacSlew_UlspS_f32 = Pos
TS 2.101k_ILOAFdbackSignalSclFacSlew_UlspS_f32 = Def
 TS 2.102PICurrCntrl InverterFailSclFac Uls M f32 = Min TS 2.103PICurrCntrl InverterFailSclFac Uls M f32 = Max TS 2.104PICurrCntrl InverterFailSclFac Uls M f32 = mid
TS 2.105k_deadtimeVScale_UIs_f32 = Min
TS 2.106k_deadtimeVScale_UIs_f32 = Max/Def
TS 2.107k_deadtimeVScale_UIs_f32 = Pos
IS 2.107k_deadtimeVScale_UIs_f32 = Pos
TS 2.108k_MtrCurrQaxRefModifDsb_Cnt_Igc = Min/Def
TS 2.109k_MtrCurrQaxRefModifDsb_Cnt_Igc = Max
TS 2.110MtrCurrQax_Amp_f32 = Min
TS 2.111MtrCurrQax_Amp_f32 = Max
TS 2.112MtrCurrQax_Amp_f32 = Zero
TS 2.113MtrCurrQax_Amp_f32 = Pos
TS 2.114MtrCurrQax_Amp_f32 = Neg
 TS 2.115MtrCurrDax Amp_f32 = Min
TS 2.116MtrCurrDax_Amp_f32 = Max
TS 2.117MtrCurrDax_Amp_f32 = Zero
TS 2.117MtrCurrDax_Amp_f32 = Pos
TS 2.118MtrCurrDax_Amp_f32 = Pos
TS 2.119MtrCurrDax_Amp_f32 = Neg
TS 2.120MtrCtrl_Vecu_Volt_M_f32= Min
TS 2.121MtrCtrl_Vecu_Volt_M_f32 = Max
TS 2.122MtrCtrl_Vecu_Volt_M_f32 = Pos
TS 2.123ModIdxSrlComSvcDft_Cnt_lgc = Min
IS 2.123ModIdxSrIComSvcDrt_Cnt_Igc = Min
TS 2.124ModIdxSrIComSvcDrt_Cnt_Igc = Max
TS 2.125SysState_Cnt_T_Enum =>RTE_MODE_StaMd_Mode_DISABLE
TS 2.126SysState_Cnt_T_Enum =>RTE_MODE_StaMd_Mode_OPF
TS 2.127SysState_Cnt_T_Enum =>RTE_MODE_StaMd_Mode_OPERATE
TS 2.128SysState_Cnt_T_Enum=>RTE_MODE_StaMd_Mode_WARMINIT
TS 2.129SysState_Cnt_T_Enum=>RTE_TRANSITION_StaMd_Mode
TS 2.130FastDataAccessBufIndex_Cnt_M_u16 = Min
TS 2.131FastDataAccessBufIndex_Cnt_M_u16 = Min
 TS 2.131FastDataAccessBufIndex_Cnt_M_u16 = Max TS 2.132MotCurrLoaMtgtnEn_Cnt_lgc = Min TS 2.133MotCurrLoaMtgtnEn_Cnt_lgc = Max
 TS 2.134IvtrLoaMtgtnEn_Cnt_lgc = Min
TS 2.135IvtrLoaMtgtnEn_Cnt_lgc = Max
TS 2.136SlowDataAccessBufIndex_Cnt_M_u16 = Min
 TS 2.137SlowDataAccessBufIndex_Cnt_M_u16 = Max TS 2.138t_CommOffsetTblX_UIs_u3p13[2] = Min TS 2.139t_CommOffsetTblX_UIs_u3p13[2] = Max
 TS 2.140t_CommOffsetTbIX_UIs_u3p13[2] = Pos
TS 2.140t_CommOffsetTbIX_OIS_LISP13[2] = Pos
TS 2.141t_CommOffsetTbIY_Cnt_u16[2] = Min
TS 2.142t_CommOffsetTbIY_Cnt_u16[2] = Max
TS 2.143t_CommOffsetTbIY_Cnt_u16[2] = Pos
TS 2.144k_MtrCtrlVirualResDax_Ohm_f32 = Min/Def
TS 2.145k_MtrCtrlVirualResDax_Ohm_f32 = Max
TS 2.146k_MtrCtrlVirualResDax_Ohm_f32 = Pos
TS 2.147k_MtrCtrlVirualResQax_Ohm_f32 = Min/Def TS 2.147k_MtrCtrlVirualResQax_Ohm_f32 = Min/Def TS 2.148k_MtrCtrlVirualResQax_Ohm_f32 = Max TS 2.149k_MtrCtrlVirualResQax_Ohm_f32 = Pos TS 2.150k_VoltSatDaxPolyCoeff_Uls_f32 = Min TS 2.151k_VoltSatDaxPolyCoeff_Uls_f32 = Max TS 2.152k_VoltSatDaxPolyCoeff_Uls_f32 = Zero/Def
TS 2.153k_VoltSatDaxPolyCoeff_Uls_f32 = Zero
TS 2.153k_VoltSatDaxPolyCoeff_Uls_f32 = Neg
TS 2.154k_VoltSatDaxPolyCoeff_Uls_f32 = Pos
TS 2.155k_VoltSatQaxPolyCoeff_Uls_f32 = Min
TS 2.156k_VoltSatQaxPolyCoeff_Uls_f32 = Max
 TS 2.157k_VoltSatQaxPolyCoeff_UIs_f32 = Zero/Def
TS 2.158k_VoltSatQaxPolyCoeff_UIs_f32 = Neg
TS 2.159k_VoltSatQaxPolyCoeff_UIs_f32 = Pos
 TS 2.160MtrCurrOffComOffset_Cnt_u16 = Min
TS 2.161MtrCurrOffComOffset_Cnt_u16 = Max
TS 2.162MtrCurrOffComOffset_Cnt_u16 = Pos
 TS 2.163k_MtrVoltQaxFiltFFEnable_Cnt_lgc= Min TS 2.164k_MtrVoltQaxFiltFFEnable_Cnt_lgc= Max TS 2.165k_MtrVoltVecuFiltEnable_Cnt_lgc= Min
 TS 2.166k_MtrVoltVecuFiltEnable_Cnt_lgc= Max
 TS 2.167k_MtrVoltQaxIntegLoLim_Volt_f32 = >min
TS 2.168k_MtrVoltQaxIntegLoLim_Volt_f32 = >max
 TS 2.169k_MtrVoltQaxIntegLoLim_Volt_f32 = >neg
TS 2.170k_MtrVoltQaxIntegLoLim_Volt_f32 = >default
TS 2.171k_MtrVoltQaxIntegHiLim_Volt_f32=>min
 TS 2.172k_MtrVoltQaxIntegHiLim_Volt_f32=>max
TS 2.173k_MtrVoltQaxIntegHiLim_Volt_f32=>default
TS 2.174k_MtrVoltQaxIntegHiLim_Volt_f32=>mid
TS 2.174k_MtrVoltQaxIntegHiLim_Volt_f32=>mid
TS 2.175k_MtrVoltDaxIntegLoLim_Volt_f32=>min
TS 2.176k_MtrVoltDaxIntegLoLim_Volt_f32=>max
TS 2.176k_MtrVoltDaxIntegLoLim_Volt_f32=>neg
TS 2.178k_MtrVoltDaxIntegLoLim_Volt_f32=>default
TS 2.179k_MtrVoltDaxIntegHiLim_Volt_f32=>min
TS 2.180k_MtrVoltDaxIntegHiLim_Volt_f32=>min
TS 2.180k_MtrVoltDaxIntegHiLim_Volt_f32=>mid
TS 2.181k_MtrVoltDaxIntegHiLim_Volt_f32=>mid
TS 2.182k_MtrVoltDaxIntegHiLim_Volt_f32=>default
TS 2.183k_MtrCurrQaxRefModifRplEn_Cnt_lgc=>min
TS 2.184k_MtrCurrQaxRefModifRplEn_Cnt_lgc=>max/Default
```

Test Step 2.1 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0





Name	Input Value
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-220
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-220
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.00499999989
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00499999989
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0049999989
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0049999989
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-1024
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-1024
	0.00499999989
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0049999989
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0049999989
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0049999989
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-1024
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-1024
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-31
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-31
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-31
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-31
MtrCtrl_Vecu_Volt_M_f32[0]	5
MtrCtrl_Vecu_Volt_M_f32[1]	5
MtrCurrDaxPrevIntg_Volt_M_f32	-31
MtrCurrDaxRef_Amp_M_f32[0]	-220
MtrCurrDaxRef_Amp_M_f32[1]	-220
MtrCurrQaxCog_Amp_M_f32	-220
MtrCurrQaxPrevIntg_Volt_M_f32	-31
MtrCurrQaxRef_Amp_M_f32[0]	-220
MtrCurrQaxRef_Amp_M_f32[1]	-220
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	-3.1400001
MtrPosComputationDelay Rad M f32[1]	-3.1400001
PICurrCntrl CurrSensFailSclFac Uls M f32	0
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0
PICurrCntrl_InverterFailSclFac_Uls_M_f32	
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-1350
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1350
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	-0.996816993
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.96346009e-005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-1350
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-1350
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	-0.996816993
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	1.96346009e-005
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	10
k_DualEcuSignalSclFacSlew_UlspS_f32	10
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	10
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0
k_MtrCtrlVirualResQax_Ohm_f32	0
k MtrCurrQaxRefModifDsb Cnt Igc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k MtrVoltDaxIntegHiLim Volt f32	0
k_MtrVoltDaxIntegHiLim_Volt_f32	-31
	-31
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	
k_MtrVoltQaxIntegHiLim_Volt_f32	0
k_MtrVoltQaxIntegLoLim_Volt_f32	-31
k_MtrVoltVecuFiltEnable_Cnt_lgc	0
k_VoltSatDaxPolyCoeff_Uls_f32	-25
k_VoltSatQaxPolyCoeff_Uls_f32	-25
k_deadtimeVScale_Uls_f32	0.94999988

PICurrCntrl_Per1



Name	Input Value		
t_CommOffsetTblX_Uls_u3p13[0]	0		
t_CommOffsetTblX_Uls_u3p13[1]	0		
t_CommOffsetTblY_Cnt_u16[0]	0		
t_CommOffsetTblY_Cnt_u16[1]	0		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-220		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	0		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-220		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	0	0	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	62259	62259 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	0	0 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-3.35873365	-3.35873365 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-3.3587811	-3.3587811 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	8209	8209 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl DualEcuFailSclFac Uls M f32	0.00125000009	0.00125000009 ± 0.0625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	•
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	-

Test Step 2.2 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	220
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	220
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	2
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	2
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	1024
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	1024
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	2
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	2
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	1024





Name	Input Value		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	1024		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	31		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	31		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltQaxFE_Volt_M_f32[1]	31		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] MtrCtrl_Vecu_Volt_M_f32[0]	31		
MtrCtrl_Vecu_Volt_M_f32[1]	31		
MtrCurrDaxPrevIntg_Volt_M_f32	31		
MtrCurrDaxRef_Amp_M_f32[0]	220		
MtrCurrDaxRef_Amp_M_f32[1]	220		
MtrCurrQaxCog Amp M f32	220		
MtrCurrQaxPrevIntg_Volt_M_f32	31		
MtrCurrQaxRef_Amp_M_f32[0]	220		
MtrCurrQaxRef_Amp_M_f32[1]	220		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	3.1400001		
MtrPosComputationDelay_Rad_M_f32[1]	3.1400001		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	1		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	1		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	1		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	1350		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1350		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	50928.6016		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput Uls f32	0.996827006		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1350		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	50928.6016		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.996827006		
k CLOAFdbackSignalSclFacSlew UlspS f32	8000		
k DualEcuSignalSclFacSlew UlspS f32	8000		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	8000		
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.200000003		
k_MtrCtrlVirualResQax_Ohm_f32	0.200000003		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRpIEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	31		
k_MtrVoltDaxIntegLoLim_Volt_f32	0		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32			
	31		
k_MtrVoltQaxIntegLoLim_Volt_f32	0		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32	0 1 25		
k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32	0 1 25 25		
k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32	0 1 25 25 1		
k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTbIX_Uls_u3p13[0]	0 1 25 25 1 8192		
k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTbIX_Uls_u3p13[0] t_CommOffsetTbIX_Uls_u3p13[1]	0 1 25 25 1 8192 8192		
k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTblX_Uls_u3p13[0] t_CommOffsetTblX_Uls_u3p13[1] t_CommOffsetTblY_Cnt_u16[0]	0 1 25 25 1 8192 8192 2000		
k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTblX_Uls_u3p13[0] t_CommOffsetTblX_Uls_u3p13[1] t_CommOffsetTblY_Cnt_u16[0] t_CommOffsetTblY_Cnt_u16[1]	0 1 25 25 1 8192 8192		
k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTblX_Uls_u3p13[0] t_CommOffsetTblX_Uls_u3p13[1] t_CommOffsetTblY_Cnt_u16[0]	0 1 25 25 1 8192 8192 2000 2000		
k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTblX_Uls_u3p13[0] t_CommOffsetTblX_Uls_u3p13[1] t_CommOffsetTblY_Cnt_u16[0] t_CommOffsetTblY_Cnt_u16[1] target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0 1 25 25 25 1 8192 8192 2000 2000		
k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTblX_Uls_u3p13[0] t_CommOffsetTblX_Uls_u3p13[1] t_CommOffsetTblY_Cnt_u16[0] t_CommOffsetTblY_Cnt_u16[1] target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	0 1 25 25 25 1 8192 8192 2000 2000		
k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTblX_Uls_u3p13[0] t_CommOffsetTblX_Uls_u3p13[1] t_CommOffsetTblY_Cnt_u16[0] t_CommOffsetTblY_Cnt_u16[1] target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0 1 25 25 25 1 8192 8192 2000 2000 1 1		
k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTblX_Uls_u3p13[0] t_CommOffsetTblX_Uls_u3p13[1] t_CommOffsetTblY_Cnt_u16[0] t_CommOffsetTblY_Cnt_u16[1] target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_ptr	0 1 25 25 25 1 8192 8192 2000 2000 1 1		
k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTblX_Uls_u3p13[0] t_CommOffsetTblX_Uls_u3p13[1] t_CommOffsetTblY_Cnt_u16[0] t_CommOffsetTblY_Cnt_u16[1] target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0 1 25 25 25 1 8192 8192 2000 2000 1 1 1 1		
k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTblX_Uls_u3p13[0] t_CommOffsetTblX_Uls_u3p13[1] t_CommOffsetTblY_Cnt_u16[0] t_CommOffsetTblY_Cnt_u16[1] target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_ptr target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	0 1 25 25 25 1 8192 8192 2000 2000 1 1 1 1 1 220 5000		
k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTblX_Uls_u3p13[0] t_CommOffsetTblX_Uls_u3p13[1] t_CommOffsetTblY_Cnt_u16[0] t_CommOffsetTblY_Cnt_u16[1] target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	0 1 25 25 1 8192 8192 2000 2000 1 1 1 1 220 5000 220	Expected Value	Result
k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTblX_Uls_u3p13[0] t_CommOffsetTblX_Uls_u3p13[1] t_CommOffsetTblY_Cnt_u16[0] t_CommOffsetTblY_Cnt_u16[1] target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr target_MtrCntrl_Read_IvtrLoaMtgnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_ptr target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0 1 25 25 1 8192 8192 2000 2000 1 1 1 1 220 5000 220 4	Expected Value 5000	Result
k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTblX_Uls_u3p13[0] t_CommOffsetTblX_Uls_u3p13[1] t_CommOffsetTblY_Cnt_u16[0] t_CommOffsetTblY_Cnt_u16[1] target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr target_MtrCntrl_Read_lvtrLoaMtgnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name	0 1 25 25 1 8192 8192 2000 2000 1 1 1 1 220 5000 220 4 Actual Value	· ·	•
k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTblX_Uls_u3p13[0] t_CommOffsetTblX_Uls_u3p13[1] t_CommOffsetTblY_Cnt_u16[0] t_CommOffsetTblY_Cnt_u16[1] target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr target_MtrCntrl_Read_lvtrLoaMtgnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name MtrCntrl_Write_CommOffset_Cnt_u16(val)	0 1 25 25 1 8192 8192 2000 2000 1 1 1 1 220 5000 220 4 Actual Value 5000	5000	Result
k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTblX_Uls_u3p13[0] t_CommOffsetTblX_Uls_u3p13[1] t_CommOffsetTblY_Cnt_u16[0] t_CommOffsetTblY_Cnt_u16[1] target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MotldxSrlComSvcDft_Cnt_lgc_Val target_MtrCntrl_Read_MtrCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_Modldx_Uls_u16p16(val)	0 1 25 25 25 1 8192 8192 2000 2000 1 1 1 1 220 5000 220 4 Actual Value 5000 0	5000 0 ± 1	•
k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTbIX_Uls_u3p13[0] t_CommOffsetTbIX_Uls_u3p13[1] t_CommOffsetTbIY_Cnt_u16[0] t_CommOffsetTbIY_Cnt_u16[1] target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr target_MtrCntrl_Read_IvtLoaMtgnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_ptr target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MotCurrLoaMtgnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_ModIdx_Uls_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	0 1 25 25 25 1 8192 8192 2000 2000 1 1 1 1 220 5000 220 4 Actual Value 5000 0	5000 0 ± 1 0 ± 7.81E-03	
k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTblX_Uls_u3p13[0] t_CommOffsetTblX_Uls_u3p13[1] t_CommOffsetTblY_Cnt_u16[0] t_CommOffsetTblY_Cnt_u16[1] target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr target_MtrCntrl_Read_IvtLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MotGurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_ModIdx_Uls_u16p16(val) MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0 1 25 25 25 1 8192 8192 2000 2000 1 1 1 1 1 1 220 5000 220 4 Actual Value 5000 0 0 21.9203072 21.9203072 40943	5000 0 ± 1 0 ± 7.81E-03 21.9203072 ± 4.88E-04 21.9203072 ± 4.88E-04 40943 ± 1.52588E-05	
k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTblX_Uls_u3p13[0] t_CommOffsetTblX_Uls_u3p13[1] t_CommOffsetTblY_Cnt_u16[0] t_CommOffsetTblY_Cnt_u16[1] target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	0 1 25 25 25 1 8192 8192 2000 2000 1 1 1 1 1 220 5000 220 4 Actual Value 5000 0 0 21.9203072 21.9203072	5000 0 ± 1 0 ± 7.81E-03 21.9203072 ± 4.88E-04 21.9203072 ± 4.88E-04	



Test Step Call Trace ✓					
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	•	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	•	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•	
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~	
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•	

Test Step 2.3 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
htrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
htrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0430000015
htrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.114
/trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0610000007
/trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0579999983
htrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.43400002
ltrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.70599997
htrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	362.112
ltrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	65.1259995
ltrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.064000003
ftrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0209999997
ltrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.104000002
ltrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.063000001
htrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.356999993
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.65200001
ltrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-894.130005
htrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-888.995972
ItrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-29.9890003
ItrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	29.243
ItrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-29.7110004
ItrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	3.61899996
htrCtrl_Vecu_Volt_M_f32[0]	14.2779999
ltrCtrl_Vecu_Volt_M_f32[1]	16.6380005
htrCurrDaxPrevIntg_Volt_M_f32	19.7509995
htrCurrDaxRef_Amp_M_f32[0]	67.4899979
/trCurrDaxRef_Amp_M_f32[1]	119.721001
/trCurrQaxCog_Amp_M_f32	-181.929001
ItrCurrQaxPrevIntg_Volt_M_f32	7.82140017
ItrCurrQaxRef_Amp_M_f32[0]	-220
ItrCurrQaxRef_Amp_M_f32[1]	-220
ItrCurrQaxRpl_Amp_M_f32	0
ItrPosComputationDelay_Rad_M_f32[0]	-0.124899998
ItrPosComputationDelay Rad M f32[1]	-1.05569994
PICurrCntrl CurrSensFailSclFac Uls M f32	0.416999996
PICurrCntrl DualEcuFailSclFac Uls M f32	0.100000001
PlCurrCntrl InverterFailSclFac Uls M f32	0.787

PICurrCntrl Per1

2016-09-15, 18:23:31+0530



Input Value PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32 0.190799996 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.708000004 PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32 267.119995 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -657.130005 PICurrCntrl MtrVecuFilt M str.TermN Uls f32 48410.1016 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.0835999995 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 267.119995 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32$ -657.130005 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 48410.1016 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32$ 0.0835999995 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 10.1009998 k_DualEcuSignalSclFacSlew_UlspS_f32 11 1999998 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 4233.2002 $k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc$ 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0.0879999995 k_MtrCtrlVirualResDax_Ohm_f32 k_MtrCtrlVirualResQax_Ohm_f32 0.00999999978 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ 0 k_MtrVoltDaxIntegHiLim_Volt_f32 12.9371996 k_MtrVoltDaxIntegLoLim_Volt_f32 -0.5 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 0 25.1975002 $k_MtrVoltQaxIntegHiLim_Volt_f32$ k_MtrVoltQaxIntegLoLim_Volt_f32 -0.5 k MtrVoltVecuFiltEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 -1.59399998 k VoltSatQaxPolyCoeff Uls f32 8.35700035 k_deadtimeVScale_Uls_f32 0.950999975 t CommOffsetTblX Uls u3p13[0] 4914 $t_CommOffsetTblX_Uls_u3p13[1]$ 7782 t CommOffsetTblY Cnt u16[0] 1099 t_CommOffsetTblY_Cnt_u16[1] 1672 target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr 0 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 0 $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -72.4260025 4932 $target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr$ target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 77.189003 $target_MtrCntrl_Read_SysState_Cnt_Enum_Val$ **Actual Value Expected Value** Name Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1672 1672 62324 62324 ± 1 MtrCntrl_Write_ModIdx_Uls_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -38.0709991 -38.0709991 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 6.70963526 6.70963383 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 11.8047943 11.8047924 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 4088 4088 ± 1.52588E-05

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	-
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

12 9371996

0.101400003

12 9371996

0.101400003 ± 0.0625

MtrCurrDaxPrevIntg_Volt_M_f32

PICurrCntrl_DualEcuFailSclFac_Uls_M_f32





Test Step 2.4 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0] MtrCtrl MtrCurrDaxMaxVal Amp M f32[1]	205.820999 -206.792007
MtrCtrl MtrDampTermDax Ohm M f32[0]	0.115000002
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.057999983
MtrCtrl MtrDampTermQax Ohm M f32[0]	0.0320000015
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0869999975
MtrCtrl MtrDaxIntegralGain Ohm M f32[0]	0.0649999976
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.227
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	537.232971
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	67.9840012
MtrCtrl MtrlmpedDax Ohm M f32[0]	0.0549999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.109999999
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0270000007
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0920000002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.703000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.75199997
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	462.437012
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-685.195984
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	30.6930008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	0.219999999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	3.45499992
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-19.1830006
MtrCtrl_Vecu_Volt_M_f32[0]	22.3540001
MtrCtrl_Vecu_Volt_M_f32[1]	24.7140007
MtrCurrDaxPrevIntg_Volt_M_f32	-23.0620003
MtrCurrDaxRef_Amp_M_f32[0]	37.4550018
MtrCurrDaxRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxCog_Amp_M_f32	-55.5390015
MtrCurrQaxPrevIntg_Volt_M_f32	8.08899975
MtrCurrQaxRef_Amp_M_f32[0]	220
MtrCurrQaxRef_Amp_M_f32[1]	220
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0] MtrPosComputationDelay_Rad_M_f32[1]	-1.08599997 2.90249991
PICurrCntrl CurrSensFailSclFac Uls M f32	1
PICurrCntrl DualEcuFailSclFac Uls M f32	0.20000003
PICurrCntrl InverterFailSclFac UIs M f32	0.638000011
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.880900025
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.978999972
PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32	-657.099976
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	-194.190002
PICurrCntrl MtrVecuFilt M str.TermN Uls f32	47050.1992
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0229000002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-657.099976
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	47050.1992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0229000002
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	10
k_DualEcuSignalSclFacSlew_UlspS_f32	12.3999996
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	7088.3501
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.194999993
k_MtrCtrlVirualResQax_Ohm_f32	0.142000005
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	17.9123993
k_MtrVoltDaxIntegLoLim_Volt_f32	-0.69999988
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	19.4449997
k_MtrVoltQaxIntegLoLim_Volt_f32	-0.69999988
k MtrVoltVecuFiltEnable Cnt lgc	0

PICurrCntrl_Per1



Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	-19.4559994		
k_VoltSatQaxPolyCoeff_Uls_f32	-18.6200008		
k_deadtimeVScale_Uls_f32	0.95599997		
t_CommOffsetTblX_Uls_u3p13[0]	4170		
t_CommOffsetTblX_Uls_u3p13[1]	6749		
t_CommOffsetTblY_Cnt_u16[0]	177		
t_CommOffsetTblY_Cnt_u16[1]	340		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	83.9489975		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	335		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-145.169006		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	315	315	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	50872	50872 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0.219999999	0.219999999 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-19.1830006	-19.1830006 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	62923	62923 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	-0.69999988	-0.699999988	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.198449999	0.198449999 ± 0.0625	✓

Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.5 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-69.0940018
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	161.973007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0489999987
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.114
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.108000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0820000023
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.824000001
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.423999995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-284.230011
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	346.425995
MtrCtrl_MtrlmpedDax_Ohm_M_f32[0]	0.0710000023
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.00700000022
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.123999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0790000036

2016-09-15, 18:23:31+0530



		,	10-10
Name	Input Value		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.0089999961		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.625		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	351.605011		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	882.085999		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-24.6650009		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	18.8299999		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	26.4720001		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	6.0999999		
MtrCtrl_Vecu_Volt_M_f32[0]	14.2779999		
MtrCtrl_Vecu_Volt_M_f32[1]	16.6380005		
MtrCurrDaxPrevIntg_Volt_M_f32	-6.72300005		
MtrCurrDaxRef_Amp_M_f32[0]	94.3150024		
MtrCurrDaxRef_Amp_M_f32[1]	37.4959984		
MtrCurrQaxCog_Amp_M_f32	146.660995		
MtrCurrQaxPrevIntg_Volt_M_f32	24.8390999		
MtrCurrQaxRef_Amp_M_f32[0]	0		
MtrCurrQaxRef_Amp_M_f32[1]	0		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	2.78379989		
MtrPosComputationDelay_Rad_M_f32[1]	2.09030008		
PICurrCotrl_CurrSensFailSclFac_UIs_M_f32	0.0170000009		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32 PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.00100000005 0.653999984		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.588400006		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.40700005		
PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32	-43.1699982		
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	386.220001		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	47672		
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.660899997		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	386.220001		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	47672		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.660899997		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2305.86011		
k_DualEcuSignalSclFacSlew_UlspS_f32	13.6000004		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5143.29004		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.0260000005		
k_MtrCtrlVirualResQax_Ohm_f32	0.199000001		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	4.14720011		
k_MtrVoltDaxIntegLoLim_Volt_f32	-0.800000012		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	2.33150005		
k_MtrVoltQaxIntegLoLim_Volt_f32	-0.800000012		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	18.1009998		
k_VoltSatQaxPolyCoeff_Uls_f32	12.7729998		
k_deadtimeVScale_UIs_f32	0.984000027		
t_CommOffsetTbIX_UIs_u3p13[0]	4013		
t_CommOffsetTblX_Uls_u3p13[1]	4882		
t_CommOffsetTbIY_Cnt_u16[0]	790		
t_CommOffsetTblY_Cnt_u16[1]	931		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-144.667007		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4626		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-100.035004		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Resul
MtrCntrl_Write_CommOffset_Cnt_u16(val)	931	931	Resul
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	-146.660995	64487 ± 1	
		-146.660995 ± 7.81E-03	\
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)		_13 7772013 ± 4 00E 04	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-13.7772903	-13.7772913 ± 4.88E-04	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-13.7772903 2.75248003	2.75247979 ± 4.88E-04	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-13.7772903		



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	-

lame	Input Value	
astDataAccessBufIndex Cnt M u16	1	
ItrCntrl Read DualEcuMotCtrlMtgnEna Cnt Igc(ptr)	target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr	
ItrCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr	
trCntrl Read ModidxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModidxSrlComSvcDft Cnt lgc Val	
ItrCntrl Read MotCurrLoaMtgtnEn Cnt Igc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_var	
trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
htrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
ItrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-132.813004	
	-9.14299965	
ItrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]		
trCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.119000003	
ltrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0820000023	
ItrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0480000004	
ItrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0930000022	
ItrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.912	
trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.71200001	
trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	355.987	
trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-300.080994	
trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0939999968	
trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.075000003	
trCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0329999998	
ltrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0140000004	
ltrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.0860000029	
ltrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.0359999985	
ltrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	363.006989	
ltrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	428.059998	
ltrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	25.0079994	
ltrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	19.2439995	
trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	14.4589996	
ltrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-5.13000011	
trCtrl_Vecu_Volt_M_f32[0]	20.2549992	
ltrCtrl_Vecu_Volt_M_f32[1]	22.6149998	
ltrCurrDaxPrevIntg_Volt_M_f32	-17.5849991	
ltrCurrDaxRef_Amp_M_f32[0]	212.455994	
trCurrDaxRef_Amp_M_f32[1]	89.8619995	
trCurrQaxCog_Amp_M_f32	-172.485001	
ltrCurrQaxPrevIntg_Volt_M_f32	16.4962006	
trCurrQaxRef_Amp_M_f32[0]	-115.696999	
ItrCurrQaxRef_Amp_M_f32[1]	-141.417007	
trCurrQaxRpl_Amp_M_f32	0	
trPosComputationDelay_Rad_M_f32[0]	-1.3999998	
trPosComputationDelay_Rad_M_f32[1]	0.984399974	
CurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.0419999994	
CurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.00200000009	
CurrCntrl InverterFailSclFac Uls M f32	0.86199989	
PlCurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.897000015	
CurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.652999997	

2016-09-15, 18:23:31+0530



Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	29506.5		
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.499300003		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32	-784.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	29506.5		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.499300003		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7811.3999		
k_DualEcuSignalSclFacSlew_UlspS_f32	14.8000002		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5154.22021		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.0099999978		
k_MtrCtrlVirualResQax_Ohm_f32	0.0780000016		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	7.24790001		
k_MtrVoltDaxIntegLoLim_Volt_f32	-1.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	11.4308004		
k_MtrVoltQaxIntegLoLim_Volt_f32	-1.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-20.1009998		
k_VoltSatQaxPolyCoeff_Uls_f32	15.8879995		
k_deadtimeVScale_Uls_f32	0.978999972		
t_CommOffsetTblX_Uls_u3p13[0]	6717		
t_CommOffsetTblX_Uls_u3p13[1]	7750		
t_CommOffsetTblY_Cnt_u16[0]	59		
t_CommOffsetTblY_Cnt_u16[1]	1827		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	80.8180008		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	970		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-184.522003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	970	970	-
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	31.0679932	31.0679932 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	10.6815271	10.6815271 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-2.84744525	-2.84744525 ± 4.88E-04	-
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	29369	29369 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	-1.5	-1.5	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.000149999978	0.000149999978 ± 0.0625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	-
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~





Test Step 2.7 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrChtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-146.173996 -213.335007
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1] MtrCtrl MtrDampTermDax Ohm M f32[0]	0.0270000007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.014999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0680000037
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.063000001
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.40199995
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.232999995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	115.644997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	546.737976
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0160000008
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0869999975
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.032999998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.89699996
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.37399995
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-766.185974 -58.2080002
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-20.0429993
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	7.43900013
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.3130002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	3.05299997
MtrCtrl_Vecu_Volt_M_f32[0]	13.085
MtrCtrl_Vecu_Volt_M_f32[1]	15.4449997
MtrCurrDaxPrevIntg_Volt_M_f32	-17.3029995
MtrCurrDaxRef_Amp_M_f32[0]	-108.124001
MtrCurrDaxRef_Amp_M_f32[1]	178.639008
MtrCurrQaxCog_Amp_M_f32	39.7939987
MtrCurrQaxPrevIntg_Volt_M_f32	19.8957996
MtrCurrQaxRef_Amp_M_f32[0]	140.470001
MtrCurrQaxRef_Amp_M_f32[1]	93.5790024
MtrCurrQaxRpI_Amp_M_f32 MtrPosComputationDelay Rad M f32[0]	0 2.79139996
MtrPosComputationDelay_Rad_M_f32[1]	0.0716999993
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.961000025
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.00300000003
PICurrCntrl InverterFailSclFac Uls M f32	0.958999991
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.954400003
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.123000003
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-627.179993
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	947.73999
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	39240.1992
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.217500001
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	-627.179993
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	947.73999
PICurrCotrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	39240.1992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32 k_CLOAFdbackSignalSclFacSlew_UIspS_f32	0.217500001 2988.07007
k DualEcuSignalSclFacSlew UlspS f32	16
k ILOAFdbackSignalSclFacSlew UlspS f32	1052.21997
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.165000007
k_MtrCtrlVirualResQax_Ohm_f32	0.192000002
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	10.6739998
k_MtrVoltDaxIntegLoLim_Volt_f32	-2.5
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	22.7896004
k_MtrVoltQaxIntegLoLim_Volt_f32 k MtrVoltVecuFiltEnable Cnt lgc	-2.5 1
K_INIL VOILVECUI IIILIIADIE_CIIL_IYC	1

PICurrCntrl_Per1



Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	-0.714999974		
k_VoltSatQaxPolyCoeff_Uls_f32	10.5810003		
k_deadtimeVScale_Uls_f32	0.996999979		
t_CommOffsetTblX_Uls_u3p13[0]	3841		
t_CommOffsetTblX_Uls_u3p13[1]	4727		
t_CommOffsetTblY_Cnt_u16[0]	222		
t_CommOffsetTblY_Cnt_u16[1]	974		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-44.2579994		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1850		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-197.354996		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1850	1850	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	53.7850037	53.7850037 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	30.809248	30.809248 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-2.45619059	-2.45619082 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	17962	17962 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.00499999989	0.00499999989 ± 0.0625	✓

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	-
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	_

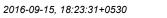
Test Step 2.8 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-91.4420013
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	133.692993
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0759999976
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0240000002
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0120000001
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0710000023
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.349000007
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.930000007
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	923.77301
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	220.951996
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0430000015
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0329999998

2016-09-15, 18:23:31+0530





T TOGITOTIUI_T CIT			
Name	Input Value		
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.00800000038		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.018999994		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.12600005		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.60000002		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-994.463989		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-659.200989		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	22.5750008		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	22.8969994		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-8.79500008		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	27.5049992 25.4869995		
MtrCtrl_Vecu_Volt_M_f32[0] MtrCtrl_Vecu_Volt_M_f32[1]	27.8470001		
MtrCurrDaxPrevIntg Volt M f32	-6.4460001		
MtrCurrDaxRef_Amp_M_f32[0]	-220		
MtrCurrDaxRef_Amp_M_f32[1]	-220		
MtrCurrQaxCog_Amp_M_f32	161.921005		
MtrCurrQaxPrevIntg_Volt_M_f32	18.0524998		
MtrCurrQaxRef_Amp_M_f32[0]	-82.2979965		
MtrCurrQaxRef_Amp_M_f32[1]	46.8180008		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	3.01180005		
MtrPosComputationDelay_Rad_M_f32[1]	-2.14400005		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.681999981		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0040000019		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.151999995		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.910700023		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.742999971		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	269.399994		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	17955.1992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.958999991		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	269.399994		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	17955.1992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.958999991 5278.47998		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32 k_DualEcuSignalSclFacSlew_UlspS_f32	17.2000008		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	6189.22021		
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt Igc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k MtrCtrlVirualResDax Ohm f32	0.00700000022		
k_MtrCtrlVirualResQax_Ohm_f32	0.140000001		
k MtrCurrQaxRefModifDsb Cnt Igc	1		
k MtrCurrQaxRefModifRplEn Cnt lgc	0		
k MtrVoltDaxIntegHiLim Volt f32	30.5515995		
k_MtrVoltDaxIntegLoLim_Volt_f32	-3.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	27.4305		
k_MtrVoltQaxIntegLoLim_Volt_f32	-3.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-17.1700001		
k_VoltSatQaxPolyCoeff_Uls_f32	11.927		
k_deadtimeVScale_Uls_f32	0.998000026		
t_CommOffsetTbIX_UIs_u3p13[0]	1065		
t_CommOffsetTblX_Uls_u3p13[1]	1483		
t_CommOffsetTblY_Cnt_u16[0]	45		
t_CommOffsetTblY_Cnt_u16[1]	1687		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrOffComOffcet_Cnt_u16_ptr	-40.9220009 4262		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr			
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val	152.016006		
		Europead Malica	la.
Name MtrCottl Write CommOffeet Cot (116(vol.))	Actual Value	Expected Value	Resu
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4262	4262	
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1 -115.103004 ± 7.81E-03	
MtrCntrl Write MtrCurrOavEinelDef Ame (200:e1)	11E 102004	1-113.1U3UU4 ± 7.81E-U3	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-115.103004		
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	19.7938633	19.7938614 ± 4.88E-04	•





Name	Actual Value	Expected Value	Result
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.00184999988	0.00184999988 ± 0.0625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	-
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
/trCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc(ptr)	target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr
/trCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt Igc ptr
htrCntrl Read ModidxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt Igc Val
trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
ItrCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val
ItrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr
/trCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
ItrCntrl_Read_SysState_Cnt_Enum(Val)	target MtrCntrl Read SysState Cnt Enum Val
htrCtrl MtrCurrDaxMaxVal Amp M f32[0]	171.485992
htrCtrl MtrCurrDaxMaxVal Amp M f32[1]	163.787003
ItrCtrl MtrDampTermDax Ohm M f32[0]	0.114
ttrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl MtrDampTermQax Ohm M f32[0]	0.0179999992
htrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0460000001
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.167999998
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.62100005
MtrCtrl MtrDaxPropotionalGain Ohm M f32[0]	720.525024
ItrCtrl MtrDaxPropotionalGain_Onlin_in_i32[0]	-487.845001
trCtrl_MtrImpedDax Ohm M f32[0]	0.096000008
ltrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.103
	0.075000003
AtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.073000003
NtrCtrl_MtrImpedQax_Ohm_M_f32[1] NtrCtrl MtrQaxIntegralGain Ohm M f32[0]	1.60500002
	1.33500004
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-418.748993 -500.754000
htrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	590.754028
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-11.3319998
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-5.40700006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-22.3460007
AtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-17.8169994
/trCtrl_Vecu_Volt_M_f32[0]	16.8080006
/trCtrl_Vecu_Volt_M_f32[1]	19.1679993
/trCurrDaxPrevIntg_Volt_M_f32	14.7060003
htrCurrDaxRef_Amp_M_f32[0]	220
/trCurrDaxRef_Amp_M_f32[1]	220
MtrCurrQaxCog_Amp_M_f32	177.763
MtrCurrQaxPrevIntg_Volt_M_f32	12.4979
MtrCurrQaxRef_Amp_M_f32[0]	160.044006
MtrCurrQaxRef_Amp_M_f32[1]	165.242004
/trCurrQaxRpI_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	0.170000002
MtrPosComputationDelay_Rad_M_f32[1]	-2.78010011
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.426999986





Name	Input Value		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0049999989		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.469999999		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.194700003		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.860000014		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-340.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	31081.1992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.797699988		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-340.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	31081.1992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.797699988		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	5764.10986		
k_DualEcuSignalSclFacSlew_UlspS_f32	18.3999996		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3350.96997		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.175999999		
k_MtrCtrlVirualResQax_Ohm_f32	0.061999999		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	12.2978001		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	12.2735996		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	21.7950001		
k_VoltSatQaxPolyCoeff_Uls_f32	21.1380005		
k_deadtimeVScale_UIs_f32	0.958999991		
t_CommOffsetTblX_Uls_u3p13[0]	4432		
t_CommOffsetTblX_Uls_u3p13[1]	5751		
t_CommOffsetTblY_Cnt_u16[0]	132		
t CommOffsetTblY Cnt u16[1]	216		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target MtrCntrl Read MtrCurrDax Amp f32 Val	75.0830002		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3800		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	54.1119995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	3800	3800	
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	
MtrCntrl Write MtrCurrQaxFinalRef Amp f32(val)	-17.7189941	-17.7189941 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	4.31741476	4.31741476 ± 4.88E-04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	2.0861342	2.0861342 ± 4.88E-04	
MtrCntrl Write PhaseAdvanceFinal Rev u0p16(val)	13462	13462 ± 1.52588E-05	
MtrCurrDaxPrevIntg Volt M f32	0	0	
MILOUITEUM TOVING_VOIL_IVI_IOZ	U	U	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	-
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•

lame	Input Value	
astDataAccessBufIndex_Cnt_M_u16	0	
//dtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	
ltrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
ItrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
ItrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
ltrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
ItrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
ltrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	106.072998	
ItrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-112.455002	
ltrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.119000003	
htrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0659999996	
ItrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0109999999	
htrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.079999982	
htrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.20299995	
ItrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.354000002	
htrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	868.213013	
htrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	949.690002	
ItrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112000003	
ItrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0930000022	
htrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0579999983	
htrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.104000002	
trCtrl MtrQaxIntegralGain Ohm M f32[0]	0.319999993	
trCtrl MtrQaxIntegralGain Ohm M f32[1]	0.254999995	
/trCtrl MtrQaxPropotionalGain Ohm M f32[0]	-236.619003	
/trCtrl MtrQaxPropotionalGain Ohm M f32[1]	-663,224976	
trCtrl MtrVoltDaxFF Volt M f32[0]	-15.8149996	
ItrCtrl MtrVoltDaxFF Volt M f32[1]	-9.85200024	
htrCtrl MtrVoltQaxFF Volt M f32[0]	-23.448	
trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-24.9260006	
ItrCtrl Vecu Volt M f32[0]	5.56799984	
htrCtrl_Vecu_Volt_M_f32[1]	7.92799997	
ItrCurrDaxPrevIntg Volt M f32	5.13399982	
htrCurrDaxRef_Amp_M_f32[0]	0	
ItrCurrDaxRef Amp M f32[1]	0	
/trCurrQaxCog_Amp_M_f32	160.160004	
ItrCurrQaxPrevIntg Volt M f32	12.7323999	
ItrCurrQaxRef_Amp_M_f32[0]	-65.1900024	
trCurrQaxRef_Amp_M_f32[1]	-216.972	
ltrCurrQaxRpl_Amp_M_f32	0	
ttrPosComputationDelay_Rad_M_f32[0]	2.44899988	
htrPosComputationDelay_Rad_M_f32[1]	1.2507	
ICurrCntrl CurrSensFailSclFac Uls M f32	0.109999999	
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0060000005	
PICurrCntrl InverterFailSclFac Uls M f32	0.214000002	

2016-09-15, 18:23:31+0530



FIGUITORIUI_FEIT		10	12010
Name	Input Value		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.85650003		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.504000008		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	570.700012		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-784.130005		
PICurrCntrl MtrVecuFilt M str.TermN Uls f32	38607.8008		
PICurrCntrl MtrVecuFilt M str.TermD Uls f32	0.253199995		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput UIs f32	570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-784.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	38607.8008		
PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.253199995		
k CLOAFdbackSignalSclFacSlew UlspS f32	5287.27002		
k DualEcuSignalSclFacSlew UlspS f32	19.6000004		
k ILOAFdbackSignalSclFacSlew UlspS f32	3540.21997		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k MtrCtrlFeedbackControlDisable Cnt lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.032999998		
k_MtrCtrlVirualResQax_Ohm_f32	0.0209999997		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	21.5352001		
k_MtrVoltDaxIntegLoLim_Volt_f32	-6.5999999		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	28.4337997		
k_MtrVoltQaxIntegLoLim_Volt_f32	-6.5999999		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	0.853999972		
k_VoltSatQaxPolyCoeff_Uls_f32	-2.3499999		
k_deadtimeVScale_Uls_f32	0.975000024		
t_CommOffsetTbIX_UIs_u3p13[0]	4529		
t_CommOffsetTblX_Uls_u3p13[1]	6659		
t_CommOffsetTblY_Cnt_u16[0]	120		
t_CommOffsetTblY_Cnt_u16[1]	597		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	74.0660019		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2932		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-17.6900005		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	597	597	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63897	63897 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-4.90475225	-4.90475225 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	2.32707381	2.32707429 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	13781	13781 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.00354999979	0.00354999979 ± 0.0625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	-
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	-
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	-





Test Step 2.11 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrChtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val 24.6130009
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0] MtrCtrl MtrCurrDaxMaxVal Amp M f32[1]	-20.9400005
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.076999996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.103
MtrCtrl MtrDampTermQax Ohm M f32[0]	0.0529999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.023
MtrCtrl MtrDaxIntegralGain Ohm M f32[0]	1.88600004
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.31599998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	417.908997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-364.772003
MtrCtrl MtrImpedDax Ohm M f32[0]	0.125
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0099999978
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0729999989
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.0450000018
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.368000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.432000011
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-959.400024
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-873.330017
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-28.7189999
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-12.0450001
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-29.9890003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	29.243
MtrCtrl_Vecu_Volt_M_f32[0]	17.9899998
MtrCtrl_Vecu_Volt_M_f32[1]	20.3500004
MtrCurrDaxPrevIntg_Volt_M_f32	9.50599957
MtrCurrDaxRef_Amp_M_f32[0]	-115.696999
MtrCurrDaxRef_Amp_M_f32[1]	-141.417007
MtrCurrQaxCog_Amp_M_f32	175.421997
MtrCurrQaxPrevIntg_Volt_M_f32	27.8554001
MtrCurrQaxRef_Amp_M_f32[0]	-146.723007
MtrCurrQaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	-2.56599998
MtrPosComputationDelay_Rad_M_f32[1]	0.2095
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.591000021
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32 PICurrCntrl InverterFailSclFac Uls M f32	0.00700000022 0.361999989
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.692700028
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.643000007
PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32	0
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	386.220001
PICurrCntrl MtrVecuFilt M str.TermN Uls f32	21678.8008
PICurrCntrl MtrVecuFilt M str.TermD Uls f32	0.0943000019
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	0
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	386.220001
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	21678.8008
PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.0943000019
k CLOAFdbackSignalSclFacSlew UlspS f32	5215.41016
k_DualEcuSignalSclFacSlew_UlspS_f32	20.7999992
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	1901.98999
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.138999999
k_MtrCtrlVirualResQax_Ohm_f32	0.192000002
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	10.9145002
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.5999999
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	28.5716991
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.5999999
k MtrVoltVecuFiltEnable Cnt lgc	0

MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)

 $MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)$

MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)

MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)

PICurrCntrl_DualEcuFailSclFac_Uls_M_f32

MtrCntrl_Write_CommOffset_Cnt_u16

MtrCurrDaxPrevIntg_Volt_M_f32

PICurrCntrl_Per1

2016-09-15, 18:23:31+0530



-220 ± 7.81E-03

0

-7.64182472 ± 4.88E-04

18.5529175 ± 4.88E-04

0.00960000046 ± 0.0625

63646 ± 1.52588E-05

Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	-10.7819996		
k_VoltSatQaxPolyCoeff_Uls_f32	0.375		
k_deadtimeVScale_Uls_f32	0.986000001		
t_CommOffsetTblX_Uls_u3p13[0]	1565		
t_CommOffsetTblX_Uls_u3p13[1]	4914		
t_CommOffsetTblY_Cnt_u16[0]	118		
t_CommOffsetTblY_Cnt_u16[1]	769		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-149.003006		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4611		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-214.828995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	769	769	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	64618	64618 ± 1	✓

-220

63646

-7.64182472

18.5529175

0.00960000046

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~

MtrCntrl_Write_CommOffset_Cnt_u16

Test Step 2.12 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-166.035004
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	183.065002
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0480000004
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.122000001
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.123999998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.509000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.29100001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-447.415009
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-391.990997
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.114
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0860000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.104000002
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.00600000005

PICurrCntrl Per1

2016-09-15, 18:23:31+0530



Input Value MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 0.213 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] 0.442000002 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] 451.035004 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] 847.624023 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -19.4680004 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] -24.3309994 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] 30.6930008 $MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]$ 0.219999999 MtrCtrl_Vecu_Volt_M_f32[0] 20.6809998 MtrCtrl_Vecu_Volt_M_f32[1] 23 0410004 MtrCurrDaxPrevIntg_Volt_M_f32 24.7740002 MtrCurrDaxRef_Amp_M_f32[0] 140 470001 MtrCurrDaxRef_Amp_M_f32[1] 93.5790024 MtrCurrQaxCog_Amp_M_f32 -8 45100021 MtrCurrQaxPrevIntg_Volt_M_f32 6.23339987 MtrCurrQaxRef_Amp_M_f32[0] -208.287994 MtrCurrQaxRef_Amp_M_f32[1] -27.9839993 MtrCurrQaxRpl_Amp_M_f32 $MtrPosComputationDelay_Rad_M_f32[0]$ 0.271100014 MtrPosComputationDelay_Rad_M_f32[1] 0.3134 0.127000004 $PICurrCntrl_CurrSensFailSclFac_Uls_M_f32$ PICurrCntrl_DualEcuFailSclFac_Uls_M_f32 0.0080000038 $PICurrCntrl_InverterFailSclFac_Uls_M_f32$ 0.860000014 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.588100016 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.179000005 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 -627.179993 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -627.179993 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 18254 6992 PICurrCntrl MtrVecuFilt M str.TermD Uls f32 0.766499996 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32 -627.179993 PICurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32 -627.179993 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32 18254 6992 PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32 0.766499996 k CLOAFdbackSignalSclFacSlew UlspS f32 7975.79004 k_DualEcuSignalSclFacSlew_UlspS_f32 22 k ILOAFdbackSignalSclFacSlew_UlspS_f32 3201.42993 $k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc$ k MtrCtrlFeedbackControlDisable_Cnt_lgc k_MtrCtrlVirualResDax_Ohm_f32 0.0209999997 k MtrCtrlVirualResQax Ohm f32 0.101999998 k_MtrCurrQaxRefModifDsb_Cnt_lgc k MtrCurrQaxRefModifRplEn Cnt lgc 0 $k_MtrVoltDaxIntegHiLim_Volt_f32$ 7.97240019 k_MtrVoltDaxIntegLoLim_Volt_f32 -5.5 $k_MtrVoltQaxFiltFFEnable_Cnt_lgc$ k_MtrVoltQaxIntegHiLim_Volt_f32 1.26639998 k MtrVoltQaxIntegLoLim Volt f32 -5.5 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 3 04200006 k_VoltSatQaxPolyCoeff_Uls_f32 14 8559999 k_deadtimeVScale_Uls_f32 0.984000027 t_CommOffsetTblX_Uls_u3p13[0] 1262 t_CommOffsetTblX_Uls_u3p13[1] 5333 t_CommOffsetTblY_Cnt_u16[0] 311 t_CommOffsetTblY_Cnt_u16[1] 1141 $target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr$ target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr 0 $target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val$ target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr -124.758003 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val $target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr$ 3668 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -96.3310013 $target_MtrCntrl_Read_SysState_Cnt_Enum_Val$ 2 Name **Actual Value Expected Value** Result MtrCntrl Write CommOffset Cnt u16(val) 3668 3668 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 0 -199.83699 -199.83699 ± 7.81E-03 MtrCntrl Write MtrCurrQaxFinalRef Amp f32(val) MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -4.30940723 -4.30940723 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 2.37390137 2.37390161 ± 4.88E-04 57231 ± 1.52588E-05 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 57231 MtrCurrDaxPrevIntg_Volt_M_f32 7.97240019 7.97240019

0.00525000039

PICurrCntrl_DualEcuFailSclFac_Uls_M_f32

0.00525000039 ± 0.0625



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•

Test Step 2.13 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
/trCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
ftrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
htrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	140.289001
ltrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	178.235992
ltrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0560000017
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.120999999
/trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0769999996
/ltrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.029999993
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0
htrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	248.748993
htrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	78.5080032
htrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0960000008
ltrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0109999999
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.019999996
htrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0879999995
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.155000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.0590000004
htrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	853.911011
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-267.251007
htrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-29.7110004
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	3.61899996
ItrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-24.6650009
1trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	18.8299999
MtrCtrl_Vecu_Volt_M_f32[0]	14.2779999
/trCtrl_Vecu_Volt_M_f32[1]	16.6380005
MtrCurrDaxPrevIntg Volt M f32	-14.5480003
htrCurrDaxRef_Amp_M_f32[0]	-213.026993
MtrCurrDaxRef_Amp_M_f32[1]	-66.7229996
/trCurrQaxCog Amp M f32	-35.144001
ItrCurrQaxPrevIntg Volt M f32	1.25670004
/trCurrQaxRef Amp M f32[0]	31.5869999
ItrCurrQaxRef Amp M f32[1]	-186.395996
ItrCurrQaxRpl Amp M f32	0
ItrPosComputationDelay Rad M f32[0]	-0.886900008
htrPosComputationDelay Rad M f32[1]	2.77320004
PlCurrCntrl CurrSensFailSclFac Uls M f32	0.23999995
PlCurrCntrl DualEcuFailSclFac Uls M f32	0.0089999961
PICurrCntrl InverterFailSclFac Uls M f32	0.75999999

PICurrCntrl_Per1

2016-09-15, 18:23:31+0530



Name	Input Value		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.671500027		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.572000027		
PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-10.21		
PICurrCntrl MtrVecuFilt M str.TermN UIs f32	23863		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.1391		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-10.21		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	23863		
PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.1391		
k CLOAFdbackSignalSclFacSlew UlspS f32	6335.39014		
k DualEcuSignalSclFacSlew UlspS f32	23.2000008		
k ILOAFdbackSignalSclFacSlew UlspS f32	7999.74023		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k MtrCtrlFeedbackControlDisable Cnt lgc	0		
k MtrCtrlVirualResDax Ohm f32	0.172000006		
k MtrCtrlVirualResQax Ohm f32	0.128999993		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k MtrVoltDaxIntegHiLim Volt f32	18.8404007		
k_MtrVoltDaxIntegLoLim_Volt_f32	-6.5		
k MtrVoltQaxFiltFFEnable Cnt lgc	0		
k MtrVoltQaxIntegHiLim Volt f32	8.35560036		
k MtrVoltQaxIntegLoLim Volt f32	-6.5		
k MtrVoltVecuFiltEnable Cnt lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-1.08800006		
k_VoltSatQaxPolyCoeff_Uls_f32	-2.53399992		
k_deadtimeVScale_Uls_f32	0.995000005		
t_CommOffsetTblX_Uls_u3p13[0]	1229		
t_CommOffsetTblX_Uls_u3p13[1]	1416		
t_CommOffsetTblY_Cnt_u16[0]	1102		
t_CommOffsetTblY_Cnt_u16[1]	1272		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	50.0610008		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4293		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-168.113007		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1272	1272	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	65208	65208 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	66.7310028	66.7310028 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-1.13344944	-1.13344932 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	14.1613216	14.1613216 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	55452	55452 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0119000003	0.0119000003 ± 0.0625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	-
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	-
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	-



Test Step 2.14 (Repeat Count = 1)	🗸
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val) MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	target_MtrCntrl_Read_SysState_Cnt_Enum_Val 91.8850021
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	182.261002
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.064000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0869999975
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.101999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0430000015
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	2
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	2
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-813.039001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	76.7679977
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.00700000022
MtrCtrl_MtrImpedDax_Ohm_M_f32[1] MtrCtrl_MtrImpedOax_Ohm_M_f32[0]	0.0839999989 0.0189999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[0] MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.075000003
MtrCtrl MtrQaxIntegralGain Ohm M f32[0]	1.91199994
MtrCtrl MtrQaxIntegralGain Ohm M f32[1]	1.33000004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	572.697998
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-525.994019
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	3.45499992
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-19.1830006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	25.0079994
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	19.2439995
MtrCtrl_Vecu_Volt_M_f32[0]	21.2989998
MtrCtrl_Vecu_Volt_M_f32[1]	23.6590004
MtrCurrDaxPrevIntg_Volt_M_f32	6.20800018
MtrCurrDaxRef_Amp_M_f32[0]	-212.632996
MtrCurrOaxRef_Amp_M_f32[1] MtrCurrOaxCoa_Amp_M_f32	-205.085007 79.6880035
MtrCurrQaxCog_Amp_M_f32 MtrCurrQaxPrevIntg Volt M f32	28.2577
MtrCurrQaxRef Amp M f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	0.291799992
MtrPosComputationDelay_Rad_M_f32[1]	-2.62470007
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.0549999997
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0099999978
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.020999997
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.627399981
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32	0.523000002 1118
PICurrCntrl_MtrVecuFilt_M_str.Previnput_Uis_f32 PICurrCntrl MtrVecuFilt M str.PrevOutput UIs f32	570.700012
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	1838.12
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.523899972
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	570.700012
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	1838.12
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.523899972
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	5138.27002
k_DualEcuSignalSclFacSlew_UlspS_f32	24.399996
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	1882.53003
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc k_MtrCtrlVirualResDax_Ohm_f32	0 0.0289999992
k_MtrCtrlVirualResQax_Onm_f32	0.181999996
k MtrCurrQaxRefModifDsb Cnt Igc	1
k MtrCurrQaxRefModifRplEn Cnt lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	11.2284002
k_MtrVoltDaxIntegLoLim_Volt_f32	-7.5
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	7.49779987
k_MtrVoltQaxIntegLoLim_Volt_f32	-7.5
k MtrVoltVecuFiltEnable Cnt Igc	0

 $MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)$

PICurrCntrl_DualEcuFailSclFac_Uls_M_f32

MtrCurrDaxPrevIntg_Volt_M_f32

PICurrCntrl_Per1

2016-09-15, 18:23:31+0530



29984 ± 1.52588E-05

0.00694999937 ± 0.0625

0

Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	-15.625		
k_VoltSatQaxPolyCoeff_Uls_f32	3.14400005		
k_deadtimeVScale_Uls_f32	0.977999985		
t_CommOffsetTblX_Uls_u3p13[0]	4858		
t_CommOffsetTblX_Uls_u3p13[1]	7209		
t_CommOffsetTblY_Cnt_u16[0]	1186		
t_CommOffsetTblY_Cnt_u16[1]	1407		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3506		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-5.66300011		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1407	1407	•
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	64094	64094 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-3.98600006	-3.98600006 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-16.3353996	-16.3353996 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	16.3873444	16.3873444 ± 4.88E-04	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	-
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	-
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

29984

0.00694999937

0

Test Step 2.15 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-166.035004
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	183.065002
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0480000004
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.122000001
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.123999998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.509000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.29100001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-447.415009
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-391.990997
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.114
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0860000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.104000002
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.00600000005

PICurrCntrl Per1

2016-09-15, 18:23:31+0530



Input Value MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 0.213 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] 0.442000002 MtrCtrl MtrQaxPropotionalGain Ohm M f32[0] 451.035004 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] 847.624023 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -19.4680004 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] -24.3309994 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] 30.6930008 $MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]$ 0.219999999 MtrCtrl_Vecu_Volt_M_f32[0] 24.8479996 MtrCtrl_Vecu_Volt_M_f32[1] 27 2080002 MtrCurrDaxPrevIntg_Volt_M_f32 24.7740002 MtrCurrDaxRef_Amp_M_f32[0] 140 470001 MtrCurrDaxRef_Amp_M_f32[1] 93.5790024 MtrCurrQaxCog_Amp_M_f32 -8 45100021 MtrCurrQaxPrevIntg_Volt_M_f32 5.45940018 MtrCurrQaxRef_Amp_M_f32[0] -208.287994 MtrCurrQaxRef_Amp_M_f32[1] -27.9839993 MtrCurrQaxRpl_Amp_M_f32 $MtrPosComputationDelay_Rad_M_f32[0]$ 1.84300005 MtrPosComputationDelay_Rad_M_f32[1] -1.47350001 $PICurrCntrl_CurrSensFailSclFac_Uls_M_f32$ 0.127000004 PICurrCntrl_DualEcuFailSclFac_Uls_M_f32 0.0109999999 $PICurrCntrl_InverterFailSclFac_Uls_M_f32$ 0.860000014 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.594299972 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.179000005 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 -717.299988 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -38.7999992 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 30983.1992 PICurrCntrl MtrVecuFilt M str.TermD Uls f32 0.636799991 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32 -717.299988 PICurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32 -38.7999992 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32 30983 1992 PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32 0.636799991 k CLOAFdbackSignalSclFacSlew UlspS f32 7975 79004 25.6000004 k_DualEcuSignalSclFacSlew_UlspS_f32 k ILOAFdbackSignalSclFacSlew UlspS f32 3201.42993 $k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc$ k MtrCtrlFeedbackControlDisable_Cnt_lgc k_MtrCtrlVirualResDax_Ohm_f32 0.0209999997 k MtrCtrlVirualResQax Ohm f32 0.101999998 k_MtrCurrQaxRefModifDsb_Cnt_lgc k MtrCurrQaxRefModifRplEn Cnt lgc 0 $k_MtrVoltDaxIntegHiLim_Volt_f32$ 7.56930017 k_MtrVoltDaxIntegLoLim_Volt_f32 -2.5999999 $k_MtrVoltQaxFiltFFEnable_Cnt_lgc$ k_MtrVoltQaxIntegHiLim_Volt_f32 18.6809006 -2 5999999 k MtrVoltQaxIntegLoLim Volt f32 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 3 04200006 k_VoltSatQaxPolyCoeff_Uls_f32 14 8559999 k_deadtimeVScale_Uls_f32 0.984000027 t_CommOffsetTblX_Uls_u3p13[0] 1262 t_CommOffsetTblX_Uls_u3p13[1] 5333 t_CommOffsetTblY_Cnt_u16[0] 311 t_CommOffsetTblY_Cnt_u16[1] 1141 $target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr$ 0 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr 0 $target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val$ target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr -124.758003 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val $target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr$ 3668 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -96.3310013 $target_MtrCntrl_Read_SysState_Cnt_Enum_Val$ 2 Name **Actual Value Expected Value** Result MtrCntrl Write CommOffset Cnt u16(val) 3668 3668 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 0 -199.83699 -199.83699 ± 7.81E-03 MtrCntrl Write MtrCurrQaxFinalRef Amp f32(val) MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -4 38888121 -4.38888121 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 2.22353935 2.22353911 ± 4.88E-04 7730 ± 1.52588E-05 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 7730 MtrCurrDaxPrevIntg_Volt_M_f32 7.56930017 7.56930017

0.0142000001

PICurrCntrl_DualEcuFailSclFac_Uls_M_f32

0.0142000001 ± 0.0625



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
//dtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	
ltrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
ItrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
ItrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
ltrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
ItrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
ltrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-216.921997	
ItrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-184.923996	
ltrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0370000005	
ItrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0379999988	
ItrCtrl MtrDampTermQax Ohm M f32[0]	0.0549999997	
ItrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0850000009	
ItrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.5	
ItrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.824000001	
htrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-1024	
trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-1024	
ItrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.070000003	
ltrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0130000003	
/trCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029	
htrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0160000008	
/trCtrl MtrQaxIntegralGain Ohm M f32[0]	0.286000013	
/trCtrl MtrQaxIntegralGain Ohm M f32[1]	1.41499996	
/trCtrl MtrQaxPropotionalGain Ohm M f32[0]	-730.362	
/trCtrl MtrQaxPropotionalGain Ohm M f32[1]	-412.898987	
/trCtrl MtrVoltDaxFF Volt M f32[0]	14.4589996	
trCtrl MtrVoltDaxFF Volt M f32[1]	-5.13000011	
trCtrl MtrVoltQaxFF Volt M f32[0]	22.5750008	
ItrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	22.8969994	
trCtrl Vecu Volt M f32[0]	18.7189999	
trCtrl_Vecu_Volt_M_f32[1]	21.0790005	
htrCurrDaxPrevintg Volt M f32	15.9169998	
htrCurrDaxRef_Amp_M_f32[0]	-69.0940018	
ItrCurrDaxRef Amp M f32[1]	161.973007	
htrCurrQaxCog_Amp_M_f32	-152.050995	
ItrCurrQaxPrevIntg Volt M f32	20.0867996	
ItrCurrQaxRef_Amp_M_f32[0]	-200.556	
ItrCurrQaxRef_Amp_M_f32[1]	-98.4449997	
ltrCurrQaxRpl_Amp_M_f32	0	
ItrPosComputationDelay_Rad_M_f32[0]	0.309399992	
htrPosComputationDelay_Rad_M_f32[1]	-2.01609993	
CurrCntrl CurrSensFailSclFac Uls M f32	0.418000013	
PlCurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.01200000013	
PICUITCHTI_DualEcuraliscirac_ois_M_132 PICUITCHTI InverterFailSclFac Uls M f32	0.30000001	

PICurrCntrl Per1

2016-09-15, 18:23:31+0530



Input Value PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32 0.902100027 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.675000012 PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32 1118 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -194.190002 PICurrCntrl MtrVecuFilt M str.TermN Uls f32 8419.69043 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.634800017 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 1118 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32$ -194.190002 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 8419.69043 0.634800017 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32$ k_CLOAFdbackSignalSclFacSlew_UlspS_f32 6857.12012 k_DualEcuSignalSclFacSlew_UlspS_f32 26 7999992 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 2799.87988 $k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc$ 1 k_MtrCtrlFeedbackControlDisable_Cnt_lgc k_MtrCtrlVirualResDax_Ohm_f32 0.0289999992 k_MtrCtrlVirualResQax_Ohm_f32 0.188999996 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ 0 18.2152004 k_MtrVoltDaxIntegHiLim_Volt_f32 $k_MtrVoltDaxIntegLoLim_Volt_f32$ -3.5 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 18 2434006 $k_MtrVoltQaxIntegHiLim_Volt_f32$ k_MtrVoltQaxIntegLoLim_Volt_f32 3.5 k MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 12.026 k VoltSatQaxPolyCoeff Uls f32 -23.2660007 0.999000013 k_deadtimeVScale_Uls_f32 t CommOffsetTblX Uls u3p13[0] 4342 $t_CommOffsetTblX_Uls_u3p13[1]$ 7724 t CommOffsetTblY Cnt u16[0] 1124 t_CommOffsetTblY_Cnt_u16[1] 1178 target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr 1 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ 0 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 136.341003 $target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr$ 3317 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 3.89299989 target_MtrCntrl_Read_SysState_Cnt_Enum_Val **Actual Value Expected Value** Name Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 3317 3317 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 0 0 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -48.5050049 -48.5050049 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 4.99151659 4.99151707 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -0 186503217 -0.186503321 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 20001 20001 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

0.00865000021

0.00865000021 ± 0.0625

PICurrCntrl_DualEcuFailSclFac_Uls_M_f32



Test Step 2.17 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrOffCorrOffcot Ont (46(atr))	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val) MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	target_MtrCntrl_Read_SysState_Cnt_Enum_Val -82.2979965
	46.8180008
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1] MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.114
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00600000005
MtrCtrl MtrDampTermQax Ohm M f32[0]	0.0710000023
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.057
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.15900004
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.762000024
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	1024
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	1024
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0270000007
MtrCtrl MtrImpedQax Ohm M f32[0]	0.120999999
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0909999982
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.87699997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.648999989
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-603.161987
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-712.994019
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-7.3130002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	3.05299997
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-11.3319998
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-5.40700006
MtrCtrl_Vecu_Volt_M_f32[0]	16.4099998
MtrCtrl_Vecu_Volt_M_f32[1]	18.7700005
MtrCurrDaxPrevIntg_Volt_M_f32	-24.1620007
MtrCurrDaxRef_Amp_M_f32[0]	-132.813004
MtrCurrDaxRef_Amp_M_f32[1]	-9.14299965
MtrCurrQaxCog_Amp_M_f32	-51.1100006
MtrCurrQaxPrevIntg_Volt_M_f32	13.3757
MtrCurrQaxRef_Amp_M_f32[0]	67.4899979
MtrCurrQaxRef_Amp_M_f32[1]	119.721001
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	-2.29579997
MtrPosComputationDelay_Rad_M_f32[1]	0.0511999987
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.423999995
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0130000003
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.395000011
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.712199986
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.651000023
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-38.7999992
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	12079.9004
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.298200011
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-38.7999992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	12079.9004
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.298200011
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3678.44995
k_DualEcuSignalSclFacSlew_UlspS_f32	28
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	7603.6001
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.0439999998
k_MtrCtrlVirualResQax_Ohm_f32	0.166999996
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	30.1203003
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.5
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	8.95559978
k MtrVoltQaxIntegLoLim Volt f32	-4.5

PICurrCntrl_Per1



Name	Input Value		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	24.5209999		
k_VoltSatQaxPolyCoeff_Uls_f32	-20.1860008		
k_deadtimeVScale_Uls_f32	0.99000001		
t_CommOffsetTblX_Uls_u3p13[0]	1516		
t_CommOffsetTblX_Uls_u3p13[1]	5882		
t_CommOffsetTblY_Cnt_u16[0]	1813		
t_CommOffsetTblY_Cnt_u16[1]	183		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3803		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	45.3779984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	3803	3803	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	118.599998	118.599998 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-2.68405437	-2.68405461 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.15912819	-4.15912867 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	14800	14800 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	✓
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0164999999	0.0164999999 ± 0.0625	~

Actual Function	Count	Expected Function	Count	Result
MtrCntrl Read MtrCurrQax Amp f32	1	MtrCntrl Read MtrCurrQax Amp f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
ntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	_

Test Step 2.18 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	160.044006
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	165.242004
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0759999976
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0719999969
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.116999999
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0860000029
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.25100005
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.801999986
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	0
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	0
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0529999994

2016-09-15, 18:23:31+0530



PICurrCntrl Per1 Input Value MtrCtrl_MtrImpedDax_Ohm_M_f32[1] 0.0939999968 0.0549999997 MtrCtrl_MtrImpedQax_Ohm_M_f32[0] MtrCtrl MtrlmpedQax Ohm M f32[1] 0.0489999987 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 0.414000005 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] 1.66700006 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] 158.016998 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] -944.586975 $MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]$ -8.79500008 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] 27.5049992 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -15 8149996 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] -9.85200024 MtrCtrl_Vecu_Volt_M_f32[0] 14 2779999 MtrCtrl_Vecu_Volt_M_f32[1] 16.6380005 MtrCurrDaxPrevIntg_Volt_M_f32 30 7700005 MtrCurrDaxRef_Amp_M_f32[0] -146.173996 MtrCurrDaxRef_Amp_M_f32[1] -213.335007 MtrCurrQaxCog_Amp_M_f32 76.5339966 MtrCurrQaxPrevIntg_Volt_M_f32 10.9584999 MtrCurrQaxRef_Amp_M_f32[0] 37.4550018 MtrCurrQaxRef_Amp_M_f32[1] -2.84500003 MtrCurrQaxRpl_Amp_M_f32 -2.84590006 MtrPosComputationDelay_Rad_M_f32[0] MtrPosComputationDelay_Rad_M_f32[1] 1 55879998 PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.347000003 PICurrCntrl DualEcuFailSclFac Uls M f32 0.0140000004 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.470999986 PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32 0.676199973 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.307999998 PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32 -657.099976 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 20.7000008 PICurrCntrl MtrVecuFilt M str.TermN Uls f32 47476.6016 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.333499998 PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput UIs f32 -657.099976 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 20.7000008 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 47476.6016 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32$ 0.333499998 $k_CLOAFdbackSignalSclFacSlew_UlspS_f32$ 2757.25 29.2000008 k DualEcuSignalSclFacSlew UlspS f32 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 7944.70996 k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc k_MtrCtrlFeedbackControlDisable_Cnt_lgc k_MtrCtrlVirualResDax_Ohm_f32 0.104999997 k_MtrCtrlVirualResQax_Ohm_f32 0.0839999989 k_MtrCurrQaxRefModifDsb_Cnt_lgc $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ k_MtrVoltDaxIntegHiLim_Volt_f32 6.46549988 k MtrVoltDaxIntegLoLim Volt f32 -6 5999999 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k MtrVoltQaxIntegHiLim_Volt_f32 1 87349999 k_MtrVoltQaxIntegLoLim_Volt_f32 -6.5999999 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 20.5340004 k_VoltSatQaxPolyCoeff_Uls_f32 -22.2229996 k_deadtimeVScale_Uls_f32 0.952000022 t_CommOffsetTblX_Uls_u3p13[0] 1188 t_CommOffsetTblX_Uls_u3p13[1] 7029 t_CommOffsetTblY_Cnt_u16[0] 422 t_CommOffsetTblY_Cnt_u16[1] 1383 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr $target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr$ 1 $target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val$ $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 1.62199998 4003 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 103 652 target_MtrCntrl_Read_SysState_Cnt_Enum_Val **Actual Value Expected Value** Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 4003 4003 MtrCntrl Write Modldx Uls u16p16(val) 0 0 + 1-79.3789978 ± 7.81E-03 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -79.3789978 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 27.5049992 27.5049992 ± 4.88E-04 -9.85200024 -9.85200024 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)

36230

36230 ± 1.52588E-05





Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl DualEcuFailSclFac Uls M f32	0.0103500001	0.0103500001 ± 0.0625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Name	Input Value
FastDataAccessBufIndex Cnt M u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr
MtrCntrl Read IvtrLoaMtqtnEn Cnt Igc(ptr)	target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr
MtrCntrl Read ModIdxSrlComSvcDft Cnt Igc(Val)	target_MtrCntrl_Read_ModidxSrlComSvcDft Cnt lgc Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val
MtrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr
MtrCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl MtrCurrDaxMaxVal Amp M f32[0]	-65.1900024
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-216.972
MtrCtrl MtrDampTermDax Ohm M f32[0]	0.0130000003
MtrCtrl MtrDampTermDax Ohm M f32[1]	0.0759999976
MtrCtrl MtrDampTermDax_Offin_M_i32[0]	0.0610000007
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.098999995
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.46000008
MtrCtrl MtrDaxIntegralGain Ohm M f32[1]	1.10699999
WtrOtrl_MtrDaxPropotionalGain_Ohm_M_i32[0]	-980.567993
MtrCtrl MtrDaxPropotionalGain Ohm M f32[1]	-630.098022
MtrCtrl MtrImpedDax Ohm M f32[0]	0.0170000009
WtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0410000011
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0350000001
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.119999997
MtrCtrl MtrQaxIntegralGain Ohm M f32[0]	1.87699997
MtrCtrl MtrQaxIntegralGain Ohm M f32[1]	0.648999989
MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	764.937988
VtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-605.708008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-22.3460007
VtrCtrl MtrVoltDaxFF Volt M f32[1]	-17.8169994
MtrCtrl MtrVoltQaxFF Volt M f32[0]	-28.7189999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-12.0450001
MtrCtrl_Vecu_Volt_M_f32[0]	22.3540001
MtrCtrl_Vecu_Volt_M_f32[1]	24.7140007
MtrCurrDaxPrevIntg_Volt_M_f32	11.6429996
MtrCurrDaxRef Amp M f32[0]	-91.4420013
MtrCurrDaxRef Amp M f32[1]	133.692993
MtrCurrQaxCog_Amp_M_f32	-161.751999
MtrCurrQaxPrevIntg Volt M f32	4.84670019
MtrCurrQaxRef Amp M f32[0]	94.3150024
MtrCurrQaxRef_Amp_M_f32[1]	37.4959984
MtrCurrQaxRpl Amp M f32	0
MtrPosComputationDelay Rad M f32[0]	-1.80789995
MtrPosComputationDelay Rad M f32[1]	1.37609994

2016-09-15, 18:23:31+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.768000007		
PICurrCntrl DualEcuFailSclFac Uls M f32	0.0149999997		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.610000014		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.385100007		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.82099998		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	570.700012		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-340.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	3431.37012		
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.870999992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-340.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	3431.37012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.870999992		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3636.04004		
k_DualEcuSignalSclFacSlew_UlspS_f32	30.3999996		
k ILOAFdbackSignalSclFacSlew UlspS f32	902.40802		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k MtrCtrlVirualResDax Ohm f32	0.023		
k MtrCtrlVirualResQax Ohm f32	0.158000007		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k MtrCurrQaxRefModifRplEn Cnt lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	13.1237001		
k MtrVoltDaxIntegLoLim Volt f32	-7.19999981		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	22.1184998		
k MtrVoltQaxIntegLoLim Volt f32	-4.5999999		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	24.6590004		
k_VoltSatQaxPolyCoeff_Uls_f32	0.441000015		
k deadtimeVScale Uls f32	0.996999979		
t_CommOffsetTblX_Uls_u3p13[0]	786		
t_CommOffsetTblX_Uls_u3p13[1]	5267		
t_CommOffsetTblY_Cnt_u16[0]	267		
t_CommOffsetTblY_Cnt_u16[1]	723		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1231		
target MtrCntrl Read MtrCurrQax Amp f32 Val	99.348999		
target MtrCntrl Read SysState Cnt Enum Val	2		
Name	Actual Value	Expected Value	Resul
MtrCntrl Write CommOffset Cnt u16(val)	1231	1231	
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	0	0 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-4.15818071	-4.15818119 ± 4.88E-04	
MtrCntrl Write MtrQaxVoltage Volt f32(val)	2.74950099	2.74950123 ± 4.88E-04	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	36389	36389 ± 1.52588E-05	
MtrCurrDaxPrevIntg Volt M f32	13.1237001	13.1237001	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•

Test Step 2.20 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
/trCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
/trCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
ftrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
ftrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
ftrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
htrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-208.287994
htrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-27.9839993
ltrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0869999975
/trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00700000022
/trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.123000003
/trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.112999998
htrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.075000003
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.62199998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	376.326996
htrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	721.965027
htrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.107000001
ltrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0109999999
htrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0520000011
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.00800000038
htrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.558000028
htrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.483999997
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	614.892029
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	1012.16998
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	15.5469999
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-22.8390007
1trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-29.7110004
1trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	3.61899996
MtrCtrl_Vecu_Volt_M_f32[0]	14.2779999
/trCtrl_Vecu_Volt_M_f32[1]	16.6380005
MtrCurrDaxPrevIntg Volt M f32	27.9990005
/trCurrDaxRef_Amp_M_f32[0]	106.072998
/trCurrDaxRef_Amp_M_f32[1]	-112.455002
/trCurrQaxCog Amp M f32	131.306
ItrCurrQaxPrevIntg Volt M f32	22.6445999
/trCurrQaxRef Amp M f32[0]	-108.124001
ItrCurrQaxRef Amp M f32[1]	178.639008
ItrCurrQaxRpl Amp M f32	0
ItrPosComputationDelay Rad M f32[0]	2.47379994
/trPosComputationDelay Rad M f32[1]	2.7420001
PlCurrCntrl CurrSensFailSclFac Uls M f32	0.247999996
PlCurrCntrl DualEcuFailSclFac Uls M f32	0.0160000008
PICurrCntrl InverterFailSclFac Uls M f32	0.602999985

PICurrCntrl Per1

2016-09-15, 18:23:31+0530



Input Value PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32 0.106299996 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.317000002 PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 22.2399998 PICurrCntrl MtrVecuFilt M str.TermN Uls f32 13842.5 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.916499972 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 0 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32$ 22.2399998 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 13842.5 0.916499972 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32$ 4450.8501 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 k_DualEcuSignalSclFacSlew_UlspS_f32 31 6000004 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 2508.87012 $k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc$ 1 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0.189999998 k_MtrCtrlVirualResDax_Ohm_f32 k_MtrCtrlVirualResQax_Ohm_f32 0.118000001 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ k_MtrVoltDaxIntegHiLim_Volt_f32 26.2252998 k_MtrVoltDaxIntegLoLim_Volt_f32 -8.19999981 k_MtrVoltQaxFiltFFEnable_Cnt_lgc $k_MtrVoltQaxIntegHiLim_Volt_f32$ 18 4771004 k_MtrVoltQaxIntegLoLim_Volt_f32 -5.5 k MtrVoltVecuFiltEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 10.3699999 k VoltSatQaxPolyCoeff Uls f32 11.9610004 k_deadtimeVScale_Uls_f32 0.985000014 t CommOffsetTblX Uls u3p13[0] 1729 $t_CommOffsetTblX_Uls_u3p13[1]$ 3269 t CommOffsetTblY Cnt u16[0] 502 t_CommOffsetTblY_Cnt_u16[1] 707 target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr 1 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 0 $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -41.5750008 $target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr$ 727 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 41.1769981 $target_MtrCntrl_Read_SysState_Cnt_Enum_Val$ 0 **Actual Value Expected Value** Name Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 707 707 64552 ± 1 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 64552 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -220 -220 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 6.52048111 6.52048111 ± 4.88E-04 -12.4609261 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -12 4609261 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 53542 53542 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	-
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

0.012050001

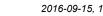
0.012050001 ± 0.0625





Test Step 2.21 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrChtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-65.1900024 -216.972
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1] MtrCtrl MtrDampTermDax Ohm M f32[0]	0.0130000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0759999976
MtrCtrl MtrDampTermQax Ohm M f32[0]	0.0610000007
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0989999995
MtrCtrl MtrDaxIntegralGain Ohm M f32[0]	0.46000008
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.10699999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-229.300995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-277.625
MtrCtrl MtrImpedDax Ohm M f32[0]	0.0170000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0410000011
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0350000001
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.119999997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	764.937988
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-605.708008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-22.3460007
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-17.8169994
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.7189999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-12.0450001
MtrCtrl_Vecu_Volt_M_f32[0]	20.2549992
MtrCtrl_Vecu_Volt_M_f32[1]	22.6149998
MtrCurrDaxPrevIntg_Volt_M_f32	11.6429996
MtrCurrDaxRef_Amp_M_f32[0]	-91.4420013
MtrCurrDaxRef_Amp_M_f32[1]	133.692993
MtrCurrQaxCog_Amp_M_f32	-161.751999
MtrCurrQaxPrevIntg_Volt_M_f32	6.56619978
MtrCurrQaxRef_Amp_M_f32[0]	94.3150024
MtrCurrQaxRef_Amp_M_f32[1]	37.4959984
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	-1.15460002
MtrPosComputationDelay_Rad_M_f32[1]	-2.14849997
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.768000007
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32 PICurrCntrl InverterFailSclFac Uls M f32	0.0170000009 0.610000014
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.495900005
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.82099998
PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32	0
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	-43.1699982
PICurrCntrl MtrVecuFilt M str.TermN Uls f32	46503,6992
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.730000019
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput UIs f32	0
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	46503.6992
PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.730000019
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3636.04004
k_DualEcuSignalSclFacSlew_UlspS_f32	32.7999992
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	902.40802
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.023
k_MtrCtrlVirualResQax_Ohm_f32	0.158000007
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	29.5695
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.19999981
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	14.8902998
k_MtrVoltQaxIntegLoLim_Volt_f32	-6.5
k_MtrVoltVecuFiltEnable_Cnt_lgc	1

PICurrCntrl_Per1





Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	24.6590004		
k_VoltSatQaxPolyCoeff_Uls_f32	0.441000015		
k_deadtimeVScale_Uls_f32	0.996999979		
t_CommOffsetTblX_Uls_u3p13[0]	786		
t_CommOffsetTblX_Uls_u3p13[1]	5267		
t_CommOffsetTblY_Cnt_u16[0]	267		
t_CommOffsetTblY_Cnt_u16[1]	723		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1231		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	99.348999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1231	1231	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-1.81267118	-1.81267118 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.64375353	4.64375353 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	49611	49611 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	29.5695	29.5695	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0211000014	0.0211000014 ± 0.0625	✓

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	-
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	_

Test Step 2.22 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-65.1900024
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-216.972
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0130000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0759999976
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0610000007
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0989999995
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.460000008
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.10699999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-980.567993
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-630.098022
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0170000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0410000011

2016-09-15, 18:23:31+0530





Name	Input Value		
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0350000001		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.119999997		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	2		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	2		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	764.937988		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-605.708008		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-22.3460007		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-17.8169994		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.7189999		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-12.0450001		
MtrCtrl_Vecu_Volt_M_f32[0]	13.085		
MtrCtrl_Vecu_Volt_M_f32[1]	15.4449997		
MtrCurrDaxPrevIntg_Volt_M_f32	11.6429996		
MtrCurrDaxRef_Amp_M_f32[0]	-91.4420013		
MtrCurrOavCor Amp M f32[1]	133.692993 -161.751999		
MtrCurrQaxCog_Amp_M_f32 MtrCurrQaxPrevIntg_Volt_M_f32	8.98110008		
MtrCurrQaxRef_Amp_M_f32[0]	94.3150024		
MtrCurrQaxRef Amp M f32[1]	37.4959984		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	-1.81869996		
MtrPosComputationDelay_Rad_M_f32[1]	1.82729995		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.768000007		
PICurrCntrl DualEcuFailSclFac Uls M f32	0.0179999992		
PICurrCntrl_InverterFailSclFac_UIs_M_f32	0.610000014		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.72359997		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.82099998		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	42029.6016		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.316399992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	42029.6016		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.316399992		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3636.04004		
k_DualEcuSignalSclFacSlew_UlspS_f32	34		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	902.40802		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc k_MtrCtrlVirualResDax_Ohm_f32	0.023		
k_MtrCtrlVirualResQax_Ohm_f32	0.158000007		
k MtrCurrQaxRefModifDsb Cnt Igc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	2.74320006		
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.1999998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	12.4398003		
k_MtrVoltQaxIntegLoLim_Volt_f32	-7.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	24.6590004		
k_VoltSatQaxPolyCoeff_Uls_f32	0.441000015		
k_deadtimeVScale_Uls_f32	0.996999979		
t_CommOffsetTblX_Uls_u3p13[0]	786		
t_CommOffsetTblX_Uls_u3p13[1]	5267		
t_CommOffsetTblY_Cnt_u16[0]	267		
t_CommOffsetTblY_Cnt_u16[1]	723		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDffComOffcot_Cnt_u16_ptr	-126.640999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	99.348999		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val	99.348999		
		Everated Value	P "
Name MtrCottel Write CommOffeet Cot u46(vol)	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1231	1231	
MtrCntrl_Write_ModIdx_UIs_u16p16(val) MtrCntrl_Write_MtrCurrOavFinalPof_Amp_f32(val)	0 220	0±1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-4.21291018	220 ± 7.81E-03 -4.21291018 ± 4.88E-04	
MtrCntrl Write MtrDayVoltage Volt f32(val)		-4.4 129 IU IO I 4.00E-U4	_
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)		2 66488481 + 4 885 04	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	2.66488457 36065	2.66488481 ± 4.88E-04 36065 ± 1.52588E-05	Ž

PICurrCntrl_Per1

2016-09-15, 18:23:31+0530



 Name
 Actual Value
 Expected Value
 Result

 PICurrCntrl_DualEcuFailSclFac_Uls_M_f32
 0.013749999
 0.013749999 ± 0.0625
 ✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
VtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc(ptr)	target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr
/trCntrl Read IvtrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr
/trCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModidxSrlComSvcDft Cnt Igc Val
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val
AtrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
MtrCntrl Read SysState Cnt Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
htrCtrl MtrCurrDaxMaxVal Amp M f32[0]	-208.287994
	-208.267.994
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	0.0869999975
/trCtrl_MtrDampTermDax_Ohm_M_f32[0]	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00700000022 0.123000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.123000003
/trCtrl_MtrDampTermQax_Ohm_M_f32[1]	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.62199998
htrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	376.326996
htrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	721.965027
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.107000001
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.010999999
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0520000011
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.00800000038
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.558000028
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.483999997
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	614.892029
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	1012.16998
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	15.5469999
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-22.8390007
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-29.7110004
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	3.61899996
/trCtrl_Vecu_Volt_M_f32[0]	25.4869995
/trCtrl_Vecu_Volt_M_f32[1]	27.8470001
/trCurrDaxPrevIntg_Volt_M_f32	27.9990005
/trCurrDaxRef_Amp_M_f32[0]	106.072998
/trCurrDaxRef_Amp_M_f32[1]	-112.455002
/trCurrQaxCog_Amp_M_f32	131.306
/ltrCurrQaxPrevIntg_Volt_M_f32	30.6711006
/trCurrQaxRef_Amp_M_f32[0]	-108.124001
//trCurrQaxRef_Amp_M_f32[1]	178.639008
/ltrCurrQaxRpI_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	2.97160006
MtrPosComputationDelay_Rad_M_f32[1]	1.66129994
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.247999996

PICurrCntrl_Per1

2016-09-15, 18:23:31+0530



Name	Input Value		
PICurrCntrl DualEcuFailSclFac Uls M f32	0.0189999994		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.602999985		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.0351000018		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.317000002		
PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	267.119995		
PICurrCntrl MtrVecuFilt M str.TermN UIs f32	17234.5		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.71329999		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput UIs f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	267.119995		
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	17234.5		
PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32	0.71329999		
k CLOAFdbackSignalSclFacSlew UlspS f32	4450.8501		
k DualEcuSignalSclFacSlew UlspS f32	35.2000008		
k_Dualecusignalsciracsiew_disps_i32 k_ILOAFdbackSignalSciFacSiew_UlspS_f32	2508.87012		
k_ILOAFdbacksigiralsdiFacsiew_0isp5_i52 k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.18999998		
k MtrCtrlVirualResQax Ohm f32	0.118000001		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k MtrCurrQaxRefModifRplEn Cnt lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	29.1569004 -11.1999998		
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc	-11.1999998		
	28.1117992		
k_MtrVoltQaxIntegHiLim_Volt_f32	28.1117992 -2.5999999		
k_MtrVoltQaxIntegLoLim_Volt_f32	-2.5999999		
k_MtrVoltVecuFiltEnable_Cnt_lgc	10.3699999		
k_VoltSatDaxPolyCoeff_Uls_f32	11.9610004		
k_VoltSatQaxPolyCoeff_Uls_f32	0.985000014		
k_deadtimeVScale_Uls_f32	1729		
t_CommOffsetTbIX_UIs_u3p13[0]	3269		
t_CommOffsetTbIX_UIs_u3p13[1]	502		
t_CommOffsetTblY_Cnt_u16[0]	707		
t_CommOffsetTblY_Cnt_u16[1]	0		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr			
target_MtrCntrl_Read_MtrCurrOffComOffcot_Cot_u16_ptr	-41.5750008 727		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr			
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	41.1769981		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val		I=	
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	707	707	V
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	64552	64552 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	V
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	11.63941	11.63941 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-22.2434235	-22.2434235 ± 4.88E-04	*
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	58734	58734 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0233999994	0.0233999994 ± 0.0625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	-
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~



Test Step 2.24 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	31.5869999
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-186.395996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0610000007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0579999983
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0309999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.032999998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.722
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.82299995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	888.947998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-292.006989
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0120000001
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0560000017
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.050999999
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.061999999
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.93700004
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.0850000009
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-1024
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-1024
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	9.61999989
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	26.7999992
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	3.45499992
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-19.1830006
MtrCtrl_Vecu_Volt_M_f32[0]	16.8080006
MtrCtrl_Vecu_Volt_M_f32[1]	19.1679993
MtrCurrDaxPrevIntg_Volt_M_f32	4.04500008
MtrCurrDaxRef_Amp_M_f32[0]	24.6130009
MtrCurrDaxRef_Amp_M_f32[1]	-20.9400005
MtrCurrQaxCog_Amp_M_f32	19.6149998
MtrCurrQaxPrevIntg_Volt_M_f32	16.8353996
MtrCurrQaxRef_Amp_M_f32[0]	-76.8769989
MtrCurrQaxRef_Amp_M_f32[1]	-153.238998
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	2.20709991
MtrPosComputationDelay_Rad_M_f32[1]	-1.83399999
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.463999987
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0199999996
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.40900009
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.794099987
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.640999973
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-717.299988
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	267.119995
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	20241.6992
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.221200004
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-717.299988
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	267.119995
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	20241.6992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.221200004
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	897.588013
k_DualEcuSignalSclFacSlew_UlspS_f32	36.4000015
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	1232.52002
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.182999998
k_MtrCtrlVirualResQax_Ohm_f32	0.193000004
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	17.7555008
k_MtrVoltDaxIntegLoLim_Volt_f32	-12.1999998
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	22.5324993
k MtrVoltQaxIntegLoLim Volt f32	-3.5

PICurrCntrl_Per1



Name	Input Value		
k MtrVoltVecuFiltEnable Cnt lgc	0		
k VoltSatDaxPolyCoeff Uls f32	-20.6259995		
k VoltSatQaxPolyCoeff Uls f32	1.32299995		
k deadtimeVScale Uls f32	0.981000006		
t_CommOffsetTblX_Uls_u3p13[0]	2556		
t_CommOffsetTblX_Uls_u3p13[1]	4316		
t_CommOffsetTblY_Cnt_u16[0]	15		
t_CommOffsetTblY_Cnt_u16[1]	40		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	48.8400002		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3024		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-30.7789993		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	40	40	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	64290	64290 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-96.4919968	-96.4919968 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-6.91692209	-6.91692162 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	14.9676905	14.9676895 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	18506	18506 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0154499989	0.0154499989 ± 0.0625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	-
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	•
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.25 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-133.947006
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	75.7020035
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0320000015
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0869999975
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0579999983
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0500000007
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.34800005
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.11099994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-942.771973
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-380.85199
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0939999968
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0879999995
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0960000008

2016-09-15, 18:23:31+0530





Name	Input Value		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.103		
MtrCtrl MtrQaxIntegralGain Ohm M f32[0]	1.35000002		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.749000013		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	1024		
MtrCtrl MtrQaxPropotionalGain Ohm M f32[1]	1024		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-29.3530006		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	27.3040009		
	26.4720001		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]			
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	6.0999999		
MtrCtrl_Vecu_Volt_M_f32[0]	5.56799984		
MtrCtrl_Vecu_Volt_M_f32[1]	7.92799997		
MtrCurrDaxPrevIntg_Volt_M_f32	15.0869999		
MtrCurrDaxRef_Amp_M_f32[0]	-166.035004		
MtrCurrDaxRef_Amp_M_f32[1]	183.065002		
MtrCurrQaxCog_Amp_M_f32	114.531998		
MtrCurrQaxPrevIntg_Volt_M_f32	5.42920017		
MtrCurrQaxRef_Amp_M_f32[0]	191.369003		
MtrCurrQaxRef_Amp_M_f32[1]	107.137001		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	-2.30049992		
MtrPosComputationDelay_Rad_M_f32[1]	-3.12019992		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.600000024		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.020999997		
PICurrCntrl InverterFailSclFac Uls M f32	0.89999976		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.179199994		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.257999986		
	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32			
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	404.899994		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	46120.5		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.578299999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	404.899994		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	46120.5		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32	0.578299999		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2048.80005		
k_DualEcuSignalSclFacSlew_UlspS_f32	37.5999985		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3548.88989		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k MtrCtrlFeedbackControlDisable Cnt lgc	0		
k MtrCtrlVirualResDax Ohm f32	0.079000036		
k_MtrCtrlVirualResQax_Ohm_f32	0.177000001		
k_MtrCurrQaxRefModifDsb_Cnt_Igc	0		
k MtrCurrQaxRefModifRplEn Cnt lgc	0		
	2.39529991		
k_MtrVoltDaxIntegHiLim_Volt_f32			
k_MtrVoltDaxIntegLoLim_Volt_f32	-13.1999998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	13.2297001		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	21.0030003		
k_VoltSatQaxPolyCoeff_Uls_f32	-9.26399994		
k_deadtimeVScale_Uls_f32	0.950999975		
t_CommOffsetTbIX_Uls_u3p13[0]	1810		
t_CommOffsetTblX_Uls_u3p13[1]	2335		
t_CommOffsetTblY_Cnt_u16[0]	157		
t_CommOffsetTbIY_Cnt_u16[1]	712		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr	0		
· · · · · · ·			
target_MtrCntrl_Read_MtrCurrOffComOffeet_Cot_u16_etr	107.702003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4540		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4540	4540	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-7.39499664	-7.39499664 ± 7.81E-03	✓
MtrCntrl Write MtrDaxVoltage Volt f32(val)	4.1729908	4.1729908 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-29.184164	-29.184164 ± 4.88E-04	
MtrCntrl Write PhaseAdvanceFinal Rev u0p16(val)	64278	64278 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0.910027504	0.910027981	
San San Torning_roit_in_loz	0.010021007	0.010021001	

0.0256999992

PICurrCntrl_DualEcuFailSclFac_Uls_M_f32

0.0256999992 ± 0.0625



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	-
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	
/trCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
//trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl MtrCurrDaxMaxVal Amp M f32[0]	-105.246002	
MtrCtrl MtrCurrDaxMaxVal Amp M f32[1]	41.6290016	
MtrCtrl MtrDampTermDax Ohm M f32[0]	0.108000003	
/trCtrl MtrDampTermDax Ohm M f32[1]	0.0820000023	
/trCtrl MtrDampTermQax Ohm M f32[0]	0.0979999974	
/trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0850000009	
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.266000003	
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.08399999	
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-150.298996	
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-165.235001	
/trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0649999976	
/trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.100000001	
/trCtrl_MtrImpedQax_Ohm_M_f32[0]	0.112000003	
/trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0930000022	
/trCtrl MtrQaxIntegralGain Ohm M f32[0]	1.91199994	
/trCtrl MtrQaxIntegralGain Ohm M f32[1]	0.56400001	
/trCtrl MtrQaxPropotionalGain Ohm M f32[0]	0	
/trCtrl MtrQaxPropotionalGain Ohm M f32[1]	0	
MtrCtrl MtrVoltDaxFF Volt M f32[0]	-6.06799984	
MtrCtrl MtrVoltDaxFF Volt M f32[1]	-7.83199978	
MtrCtrl MtrVoltQaxFF Volt M f32[0]	14.4589996	
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-5.13000011	
AtrCtrl Vecu Volt M f32[0]	17.9899998	
/trCtrl_Vecu_Volt_M_f32[1]	20.3500004	
MtrCurrDaxPrevIntg Volt M f32	-9.29100037	
-	140.289001	
/trCurrDaxRef_Amp_M_f32[0] /trCurrDaxRef Amp M f32[1]	178.235992	
	34.7879982	
htrCurrQaxCog_Amp_M_f32	26.5946007	
AtrCurrQaxPrevIntg_Volt_M_f32	-147.343002	
htrCurrQaxRef_Amp_M_f32[0]		
ItrCurrQaxRef_Amp_M_f32[1]	127.972 0	
ItrCurrQaxRpl_Amp_M_f32	-3.06900001	
htrPosComputationDelay_Rad_M_f32[0]	-3.06900001	
MtrPosComputationDelay_Rad_M_f32[1]		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.509000003	
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32 PICurrCntrl InverterFailSclFac Uls M f32	0.0219999999 0.446999997	

PICurrCntrl_Per1

2016-09-15, 18:23:31+0530



Name	Input Value		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.8125		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.331999987		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-657.130005		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	865.320007		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	40399.6016		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0255999994		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-657.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	865.320007		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	40399.6016		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0255999994		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3449.11011		
k_DualEcuSignalSclFacSlew_UlspS_f32	38.7999992		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	7870.1001		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.164000005		
k_MtrCtrlVirualResQax_Ohm_f32	0.0610000007		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	17.9570999		
k_MtrVoltDaxIntegLoLim_Volt_f32	-14.1999998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	16.1431007		
k_MtrVoltQaxIntegLoLim_Volt_f32	-6.5999999		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-13.4589996		
k_VoltSatQaxPolyCoeff_Uls_f32	12.7139997		
k_deadtimeVScale_Uls_f32	0.981999993		
t_CommOffsetTblX_Uls_u3p13[0]	1360		
t_CommOffsetTblX_Uls_u3p13[1]	5743		
t_CommOffsetTblY_Cnt_u16[0]	1436		
t_CommOffsetTblY_Cnt_u16[1]	1891		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	5.72399998		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	808		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	177.046997		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1891	1891	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	64356	64356 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-182.130997	-182.130997 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-15.1034212	-15.1034212 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	9.1640892	9.1640892 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	22830	22830 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0171499997	0.0171499997 ± 0.0625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	✓





Test Step 2.27 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl MtrCurrDaxMaxVal Amp M f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0480000004
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0930000022
VtrCtrl MtrDampTermQax Ohm M f32[0]	0.020999997
VtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0659999996
VtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.153
VtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.69299996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-43.257
VtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-508.920013
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0549999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0979999974
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0099999998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.363999993
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.65900004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-185.072998
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-920.171997
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-22.0189991
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	13.2709999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.3130002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	3.05299997
MtrCtrl_Vecu_Volt_M_f32[0]	20.6809998
MtrCtrl_Vecu_Volt_M_f32[1]	23.0410004
MtrCurrDaxPrevIntg_Volt_M_f32	21.4680004
MtrCurrDaxRef_Amp_M_f32[0]	91.8850021
	182.261002
MtrCurrDaxRef_Amp_M_f32[1]	
MtrCurrQaxCog_Amp_M_f32	91.9309998
MtrCurrQaxPrevIntg_Volt_M_f32	18.1345997
MtrCurrQaxRef_Amp_M_f32[0]	6.18900013
MtrCurrQaxRef_Amp_M_f32[1]	83.0540009
MtrCurrQaxRpI_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	-1.16139996
MtrPosComputationDelay_Rad_M_f32[1]	-0.311699986
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.256999999
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.023
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.933000028
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0346999988
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.460000008
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-194.190002
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-657.099976
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	25640.4004
PlCurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.40000006
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput Uls f32	-194.190002
PlCurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-657.099976
PICurrCntrl MtrVoltQaxFFFilt_M_str.PrevOutput_Ois_i32	25640.4004
PICUTCHUI_MILVOILQAXFFFIII_M_SILTETHIN_UIS_132 PICUTCHTI MtrVoltQaxFFFIII M str.TermD UIs f32	0.40000006
C_CLOAFdbackSignalSclFacSlew_UlspS_f32	876.684998
C_DualEcuSignalSclFacSlew_UlspS_f32	40
x_ILOAFdbackSignalSclFacSlew_UlspS_f32	2879.57007
x_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
<pre>c_MtrCtrlFeedbackControlDisable_Cnt_lgc</pre>	1
c_MtrCtrlVirualResDax_Ohm_f32	0.00200000009
c_MtrCtrlVirualResQax_Ohm_f32	0.0710000023
<_MtrCurrQaxRefModifDsb_Cnt_lgc	1
<_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
<_MtrVoltDaxIntegHiLim_Volt_f32	3.07459998
k_MtrVoltDaxIntegLoLim_Volt_f32	-15.1999998
k MtrVoltQaxFiltFFEnable Cnt lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	5.46850014
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.5999999

PICurrCntrl_Per1



Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	10.7320004		
k_VoltSatQaxPolyCoeff_Uls_f32	3.59299994		
k_deadtimeVScale_Uls_f32	0.959999979		
t_CommOffsetTblX_Uls_u3p13[0]	4701		
t_CommOffsetTblX_Uls_u3p13[1]	5063		
t_CommOffsetTblY_Cnt_u16[0]	155		
t_CommOffsetTblY_Cnt_u16[1]	873		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.3040009		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	650		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-9.31999969		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	650	650	-
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-85.7419968	-85.7419968 ± 7.81E-03	-
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0.138330251	0.138330266 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.79800606	4.79800606 ± 4.88E-04	-
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	53723	53723 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	-
PICurrCntrl DualEcuFailSclFac Uls M f32	0.0280000009	0.0280000009 ± 0.0625	✓

Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	-
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	-
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	-
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	-
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	-

Test Step 2.28 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0680000037
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.063000001
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0719999969
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0289999992
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.75100005
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.246999994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	292.062988
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	884.252991
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.104000002
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.063000001





Name	Input Value		
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.114		
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.0860000029		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.74000001		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.890999973 260.899994		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	994.27301		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-8.32400036		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-26.5540009		
MtrCtrl MtrVoltQaxFF Volt M f32[0]	-8.79500008		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	27.5049992		
MtrCtrl_Vecu_Volt_M_f32[0]	14.2779999		
MtrCtrl_Vecu_Volt_M_f32[1]	16.6380005		
MtrCurrDaxPrevIntg_Volt_M_f32	24.8929996		
MtrCurrDaxRef_Amp_M_f32[0]	-218.035004		
MtrCurrDaxRef_Amp_M_f32[1]	11.6370001		
MtrCurrQaxCog_Amp_M_f32	21.4759998		
MtrCurrQaxPrevIntg_Volt_M_f32	28.968399		
MtrCurrQaxRef_Amp_M_f32[0]	-105.246002		
MtrCurrQaxRef_Amp_M_f32[1]	41.6290016		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	-1.21449995		
MtrPosComputationDelay_Rad_M_f32[1]	2.99020004 0.950999975		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 PICurrCntrl DualEcuFailSclFac Uls M f32	0.950999975		
PICurrCntrl InverterFailScIFac UIs M f32	0.024000002		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.899699986		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.944000006		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	386.220001		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	570.700012		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	35039		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.588		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	386.220001		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	35039		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.588		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	5931.81982		
k_DualEcuSignalSclFacSlew_UlspS_f32	41.2000008		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3685.94995		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.112000003		
k_MtrCtrlVirualResQax_Ohm_f32 k_MtrCurrQaxRefModifDsb_Cnt_lgc	0.0540000014		
k MtrCurrQaxRefModifRplEn Cnt lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	1.05350006		
k_MtrVoltDaxIntegLoLim_Volt_f32	-16.5		
k MtrVoltQaxFiltFFEnable Cnt lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	14.8121996		
k_MtrVoltQaxIntegLoLim_Volt_f32	-5.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	2.45499992		
k_VoltSatQaxPolyCoeff_Uls_f32	16.9769993		
k_deadtimeVScale_Uls_f32	0.961000025		
t_CommOffsetTblX_Uls_u3p13[0]	2294		
t_CommOffsetTblX_Uls_u3p13[1]	6390		
t_CommOffsetTblY_Cnt_u16[0]	11		
t_CommOffsetTblY_Cnt_u16[1]	125		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	20.6140009		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	20.6149998 631		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val	-161.352005 1		
		Expected Value	Pacul
Name MtrCotrl Write CommOffset Cot u16(val)	Actual Value	Expected Value	Resu
MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_ModIdx_UIs_u16p16(val)	125 62980	125 62980 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-126.722	-126.722 ± 7.81E-03	
WILL OHER WILL WILLOUT WAXFILIAINEL ALTIP 132(Val)		-9.57793903 ± 4.88E-04	
	-9 57793808		
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-9.57793808 9.82513428		
	-9.57793808 9.82513428 44809	9.82513523 ± 4.88E-04 44809 ± 1.52588E-05	





Name	Actual Value	Expected Value	Result
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0188499987	0.0188499987 ± 0.0625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

lame	Input Value	
astDataAccessBufIndex_Cnt_M_u16	0	
/trCntrl Read DualEcuMotCtrlMtgnEna Cnt Igc(ptr)	target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr	
/trCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr	
/trCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val	
/trCntrl Read MotCurrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr	
/trCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val	
/trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
ItrCtrl MtrCurrDaxMaxVal Amp M f32[0]	-213.026993	
ltrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996	
ItrCtrl MtrDampTermDax Ohm M f32[0]	0.0480000004	
trCtrl MtrDampTermDax Ohm M f32[1]	0.0930000022	
ItrCtrl MtrDampTermQax Ohm M f32[0]	0.0209999997	
htrCtrl MtrDampTermQax Ohm M f32[1]	0.0659999996	
htrCtrl MtrDaxIntegralGain Ohm M f32[0]	1.153	
trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.69299996	
trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-43.257	
trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-508.920013	
ItrCtrl MtrImpedDax Ohm M f32[0]	0.0549999997	
ItrCtrl MtrImpedDax Ohm M f32[1]	0.0979999974	
trCtrl MtrImpedQax Ohm M f32[0]	0.125	
htrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0099999978	
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.363999993	
ItrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.65900004	
htrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	185.072998	
htrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-920.171997	
1trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-22.0189991	
ltrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	13.2709999	
ItrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.3130002	
ItrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	3.05299997	
ItrCtrl_Vecu_Volt_M_f32[0]	22.3540001	
ItrCtrl_Vecu_Volt_M_f32[1]	24.7140007	
htrCurrDaxPrevIntg_Volt_M_f32	21.4680004	
htrCurrDaxRef_Amp_M_f32[0]	91.8850021	
ftrCurrDaxRef_Amp_M_f32[1]	182.261002	
/trCurrQaxCog_Amp_M_f32	91.9309998	
htrCurrQaxPrevIntg_Volt_M_f32	9.05210018	
ltrCurrQaxRef_Amp_M_f32[0]	6.18900013	
ltrCurrQaxRef_Amp_M_f32[1]	83.0540009	
trCurrQaxRpl_Amp_M_f32	0	
trPosComputationDelay_Rad_M_f32[0]	-3.1400001	
ItrPosComputationDelay_Rad_M_f32[1]	-3.1400001	
CurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.256999999	
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0250000004	
ClCurrCntrl_InverterFailSclFac_Uls_M_f32	0.933000028	

PICurrCntrl Per1

2016-09-15, 18:23:31+0530



Input Value PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32 0.786800027 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.460000008 PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32 -784.130005 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 PICurrCntrl MtrVecuFilt M str.TermN Uls f32 36325.3984 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.365999997 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 -784.130005 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32$ 0 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 36325.3984 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32$ 0.365999997 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 876.684998 k_DualEcuSignalSclFacSlew_UlspS_f32 42 4000015 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 2879.57007 $k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc$ 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc k_MtrCtrlVirualResDax_Ohm_f32 0.00200000009 k_MtrCtrlVirualResQax_Ohm_f32 0.0710000023 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ 0 k_MtrVoltDaxIntegHiLim_Volt_f32 29.9500999 $k_MtrVoltDaxIntegLoLim_Volt_f32$ -17.5 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 27 7511997 $k_MtrVoltQaxIntegHiLim_Volt_f32$ k_MtrVoltQaxIntegLoLim_Volt_f32 -6.5 k MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 10.7320004 k VoltSatQaxPolyCoeff Uls f32 3.59299994 k_deadtimeVScale_Uls_f32 0.959999979 t CommOffsetTblX Uls u3p13[0] 4701 $t_CommOffsetTblX_Uls_u3p13[1]$ 5063 t CommOffsetTblY Cnt u16[0] 155 t_CommOffsetTblY_Cnt_u16[1] 873 target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr 0 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ 0 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 59.3040009 $target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr$ 650 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -9.31999969 target_MtrCntrl_Read_SysState_Cnt_Enum_Val **Actual Value Expected Value** Name Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 650 650 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 0 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -85.7419968 -85.7419968 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -0.185179308 -0.185179353 ± 4.88E-04 -4 7964263 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -4 7964263 + 4 88F-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 419 419 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

0.0303000007

0.0303000007 ± 0.0625



Test Step 2.30 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val) MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
MtrCntrl Read SysState Cnt Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0680000037
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.063000001
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0719999969
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0289999992
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.75100005
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.246999994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	292.062988
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	884.252991
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.104000002
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.063000001
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.114
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0860000029
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.7400001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.890999973
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-260.899994
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-994.27301
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-8.32400036
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-26.5540009
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-8.79500008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	27.5049992 14.2779999
MtrCtrl_Vecu_Volt_M_f32[0] MtrCtrl_Vecu_Volt_M_f32[1]	16.6380005
MtrCurrDaxPrevIntg_Volt_M_f32	24.8929996
MtrCurrDaxRef_Amp_M_f32[0]	-218.035004
MtrCurrDaxRef_Amp_M_f32[1]	11.6370001
MtrCurrQaxCog_Amp_M_f32	21.4759998
MtrCurrQaxPrevIntg Volt M f32	16.3929005
MtrCurrQaxRef Amp M f32[0]	-105.246002
MtrCurrQaxRef_Amp_M_f32[1]	41.6290016
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	3.1400001
MtrPosComputationDelay_Rad_M_f32[1]	3.1400001
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.950999975
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0260000005
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.626999974
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0658000037
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.944000006
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	947.73999
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	0
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	10763.7002
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.228200004
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	947.73999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	0
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	10763.7002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 k CLOAFdbackSignalSclFacSlew UlspS f32	0.228200004 5931.81982
k_DualEcuSignalSclFacSlew_UlspS_f32	43.599985
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3685,94995
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc	1
k MtrCtrlFeedbackControlDisable Cnt lgc	0
k MtrCtrlVirualResDax Ohm f32	0.112000003
k_MtrCtrlVirualResQax_Ohm_f32	0.0540000014
k MtrCurrQaxRefModifDsb Cnt Igc	1
k MtrCurrQaxRefModifRplEn Cnt lgc	0
k MtrVoltDaxIntegHiLim Volt f32	14.7652998
k_MtrVoltDaxIntegLoLim_Volt_f32	-18.5
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	14.1267004
k MtrVoltQaxIntegLoLim Volt f32	-7.5

PICurrCntrl_Per1



Name	Input Value		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	2.45499992		
k_VoltSatQaxPolyCoeff_Uls_f32	16.9769993		
k_deadtimeVScale_Uls_f32	0.961000025		
t_CommOffsetTblX_Uls_u3p13[0]	2294		
t_CommOffsetTblX_Uls_u3p13[1]	6390		
t_CommOffsetTblY_Cnt_u16[0]	11		
t_CommOffsetTblY_Cnt_u16[1]	125		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	20.6149998		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	631		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-161.352005		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	125	125	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	62980	62980 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-126.722	-126.722 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-7.34835768	-7.34835768 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-11.5875702	-11.5875702 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	5878	5878 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0205500014	0.0205500014 ± 0.0625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	-
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	•
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.31 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	205.820999
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-206.792007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0120000001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0710000023
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0270000007
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0149999997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.898999989
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.23500001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-113.670998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-827.208008
MtrCtrl_MtrlmpedDax_Ohm_M_f32[0]	0.0270000007
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0920000002
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.096000008

2016-09-15, 18:23:31+0530





Name	Input Value		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0109999999		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.53100002		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.80499995		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-277.385986		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-517.232971		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	3.87299991		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	19.2730007		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-22.3460007		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-17.8169994		
MtrCtrl_Vecu_Volt_M_f32[0]	20.2549992		
MtrCtrl_Vecu_Volt_M_f32[1]	22.6149998		
MtrCurrDaxPrevIntg_Volt_M_f32	-29.7089996		
MtrCurrDavRef_Amp_M_f32[0]	-216.921997 -184.923996		
MtrCurrOavCog Amp M #32	-124.709999		
MtrCurrQaxCog_Amp_M_f32 MtrCurrQaxPrevIntg_Volt_M_f32	3.45029998		
MtrCurrQaxRef_Amp_M_f32[0]	-213.026993		
MtrCurrQaxRef_Amp_M_f32[1]	-66.7229996		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	0		
MtrPosComputationDelay_Rad_M_f32[1]	0		
PICurrCntrl CurrSensFailSclFac Uls M f32	0.735000014		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0270000007		
PICurrCntrl InverterFailSclFac Uls M f32	0.151999995		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.199900001		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.30399999		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	269.399994		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	13385.9004		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.58950001		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	269.399994		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	13385.9004		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.58950001		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	1279.38		
k_DualEcuSignalSclFacSlew_UlspS_f32	44.7999992		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4881.5498		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.075000003		
k_MtrCtrlVirualResQax_Ohm_f32	0.020999997		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	24.2935009		
k_MtrVoltDaxIntegLoLim_Volt_f32	-6.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	15.1322002		
k_MtrVoltQaxIntegLoLim_Volt_f32	-2.5999999		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-10.7220001		
k_VoltSatQaxPolyCoeff_Uls_f32	-13.0010004		
k_deadtimeVScale_UIs_f32	0.992999971		
t_CommOffsetTblX_Uls_u3p13[0]	1827		
t_CommOffsetTblX_Uls_u3p13[1]	5226		
t_CommOffsetTbIY_Cnt_u16[0]	1326		
t_CommOffsetTblY_Cnt_u16[1]	1829		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	0 79.6729965		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	416		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-205.514999		
target_MtrCntrl_Read_MtrCurrQax_Amp_r3z_val target_MtrCntrl_Read_SysState_Cnt_Enum_Val	-205.514999		
		Formanda d Walton	D
Name Marchal Write Commoffeet Cat (40(4a))	Actual Value	Expected Value	Resu
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1829	1829	
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	65077	65077 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-88.3169937	-88.3169937 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-1.22445941	-1.22445989 ± 4.88E-04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-20.0759087	-20.0759068 ± 4.88E-04	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) MtrCurrDaxPrevIntg_Volt_M_f32	33403	33403 ± 1.52588E-05	

0.0326000005

PICurrCntrl_DualEcuFailSclFac_Uls_M_f32

0.0326000005 ± 0.0625



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	•
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Input Value 1 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
target_introntin_read_buaicedinototrintgricha_ont_ige_ptr
target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr
target_MtrCntrl_Read_ModidxSrlComSvcDft Cnt_lgc_Val
target_MtrCntrl Read_MotCurrLoaMtgtnEn Cnt lgc ptr
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
target_MtrCntrl_Read_SysState_Cnt_Enum_Val
-69.0940018
161.973007
0.0179999992
0.046000001
0.0759999976
0.0240000002
0.43599999
0.893999994
497.348999
-685.572998
0.123999998
0.0790000036
0.00700000022
0.0839999989
1.68299997
0.0769999996
-594.544983
215.455994
-30.1380005
0.920000017
-23.448
-24.9260006
13.085
15.4449997
-9.7670002
-82.2979965
46.8180008
-185.608994
20.1585007
-212.632996
-205.085007
0
-2.02589989
-0.20999993
0.98900022
0.0280000009
0.867999971
0.249599993 0.578000009

PICurrCntrl Per1

2016-09-15, 18:23:31+0530



Input Value PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32 -340.130005 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 1118 PICurrCntrl MtrVecuFilt M str.TermN Uls f32 39404.3984 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.423099995 PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput Uls f32 -340.130005 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32$ 1118 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 39404.3984 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32$ 0.423099995 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 7139.7998 k_DualEcuSignalSclFacSlew_UlspS_f32 46 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 5357.10986 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 1 k_MtrCtrlFeedbackControlDisable_Cnt_lgc k_MtrCtrlVirualResDax_Ohm_f32 0.0689999983 k_MtrCtrlVirualResQax_Ohm_f32 0.063000001 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ k_MtrVoltDaxIntegHiLim_Volt_f32 11.7138004 $k_MtrVoltDaxIntegLoLim_Volt_f32$ -2.5 k_MtrVoltQaxFiltFFEnable_Cnt_lgc $k_MtrVoltQaxIntegHiLim_Volt_f32$ 7.54269981 k_MtrVoltQaxIntegLoLim_Volt_f32 3.5 $k_MtrVoltVecuFiltEnable_Cnt_lgc$ k_VoltSatDaxPolyCoeff_Uls_f32 -7.60300016 k VoltSatQaxPolyCoeff_Uls_f32 -11.1459999 k_deadtimeVScale_Uls_f32 0.949999988 t_CommOffsetTblX_Uls_u3p13[0] 2440 $t_CommOffsetTblX_Uls_u3p13[1]$ 3744 t CommOffsetTblY Cnt u16[0] 695 t_CommOffsetTblY_Cnt_u16[1] 1480 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 1 target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 0.486999989 $target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr$ 1059 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -118.848 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 2 **Actual Value Expected Value** Name Result 1059 MtrCntrl_Write_CommOffset_Cnt_u16(val) 1059 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 0 ± 1 0 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -19.4760132 -19.4760132 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -27.5376549 ± 4.88E-04 -27.5376549 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 10.439353 10.439353 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 50741 50741 ± 1.52588E-05 11.7138004 11 7138004 $MtrCurrDaxPrevIntg_Volt_M_f32$

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

0.0222500004

0.0222500004 ± 0.0625





Test Step 2.33 (Repeat Count = 1)	· ·
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val) MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	target_MtrCntrl_Read_SysState_Cnt_Enum_Val -132.813004
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-9.14299965
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.079999982
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.114
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.66400003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.54200006
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	133.104004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	671.512024
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0329999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1] MtrCtrl_MtrImpedOax_Ohm_M_f32[0]	0.0140000004
MtrCtrl_MtrImpedQax_Ohm_M_f32[0] MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.123000003 0.118000001
MtrCtrl_MtrQaxIntegralGain Ohm M f32[0]	0.118000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.104000001
MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	831.671021
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	382.882996
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-26.5079994
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	4.36100006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	15.5469999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-22.8390007
MtrCtrl_Vecu_Volt_M_f32[0]	25.4869995
MtrCtrl_Vecu_Volt_M_f32[1]	27.8470001
MtrCurrDaxPrevIntg_Volt_M_f32	10.5640001
MtrCurrDaxRef_Amp_M_f32[0]	160.044006
MtrCurrOaxRef_Amp_M_f32[1]	165.242004
MtrCurrQaxCog_Amp_M_f32 MtrCurrQaxPrevIntg Volt M f32	163.561005 15.6246996
MtrCurrQaxRef Amp M f32[0]	205.820999
MtrCurrQaxRef Amp M f32[1]	-206.792007
MtrCurrQaxRpl Amp M f32	0
MtrPosComputationDelay Rad M f32[0]	1.40170002
MtrPosComputationDelay_Rad_M_f32[1]	3.11820006
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.418000013
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0289999992
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.851999998
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0666999966
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0399999991
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-784.130005
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_UIs_f32	-717.299988 41423.9094
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32 PICurrCntrl MtrVecuFilt M str.TermD UIs f32	41423.8984 0.516300023
PICurrCntrl_MtrVeturiit_M_str.1ermD_uis_132 PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput Uls f32	-784.130005
PICurrCntrl_MtrVoltQaxFFFiit_M_str.PrevOutput_Uls_f32	-764.130003 -717.299988
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	41423.8984
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32	0.516300023
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	1592.43005
k_DualEcuSignalSclFacSlew_UlspS_f32	47.2000008
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5074.45996
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.137999997
k_MtrCtrlVirualResQax_Ohm_f32	0.172000006
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32 k_MtrVoltDaxIntegLoLim_Volt_f32	8.20600033 -2.7999995
k MtrVoltQaxFiltFFEnable Cnt lgc	-2.79999999
	21.5585003
k intrvoit@axinteghiLim voit 132	
k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32	-4.5

PICurrCntrl_Per1

2016-09-15, 18:23:31+0530



Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	-13.408		
k_VoltSatQaxPolyCoeff_Uls_f32	22.0909996		
k_deadtimeVScale_Uls_f32	0.973999977		
t_CommOffsetTblX_Uls_u3p13[0]	1614		
t_CommOffsetTblX_Uls_u3p13[1]	6513		
t_CommOffsetTblY_Cnt_u16[0]	170		
t_CommOffsetTblY_Cnt_u16[1]	1069		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-190.440994		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	361		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-72.4260025		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1069	1069	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63832	63832 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	25.2546139	25.2546101 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-9.89244843	-9.89244652 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	52802	52802 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0348999985	0.0348999985 ± 0.0625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	-
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	-
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.34 (Repeat Count = 1)			
Name	Input Value		
FastDataAccessBufIndex_Cnt_M_u16	1		
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr		
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr		
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val		
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr		
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val		
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr		
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val		
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val		
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993		
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996		
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0480000004		
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0930000022		
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0209999997		
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0659999996		
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.0480000004		
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.148		
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-987.179016		
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-952.34198		
MtrCtrl_MtrlmpedDax_Ohm_M_f32[0]	0.0549999997		
MtrCtrl_MtrlmpedDax_Ohm_M_f32[1]	0.0979999974		
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.125		
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.0099999978		

© Report created by TESSY V3.1.13, report template V2.1





	Input Value		
/ltrQaxIntegralGain_Ohm_M_f32[0]	1.40900004		
/trQaxIntegralGain_Ohm_M_f32[1]	1.56299996		
/trQaxPropotionalGain_Ohm_M_f32[0]	-247.072998		
/trQaxPropotionalGain Ohm M f32[1]	-40.618		
/trVoltDaxFF_Volt_M_f32[0]	-31		
/trVoltDaxFF_Volt_M_f32[1]	-31		
/trVoltQaxFF_Volt_M_f32[0]	-7.3130002		
/trVoltQaxFF_Volt_M_f32[1]	3.05299997		
/ecu_Volt_M_f32[0]	16.8080006		
/ecu_Volt_M_f32[1]	19.1679993		
axPrevIntg Volt M f32	-21.3630009		
axRef Amp M f32[0]	-65.1900024		
axRef Amp M f32[1]	-216.972		
taxCog_Amp_M_f32	91.9309998		
taxPrevIntg_Volt_M_f32	20.7061996		
eaxRef Amp M f32[0]	-69.0940018		
axRef_Amp_M_f32[1]	161.973007		
taxRpl_Amp_M_f32	0		
	1.49730003		
omputationDelay_Rad_M_f32[0]			
omputationDelay_Rad_M_f32[1]	-2.9454		
htrl_CurrSensFailSclFac_Uls_M_f32	0.0839999989		
htrl_DualEcuFailSclFac_Uls_M_f32	0.029999993		
htrl_InverterFailSclFac_UIs_M_f32	0.887000024		
htrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.235300004		
ntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.460000008		
htrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	386.220001		
ntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1118		
ntrl_MtrVecuFilt_M_str.TermN_Uls_f32	4218.1001		
ntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.665600002		
ntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	386.220001		
ntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1118		
trl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	4218.1001		
ntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.665600002		
FdbackSignalSclFacSlew_UlspS_f32	5847.47021		
cuSignalSclFacSlew_UlspS_f32	48.4000015		
dbackSignalSclFacSlew_UlspS_f32	1025.58997		
CurrLoopSecOrTranFcEnable_Cnt_lgc	0		
FeedbackControlDisable_Cnt_lgc	1		
VirualResDax_Ohm_f32	0.0179999992		
VirualResQax_Ohm_f32	0.0780000016		
rQaxRefModifDsb_Cnt_lgc	0		
rQaxRefModifRplEn_Cnt_lgc	0		
tDaxIntegHiLim_Volt_f32	12.6604004		
tDaxIntegLoLim_Volt_f32	-3.5		
tQaxFiltFFEnable_Cnt_lgc	0		
tQaxIntegHiLim_Volt_f32	22.7973995		
tQaxIntegLoLim Volt f32	-6.5999999		
tVecuFiltEnable Cnt lgc	0		
tDaxPolyCoeff Uls f32	-4.08099985		
tQaxPolyCoeff Uls f32	7.89599991		
·			
meVScale_Uls_f32	0.961000025		
OffsetTbIX_Uls_u3p13[0]	1147		
OffsetTbIX_UIs_u3p13[1]	4096		
OffsetTblY_Cnt_u16[0]	189		
OffsetTblY_Cnt_u16[1]	988		
trCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
trCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
trCntrl_Read_MtrCurrDax_Amp_f32_Val	83.9489975		
trCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3069		
trCntrl_Read_MtrCurrQax_Amp_f32_Val	83.9489975		
trCntrl_Read_SysState_Cnt_Enum_Val	2		
	Actual Value	Expected Value	Result
Write_CommOffset_Cnt_u16(val)	988	988	-
Write_ModIdx_UIs_u16p16(val)	62980	62980 ± 1	✓
Write_MtrCurrQaxFinalRef_Amp_f32(val)	70.0420074	70.0420074 ± 7.81E-03	
			-
·			
Write_MtrDaxVoltage_Volt_f32(val) Write_MtrQaxVoltage_Volt_f32(val) Write_PhaseAdvanceFinal_Rev_u0p16(val) axPrevIntg_Volt_M_f32 ttrl_DualEcuFailSclFac_Uls_M_f32	18.3921909 1.01989996 50621 -3.5 0.0239499994	18.3921909 ± 4.88E-04 1.01990008 ± 4.88E-04 50621 ± 1.52588E-05 -3.5 0.0239499994 ± 0.0625	



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	-

Input Value	
1	
target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr	
· · · · · · · · · · · · · · · · · · ·	
target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
-212.632996	
-205.085007	
0.0680000037	
0.063000001	
0.0719999969	
0.0289999992	
0.588999987	
1.73000002	
-655.848999	
-834.401001	
0.104000002	
0.063000001	
0.114	
0.0860000029	
0.720000029	
1.65400004	
868.789001	
-349.798004	
31	
31	
-8.79500008	
27.5049992	
5.56799984	
7.92799997	
3.15400004	
-146.723007	
-121.943001	
21.4759998	
28.3425999	
-132.813004	
-9.14299965	
0	
-0.65140003	
-1,56369996	
0.49900013	
0.030999995	
0.825999975	
0.904100001 0.94400006	

PICurrCntrl Per1

2016-09-15, 18:23:31+0530



Input Value PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32 -627.179993 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -657.130005 PICurrCntrl MtrVecuFilt M str.TermN Uls f32 21877.4004 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.363599986 PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput Uls f32 -627.179993 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32$ -657.130005 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 21877.4004 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32$ 0.363599986 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 1867.13 k_DualEcuSignalSclFacSlew_UlspS_f32 49 5999985 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 506.598999 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc k_MtrCtrlVirualResDax_Ohm_f32 0.180999994 k_MtrCtrlVirualResQax_Ohm_f32 0.0680000037 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ k_MtrVoltDaxIntegHiLim_Volt_f32 1.13619995 $k_MtrVoltDaxIntegLoLim_Volt_f32$ -1.5 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 0 $k_MtrVoltQaxIntegHiLim_Volt_f32$ 15 474 k_MtrVoltQaxIntegLoLim_Volt_f32 -4.5999999 $k_MtrVoltVecuFiltEnable_Cnt_lgc$ Λ k_VoltSatDaxPolyCoeff_Uls_f32 7.38500023 k VoltSatQaxPolyCoeff_Uls_f32 -23.0559998 k_deadtimeVScale_Uls_f32 0.972000003 t_CommOffsetTblX_Uls_u3p13[0] 3464 t_CommOffsetTblX_Uls_u3p13[1] 6250 t CommOffsetTblY Cnt u16[0] 1218 t_CommOffsetTblY_Cnt_u16[1] 1360 target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr 0 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 1 target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val 0 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -144.667007 $target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr$ 3103 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -144.667007 target_MtrCntrl_Read_SysState_Cnt_Enum_Val **Expected Value** Name **Actual Value** Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1360 1360 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 63700 63700 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -30.6189995 -30.6189995 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -7.32916069 -7.32916069 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -2.38035393 -2.38035417 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 29567 29567 ± 1.52588E-05 $MtrCurrDaxPrevIntg_Volt_M_f32$

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

0.0372000001

0.0372000001 ± 0.0625



Test Step 2.36 (Repeat Count = 1)	· ·
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val) MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	target_MtrCntrl_Read_SysState_Cnt_Enum_Val 205.820999
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-206.792007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0120000001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0710000023
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0270000007
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0149999997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.5
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.47299999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-143.399002
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-302.690002
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0270000007
MtrCtrl_MtrlmpedOax_Ohm_M_f32[1]	0.0920000002
MtrCtrl_MtrImpedQax_Ohm_M_f32[0] MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.096000008
MtrCtrl MtrQaxIntegralGain Ohm M f32[0]	0.0109999999
MtrCtrl MtrQaxIntegralGain Ohm M f32[1]	1.847
MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	-611.046997
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	804.908997
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	0
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	0
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-22.3460007
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-17.8169994
MtrCtrl_Vecu_Volt_M_f32[0]	17.9899998
MtrCtrl_Vecu_Volt_M_f32[1]	20.3500004
MtrCurrDaxPrevIntg_Volt_M_f32	17.5130005
MtrCurrDayRef_Amp_M_f32[0]	-208.287994
MtrCurrOavCog Amp M #32	-27.9839993 -124.709999
MtrCurrQaxCog_Amp_M_f32 MtrCurrQaxPrevIntg Volt M f32	22.6252003
MtrCurrQaxRef Amp M f32[0]	-146.173996
MtrCurrQaxRef Amp M f32[1]	-213.335007
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	-2.61339998
MtrPosComputationDelay_Rad_M_f32[1]	-1.96640003
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.959999979
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0320000015
PICurrCntrl_InverterFailSclFac_UIs_M_f32	0.0370000005
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.339300007
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32	0.30399999 -10.21
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32 PICurrCntrl MtrVecuFilt M str.PrevOutput UIs f32	-10.21 -194.190002
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	33920.5
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.583899975
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-10.21
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	33920.5
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.583899975
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	211.854996
k_DualEcuSignalSclFacSlew_UlspS_f32	50.7999992
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	1900.18005
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1 0
k_MtrCtrlVirualResDax_Ohm_f32	0.156000003
k MtrCtrlVirualResQax Ohm f32	0.142000005
k MtrCurrQaxRefModifDsb Cnt Igc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	13.5790997
k_MtrVoltDaxIntegLoLim_Volt_f32	-3.599999
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	21.0468998
k_MtrVoltQaxIntegLoLim_Volt_f32	-5.5
k_MtrVoltVecuFiltEnable_Cnt_lgc	0

PICurrCntrl_Per1



Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	-10.6560001		
k_VoltSatQaxPolyCoeff_Uls_f32	6.90999985		
k_deadtimeVScale_Uls_f32	0.958999991		
t_CommOffsetTblX_Uls_u3p13[0]	6160		
t_CommOffsetTblX_Uls_u3p13[1]	6291		
t_CommOffsetTblY_Cnt_u16[0]	1130		
t_CommOffsetTblY_Cnt_u16[1]	1422		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-184.522003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	758		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	80.8180008		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1422	1422	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	62849	62849 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-21.4639969	-21.4639969 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-17.1976204	-17.1976204 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-1.37385917	-1.37386143 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	21062	21062 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0256500021	0.0256500021 ± 0.0625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	-
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.37 (Repeat Count = 1)	v v
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-69.0940018
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	161.973007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0179999992
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0460000001
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0759999976
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0240000002
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.53400004
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.579999983
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-341.976013
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-806.22998
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.123999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0790000036
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.00700000022
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.083999989

2016-09-15, 18:23:31+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.657000005		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.386000007		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	268.286011		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-542.14502		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-15.1960001		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-2.83699989		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-23.448		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-24.9260006		
MtrCtrl_Vecu_Volt_M_f32[0]	26.6809998		
MtrCtrl_Vecu_Volt_M_f32[1]	29.0410004		
MtrCurrDaxPrevIntg_Volt_M_f32	-17.0869999		
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999		
MtrCurrDaxRef Amp M f32[1]	-186.395996		
MtrCurrQaxCog_Amp_M_f32	-185.608994		
MtrCurrQaxPrevIntg_Volt_M_f32	15.4982004		
MtrCurrQaxRef_Amp_M_f32[0]	-91.4420013		
MtrCurrQaxRef_Amp_M_f32[1]	133.692993		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay Rad M f32[0]	1.40170002		
MtrPosComputationDelay_Rad_M_f32[1]	-3.11820006		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.63499999		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0329999998		
	0.0329999990		
PICurrCntrl_InverterFailSclFac_Uls_M_f32 PICurrCntrl_MtrCurrDaySatFuvPatio_Uls_M_f32			
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.874000013		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 PICurrCntrl_MtrVccuEilt_M_ctr_Provincut_Llls_f32	0.578000009		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	570.700012		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	386.220001		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	20769.3008		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.513000011		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	386.220001		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	20769.3008		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.513000011		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	670.247986		
k_DualEcuSignalSclFacSlew_UlspS_f32	52		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2501.06006		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.188999996		
k_MtrCtrlVirualResQax_Ohm_f32	0.138999999		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	8.58329964		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.80000019		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	19.8449993		
k_MtrVoltQaxIntegLoLim_Volt_f32	-6.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	10.2069998		
k_VoltSatQaxPolyCoeff_Uls_f32	0.906000018		
k_deadtimeVScale_Uls_f32	0.975000024		
t_CommOffsetTbIX_UIs_u3p13[0]	1016		
t_CommOffsetTblX_Uls_u3p13[1]	2286		
t_CommOffsetTblY_Cnt_u16[0]	110		
t_CommOffsetTblY_Cnt_u16[1]	537		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-197.354996		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1734		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-44.2579994		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1734	1734	- Court
MtrCntrl Write Modldx Uls u16p16(val)	0	0 ± 1	-
MtrCntrl Write MtrCurrQaxFinalRef Amp f32(val)	94.1669922	94.1669922 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-0.0155182956	-0.0155182956 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	30.2249985	30.2249966 ± 4.88E-04	
MtrCntrl Write PhaseAdvanceFinal Rev u0p16(val)	14615	14615 ± 1.52588E-05	•
MtrCurrDaxPrevIntg Volt M f32	0	0	
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0394999981	0.0394999981 ± 0.0625	~
1 TOUT OTHER DUBLICUT AND ON ACCOUNT OF	0.000700001	0.000+00001 I 0.0020	_



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.38 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-132.813004
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-9.14299965
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.114
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.92999995
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.70000005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	524.476013
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-993.447021
MtrCtrl MtrlmpedDax Ohm M f32[0]	0.032999998
MtrCtrl MtrlmpedDax Ohm M f32[1]	0.0140000004
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.123000003
MtrCtrl MtrlmpedQax Ohm M f32[1]	0.118000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.713999987
MtrCtrl MtrQaxIntegralGain Ohm M f32[1]	0.0410000011
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-823.801025
MtrCtrl MtrQaxPropotionalGain Ohm M f32[1]	-751.585022
MtrCtrl MtrVoltDaxFF Volt M f32[0]	4.92999983
MtrCtrl MtrVoltDaxFF Volt M f32[1]	14.6809998
MtrCtrl MtrVoltQaxFF Volt M f32[0]	15.5469999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-22.8390007
MtrCtrl Vecu Volt M f32[0]	16.882
MtrCtrl_Vecu_Volt_M_f32[1]	19.2420006
MtrCurrDaxPrevIntg Volt M f32	-23.8190002
MtrCurrDaxRef_Amp_M_f32[0]	-133.947006
MtrCurrDaxRef Amp M f32[1]	75.7020035
MtrCurrQaxCog Amp M f32	163.561005
MtrCurrQaxPrevIntg Volt M f32	8.19719982
MtrCurrQaxRef Amp M f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.787003
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	-3.01670003
MtrPosComputationDelay Rad M f32[1]	2.24819994
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.141000003
PICurrCntrl DualEcuFailSclFac Uls M f32	0.0340000018

PICurrCntrl Per1

2016-09-15, 18:23:31+0530



Input Value PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.0670000017 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.445899993 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.0399999991 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32 -38.7999992 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -784.130005 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 38607.8008 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.00150000001 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32$ -38.7999992 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 -784.130005 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 38607 8008 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.00150000001 k CLOAFdbackSignalSclFacSlew UlspS f32 3129 08008 k_DualEcuSignalSclFacSlew_UlspS_f32 53.2000008 k ILOAFdbackSignalSclFacSlew UlspS f32 51 4420013 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc k_MtrCtrlFeedbackControlDisable_Cnt_lgc k_MtrCtrlVirualResDax_Ohm_f32 0.104999997 k_MtrCtrlVirualResQax_Ohm_f32 0.0329999998 $k_MtrCurrQaxRefModifDsb_Cnt_lgc$ 0 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 1.54809999 $k_MtrVoltDaxIntegHiLim_Volt_f32$ k_MtrVoltDaxIntegLoLim_Volt_f32 -7.5 $k_MtrVoltQaxFiltFFEnable_Cnt_lgc$ Λ k_MtrVoltQaxIntegHiLim_Volt_f32 14.2641001 k_MtrVoltQaxIntegLoLim_Volt_f32 -7.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 0 k VoltSatDaxPolyCoeff Uls f32 10.2329998 k_VoltSatQaxPolyCoeff_Uls_f32 -11.7980003 k deadtimeVScale Uls f32 0.962000012 t_CommOffsetTblX_Uls_u3p13[0] 1408 t_CommOffsetTblX_Uls_u3p13[1] 3505 t_CommOffsetTbIY_Cnt_u16[0] 100 t CommOffsetTblY Cnt u16[1] 429 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr 1 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 1 target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val 0 $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 152.016006 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 4045 -40.9220009 target MtrCntrl Read MtrCurrQax Amp f32 Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val **Expected Value** Name **Actual Value** Result MtrCntrl Write CommOffset Cnt u16(val) 429 429 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 63045 63045 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 0.225997925 0.225997925 ± 7.81E-03 10.0092726 10.0092726 ± 4.88E-04 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -15.5712671 -15.5712671 ± 4.88E-04 50259 ± 1.52588E-05 50259 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) MtrCurrDaxPrevIntg_Volt_M_f32 -7.5

Test Step Call Trace ✓				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

0.027350001

0.027350001 ± 0.0625





Test Step 2.39 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-146.173996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-213.335007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.023
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.119000003 0.0659999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1] MtrCtrl MtrDaxIntegralGain Ohm M f32[0]	1.80900002
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.64300001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-495.540985
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-831.38501
MtrCtrl MtrImpedDax Ohm M f32[0]	0.0869999975
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.032999998
MtrCtrl MtrImpedQax Ohm M f32[0]	0.0700000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.71700001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.625
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	799.594971
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	445.729004
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	30.9510002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-20.6159992
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-31
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-31
MtrCtrl_Vecu_Volt_M_f32[0]	5.70200014
MtrCtrl_Vecu_Volt_M_f32[1]	8.06200027
MtrCurrDaxPrevIntg_Volt_M_f32	5.15299988
MtrCurrDaxRef_Amp_M_f32[0]	-105.246002
MtrCurrDaxRef_Amp_M_f32[1]	41.6290016
MtrCurrQaxCog_Amp_M_f32	-209.716003
MtrCurrQaxPrevIntg_Volt_M_f32	15.6709003
MtrCurrQaxRef_Amp_M_f32[0]	106.072998
MtrCurrQaxRef_Amp_M_f32[1]	-112.455002
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	-1.76530004
MtrPosComputationDelay_Rad_M_f32[1]	1.05859995
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.699000001
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0350000001
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.828000009
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.369500011
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.476000011
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-194.190002
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	947.73999
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	21678.8008
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.862100005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-194.190002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	947.73999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	21678.8008 0.862100005
k CLOAFdbackSignalSclFacSlew UlspS f32	3473.06006
k DualEcuSignalSclFacSlew UlspS f32	54.4000015
k_Dualecusignalsciracsiew_disps_is2 k_ILOAFdbackSignalSciFacSiew_UlspS_f32	466.734985
k_ILOAFdbacksignalscifacsiew_0isps_isz k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.0130000003
k_MtrCtrlVirualResQax_Ohm_f32	0.18999998
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k MtrCurrQaxRefModifRplEn Cnt Igc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	13.2995005
k MtrVoltDaxIntegLoLim Volt f32	-2.5999999
k_MtrVoltQaxFiltFFEnable Cnt lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	17.0296993
k_MtrVoltQaxIntegLoLim_Volt_f32	-2.599999
k MtrVoltVecuFiltEnable Cnt lgc	0

PICurrCntrl_Per1



Name	Input Value		
k VoltSatDaxPolyCoeff Uls f32	22.2140007		
k VoltSatQaxPolyCoeff Uls f32	-4.26499987		
k deadtimeVScale Uls f32	0.958000004		
t CommOffsetTbIX UIs u3p13[0]	1556		
t CommOffsetTbIX UIs u3p13[1]	5071		
t CommOffsetTblY Cnt u16[0]	718		
t CommOffsetTblY Cnt u16[1]	721		
target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr	0		
target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr	0		
target MtrCntrl Read ModIdxSrlComSvcDft Cnt Igc Val	0		
target MtrCntrl Read MotCurrLoaMtqtnEn Cnt lqc ptr	1		
target MtrCntrl Read MtrCurrDax Amp f32 Val	54.1119995		
target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	1747		
target MtrCntrl Read MtrCurrQax Amp f32 Val	75.0830002		
target MtrCntrl Read SysState Cnt Enum Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl Write CommOffset Cnt u16(val)	721	721	- 100 U.I.
MtrCntrl Write Modldx Uls u16p16(val)	62783	62783 ± 1	_
MtrCntrl Write MtrCurrQaxFinalRef Amp f32(val)	220	220 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	1.40329123	1.40329123 ± 4.88E-04	_
MtrCntrl Write MtrQaxVoltage Volt f32(val)	5.27919054	5.27919054 ± 4.88E-04	
MtrCntrl Write PhaseAdvanceFinal Rev u0p16(val)	49833	49833 ± 1.52588E-05	_
MtrCurrDaxPrevIntg Volt M f32	-2.5999999	-2.5999999	
PICurrCntrl DualEcuFailSclFac Uls M f32	0.0417999998	0.0417999998 ± 0.0625	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	-
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	-
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	-
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	-
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	-
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	-
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	-
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	-
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	-
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	-
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	-

Test Step 2.40 (Repeat Count = 1)	<u> </u>
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-91.4420013
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	133.692993
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.122000001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.123999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.103
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.10899997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.992999971
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-192.371002
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	695.664001
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.00800000038
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0189999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007

PICurrCntrl_Per1



Name	Input Value		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.611000001		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.84599996		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	875.080017		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-275.667999		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	29.0550003		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-17.6779995		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	31		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	31		
MtrCtrl_Vecu_Volt_M_f32[0]	18.3600006		
MtrCtrl_Vecu_Volt_M_f32[1]	20.7199993		
MtrCurrDaxPrevIntg_Volt_M_f32	-18.4589996		
MtrCurrDaxRef_Amp_M_f32[0]	-213.026993		
MtrCurrOavCoa Amp_M_f32[1]	-66.7229996 77.189003		
MtrCurrQaxCog_Amp_M_f32	15.4617996		
MtrCurrQaxPrevIntg_Volt_M_f32 MtrCurrQaxRef Amp M f32[0]	24.6130009		
MtrCurrQaxRef_Amp_M_f32[1]	-20.9400005		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	0.96390003		
MtrPosComputationDelay_Rad_M_f32[1]	-1.9605		
PICurrCntrl CurrSensFailSclFac Uls M f32	0.495000005		
PICurrCntrl DualEcuFailSclFac Uls M f32	0.0359999985		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.50999999		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.244599998		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.736000001		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-194.190002		
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	269.399994		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	18254.6992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.245199993		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-194.190002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	269.399994		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	18254.6992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.245199993		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	1865.18005		
k_DualEcuSignalSclFacSlew_UlspS_f32	55.5999985		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	7841.00977		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.171000004		
k_MtrCtrlVirualResQax_Ohm_f32	0.0909999982		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	4.52769995		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	14.1113997		
k_MtrVoltQaxIntegLoLim_Volt_f32	-3.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-15.96		
k_VoltSatQaxPolyCoeff_Uls_f32	16.2980003		
k_deadtimeVScale_UIs_f32	0.972000003		
t_CommOffsetTbIX_UIs_u3p13[0]	401		
t_CommOffsetTblX_Uls_u3p13[1]	1457		
t_CommOffsetTblY_Cnt_u16[0]	1020		
t_CommOffsetTblY_Cnt_u16[1]	1562		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr			
target_MtrCntrl_Read_MtrCurrOffComOffcet_Cnt_u16_ptr	-17.6900005 951		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	74.0660019		
target_MtrCntrl_Read_sysState_Cnt_Enum_Val	2		
		Eupopted Value	Down
Name	Actual Value	Expected Value	Resu
MtrCntrl_Write_CommOffset_Cnt_u16(val)	951	951	
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	0	0 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-98.1290054	-98.1290054 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-14.9265556	-14.9265556 ± 4.88E-04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	26.1750889	26.1750889 ± 4.88E-04	
MtrCurrPayProvilete, Volt. M. #32	39682	39682 ± 1.52588E-05	
MtrCurrDaxPrevIntg_Volt_M_f32	4.52769995 0.0290499981	4.52769995	
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0290499901	0.0290499981 ± 0.0625	



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	-
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Name	Input Value
FastDataAccessBufIndex Cnt M u16	0
/trCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc(ptr)	target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr
/trCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt Igc ptr
/trCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt Igc Val
MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr
MtrCntrl Read MtrCurrDax Amp f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr
MtrCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
/trCtrl MtrCurrDaxMaxVal Amp M f32[0]	171.485992
/trCtrl MtrCurrDaxMaxVal Amp M f32[1]	163.787003
htrCtrl MtrDampTermDax Ohm M f32[0]	0.076999996
/trCtrl MtrDampTermDax Ohm M f32[1]	0.029999993
MtrCtrl MtrDampTermQax Ohm M f32[0]	0.0970000029
/trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0480000004
/trCtrl MtrDaxIntegralGain Ohm M f32[0]	1.11199999
/trCtrl MtrDaxIntegralGain Ohm M f32[1]	1.02100003
/trCtrl MtrDaxPropotionalGain Ohm M f32[0]	210,968002
/trCtrl MtrDaxPropotionalGain Ohm M f32[1]	-929.856018
trCtrl MtrImpedDax Ohm M f32[0]	0.075000003
/trCtrl MtrImpedDax Ohm M f32[1]	0.0649999976
/trCtrl MtrImpedQax Ohm M f32[0]	0.0529999994
/trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968
/trCtrl MtrQaxIntegralGain Ohm M f32[0]	1.40199995
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.079000036
htrCtrl MtrQaxPropotionalGain Ohm M f32[0]	203.302002
trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	608.874023
trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-22.0189991
htrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	13.2709999
ItrCtrl MtrVoltQaxFF Volt M f32[0]	0
trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	0
/trCtrl_Vecu_Volt_M_f32[0]	22.7800007
/trCtrl_Vecu_Volt_M_f32[1]	25.1399994
/trCurrDaxPrevIntg_Volt_M_f32	-4.07800007
htrCurrDaxRef_Amp_M_f32[0]	-212.632996
/trCurrDaxRef_Amp_M_f32[1]	-205.085007
/trCurrQaxCog_Amp_M_f32	-145.169006
/trCurrQaxPrevIntg_Volt_M_f32	30.3006992
/trCurrQaxRef_Amp_M_f32[0]	-166.035004
ItrCurrQaxRef_Amp_M_f32[1]	183.065002
ItrCurrQaxRpl_Amp_M_f32	0
htrPosComputationDelay_Rad_M_f32[0]	2.75900006
/trPosComputationDelay_Rad_M_f32[1]	-1.09109998
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.264999986
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0370000005
PICurrCntrl InverterFailSclFac Uls M f32	0.444000006

PICurrCntrl Per1

2016-09-15, 18:23:31+0530



Input Value PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32 0.477999985 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.499000013 PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32 20.7000008 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -340.130005 PICurrCntrl MtrVecuFilt M str.TermN Uls f32 23863 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.404900014 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 20.7000008 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32$ -340.130005 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 23863 0.404900014 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32$ 4784.52979 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 k_DualEcuSignalSclFacSlew_UlspS_f32 56 7999992 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 1499.40002 $k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc$ 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0.0649999976 k_MtrCtrlVirualResDax_Ohm_f32 k_MtrCtrlVirualResQax_Ohm_f32 0.0179999992 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ 0 k_MtrVoltDaxIntegHiLim_Volt_f32 3.72659993 k_MtrVoltDaxIntegLoLim_Volt_f32 -4.5999999 k_MtrVoltQaxFiltFFEnable_Cnt_lgc $k_MtrVoltQaxIntegHiLim_Volt_f32$ 12 3562002 k_MtrVoltQaxIntegLoLim_Volt_f32 -4.5 k MtrVoltVecuFiltEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 -21.0760002 k VoltSatQaxPolyCoeff Uls f32 22.4570007 k_deadtimeVScale_Uls_f32 0.961000025 t CommOffsetTblX Uls u3p13[0] 868 $t_CommOffsetTblX_Uls_u3p13[1]$ 1049 t CommOffsetTblY Cnt u16[0] 1020 t_CommOffsetTblY_Cnt_u16[1] 1034 target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr 0 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 0 $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ 0 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -214.828995 $target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr$ 1164 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -149.003006 $target_MtrCntrl_Read_SysState_Cnt_Enum_Val$ 3 **Actual Value Expected Value** Name Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1034 1034 62980 62980 ± 1 MtrCntrl_Write_ModIdx_Uls_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -20.8659973 -20.8659973 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 15.7318239 15.7318239 ± 4.88E-04 $MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)$ 15 2233725 15.2233725 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 37141 37141 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	-
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	-
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	-

0.0441000015

0.0441000015 ± 0.0625

PICurrCntrl_DualEcuFailSclFac_Uls_M_f32





Test Step 2.42 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val) MtrCntrl Read SysState Cnt Enum(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	target_MtrCntrl_Read_SysState_Cnt_Enum_Val 106.072998
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-112.455002
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.101999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0430000015
MtrCtrl MtrDampTermQax Ohm M f32[0]	0.0560000017
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.120999999
MtrCtrl MtrDaxIntegralGain Ohm M f32[0]	0.802999973
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.56299996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-946.299988
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-752.830994
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0579999983
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.104000002
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0170000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0410000011
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.944999993
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.273
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-629.994019
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	916.687988
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-8.32400036
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-26.5540009
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-15.9300003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-19.7889996
MtrCtrl_Vecu_Volt_M_f32[0]	21.2910004
MtrCtrl_Vecu_Volt_M_f32[1]	23.6509991
MtrCurrDaxPrevIntg_Volt_M_f32	0.981999993
MtrCurrDaxRef_Amp_M_f32[0]	205.820999
MtrCurrDaxRef_Amp_M_f32[1]	-206.792007
MtrCurrQaxCog_Amp_M_f32	-100.035004
MtrCurrQaxPrevIntg_Volt_M_f32	12.4246998
MtrCurrQaxRef_Amp_M_f32[0]	140.289001
MtrCurrQaxRef_Amp_M_f32[1]	178.235992
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	-2.51740003
MtrPosComputationDelay_Rad_M_f32[1]	-0.283300012 0.50000005
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.592999995 0.037999988
PICurrCntrl InverterFailScIFac Uls M f32	0.638999999
	0.0196000002
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.0350000002
PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32	-340.130005
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	-340.130005 -784.130005
PICurrCntrl MtrVecuFilt M str.TermN Uls f32	1838.12
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.1611
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput UIs f32	-340.130005
PICurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32	-784.130005
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	1838.12
PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.1611
k CLOAFdbackSignalSclFacSlew UlspS f32	6671
k_DualEcuSignalSclFacSlew_UlspS_f32	58
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3327.94995
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.165999994
k_MtrCtrlVirualResQax_Ohm_f32	0.196999997
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	13.8471003
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.69999981
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	4.8657999
k_MtrVoltQaxIntegLoLim_Volt_f32	-6.5999999
k MtrVoltVecuFiltEnable Cnt lgc	0

PICurrCntrl_Per1

2016-09-15, 18:23:31+0530



Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	12.7969999		
k_VoltSatQaxPolyCoeff_Uls_f32	-5.26999998		
k_deadtimeVScale_Uls_f32	0.968999982		
t_CommOffsetTblX_Uls_u3p13[0]	3006		
t_CommOffsetTblX_Uls_u3p13[1]	6971		
t_CommOffsetTblY_Cnt_u16[0]	136		
t_CommOffsetTblY_Cnt_u16[1]	593		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-96.3310013		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3660		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-124.758003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	593	593	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63504	63504 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-16.5058613	-16.5058613 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-12.3771486	-12.3771486 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	16184	16184 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	13.8471003	13.8471003	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.030749999	0.030749999 ± 0.0625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	~

Test Step 2.43 (Repeat Count = 1)	· ·
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-91.4420013
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	133.692993
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.00800000038
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.098999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.064000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0869999975
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.925000012
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.28699994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-467.540985
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	559.55603
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.072999989
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0450000018
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0130000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0970000029

© Report created by TESSY V3.1.13, report template V2.1

2016-09-15, 18:23:31+0530



PICurrCntrl_Per1

Name	Input Value		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.53100002		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.621999979		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	363.421997		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-896.711975		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	3.87299991		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	19.2730007		
	18.8899994		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	6.02799988		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]			
MtrCtrl_Vecu_Volt_M_f32[0]	12.1129999		
MtrCtrl_Vecu_Volt_M_f32[1]	14.4729996		
MtrCurrDaxPrevIntg_Volt_M_f32	22.4890003		
MtrCurrDaxRef_Amp_M_f32[0]	-69.0940018		
MtrCurrDaxRef_Amp_M_f32[1]	161.973007		
MtrCurrQaxCog_Amp_M_f32	-184.522003		
MtrCurrQaxPrevIntg_Volt_M_f32	24.3127995		
MtrCurrQaxRef_Amp_M_f32[0]	91.8850021		
MtrCurrQaxRef_Amp_M_f32[1]	182.261002		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	-2.45490003		
MtrPosComputationDelay_Rad_M_f32[1]	-1.48280001		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.287		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0390000008		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.202999994		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0443000011		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.128999993		
PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32	22.2399998		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	386.220001		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	30983.1992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.328200012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	22.2399998		
	386.220001		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_UIs_f32	30983.1992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32			
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.328200012		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	1324.80005		
k_DualEcuSignalSclFacSlew_UlspS_f32	59.2000008		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	754.981018		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.0610000007		
k_MtrCtrlVirualResQax_Ohm_f32	0.112000003		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	14.0340996		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.80000019		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	26.7854996		
k MtrVoltQaxIntegLoLim Volt f32	-4.5999999		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k VoltSatDaxPolyCoeff Uls f32	0.541999996		
k_VoltSatQaxPolyCoeff_Uls_f32	5.81099987		
k deadtimeVScale Uls f32	0.957000017		
t CommOffsetTbIX UIs u3p13[0]	205		
t_CommOffsetTblX_Uls_u3p13[1]	4096		
t_CommOffsetTblY_Cnt_u16[0]	34		
	96		
t_CommOffsetTblY_Cnt_u16[1]			
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-168.113007		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2573		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	50.0610008		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Resu
MtrCntrl_Write_CommOffset_Cnt_u16(val)	96	96	
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	62717	62717 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-8.85189724	-8.85189533 ± 4.88E-04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	7.48476315	7.48476171 ± 4.88E-04	
INTE CHELL TALL CONTROL OF THE CONTR		30868 ± 1.52588E-05	
MtrCntrl Write PhaseAdvanceFinal Pov u0n16(val)			
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) MtrCurrDaxPrevIntg_Volt_M_f32	30868	0	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	•
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.44 (Repeat Count = 1) Name	Input Value
FastDataAccessBufIndex Cnt M u16	0
MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc(ptr)	target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val
	target_MtrCntrl_Read_MotCurrLoaMtgtnEn Cnt_lgc_val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr) MtrCntrl Read MtrCurrDax Amp f32(Val)	
	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
AttCatal Read MtrCurrOpy App. (230/cl)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-91.4420013
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	133.692993
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.00499999989
/trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00499999989
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0939999968
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0710000023
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.869000018
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.84599996
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	181.75
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	166.714005
/trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.104000002
/trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.00600000005
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.107000001
/trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0109999999
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.98500001
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.287999988
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-837.336975
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-656.465027
MtrCtrl MtrVoltDaxFF Volt M f32[0]	-30.1380005
MtrCtrl MtrVoltDaxFF Volt M f32[1]	0.920000017
/trCtrl MtrVoltQaxFF Volt M f32[0]	-8.16100025
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	24.6310005
/trCtrl_Vecu_Volt_M_f32[0]	26.3600006
/trCtrl_Vecu_Volt_M_f32[1]	28.7199993
MtrCurrDaxPrevIntg Volt M f32	-4.96700001
MtrCurrDaxRef Amp M f32[0]	-132.813004
MtrCurrDaxRef Amp M f32[1]	-9.14299965
MtrCurrQaxCog Amp M f32	-197.354996
MtrCurrQaxPrevIntg Volt M f32	18.0771999
MtrCurrQaxRef Amp M f32[0]	-218.035004
	-218.035004 11.6370001
MtrCurrQaxRef_Amp_M_f32[1]	0
MtrCurrQaxRpl_Amp_M_f32	·
MtPosComputationDelay_Rad_M_f32[0]	-0.164199993
MtrPosComputationDelay_Rad_M_f32[1]	3.03530002
PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.602999985
PlCurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.039999991
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.887000024
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.169799998
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.649999976

2016-09-15, 18:23:31+0530



PICurrCntrl_Per1

		• "	
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-627.179993		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	8419.69043		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0151000004		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32	-627.179993		
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	8419.69043		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0151000004		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	771.372986		
k_DualEcuSignalSclFacSlew_UlspS_f32	60.4000015		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	798.940002		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k MtrCtrlVirualResDax Ohm f32	0.070000003		
k_MtrCtrlVirualResQax_Ohm_f32	0.0270000007		
k MtrCurrQaxRefModifDsb Cnt Igc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	18.6986008		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.9000001		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	10.9334002		
k MtrVoltQaxIntegLoLim Volt f32	-5.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-20.1189995		
k VoltSatQaxPolyCoeff Uls f32	-7.09100008		
k_deadtimeVScale_UIs_f32	0.995999992		
t_CommOffsetTblX_Uls_u3p13[0]	2212		
t_CommOffsetTblX_Uls_u3p13[1]	4742		
t_CommOffsetTblY_Cnt_u16[0]	109		
t_CommOffsetTblY_Cnt_u16[1]	367		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-5.66300011		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1243		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Resul
MtrCntrl_Write_CommOffset_Cnt_u16(val)	367	367	•
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	65273	65273 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-20.6800079	-20.6800079 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-0.0375764705	-0.0375764593 ± 4.88E-04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-26.2545357	-26.2545357 ± 4.88E-04	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	31070	31070 ± 1.52588E-05	
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.032449998	0.032449998 ± 0.0625	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	-
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	-
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	-
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	-
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	-
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	-
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	-
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	-
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	-
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	-
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	-
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	-





Test Step 2.45 (Repeat Count = 1)	· ·
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val) MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	target_MtrCntrl_Read_SysState_Cnt_Enum_Val 171.485992
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	163.787003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0370000005
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0379999988
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.80200005
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.740999997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-489.436005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	938.341003
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0199999996
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0879999995
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0120000001
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.0560000017
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.10899997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.479999989
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	916.997009 1002.97998
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-26.5079994
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	4.36100006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	15.1960001
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-2.83699989
MtrCtrl_Vecu_Volt_M_f32[0]	5.33099985
MtrCtrl_Vecu_Volt_M_f32[1]	7.69099998
MtrCurrDaxPrevIntg_Volt_M_f32	6.17600012
MtrCurrDaxRef_Amp_M_f32[0]	-146.173996
MtrCurrDaxRef_Amp_M_f32[1]	-213.335007
MtrCurrQaxCog_Amp_M_f32	152.016006
MtrCurrQaxPrevIntg_Volt_M_f32	1.08770001
MtrCurrQaxRef_Amp_M_f32[0]	-216.921997 -184.923996
MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRpl Amp M f32	0
MtrPosComputationDelay Rad M f32[0]	-0.249500006
MtrPosComputationDelay_Rad_M_f32[1]	2.82990003
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.432999998
PlCurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0410000011
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0109999999
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.335599989
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.851999998
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-10.21
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	12079.9004
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32 PICurrCntrl_MtrVeltOavEFFilt_M_str_Provipout_Lile_f32	0.620700002
PICurrCntrl_MtrVoltQaxFFFiit_M_str.PrevInput_UIs_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.PrevOutput_UIs_f32	-43.1699982 -10.21
PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermN_Uls_f32	12079.9004
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.620700002
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2475.81006
k_DualEcuSignalSclFacSlew_UlspS_f32	61.5999985
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2645.06006
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.179000005
k_MtrCtrlVirualResQax_Ohm_f32	0.0120000001
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	7.70550005
k_MtrVoltDaxIntegHiLim_Volt_f32	7.70650005 -4.099999
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.0999999 1
k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntedHiLim_Volt_f32	0.614899993
k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32	0.614899993 -6.5

PICurrCntrl_Per1



Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	-1.26999998		
k VoltSatQaxPolyCoeff Uls f32	16.9449997		
k deadtimeVScale Uls f32	0.962000012		
t CommOffsetTblX Uls u3p13[0]	4809		
t CommOffsetTblX Uls u3p13[1]	5553		
t CommOffsetTblY Cnt u16[0]	663		
t CommOffsetTblY Cnt u16[1]	905		
target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr	0		
target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr	0		
target MtrCntrl Read ModldxSrlComSvcDft Cnt lqc Val	1		
target MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc ptr	0		
target MtrCntrl Read MtrCurrDax Amp f32 Val	114.946999		
target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	1956		
target MtrCntrl Read MtrCurrQax Amp f32 Val	-198.285995		
	-196.265995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1	1	1
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1956	1956	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0.0172781665	0.0172781646 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.80996847	-4.80996895 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	30128	30128 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0487000011	0.0487000011 ± 0.0625	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	-
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.46 (Repeat Count = 1)	🗸
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	106.072998
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-112.455002
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.116999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0860000029
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.114
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.00600000005
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.579
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.407999992
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-297.562012
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	435.532013
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0189999994





Name	Input Value		
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.075000003		
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0939999968		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0879999995		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.878000021		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.247999996		
MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	791.299988		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-595.505981		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	10.0620003		
MtrCtrl MtrVoltDaxFF Volt M f32[1]	10.7410002		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-4.92999983		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	14.6809998		
	18.2229996		
MtrCtrl_Vecu_Volt_M_f32[0]			
MtrCtrl_Vecu_Volt_M_f32[1]	20.5830002		
MtrCurrDaxPrevIntg_Volt_M_f32	2.41400003		
MtrCurrDaxRef_Amp_M_f32[0]	-91.4420013		
MtrCurrDaxRef_Amp_M_f32[1]	133.692993		
MtrCurrQaxCog_Amp_M_f32	54.1119995		
MtrCurrQaxPrevIntg_Volt_M_f32	11.5314999		
MtrCurrQaxRef_Amp_M_f32[0]	138.595001		
MtrCurrQaxRef_Amp_M_f32[1]	-157.388		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	1.6595		
MtrPosComputationDelay_Rad_M_f32[1]	2.08319998		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.890999973		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0419999994		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.76700002		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.976100028		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.837000012		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	267.119995		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	570.700012		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	47476.6016		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.878300011		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput UIs f32	267.119995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	47476.6016		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.878300011		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	4075.97998		
k_DualEcuSignalSclFacSlew_UlspS_f32	62.7999992		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	1135.18994		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k MtrCtrlFeedbackControlDisable Cnt Igc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.0430000015		
k_MtrCtrlVirualResQax_Ohm_f32	0.0289999992		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	6.86969995		
k_MtrVoltDaxIntegLoLim_Volt_f32	-7.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k MtrVoltQaxIntt i Enable_Gn_gc	0.205699995		
k MtrVoltQaxIntegLoLim Volt f32			
	-7.5 1		
k_MtrVoltVecuFiltEnable_Cnt_lgc			
k_VoltSatDaxPolyCoeff_Uls_f32	-11.4569998		
k_VoltSatQaxPolyCoeff_Uls_f32	0.670000017		
k_deadtimeVScale_Uls_f32	0.958000004		
t_CommOffsetTbIX_UIs_u3p13[0]	4424		
t_CommOffsetTbIX_UIs_u3p13[1]	7552		
t_CommOffsetTbIY_Cnt_u16[0]	1052		
t_CommOffsetTblY_Cnt_u16[1]	1891		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	3.89299989		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1889		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1889	1889	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-211.5	-211.5 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	8.15544415	8.15544415 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	28.5562553	28.5562592 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	24630	24630 ± 1.52588E-05	~





Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl DualEcuFailSclFac Uls M f32	0.0341499969	0.0341499969 ± 0.0625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.47 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	24.6130009
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-20.9400005
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0610000007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.098999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.00499999989
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.00499999989
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.270000011
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.89100003
MtrCtrl MtrDaxPropotionalGain Ohm M f32[0]	898.598999
/trCtrl MtrDaxPropotionalGain Ohm M f32[1]	416.613007
MtrCtrl MtrImpedDax Ohm M f32[0]	0.116999999
MtrCtrl MtrImpedDax Ohm M f32[1]	0.019999996
MtrCtrl MtrImpedQax Ohm M f32[0]	0.0649999976
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.10000001
MtrCtrl MtrQaxIntegralGain Ohm M f32[0]	0.0799999982
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.699000001
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	378.188995
MtrCtrl MtrQaxPropotionalGain Ohm M f32[1]	157.612
VtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	26.9950008
MtrCtrl MtrVoltDaxFF Volt M f32[1]	7.13500023
MtrCtrl MtrVoltQaxFF Volt M f32[0]	30.9510002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-20.6159992
MtrCtrl Vecu Volt M f32[0]	28.0200005
MtrCtrl Vecu Volt M f32[1]	30.3799992
MtrCurrDaxPrevIntg_Volt_M_f32	17.8910007
MtrCurrDaxRef Amp M f32[0]	171.485992
MtrCurrDaxRef_Amp_M_f32[1]	163.787003
MtrCurrQaxCog Amp M f32	-17.6900005
MtrCurrQaxPrevIntg_Volt_M_f32	10.2707996
	-100.282997
MtrCurrQaxRef_Amp_M_f32[0]	-100.282997 -120.248001
MtrCurrQaxRef_Amp_M_f32[1]	-120.248001
VtrCurrQaxRpl_Amp_M_f32	· ·
MtrPosComputationDelay_Rad_M_f32[0]	1.40610003
MtrPosComputationDelay_Rad_M_f32[1]	1.39110005

PICurrCntrl Per1

2016-09-15, 18:23:31+0530



Input Value PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.978999972 PICurrCntrl_DualEcuFailSclFac_Uls_M_f32 0.0430000015 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.845000029 $PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32$ 0.128399998 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.428000003 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32 267.119995 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -38.7999992 $PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32$ 3431.37012 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.436399996 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32$ 267 119995 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 -38.7999992 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 3431 37012 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.436399996 k CLOAFdbackSignalSclFacSlew UlspS f32 6201 14014 k_DualEcuSignalSclFacSlew_UlspS_f32 64 194.557007 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 $k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc$ k_MtrCtrlFeedbackControlDisable_Cnt_lgc k_MtrCtrlVirualResDax_Ohm_f32 0.0469999984 k_MtrCtrlVirualResQax_Ohm_f32 0.164000005 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 $k_MtrVoltDaxIntegHiLim_Volt_f32$ 26.1525002 k_MtrVoltDaxIntegLoLim_Volt_f32 -2.5999999 $k_MtrVoltQaxFiltFFEnable_Cnt_lgc$ 0 k_MtrVoltQaxIntegHiLim_Volt_f32 4.69950008 k_MtrVoltQaxIntegLoLim_Volt_f32 -2.5999999 k_MtrVoltVecuFiltEnable_Cnt_lgc -12.8179998 k_VoltSatDaxPolyCoeff_Uls_f32 $k_VoltSatQaxPolyCoeff_Uls_f32$ 12.3579998 k deadtimeVScale Uls f32 0.972000003 t_CommOffsetTblX_Uls_u3p13[0] 705 t CommOffsetTblX Uls u3p13[1] 4996 t_CommOffsetTblY_Cnt_u16[0] 1077 t_CommOffsetTblY_Cnt_u16[1] 1690 $target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr$ 0

target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1690	1690	~
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	63700	63700 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-102.557999	-102.557999 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-20.7308865	-20.7308884 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-21.0288715	-21.0288715 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	55395	55395 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0510000028	0.0510000028 ± 0.0625	•

1

0

45.3779984

4409 59.7319984

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

 $target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr$

target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val

 $target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val$

 $target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr$

target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target _MtrCntrl_Read_MtrCurrDax_Amp_f32_Val



Fest Step 2.48 (Repeat Count = 1)	Innut Value
lame	Input Value
rastDataAccessBufIndex_Cnt_M_u16	1
<pre>AtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr) AtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)</pre>	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl Read MtrCurrDax Amp f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
/trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
ItrCntrl Read SysState Cnt Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-166.035004
ItrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	183.065002
ltrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0560000017
ltrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0469999984
ltrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.125650004
ltrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.125650004
ltrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.70599997
trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.759000003
trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-535.288025
trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-568.218018
trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0970000029
trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0160000008
ltrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0549999997
ltrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0979999974
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.624000013
ltrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.85000002
ltrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-327.623993
trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	577.963989
trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	21.0270004
trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-7.53299999
trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	29.0550003
ltrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-17.6779995
trCtrl_Vecu_Volt_M_f32[0]	6.83099985
ItrCtrl_Vecu_Volt_M_f32[1]	9.19099998
ItrCurrDaxPrevIntg_Volt_M_f32	15.6529999
ItrCurrDaxRef_Amp_M_f32[0]	106.072998
ltrCurrDaxRef_Amp_M_f32[1] ltrCurrQaxCog Amp M f32	-112.455002 -214.828995
ItrCurrQaxPrevIntg Volt M f32	5.7888999
ItrCurrQaxRef_Amp_M_f32[0]	-68.6760025
ItrCurrQaxRef Amp M f32[1]	-96.776001
ltrCurrQaxRpl_Amp_M_f32	0
ItrPosComputationDelay_Rad_M_f32[0]	3.07680011
trPosComputationDelay_Rad_M_f32[1]	1.0194
CurrCntrl CurrSensFailSclFac Uls M f32	0.610000014
CurrCntrl DualEcuFailSclFac Uls M f32	0.043999998
CurrCntrl InverterFailSclFac Uls M f32	0.95999979
ICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.593599975
CurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.291000009
ICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	404.899994
ICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002
CurrCntrl MtrVecuFilt M str.TermN Uls f32	13842.5
CurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.35710001
CurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	404.899994
CurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002
CurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	13842.5
CurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.35710001
CLOAFdbackSignalSclFacSlew_UlspS_f32	4211.16016
_DualEcuSignalSclFacSlew_UlspS_f32	65.1999969
ILOAFdbackSignalSclFacSlew_UlspS_f32	3085.33008
_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
_MtrCtrlVirualResDax_Ohm_f32	0.0430000015
_MtrCtrlVirualResQax_Ohm_f32	0.119000003
_MtrCurrQaxRefModifDsb_Cnt_lgc	0
_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
_MtrVoltDaxIntegHiLim_Volt_f32	8.83409977
_MtrVoltDaxIntegLoLim_Volt_f32	-12.5
_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
_MtrVoltQaxIntegHiLim_Volt_f32	13.2000999

PICurrCntrl_Per1



Name	Input Value		
k_MtrVoltQaxIntegLoLim_Volt_f32	-12.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-20.3659992		
k_VoltSatQaxPolyCoeff_Uls_f32	8.98099995		
k_deadtimeVScale_Uls_f32	0.990999997		
t_CommOffsetTblX_Uls_u3p13[0]	1580		
t_CommOffsetTblX_Uls_u3p13[1]	2671		
t_CommOffsetTblY_Cnt_u16[0]	161		
t_CommOffsetTblY_Cnt_u16[1]	1743		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	103.652		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4487		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	1.62199998		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1743	1743	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	64946	64946 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	118.052994	118.052994 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	4.14188147	4.14188147 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	8.11206532	8.11206436 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	15557	15557 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0358499996	0.0358499996 ± 0.0625	•

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	•
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	_

Test Step 2.49 (Repeat Count = 1)		✓
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	1	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	140.289001	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	178.235992	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.123000003	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0430000015	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.114	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.829	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.39300001	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-515.534973	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-508.975006	
MtrCtrl_MtrlmpedDax_Ohm_M_f32[0]	0.120999999	
MtrCtrl_MtrlmpedDax_Ohm_M_f32[1]	0.090999982	

2016-09-15, 18:23:31+0530





Name	Input Value		
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.019999996		
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.087999995		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.954		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.512000024		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	305.28299		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-513.950012		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.9390001		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-24.3929996		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-22.0189991		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	13.2709999		
MtrCtrl_Vecu_Volt_M_f32[0]	8.61200047		
MtrCtrl_Vecu_Volt_M_f32[1]	10.9720001		
MtrCurrDaxPrevIntg_Volt_M_f32	-19.3589993		
MtrCurrDaxRef_Amp_M_f32[0]	-91.4420013		
MtrCurrDaxRef_Amp_M_f32[1]	133.692993		
MtrCurrQaxCog Amp M f32	-96.3310013		
MtrCurrQaxPrevIntg_Volt_M_f32	14.2783003		
MtrCurrQaxRef_Amp_M_f32[0]	-139.906998		
MtrCurrQaxRef_Amp_M_f32[1]	115.814003		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	2.72160006		
MtrPosComputationDelay_Rad_M_f32[1]	2.10240006		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.791999996		
PICurrCntrl DualEcuFailSclFac Uls M f32	0.0450000018		
PICurrCntrl InverterFailSclFac Uls M f32	0.0799999982		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.619400024		
PICurrCntrl MtrCurrQaxSatFluxRatio_Ois_M_132	0.783999979		
PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32	865.320007		
	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32			
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	46503.6992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.198599994		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	865.320007		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	46503.6992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.198599994		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7813.02979		
k_DualEcuSignalSclFacSlew_UlspS_f32	66.4000015		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	6623.8501		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.120999999		
k_MtrCtrlVirualResQax_Ohm_f32	0.10000001		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	30.1000996		
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	5.77519989		
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-24.5739994		
k_VoltSatQaxPolyCoeff_Uls_f32	-11.3669996		
k_deadtimeVScale_Uls_f32	0.952000022		
t_CommOffsetTbIX_UIs_u3p13[0]	908		
t_CommOffsetTbIX_Uls_u3p13[1]	5956		
t_CommOffsetTblY_Cnt_u16[0]	578		
t_CommOffsetTblY_Cnt_u16[1]	1247		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	99.348999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2730		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Resul
MtrCntrl_Write_CommOffset_Cnt_u16(val)	2730	2730	
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	212.145004	212.145004 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-4.1812501	-4.1812501 ± 4.88E-04	
MtrCntrl Write MtrQaxVoltage Volt_i32(val)	2.27480721	2.27480721 ± 4.88E-04	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	10742	10742 ± 1.52588E-05	
MtrCurrDayPrevinta, Volt M, f32	0	0	

MtrCurrDaxPrevIntg_Volt_M_f32

PICurrCntrl_Per1



Name	Actual Value	Expected Value	Result
PICurrCntrl DualEcuFailSclFac Uls M f32	0.0533000007	0.0533000007 ± 0.0625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.50 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl MtrCurrDaxMaxVal Amp M f32[0]	91.8850021
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	182.261002
MtrCtrl MtrDampTermDax Ohm M f32[0]	0.030999995
MtrCtrl MtrDampTermDax Ohm M f32[1]	0.032999998
MtrCtrl MtrDampTermQax Ohm M f32[0]	0.115000002
MtrCtrl MtrDampTermQax Ohm M f32[1]	0.0579999983
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.545000017
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.884000003
MtrCtrl MtrDaxPropotionalGain Ohm M f32[0]	-366.040009
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-870.554993
MtrCtrl_MtrlmpedDax_Ohm_M_f32[0]	0.0049999989
MtrCtrl_MtrlmpedDax_Ohm_M_f32[1]	0.0049999989
MtrCtrl MtrlmpedQax Ohm M f32[0]	0.0189999994
MtrCtrl MtrImpedQax Ohm M f32[1]	0.075000003
MtrCtrl MtrQaxIntegralGain Ohm M f32[0]	0.69099985
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.344000012
MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	592.877014
MtrCtrl MtrQaxPropotionalGain Ohm M f32[1]	559.130005
MtrCtrl MtrVoltDaxFF Volt M f32[0]	5.68100023
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	2.06599998
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-8.32400036
MtrCtrl MtrVoltQaxFF Volt M f32[1]	-26.5540009
MtrCtrl_Vecu_Volt_M_f32[0]	24.4610004
MtrCtrl_Vecu_Volt_M_f32[1]	26.8209991
MtrCurrDaxPrevIntg Volt M f32	29.6800003
MtrCurrDaxRef Amp M f32[0]	160.044006
MtrCurrDaxRef Amp M f32[1]	165.242004
MtrCurrQaxCog Amp M f32	-168.113007
MtrCurrQaxPrevIntg Volt M f32	18.2201996
MtrCurrQaxRef Amp M f32[0]	-82.2979965
MtrCurrQaxRef Amp M f32[1]	46.8180008
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	-2.8894999
MtrPosComputationDelay_Rad_M_f32[1]	0.699500024
	0.785000024
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.763000020

PICurrCntrl_Per1

2016-09-15, 18:23:31+0530



Name
PICURCINIT_INVERTERIBLES Tax_RIBLE_N_RIB_2
PICUrrCntf MtrCurrDaxSatFuxRatio_Uis_M_132
PICUrrCnit MtrCurrQaxSalFiuxRatio_Uis_M.132 0.871999979 PICUrrCnit MtrVecuFilt M. str Previnput_Uis_132 20.7000008 PICUrrCnit MtrVecuFilt M. str Previnput_Uis_132 4209.0016 PICUrrCnit MtrVecuFilt M. str TermD_Uis_132 4209.0016 PICUrrCnit MtrVecuFilt M. str. Previnput_Uis_132 20.7000008 PICUrrCnit MtrVecuFilt M. str. Previnput_Uis_132 20.7000008 PICUrrCnit MtrVelQaxFFFII M. str. Previnput_Uis_132 20.7000008 PICUrrCnit MtrVelQaxFFFII M. str. Previnput_Uis_132 4209.6016 PICUrrCnit MtrVelQaxFFII M. str. Previnput_Uis_132 4209.6016 PICUrrCnit MtrVelQaxFFII M. str. Previnput_Uis_132 4209.6016 PICUrrCnit MtrVelQaxFFII M. str. Previnput_Uis_132 471.450008 PICURCNIT MtrVelQaxFII Mtr. Previnput_Uis_132 471.450008 PICURCNIT MtrVelQaxFII MtrVelQaxFII Mtr. Previnput_Uis_132 471.450008 PICURCNIT MtrVelQaxFII MtrV
PICUrrCntf_MirVecuFit_Mst_PrevInput_Uis_r32 267.119995 PICUrrCntf_MirVecuFit_Mst_FrevOutput_Uis_r32 20.7000008 PICUrrCntf_MirVecuFit_Mst_FremD_Uis_r32 42029.9016 PICUrrCntf_MirVecuFit_Mst_FremD_Uis_r32 0.0784000009 PICUrrCntf_MirVolCaxFFFit_Mst_PrevInput_Uis_r32 267.119995 PICUrrCntf_MirVolCaxFFFit_Mst_PrevInput_Uis_r32 20.7000008 PICUrrCntf_MirVolCaxFFFit_Mst_PrevInput_Uis_r32 42029.8016 PICUrrCntf_MirVolCaxFFFit_Mst_PrevInput_Uis_r32 42029.8016 PICUrrCntf_MirVolCaxFFFit_Mst_PrevInput_Uis_r32 0.784000009 k_CLOAFdbackSignalSclFacSlew_UispS_r32 67.5999985 k_DUAREDASSignalSclFacSlew_UispS_r32 67.5999985 k_INCCHTCurlcopSecOTTranFcEnable_Cnt_lgc 0 k_MirCritCurl-LoopSecOTTranFcEnable_Cnt_lgc 0 k_MirCurlvirualResDax_Chm_r32 0.0799999999999999999999999999999999999
PICUrrChit MirVecuFit M. str. PrevOutput_Uls_[32
PCurrChrl MrtVecuFit M_str. TermN_Uis_32
PlCurrCht Mit/voltQarFFILM_str.TermD_UIs_G2
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uis_f32 20.7000008 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32 42029.6016 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32 0.764000009 kCLOAFdbackSignalSciFacSlew_UIspS_f32 712.456008 kDualEcuSignalSciFacSlew_UIspS_f32 67.5999985 kLLOAFdbackSignalSciFacSlew_UIspS_f32 303.729004 kMtrCtrICnLoopSecOrTranFcEnable_Cnt_Igc 1 kMtrCtrIFeedbackControlDisable_Cnt_Igc 0 kMtrCtrIFeedbackControlDisable_Cnt_Igc 0.00000001 kMtrCtrIFeedbackControlDisable_Cnt_Igc 0.0799999982 kMtrCutrQaxRefModifDs_Cnt_Igc 0 kMtrCutrQaxRefModifDs_Cnt_Igc 0 kMtrCutrQaxRefModifDs_Cnt_Igc 0 kMtrVoltDaxInteglo.Lim_Volt_f32 410.5 kMtrVoltDaxInteglo.Lim_Volt_f32 10.5 kMtrVoltDaxInteglo.Lim_Volt_f32 12.551002 kMtrVoltDaxInteglo.Lim_Volt_f32 10.5 kMtrVoltDaxInteglo.Lim_Volt_f32 10.5 kMtrVoltDaxInteglo.Lim_Volt_f32 10.5 kMtrVoltDaxInteglo.Lim_Volt_f32 10.5 kMtrVoltDaxInteglo.Lim_Volt_f32 10
PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermN_Uis_f32 42029.6016 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uis_f32 0.0784000009 k_CLOAFdbackSignalScFacSlew_UispS_f32 712.458008 k_DualEouSignalScFacSlew_UispS_f32 67.599985 k_ILCAFdbackSignalScFacSlew_UispS_f32 303.729004 k_MtrCrlCurrLoopSecOfTranFcEnable_Cnt_lgc 1 k_MtrCrlFedbackControlDisable_Cnt_lgc 0 k_MtrCrlVirualResDax_Ohm_f32 0.100000001 k_MtrCrlVirualResDax_Ohm_f32 0.799999982 k_MtrCurrQaxRefModifBsp_Cnt_lgc 0 k_MtrVoltDaxIntegHiLm_Volt_f32 8.17039967 k_MtrVoltDaxIntegHiLm_Volt_f32 10.5 k_MtrVoltQaxIntegHiLm_Volt_f32 10.5 k_MtrVoltQaxIntegHiLm_Volt_f32 12.5521002 k_MtrVoltQaxIntegHiLm_Volt_f32 10.5 k_MtrVoltQaxIntegHiLm_Volt_f32 10.5 k_MtrVoltQaxIntegHiLm_Volt_f32 10.5 k_VoltSatQaxPolyCoeff_Uis_f32 24.3269997 k_VoltSatQaxPolyCoeff_Uis_f32 18.0820007 k_OeadimeVScale_Uis_f32 0.976999998 L_CommOffsetTbiX_Uis_u3p13[1] 179 L_CommOffsetTbiY_Cnt_u16[0] </td
PICurCntri_MtrVoliQaxFFFiit_M_str.TermD_Uls_f32 0.0784000009 k_CLOAFdbackSignalSciFacSlew_UlspS_f32 67.599985 k_LIOAFdbackSignalSciFacSlew_UlspS_f32 303.729004 k_MtrCotrCurt.copSecOrTranFcEnable_Cnt_lgc 1 k_MtrCtrICurt.copSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrVirualResDax_Ohm_f32 0.100000001 k_MtrCtrVirualResQax_Ohm_f32 0.0799999982 k_MtrCurCaxRefModiffSb_Cnt_lgc 0 k_MtrCurDaxRefModiffSb_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 8.17039967 k_MtrVoltDaxIntegHiLim_Volt_f32 -10.5 k_MtrVoltQaxFiltFFenable_Cnt_lgc 0 k_MtrVoltQaxFiltFFenable_Cnt_lgc 0 k_MtrVoltQaxFiltFFenable_Cnt_lgc 10.5 k_MtrVoltQaxFiltFenable_Cnt_lgc 10.5 k_MtrVoltQaxFiltFenable_Cnt_lgc 24.328997 k_VoltSatDaxPolyCoeff_Uls_f32 -24.328997 k_VoltSatDaxPolyCoeff_Uls_f32 -10.820007 k_CommOffsetTbtX_Uls_u3p13() 7 t_CommOffsetTbtX_Uls_u3p13() 7 t_CommOffsetTbtX_Uls_u3p13() 452 t_arget_MtrCntr_l_Read_UndIctNotCtriMtgnEna_Cnt_lgc_ptr
k_CLOAF dbackSignalSclFacSlew_UlspS_f32 712.458008 k_DualEcuSignalSclFacSlew_UlspS_f32 67.5999985 k_ILOAF dbackSignalSclFacSlew_UlspS_f32 303.729004 k_MtrCtrUcrtLoopSecOTranFcEnable_Cnt_lgc 1 k_MtrCtrUricalResDax_Ohm_f32 0.100000001 k_MtrCtrVirualResQax_Ohm_f32 0.0799999982 k_MtrCurQaxRefModiffspl_Cnt_lgc 0 k_MtrVorlDaxIntegHilim_Volt_f32 0.0 k_MtrVorlDaxIntegHilim_Volt_f32 1.0.5 k_MtrVollQaxIntegHilim_Volt_f32 1.0.5 k_MtrVollQaxIntegHilim_Volt_f32 1.2.5521002 k_MtrVollQaxIntegHilim_Volt_f32 1.0.5 k_MtrVollQaxIntegLoLim_Volt_f32 1.0.5 k_MtrVollQaxIntegLoLim_Volt_f32 1.0.5 k_MtrVollQaxIntegLoLim_Volt_f32 1.0.5 k_MtrVollQaxIntegLoLim_Volt_f32 1.0.5 k_MtrVollQaxIntegLoLim_Volt_f32 1.0.5 k_VoltSalQaxPolyCoeff_Uls_f32 2.43269997 k_VoltSalQaxPolyCoeff_Uls_f32 2.43269997 k_CeaddimeyScale_Uls_Us_Jay1310] 7 t_CommOffsetTbX_Uls_Us_Jay1311 179 t_CommOffsetTbY_Cnt_u16[0] 128
k_DualEcuSignalSclFacSlew_UlspS_f32 67.5999985 k_LLOAFdbackSignalSclFacSlew_UlspS_f32 303.729004 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 1 k_MtrCtrlVirualResDax_Ohm_f32 0.100000001 k_MtrCtrlVirualResDax_Ohm_f32 0.0799999982 k_MtrCurrQaxRefModifPsb_Cnt_lgc 0 k_MtrCurrQaxRefModifPsb_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 8.17039967 k_MtrVoltDaxIntegHiLim_Volt_f32 10.5 k_MtrVoltQaxIntegLoLim_Volt_f32 10.5 k_MtrVoltQaxIntegLoLim_Volt_f32 12.5521002 k_MtrVoltQaxIntegLoLim_Volt_f32 10.5 k_MtrVoltQaxIntegLoLim_Volt_f32 10.5 k_MtrVoltQaxIntegLoLim_Volt_f32 12.5521002 k_MtrVoltQaxIntegLoLim_Volt_f32 10.5 k_MtrVoltQaxIntegLoLim_Volt_f32 10.5 k_VoltSalQaxPolyCoeff_Uls_f32 10.5 k_VoltSalQaxPolyCoeff_Uls_f32 24.3269997 k_JournolfsetTibX_Uls_u3p13[0] 7 t_CommOffsetTibX_Uls_u3p13[0] 7 t_CommOffsetTibX_Uls_u3p13[1] 179 t_CommOffsetTibY_Cnt_u16[0] 128 t_ar
k_LLOAFdbackSignalSclFacSlew_UispS_f32 303.729004 k_MtrCtrlCurtLoopSecOrTranFcEnable_Cnt_lgc 1 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.100000001 k_MtrCtrlVirualResDax_Ohm_f32 0.0799999982 k_MtrCurQaxRefModifDsb_Cnt_lgc 0 k_MtrCurDaxRefModifDsb_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 8.17039967 k_MtrVoltDaxIntegHiLim_Volt_f32 10.5 k_MtrVoltQaxIntegLoLim_Volt_f32 10.5 k_MtrVoltQaxIntegLoLim_Volt_f32 12.5521002 k_MtrVoltQaxIntegLoLim_Volt_f32 10.5 k_MtrVoltQaxIntegLoLim_Volt_f32 10.5 k_MtrVoltQaxIntegLoLim_Volt_f32 10.5 k_MtrVoltQaxIntegLoLim_Volt_f32 10.5 k_MtrVoltQaxIntegLoLim_Volt_f32 24.3269997 k_VoltSatDaxPolyCoeff_Uls_f32 24.3269997 k_VoltSatQaxPolyCoeff_Uls_f32 18.0820007 k_deadtimeVScale_Uls_f32 0.97699998 t_CommoffsetTblX_Uls_u3p13[1] 179 t_CommoffsetTblY_Cnt_u16[0] 128 t_CommoffsetTblY_Cnt_u16[1] 452 ta
k_MtrCtriCurrLoopSecOrTranFcEnable_Cnt_lgc 1 k_MtrCtriFeedbackControlDisable_Cnt_lgc 0 k_MtrCtriVirualResDax_Ohm_f32 0.100000001 k_MtrCtrVirualResDax_Ohm_f32 0.079999982 k_MtrCurQaxRefModiff0sb_Cnt_lgc 0 k_MtrVoltDaxRefModiff0sb_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 8.17039967 k_MtrVoltDaxIntegHiLim_Volt_f32 -10.5 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 0 k_MtrVoltQaxIntegLoLim_Volt_f32 12.5521002 k_MtrVoltQaxIntegLoLim_Volt_f32 -10.5 k_MtrVoltQaxIntegLoLim_Volt_f32 -10.5 k_MtrVoltQaxIntegLoLim_Volt_f32 -10.5 k_MtrVoltQaxIntegLoLim_Volt_f32 -10.5 k_VoltSatDaxPolyCoeff_Uls_f32 -8.082007 k_VoltSatDaxPolyCoeff_Uls_f32 -24.3269997 k_VoltSatQaxPolyCoeff_Uls_f32 -8.0820007 k_deadtimeVScale_Uls_f32 0.976999998 t_CommOffsetTblX_Uls_u3p13[0] 7 t_CommOffsetTblX_Uls_u3p13[1] 179 t_CommOffsetTblY_Cnt_u16[0] 128 t_CommOffsetTblY_Cnt_u16[0] 452 target_MtrCntrl_R
k_MtrCtr/FreedbackControlDisable_Cnt_igc 0 k_MtrCtr/VirualResDax_Ohm_f32 0.100000001 k_MtrCurrQaxRefModiffDsb_Cnt_igc 0 k_MtrCurrQaxRefModiffplEn_Cnt_igc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 8.17039967 k_MtrVoltQaxIntegHiLim_Volt_f32 8.17039967 k_MtrVoltQaxIntegHiLim_Volt_f32 10.5 k_MtrVoltQaxFiltFFEnable_Cnt_igc 0 k_MtrVoltQaxIntegHiLim_Volt_f32 12.5521002 k_MtrVoltQaxIntegHiLim_Volt_f32 -10.5 k_Vistage Volt_f32 -24.3269997 k_VoltSatDaxPolyCoeff_Uls_f32 -8.0820007 k_deadtimeVScale_Uls_f32 0.976999998 t_CommOffsetTblX_Uls_u3p13[0] 7 t_CommOffsetTblX_Uls_u3p13[1] 179 t_CommOffsetTblY_Cnt_u16[0] 128 t_CommOffsetTblY_Cnt_u16[1]
k_MtrCtrlVirualResDax_Ohm_r32 0.10000001 k_MtrCtrlVirualResQax_Ohm_r32 0.0799999982 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_volt_r32 8.17039967 k_MtrVoltDaxIntegLoLim_Volt_r32 -10.5 k_MtrVoltQaxIntegHiLim_volt_r32 12.5521002 k_MtrVoltQaxIntegHiLim_volt_r32 12.5521002 k_MtrVoltQaxIntegHiLim_volt_r32 -10.5 k_MtrVoltQaxIntegLoLim_volt_r32 -10.5 k_MtrVoltQaxIntegLoLim_volt_r32 -10.5 k_MtrVoltQaxIntegLoLim_volt_r32 -24.3269997 k_VoltSatDaxPolyCoeff_Uls_r32 -24.3269997 k_VoltSatDaxPolyCoeff_Uls_r32 -18.0820007 k_deadtimeVScale_Uls_r32 0.976999998 t_CommOffsetTbIX_Uls_u3p13[0] 7 t_CommOffsetTbIX_Uls_u3p13[1] 179 t_CommOffsetTbIY_Cnt_u16[0] 128 t_CommOffsetTbIY_Cnt_u16[0] 452 target_MtrCntrl_Read_bullcuMotCtrlMtgnEna_Cnt_lgc_ptr 1 target_MtrCntrl_Read_bullcuMotdxriComSvcDft_Cnt_lgc_val 0
k_MtrCtr/VirualResQax_Ohm_f32 0.079999982 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 8.17039967 k_MtrVoltDaxIntegLoLim_Volt_f32 -10.5 k_MtrVoltQaxFitIFFEnable_Cnt_lgc 0 k_MtrVoltQaxIntegHiLim_Volt_f32 12.5521002 k_MtrVoltQaxIntegLoLim_Volt_f32 -10.5 k_MtrVoltQaxIntegLoLim_Volt_f32 0 k_MtrVoltQaxIntegLoLim_Volt_f32 -10.5 k_MtrVoltQaxIntegLoLim_Volt_f32 -10.5 k_MtrVoltQaxIntegLoLim_Volt_f32 0 k_VoltSatDaxPolyCoeff_Uls_f32 -24.3269997 k_VoltSatDaxPolyCoeff_Uls_f32 -18.0820007 k_deadtimeVScale_Uls_f32 0.976999998 t_CommOffsetTbIX_Uls_u3p13[0] 7 t_CommOffsetTbIX_Uls_u3p13[1] 179 t_CommOffsetTbIY_Cnt_u16[0] 128 t_CommOffsetTbIY_Cnt_u16[1] 452 target_MtrCntrl_Read_UvtLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModidxSriComSvcDft_Cnt_lgc_val 0
k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiiLim_Volt_f32 8.17039967 k_MtrVoltQaxFills_Enable_Cnt_lgc -10.5 k_MtrVoltQaxFills_Enable_Cnt_lgc 0 k_MtrVoltQaxIntegHiiLim_Volt_f32 12.5521002 k_MtrVoltQaxIntegLo.lim_Volt_f32 -10.5 k_MtrVoltQaxIntegLo.lim_Volt_f32 -10.5 k_MtrVoltQaxIntegLo.lim_Volt_f32 0 k_MtrVoltQaxIntegLo.lim_Volt_f32 -10.5 k_MtrVoltQaxIntegLo.lim_Volt_f32 0 k_MtrVoltQaxIntegLo.lim_Volt_f32 0 k_VoltSatDaxPolyCoeff_Uls_f32 -24.3269997 k_VoltSatQaxPolyCoeff_Uls_f32 -18.0820007 k_deadtimeVScale_Uls_f32 0.976999998 t_CommOffsetTbIX_Uls_u3p13[0] 7 t_CommOffsetTbIV_Uls_u3p13[1] 179 t_CommOffsetTbIY_Cnt_u16[0] 128 t_CommOffsetTbIY_Cnt_u16[1] 452 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModldxSriComSvcDft_Cnt_lgc_Val 0
k_MtrVoltDaxIntegHiLim_Volt_f32 8.17039967 k_MtrVoltDaxIntegLoLim_Volt_f32 -10.5 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 0 k_MtrVoltQaxIntegHiLim_Volt_f32 12.5521002 k_MtrVoltQaxIntegHiLim_Volt_f32 -10.5 k_MtrVoltQaxIntegLo.lim_Volt_f32 -10.5 k_MtrVoltVocuFittEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 -24.3269997 k_VoltSatQaxPolyCoeff_Uls_f32 -18.0820007 k_deadtimeVScale_Uls_f32 0.976999998 t_CommOffsetTblX_Uls_u3p13[0] 7 t_CommOffsetTblY_Cnt_u16[0] 128 t_CommOffsetTblY_Cnt_u16[1] 452 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0
k_MtrVoltDaxIntegLoLim_Volt_f32 8.17039967 k_MtrVoltDaxIntegLoLim_Volt_f32 -10.5 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 0 k_MtrVoltQaxIntegHiLim_Volt_f32 12.5521002 k_MtrVoltQaxIntegLoLim_Volt_f32 -10.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 -24.3269997 k_VoltSatQaxPolyCoeff_Uls_f32 -18.0820007 k_deadtimeVScale_Uls_f32 0.976999998 t_CommOffsetTblX_Uls_u3p13[0] 7 t_CommOffsetTblX_Uls_u3p13[1] 179 t_CommOffsetTblY_Cnt_u16[0] 128 t_CommOffsetTblY_Cnt_u16[1] 452 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0
k_MtrVoltDaxIntegLoLim_Volt_f32 -10.5 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 0 k_MtrVoltQaxIntegHiLim_Volt_f32 12.5521002 k_MtrVoltQaxIntegLoLim_Volt_f32 -10.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 -24.3269997 k_VoltSatQaxPolyCoeff_Uls_f32 -18.0820007 k_deadtimeVScale_Uls_f32 0.976999998 t_CommOffsetTbIX_Uls_u3p13[0] 7 t_CommOffsetTbIX_Uls_u3p13[1] 179 t_CommOffsetTbIY_Cnt_u16[0] 128 t_CommOffsetTbIY_Cnt_u16[1] 452 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr 1 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0
k_MtrVoltQaxFiltFFEnable_Cnt_lgc 0 k_MtrVoltQaxIntegHiLim_Volt_f32 12.5521002 k_MtrVoltQaxIntegLoLim_Volt_f32 -10.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 -24.3269997 k_VoltSatQaxPolyCoeff_Uls_f32 -18.0820007 k_deadtimeVScale_Uls_f32 0.976999998 t_CommOffsetTbIX_Uls_u3p13[0] 7 t_CommOffsetTbIX_Uls_u3p13[1] 179 t_CommOffsetTbIY_Cnt_u16[0] 128 t_CommOffsetTbIY_Cnt_u16[1] 452 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr 1 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0
k_MtrVoltQaxIntegHiLim_Volt_f32 12.5521002 k_MtrVoltQaxIntegLoLim_Volt_f32 -10.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 -24.3269997 k_VoltSatQaxPolyCoeff_Uls_f32 -18.0820007 k_deadtimeVScale_Uls_f32 0.976999998 t_CommOffsetTbIX_Uls_u3p13[0] 7 t_CommOffsetTbIX_Uls_u3p13[1] 179 t_CommOffsetTbIY_Cnt_u16[0] 128 t_CommOffsetTbIY_Cnt_u16[1] 452 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr 1 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0
k_MtrVoltQaxIntegLoLim_Volt_f32 -10.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 -24.3269997 k_VoltSatQaxPolyCoeff_Uls_f32 -18.0820007 k_deadtimeVScale_Uls_f32 0.976999998 t_CommOffsetTbIX_Uls_u3p13[0] 7 t_CommOffsetTbIX_Uls_u3p13[1] 179 t_CommOffsetTbIY_Cnt_u16[0] 128 t_CommOffsetTbIY_Cnt_u16[1] 452 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr 1 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0
k_MtrVoltVecuFiltEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 -24.3269997 k_VoltSatQaxPolyCoeff_Uls_f32 -18.0820007 k_deadtimeVScale_Uls_f32 0.976999998 t_CommOffsetTbiX_Uls_u3p13[0] 7 t_CommOffsetTbiX_Uls_u3p13[1] 179 t_CommOffsetTbiY_Cnt_u16[0] 128 t_CommOffsetTbiY_Cnt_u16[1] 452 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr 1 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0
k_VoltSatDaxPolyCoeff_Uls_f32 -24.3269997 k_VoltSatQaxPolyCoeff_Uls_f32 -18.0820007 k_deadtimeVScale_Uls_f32 0.976999998 t_CommOffsetTbiX_Uls_u3p13[0] 7 t_CommOffsetTbiY_Cnt_u16[0] 128 t_CommOffsetTbiY_Cnt_u16[1] 452 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ivtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0
k_VoltSatQaxPolyCoeff_Uls_f32 -18.0820007 k_deadtimeVScale_Uls_f32 0.976999998 t_CommOffsetTbIX_Uls_u3p13[0] 7 t_CommOffsetTbIX_Uls_u3p13[1] 179 t_CommOffsetTbIY_Cnt_u16[0] 128 t_CommOffsetTbIY_Cnt_u16[1] 452 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr 1 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0
k_deadtimeVScale_UIs_f32 0.976999998 t_CommOffsetTbIX_UIs_u3p13[0] 7 t_CommOffsetTbIX_UIs_u3p13[1] 179 t_CommOffsetTbIY_Cnt_u16[0] 128 t_CommOffsetTbIY_Cnt_u16[1] 452 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr 1 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0
t_CommOffsetTbIX_UIs_u3p13[0] 7 t_CommOffsetTbIX_UIs_u3p13[1] 179 t_CommOffsetTbIY_Cnt_u16[0] 128 t_CommOffsetTbIY_Cnt_u16[1] 452 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr 1 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0
t_CommOffsetTbIX_UIs_u3p13[1] 179 t_CommOffsetTbIY_Cnt_u16[0] 128 t_CommOffsetTbIY_Cnt_u16[1] 452 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr 1 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0
t_CommOffsetTbIY_Cnt_u16[0] 128 t_CommOffsetTbIY_Cnt_u16[1] 452 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr 1 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0
t_CommOffsetTbIY_Cnt_u16[1] 452 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr 1 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 0
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr 1 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 0
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 0
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0
302 11 12 112 11 11 11 12 12 12 1
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 80.5459976
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 4498
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 121.994003
target_MtrCntrl_Read_SysState_Cnt_Enum_Val 1
Name Actual Value Expected Value Result
MtrCntrl_Write_CommOffset_Cnt_u16(val) 452 452 ✓
MtrCntrl_Write_Modldx_Uls_u16p16(val) 64028 ± 1 ✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 85.8150101 ± 7.81E-03 ✔
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -20.5855045 -20.5855026 ± 4.88E-04 ✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -12.1396255 ± 4.88E-04 ✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 13456 ± 1.52588E-05 ✓
MtrCurrDaxPrevIntg_Volt_M_f32 0 0 ✓
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32 0.0375499986 0.0375499986 ± 0.0625 ✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~



Test Step 2.51 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrOffCorrOffcot Ont (46(atr))	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val) MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	11.6370001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.057999983
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0500000007
MtrCtrl MtrDampTermQax Ohm M f32[0]	0.0489999987
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.114
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.17900002
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.46399999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-613.749023
MtrCtrl MtrDaxPropotionalGain Ohm M f32[1]	-825.028992
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.125650004
MtrCtrl MtrlmpedDax Ohm M f32[1]	0.125650004
MtrCtrl MtrImpedQax Ohm M f32[0]	0.116999999
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.019999996
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.202000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.88499999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	635.659973
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-88.5709991
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	19.6130009
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-29.3180008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	3.87299991
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	19.2730007
MtrCtrl_Vecu_Volt_M_f32[0]	21.3409996
MtrCtrl_Vecu_Volt_M_f32[1]	23.7010002
MtrCurrDaxPrevIntg_Volt_M_f32	-25.5009995
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrDaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxCog_Amp_M_f32	-5.66300011
MtrCurrQaxPrevIntg_Volt_M_f32	16.0422993
MtrCurrQaxRef_Amp_M_f32[0]	160.044006
MtrCurrQaxRef_Amp_M_f32[1]	165.242004
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	-1.22689998
MtrPosComputationDelay_Rad_M_f32[1]	-0.663100004
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.472000003
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0469999984
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.796000004
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.4287
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.926999986
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-657.099976
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-340.130005
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	17234.5
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0538000017
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-657.099976
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-340.130005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	17234.5
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0538000017
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	642.700012
k_DualEcuSignalSclFacSlew_UlspS_f32	68.8000031
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	7742.27002
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.0280000009
k_MtrCtrlVirualResQax_Ohm_f32	0.174999997
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRpIEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	20.6893997
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.6000004
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	19.8882008
k MtrVoltQaxIntegLoLim Volt f32	-11.6000004

PICurrCntrl_Per1



Name	Input Value		
k MtrVoltVecuFiltEnable Cnt lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	6.35500002		
k_VoltSatQaxPolyCoeff_Uls_f32	21.1189995		
k_deadtimeVScale_Uls_f32	0.967999995		
t_CommOffsetTblX_Uls_u3p13[0]	4506		
t_CommOffsetTblX_Uls_u3p13[1]	5381		
t_CommOffsetTblY_Cnt_u16[0]	1282		
t_CommOffsetTblY_Cnt_u16[1]	1346		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	41.1769981		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3770		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-41.5750008		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	3770	3770	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	165.707001	165.707001 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0.985897839	0.985897899 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.73852348	4.73852348 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	54879	54879 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0555999987	0.0555999987 ± 0.0625	•

Actual Function	Count	Expected Function	Count	Result
MtrCntrl Read MtrCurrQax Amp f32	1	MtrCntrl Read MtrCurrQax Amp f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
ntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	_

Test Step 2.52 (Repeat Count = 1)	→
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-216.921997
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-184.923996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0979999974
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0850000009
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.119000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0820000023
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.671
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.986000001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	129.369003
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-141.128998
MtrCtrl_MtrlmpedDax_Ohm_M_f32[0]	0.0659999996





Name	Input Value		
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0120000001		
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029		
	0.0160000008		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]			
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.111000001		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.83399999		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	972.747009		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	150.199997		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	28.1019993		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	0.381000012		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-30.1380005		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	0.920000017		
MtrCtrl_Vecu_Volt_M_f32[0]	28.2360001		
MtrCtrl_Vecu_Volt_M_f32[1]	30.5960007		
MtrCurrDaxPrevIntg_Volt_M_f32	28.816		
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007		
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001		
MtrCurrQaxCog_Amp_M_f32	114.946999		
MtrCurrQaxPrevIntg_Volt_M_f32	22.5016003		
MtrCurrQaxRef_Amp_M_f32[0]	-65.1900024		
MtrCurrQaxRef_Amp_M_f32[1]	-216.972		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	-1.91820002		
MtrPosComputationDelay_Rad_M_f32[1]	0.830900013		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.560000002		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0480000004		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.125		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.716899991		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.54400003		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-43.1699982		
	-657.099976		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32			
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	20241.6992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.124300003		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-657.099976		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	20241.6992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.124300003		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	4748.89014		
k_DualEcuSignalSclFacSlew_UlspS_f32	70		
k ILOAFdbackSignalSclFacSlew UlspS f32	5014.08008		
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc	1		
k MtrCtrlFeedbackControlDisable Cnt lgc	0		
k MtrCtrlVirualResDax Ohm f32	0.109999999		
k_MtrCtrlVirualResQax_Ohm_f32	0.0350000001		
	0.0330000001		
k_MtrCurrQaxRefModifDsb_Cnt_lgc			
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	8.6864996		
k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	30.9398003		
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-5.84200001		
k_VoltSatQaxPolyCoeff_Uls_f32	-16.993		
k deadtimeVScale Uls f32	0.963		
t_CommOffsetTbIX_Uls_u3p13[0]	3030		
t_CommOffsetTblX_Uls_u3p13[1]	5366		
: : : :	554		
t_CommOffsetTblY_Cnt_u16[0]			
t_CommOffsetTblY_Cnt_u16[1]	778		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-30.7789993		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4190		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	48.8400002		
	3		
	3	Forest d Males	l=
target_MtrCntrl_Read_SysState_Cnt_Enum_Val			Paguile
target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name	Actual Value	Expected Value	Result
target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name MtrCntrl_Write_CommOffset_Cnt_u16(val)	Actual Value 778	778	~
target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_ModIdx_Uls_u16p16(val)	Actual Value 778 63111	778 63111 ± 1	·
target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_ModIdx_Uls_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	Actual Value 778 63111 -180.136993	778 63111 ± 1 -180.136993 ± 7.81E-03	•
target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_ModIdx_Uls_u16p16(val)	Actual Value 778 63111	778 63111 ± 1	~
target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_ModIdx_Uls_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	Actual Value 778 63111 -180.136993	778 63111 ± 1 -180.136993 ± 7.81E-03	Result





Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	✓
PICurrCntrl DualEcuFailSclFac Uls M f32	0.0392500013	0.0392500013 ± 0.0625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

lame	Input Value
	Input value
FastDataAccessBufIndex_Cnt_M_u16	
/trCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
/trCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
/trCntrl_Read_ModidxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
ItrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
ItrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
ItrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
htrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-82.2979965
ltrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	46.8180008
ItrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0209999997
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0659999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0270000007
ItrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0149999997
ltrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.50699997
ItrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.64999998
ItrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	491.182007
ltrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	987.453979
trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0520000011
ltrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.00800000038
ltrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.00499999989
ltrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.00499999989
ltrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.82099998
ltrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.995999992
ltrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-73.2539978
ltrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-688.901978
ltrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	4.64300013
ltrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-11.7069998
ltrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-26.5079994
ltrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	4.36100006
ItrCtrl_Vecu_Volt_M_f32[0]	28.3600006
ltrCtrl_Vecu_Volt_M_f32[1]	30.2600002
ltrCurrDaxPrevIntg_Volt_M_f32	-11.5699997
ltrCurrDaxRef_Amp_M_f32[0]	-208.287994
ltrCurrDaxRef_Amp_M_f32[1]	-27.9839993
ItrCurrQaxCog_Amp_M_f32	3.89299989
trCurrQaxPrevIntg_Volt_M_f32	25.7052002
trCurrQaxRef_Amp_M_f32[0]	-146.723007
trCurrQaxRef_Amp_M_f32[1]	-121.943001
trCurrQaxRpl_Amp_M_f32	0
ltrPosComputationDelay_Rad_M_f32[0]	2.02469993
htrPosComputationDelay_Rad_M_f32[1]	-2.5934
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.745000005
ICurrCntrl DualEcuFailSclFac Uls M f32	0.0489999987

2016-09-15, 18:23:31+0530



PICurrCntrl_Per1

ricuirchui_rei i		10	12010
Name	Input Value		
PICurrCntrl InverterFailSclFac Uls M f32	0.305999994		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.720300019		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.0160000008		
PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32	1118		
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	570.700012		
PICurrCntrl MtrVecuFilt M str.TermN Uls f32	46120.5		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.107100002		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput Uls f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	46120.5		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.107100002		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	5850.64014		
k DualEcuSignalSclFacSlew UlspS f32	71.1999969		
k ILOAFdbackSignalSclFacSlew UlspS f32	2794.15991		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k MtrCtrlFeedbackControlDisable Cnt lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.039999991		
k_MtrCtrlVirualResQax_Ohm_f32	0.150999993		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k MtrCurrQaxRefModifRplEn Cnt lgc	0		
k MtrVoltDaxIntegHiLim Volt f32	5.24860001		
k MtrVoltDaxIntegLoLim Volt f32	-9.64999962		
k MtrVoltQaxFiltFFEnable Cnt lgc	0		
k MtrVoltQaxIntegHiLim Volt f32	8.75800037		
k MtrVoltQaxIntegLoLim Volt f32	-9.64999962		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k VoltSatDaxPolyCoeff Uls f32	-12.8660002		
k VoltSatQaxPolyCoeff Uls f32	6.20200014		
k deadtimeVScale Uls f32	0.986999989		
t_CommOffsetTbIX_UIs_u3p13[0]	4850		
t_CommOffsetTblX_Uls_u3p13[1]	6241		
t_CommOffsetTblY_Cnt_u16[0]	1044		
t_CommOffsetTblY_Cnt_u16[1]	1978		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	23		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	107.702003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1044	1044	/ Nesur
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	27056	27056 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-125.835999	-125.835999 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-11.7069998	-11.7069998 ± 4.88E-04	_
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.36100006	4.36100006 ± 4.88E-04	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	25821	25821 ± 1.52588E-05	
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	
PICurrCntrl DualEcuFailSclFac Uls M f32	0.0578999966	0.0578999966 ± 0.0625	
1 Touri Ontin _ Dual Lour allocit ac_013_IVI_102	0.0010000000	0.007 000000 I 0.0020	· ·

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	_





Test Step 2.54 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val 160.044006
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0] MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	165.242004
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0719999969
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0289999992
MtrCtrl MtrDampTermQax Ohm M f32[0]	0.076999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.029999993
MtrCtrl MtrDaxIntegralGain Ohm M f32[0]	1.81200004
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.92799997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-519.974976
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-32.9770012
MtrCtrl_MtrlmpedDax_Ohm_M_f32[0]	0.050999999
MtrCtrl_MtrlmpedDax_Ohm_M_f32[1]	0.061999999
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.952000022
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.88499999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-957.802979
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	641.666016
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	25.9820004
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-23.0480003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	10.0620003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	10.7410002
MtrCtrl_Vecu_Volt_M_f32[0]	22.3600006
MtrCtrl_Vecu_Volt_M_f32[1]	24.7199993
MtrCurrDaxPrevIntg_Volt_M_f32	20.1009998
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxCog_Amp_M_f32	45.3779984
MtrCurrQaxPrevIntg_Volt_M_f32	28.1571999
MtrCurrQaxRef_Amp_M_f32[0]	-208.287994 -27.9839993
MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRpl_Amp_M_f32	-27.9039993 0
MtrPosComputationDelay_Rad_M_f32[0]	-2.95309997
MtrPosComputationDelay_Rad_M_f32[1]	0.0648000017
PICurrCntrl CurrSensFailSclFac Uls M f32	0.47699998
PICurrCntrl DualEcuFailSclFac Uls M f32	0.050000007
PICurrCntrl InverterFailSclFac Uls M f32	0.25499995
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.0443000011
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.142000005
PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32	-627.179993
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	0
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	40399.6016
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.742399991
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-627.179993
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	0
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	40399.6016
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.742399991
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3971.34009
k_DualEcuSignalSclFacSlew_UlspS_f32	72.4000015
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5639.2998
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.00300000003
k_MtrCtrlVirualResQax_Ohm_f32	0.00899999961
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	17.7175007
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	4.11920023
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998
k MtrVoltVecuFiltEnable Cnt lgc	1

PICurrCntrl_Per1



Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	24.934		
k_VoltSatQaxPolyCoeff_Uls_f32	-16.5429993		
k_deadtimeVScale_Uls_f32	0.959999979		
t_CommOffsetTblX_Uls_u3p13[0]	2114		
t_CommOffsetTblX_Uls_u3p13[1]	4735		
t_CommOffsetTblY_Cnt_u16[0]	153		
t_CommOffsetTblY_Cnt_u16[1]	914		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	177.046997		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4147		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	5.72399998		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4147	4147	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-73.3619995	-73.3619995 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-4.35074377	-4.35074472 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	2.02756596	2.02756619 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	54377	54377 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl DualEcuFailSclFac Uls M f32	0.0409500003	0.0409500003 ± 0.0625	✓

Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	-
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	-
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	-

Test Step 2.55 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-65.1900024
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-216.972
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.00700000022
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0839999989
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.683000028
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.86699998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-334.098999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	800.172974
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115000002
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0579999983

PICurrCntrl_Per1



Picuricilli_Peri			ACTUAL
Name	Input Value		
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0350000001		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.119999997		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.77999997		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.85699999		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	130.878998		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-255.671997		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	14.9390001		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-29.4060001		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	26.9950008		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	7.13500023		
MtrCtrl_Vecu_Volt_M_f32[0]	18.7189999		
MtrCtrl_Vecu_Volt_M_f32[1]	21.0790005		
MtrCurrDaxPrevIntg_Volt_M_f32	-24.684		
MtrCurrDaxRef_Amp_M_f32[0]	-133.947006		
MtrCurrDaxRef_Amp_M_f32[1]	75.7020035		
MtrCurrQaxCog_Amp_M_f32	103.652		
MtrCurrQaxPrevIntg_Volt_M_f32	18.5097008		
MtrCurrQaxRef_Amp_M_f32[0]	31.5869999		
MtrCurrQaxRef_Amp_M_f32[1]	-186.395996		
MtrCurrQaxRpl Amp M f32	0		
	0.649900019		
MtrPosComputationDelay_Rad_M_f32[0] MtrPosComputationDelay_Rad_M_f32[1]	-1.9016		
PICurrCotrl_CurrSensFailSclFac_UIs_M_f32	0.039000008		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.050999999		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.84799999		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.611599982		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.144999996		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	25640.4004		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.971499979		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	0		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	25640.4004		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.971499979		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	4766.68994		
k_DualEcuSignalSclFacSlew_UlspS_f32	73.5999985		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	7056.62988		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.114		
k_MtrCtrlVirualResQax_Ohm_f32	0.155000001		
k MtrCurrQaxRefModifDsb Cnt lgc	0		
k MtrCurrQaxRefModifRplEn Cnt lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	2.74440002		
k MtrVoltDaxInteqLoLim Volt f32	-8.68999958		
k MtrVoltQaxFiltFFEnable Cnt Iqc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	9.79839993		
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-14.3280001		
k_VoltSatQaxPolyCoeff_Uls_f32	-7.07999992		
k_deadtimeVScale_UIs_f32	0.9900001		
t_CommOffsetTblX_Uls_u3p13[0]	1498		
t_CommOffsetTblX_Uls_u3p13[1]	4940		
t_CommOffsetTblY_Cnt_u16[0]	125		
t_CommOffsetTblY_Cnt_u16[1]	898		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-9.31999969		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	155		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.3040009		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Resu
		•	Resu
MtrCntrl_Write_CommOffset_Cnt_u16(val)	898	898	
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	64880	64880 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-20.2797832	-20.2797813 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.92063713	4.92063665 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)			
MtrCurrDaxPrevIntg_Volt_M_f32	31800	31800 ± 1.52588E-05	





Name	Actual Value	Expected Value	Result
PICurrCntrl DualEcuFailSclFac Uls M f32	0.0601999983	0.0601999983 ± 0.0625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

lame	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
/trCntrl Read DualEcuMotCtrlMtgnEna Cnt Igc(ptr)	target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr
/trCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr
/trCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val
/trCntrl Read MotCurrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr
MtrCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val
/trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-220
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-220
ItrCtrl MtrDampTermDax Ohm M f32[0]	0.0529999994
/trCtrl MtrDampTermDax Ohm M f32[1]	0.093999968
htrCtrl MtrDampTermQax Ohm M f32[0]	0.123000003
htrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.118000001
/trCtrl MtrDaxIntegralGain Ohm M f32[0]	1.26400006
htrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.94200003
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-771.768005
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	61.4269981
ItrCtrl MtrImpedDax Ohm M f32[0]	0.0489999987
ItrCtrl MtrImpedDax Ohm M f32[1]	0.114
htrCtrl MtrImpedQax Ohm M f32[0]	0.115000002
htrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0579999983
htrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.657999992
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.64900005
trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-279.015015
trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-333.037994
trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-17.6959991
trCtrl MtrVoltDaxFF Volt M f32[1]	-27.8540001
ItrCtrl MtrVoltQaxFF Volt M f32[0]	21.0270004
ItrCtrl MtrVoltQaxFF Volt M f32[1]	-7.53299999
trCtrl_Vecu_Volt_M_f32[0]	22.3540001
ItrCtrl Vecu Volt M f32[1]	24.7140007
ItrCurrDaxPrevintg_Volt_M_f32	-18.9759998
ItrCurrDaxRef_Amp_M_f32[0]	209.052002
MtrCurrDaxRef_Amp_M_f32[1]	-124.994003
1trCurrQaxCog Amp M f32	99.348999
ItrCurrQaxPrevIntg_Volt_M_f32	0.0860000029
ItrCurrQaxRef_Amp_M_f32[0]	-133.947006
ItrCurrQaxRef_Amp_M_f32[1]	75.7020035
trCurrQaxRpl_Amp_M_f32	0
ItrPosComputationDelay_Rad_M_f32[0]	-0.152700007
htrPosComputationDelay_Rad_M_f32[1]	1.51170003
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.941999972
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0520000011
ICurrCntrl InverterFailSclFac Uls M f32	0.619000018

PICurrCntrl_Per1

2016-09-15, 18:23:31+0530



Name	Input Value		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0478000008		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.801999986		
PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl MtrVecuFilt M str.TermN Uls f32	35039		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.570299983		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	35039		
PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.570299983		
k CLOAFdbackSignalSclFacSlew UlspS f32	7521.91016		
k DualEcuSignalSclFacSlew UlspS f32	74.8000031		
k ILOAFdbackSignalSclFacSlew UlspS f32	5032.43018		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k MtrCtrlFeedbackControlDisable Cnt lgc	1		
k MtrCtrlVirualResDax Ohm f32	0.0799999982		
k MtrCtrlVirualResQax Ohm f32	0.00600000005		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k MtrVoltDaxIntegHiLim Volt f32	17.1720009		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017		
k MtrVoltQaxFiltFFEnable Cnt lgc	0		
k MtrVoltQaxIntegHiLim Volt f32	21.2935009		
k MtrVoltQaxIntegLoLim Volt f32	-4.57000017		
k MtrVoltVecuFiltEnable Cnt lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	10.3940001		
k VoltSatQaxPolyCoeff Uls f32	10.5640001		
k deadtimeVScale Uls f32	0.967999995		
t_CommOffsetTblX_Uls_u3p13[0]	6110		
t_CommOffsetTblX_Uls_u3p13[1]	7324		
t_CommOffsetTblY_Cnt_u16[0]	940		
t_CommOffsetTblY_Cnt_u16[1]	1216		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-161.352005		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1115		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	20.6149998		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1216	1216	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63438	63438 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-23.6469955	-23.6469955 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-23.0935135	-23.0935135 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-6.24554586	-6.24554586 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	62165	62165 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl DualEcuFailSclFac Uls M f32	0.0426499993	0.0426499993 ± 0.0625	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	-
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~





Test Step 2.57 (Repeat Count = 1)	√
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val) MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	220
MtrCtrl MtrDampTermDax Ohm M f32[0]	0.0170000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0410000011
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0700000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.199
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.528
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-96.7659988
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-485.93399
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.119000003
MtrCtrl_MtrImpedDax_Ohm_M_f32[1] MtrCtrl_MtrImpedOax_Ohm_M_f32[0]	0.0820000023
MtrCtrl_MtrImpedQax_Ohm_M_f32[0] MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0489999987 0.114
MtrCtrl MtrQaxIntegralGain Ohm M f32[0]	1.79999995
MtrCtrl MtrQaxIntegralGain Ohm M f32[1]	0.797999978
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	839.791992
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-829.577026
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-11.9399996
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	29.1739998
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-14.9390001
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-24.3929996
MtrCtrl_Vecu_Volt_M_f32[0]	14.2779999
MtrCtrl_Vecu_Volt_M_f32[1]	16.6380005
MtrCurrDaxPrevIntg_Volt_M_f32	1.27900004
MtrCurrDaxRef_Amp_M_f32[0]	-200.556
MtrCurrDaxRef_Amp_M_f32[1]	-98.4449997
MtrCurrQaxCog_Amp_M_f32 MtrCurrQaxPrevIntg Volt M f32	80.5459976 10.5852003
MtrCurrQaxRef_Amp_M_f32[0]	209.052002
MtrCurrQaxRef_Amp_M_f32[1]	-124.994003
MtrCurrQaxRpl Amp M f32	0
MtrPosComputationDelay Rad M f32[0]	0.544200003
MtrPosComputationDelay_Rad_M_f32[1]	-1.23020005
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.374000013
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0529999994
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.744000018
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.797999978
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.699000001
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	570.700012
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1118
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32 PICurrCntrl_MtrVecuFilt_M_str.TermD_LIIs_f32	36325.3984 0.287999988
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32 PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput UIs f32	570.700012
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1118
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	36325.3984
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.287999988
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6822.06006
k_DualEcuSignalSclFacSlew_UlspS_f32	76
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5157.0498
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.163000003
k_MtrCtrlVirualResQax_Ohm_f32	0.0439999998
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	6.49800014
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004 0
k MtrVoltOayFiltFFFnable Cnt loc	
k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32	
k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32	17.0617008 -25.6000004

PICurrCntrl_DualEcuFailSclFac_Uls_M_f32

PICurrCntrl_Per1

2016-09-15, 18:23:31+0530



Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	11.9960003		
k_VoltSatQaxPolyCoeff_Uls_f32	-19.5869999		
k_deadtimeVScale_Uls_f32	0.998000026		
t_CommOffsetTbIX_UIs_u3p13[0]	220		
t_CommOffsetTbIX_UIs_u3p13[1]	5037		
t_CommOffsetTblY_Cnt_u16[0]	980		
t_CommOffsetTblY_Cnt_u16[1]	1528		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-205.514999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1366		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	79.6729965		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1528	1528	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	65404	65404 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	128.506012	128.506012 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-12.2062798	-12.2062798 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-7.35210037	-7.35210037 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	49174	49174 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	~

0.0625

0.0625 ± 0.0625

Test Step 2.58 (Repeat Count = 1)		V
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	0	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	0	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0130000003	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0970000029	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0970000029	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0270000007	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.10599995	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.44500005	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	308.303009	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-313.46701	
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0270000007	
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0149999997	
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.119000003	
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0820000023	

© Report created by TESSY V3.1.13, report template V2.1

PICurrCntrl Per1

2016-09-15, 18:23:31+0530



Input Value MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 1.80400002 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] 0.768000007 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] -984.03302 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] -670.601013 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -12.816 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] -15.0170002 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] 5.68100023 $MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]$ 2.06599998 MtrCtrl_Vecu_Volt_M_f32[0] 20.2549992 MtrCtrl_Vecu_Volt_M_f32[1] 22 6149998 MtrCurrDaxPrevIntg_Volt_M_f32 -9.55599976 MtrCurrDaxRef_Amp_M_f32[0] 67 4899979 MtrCurrDaxRef_Amp_M_f32[1] 119.721001 MtrCurrQaxCog_Amp_M_f32 41 1769981 MtrCurrQaxPrevIntg_Volt_M_f32 13.0853004 MtrCurrQaxRef_Amp_M_f32[0] -200.556 MtrCurrQaxRef_Amp_M_f32[1] -98.4449997 MtrCurrQaxRpl_Amp_M_f32 $MtrPosComputationDelay_Rad_M_f32[0]$ 1.14110005 MtrPosComputationDelay_Rad_M_f32[1] -2.6644001 $PICurrCntrl_CurrSensFailSclFac_Uls_M_f32$ 0.444000006 PICurrCntrl_DualEcuFailSclFac_Uls_M_f32 0.0540000014 $PICurrCntrl_InverterFailSclFac_Uls_M_f32$ 0.815999985 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.891499996 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.0939999968 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -717.299988 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 10763 7002 PICurrCntrl MtrVecuFilt M str.TermD Uls f32 0.852599978 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32 0 PICurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32 -717.299988 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32 10763 7002 PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32 0.852599978 k CLOAFdbackSignalSclFacSlew UlspS f32 1549 32996 k_DualEcuSignalSclFacSlew_UlspS_f32 77.1999969 k ILOAFdbackSignalSclFacSlew_UlspS_f32 6542.3501 $k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc$ 0 k MtrCtrlFeedbackControlDisable_Cnt_lgc k_MtrCtrlVirualResDax_Ohm_f32 0.119000003 k MtrCtrlVirualResQax Ohm f32 0.0289999992 k_MtrCurrQaxRefModifDsb_Cnt_lgc k MtrCurrQaxRefModifRplEn Cnt lgc 0 $k_MtrVoltDaxIntegHiLim_Volt_f32$ 17.6065006 k_MtrVoltDaxIntegLoLim_Volt_f32 -10.5 $k_MtrVoltQaxFiltFFEnable_Cnt_lgc$ 0 k_MtrVoltQaxIntegHiLim_Volt_f32 14.5948 -10.5 k MtrVoltQaxIntegLoLim Volt f32 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 7 44799995 k_VoltSatQaxPolyCoeff_Uls_f32 0.351999998 k_deadtimeVScale_Uls_f32 0.971000016 t_CommOffsetTblX_Uls_u3p13[0] 573 t_CommOffsetTblX_Uls_u3p13[1] 7569 t_CommOffsetTblY_Cnt_u16[0] 556 t_CommOffsetTblY_Cnt_u16[1] 934 $target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr$ 1 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr 0 $target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val$ n target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -118.848 $target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr$ 3490 0.486999989 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val $target_MtrCntrl_Read_SysState_Cnt_Enum_Val$ Name **Actual Value Expected Value** Result MtrCntrl Write CommOffset Cnt u16(val) 934 934 63635 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 63635 + 1MtrCntrl Write MtrCurrQaxFinalRef Amp f32(val) -220 -220 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 13 2904978 13.2904987 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 14 4974937 14.4974947 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 19641 19641 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32 PICurrCntrl_DualEcuFailSclFac_Uls_M_f32 0.044350002 0.044350002 ± 0.0625



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	•
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.59 (Repeat Count = 1) Name	Input Value
FastDataAccessBufIndex Cnt M u16	1
MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc(ptr)	target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModidxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-115.696999
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-141.417007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.107000001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0109999999
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.093999968
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.980000019
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.497000009
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	550.754028
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-584.435974
/trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0939999968
/trCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0270000007
/trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0149999997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.45700002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.78799999
MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	966.106995
MtrCtrl MtrQaxPropotionalGain Ohm M f32[1]	858.828003
MtrCtrl MtrVoltDaxFF Volt M f32[0]	27.9379997
/trCtrl MtrVoltDaxFF Volt M f32[1]	8.50300026
/trCtrl MtrVoltQaxFF Volt M f32[0]	19.6130009
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-29.3180008
/trCtrl_Vecu_Volt_M_f32[0]	13.085
/trCtrl_Vecu_Volt_M_f32[1]	15.4449997
MtrCurrDaxPrevIntg Volt M f32	23.0559998
MtrCurrDaxRef Amp M f32[0]	37.4550018
MtrCurrDaxRef Amp M f32[1]	-2.84500003
MtrCurrQaxCog Amp M f32	-30.7789993
	13.2370005
MtrCurrQaxPrevIntg_Volt_M_f32	67.4899979
MtrCurrQaxRef_Amp_M_f32[0]	
MtrCurrQaxRef_Amp_M_f32[1]	119.721001
/trCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	2.70779991
MtrPosComputationDelay_Rad_M_f32[1]	-1.68729997
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.208000004
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0549999997
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.451000005
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.251300007
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.681999981

2016-09-15, 18:23:31+0530



PICurrCntrl_Per1

		•	
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-627.179993		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	68.5733032		
PICurrCntrl MtrVecuFilt M str.TermD Uls f32	0.957700014		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-627.179993		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32	1118		
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	68.5733032		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.957700014		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2384.97998		
k_DualEcuSignalSclFacSlew_UlspS_f32	78.4000015		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3626.42993		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.108999997		
k_MtrCtrlVirualResQax_Ohm_f32	0.108999997		
k MtrCurrQaxRefModifDsb Cnt lgc	1		
k_MtrCurrQaxRefModifRpIEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	25.8696003		
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	26.0412998		
k_MtrVoltQaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-12.5480003		
k_VoltSatQaxPolyCoeff_Uls_f32	-9.86100006		
k_deadtimeVScale_Uls_f32	0.970000029		
t_CommOffsetTblX_Uls_u3p13[0]	1154		
t_CommOffsetTblX_Uls_u3p13[1]	5284		
t_CommOffsetTblY_Cnt_u16[0]	39		
t_CommOffsetTblY_Cnt_u16[1]	93		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-72.4260025		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2326		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-190.440994		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	2326	2326	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	150.5	150.5 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	8.37593937	8.37593937 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-28.8799	-28.8799 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	12225	12225 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0648000017	0.0648000017 ± 0.0625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	-
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~



Test Step 2.60 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrChtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val 140.470001
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0] MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	93.5790024
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0120000001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0560000017
MtrCtrl MtrDampTermQax Ohm M f32[0]	0.0170000009
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0410000011
MtrCtrl MtrDaxIntegralGain Ohm M f32[0]	1.93900001
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.79499996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-876.190002
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	798.229004
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0170000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0410000011
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0769999996
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.029999993
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.930000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.875
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	737.640991
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-190.210999
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-9.61999989
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-0.206
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	28.1019993
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	0.381000012
MtrCtrl_Vecu_Volt_M_f32[0]	25.4869995
MtrCtrl_Vecu_Volt_M_f32[1]	27.8470001
MtrCurrDaxPrevIntg_Volt_M_f32	15.3000002
MtrCurrDaxRef_Amp_M_f32[0]	94.3150024
MtrCurrDaxRef_Amp_M_f32[1]	37.4959984
MtrCurrQaxCog_Amp_M_f32	-34.6189995
MtrCurrQaxPrevIntg_Volt_M_f32	1.81389999
MtrCurrQaxRef_Amp_M_f32[0]	37.4550018
MtrCurrQaxRef_Amp_M_f32[1]	-2.84500003
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0] MtrPosComputationDelay Rad M f32[1]	0.674700022 -2.42210007
PICurrCntrl CurrSensFailSclFac Uls M f32	0.442000002
PICurrCntrl DualEcuFailSclFac Uls M f32	0.0560000017
PICurrCntrl InverterFailSclFac UIs M f32	0.184
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.864700019
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.991999984
PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32	-43.1699982
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	-657.130005
PICurrCntrl MtrVecuFilt M str.TermN Uls f32	50.7543983
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.536199987
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput Uls f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-657.130005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	50.7543983
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.536199987
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2035.48999
k_DualEcuSignalSclFacSlew_UlspS_f32	79.5999985
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5391.29004
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.133000001
k_MtrCtrlVirualResQax_Ohm_f32	0.159999996
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	19.5212002
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.1000004
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	26.7397003
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008
k MtrVoltVecuFiltEnable Cnt lgc	1

PICurrCntrl_Per1



Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	-13.3400002		
k_VoltSatQaxPolyCoeff_Uls_f32	12.0819998		
k_deadtimeVScale_Uls_f32	0.990999997		
t_CommOffsetTblX_Uls_u3p13[0]	5022		
t_CommOffsetTblX_Uls_u3p13[1]	7003		
t_CommOffsetTblY_Cnt_u16[0]	1896		
t_CommOffsetTblY_Cnt_u16[1]	1952		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	83.9489975		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2340		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	83.9489975		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	2340	2340	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	72.0740051	72.0740051 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-4.70664167	-4.70664263 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-1.54904592	-1.54904521 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	52873	52873 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	✓
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0460500009	0.0460500009 ± 0.0625	✓

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	-
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	_

Test Step 2.61 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	106.072998
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-112.455002
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.093999968
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0879999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0130000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0970000029
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.07099998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.27900004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	650.622009
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	557.583984
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0130000003
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0970000029

2016-09-15, 18:23:31+0530





Name	Input Value		
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.112999998		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.125		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.79900002		
MtrCtrl MtrQaxIntegralGain Ohm M f32[1]	0.624000013		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-125.525002		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	839.142029		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-12.2150002		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	0.874000013		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	4.64300013		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-11.7069998		
MtrCtrl_Vecu_Volt_M_f32[0]	16.8080006		
MtrCtrl_Vecu_Volt_M_f32[1]	19.1679993		
MtrCurrDaxPrevIntg_Volt_M_f32	-18.566		
MtrCurrDaxRef_Amp_M_f32[0]	212.455994		
MtrCurrDaxRef_Amp_M_f32[1]	89.8619995		
MtrCurrQaxCog_Amp_M_f32	177.046997		
MtrCurrQaxPrevIntg_Volt_M_f32	27.2450008		
MtrCurrQaxRef_Amp_M_f32[0]	94.3150024		
MtrCurrQaxRef Amp M f32[1]	37.4959984		
MtrCurrQaxRpl Amp M f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	0.0817999989		
MtrPosComputationDelay_Rad_M_f32[1]	-2.86159992		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.375999987		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.057		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0529999994		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.636099994		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.49000001		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	33.7361984		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.666299999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	33.7361984		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.666299999		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	1046.93005		
k_DualEcuSignalSclFacSlew_UlspS_f32	80.8000031		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	1066.56006		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.063000001		
k_MtrCtrlVirualResQax_Ohm_f32	0.119000003		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	13.9659004		
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.1999998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	29.7127991		
k MtrVoltQaxIntegLoLim Volt f32	-9.64999962		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-0.66900003		
k_VoltSatQaxPolyCoeff_Uls_f32	13.8260002		
k_deadtimeVScale_UIs_f32	0.959999979		
t_CommOffsetTblX_Uls_u3p13[0]	4611		
t_CommOffsetTblX_Uls_u3p13[1]	5579		
t_CommOffsetTblY_Cnt_u16[0]	730		
t_CommOffsetTblY_Cnt_u16[1]	1388		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-144.667007		
target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	3783		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-144.667007		
	2		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val			
Name	Actual Value	Expected Value	Result
	3783	3783	✓
MtrCntrl_Write_CommOffset_Cnt_u16(val)	3763		
	0	0 ± 1	~
MtrCntrl_Write_CommOffset_Cnt_u16(val)		0 ± 1 -139.550995 ± 7.81E-03	
MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0		~
MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_Modldx_Uls_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	0 -139.550995	-139.550995 ± 7.81E-03	
MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_ModIdx_Uls_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0 -139.550995 0.357355237	-139.550995 ± 7.81E-03 0.357355237 ± 4.88E-04	~

PICurrCntrl_Per1



Name	Actual Value	Expected Value	Result
PICurrCntrl DualEcuFailSclFac Uls M f32	0.0671000034	0.0671000034 ± 0.0625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	-
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
/trCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc(ptr)	target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr	
/trCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr	
//trCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt Igc Val	
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
/trCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val	
ItrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val	
ItrCntrl Read SysState Cnt Enum(Val)	target MtrCntrl Read SysState Cnt Enum Val	
htrCtrl MtrCurrDaxMaxVal Amp M f32[0]	-91.4420013	
/trCtrl MtrCurrDaxMaxVal Amp M f32[1]	133.692993	
ItrCtrl MtrDampTermDax Ohm M f32[0]	0.0700000003	
htrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0130000003	
htrCtrl MtrDampTermQax Ohm M f32[0]	0.107000001	
htrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0109999999	
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.75300002	
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.112999998	
MtrCtrl MtrDaxPropotionalGain Ohm M f32[0]	524.809998	
htrCtrl MtrDaxPropotionalGain Ohm M f32[1]	-986.283997	
trCtrl MtrImpedDax Ohm M f32[0]	0.107000001	
trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0109999999	
MtrCtrl MtrImpedQax Ohm M f32[0]	0.0850000009	
/trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.112999998	
/trCtrl MtrQaxIntegralGain Ohm M f32[0]	1.02900004	
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.546000004	
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	613.835999	
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	556.35498	
trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	22.1809998	
htrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-11.0150003	
htrCtrl MtrVoltQaxFF Volt M f32[0]	25.9820004	
MtrCtrl MtrVoltQaxFF Volt M f32[1]	-23.0480003	
MtrCtrl Vecu Volt M f32[0]	5.56799984	
/trCtrl_Vecu_Volt_M_f32[1]	7.92799997	
/trCurrDaxPrevIntg Volt M f32	-15.7600002	
/trCurrDaxRef Amp M f32[0]	-108.124001	
/trCurrDaxRef Amp M f32[1]	178.639008	
/trCurrQaxCog_Amp_M_f32	-9.31999969	
/trCurrQaxPrevIntg_Volt_M_f32	11.2662001	
htrCurrQaxRef Amp M f32[0]	212.455994	
/trCurrQaxRef_Amp_M_f32[1]	89.8619995	
/trCurrQaxRpl_Amp_M_f32	0	
MtrPosComputationDelay Rad M f32[0]	-0.130500004	
MtrPosComputationDelay Rad M f32[1]	-2.73749995	
PICurrCntrl CurrSensFailSclFac Uls M f32	0.37999995	

PICurrCntrl Per1

2016-09-15, 18:23:31+0530



Input Value PICurrCntrl DualEcuFailSclFac Uls M f32 0.0579999983 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.151999995 PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32 0.621800005 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.887000024 PICurrCntrl MtrVecuFilt_M_str.PrevInput_Uls_f32 -717.299988 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 386.220001 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 65.2260971 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.8046 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32 -717.299988 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32$ 386 220001 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 65.2260971 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32$ 0.8046 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 6758.08008 k_DualEcuSignalSclFacSlew_UlspS_f32 82 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 7037.7002 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 $k_MtrCtrlFeedbackControlDisable_Cnt_lgc$ k_MtrCtrlVirualResDax_Ohm_f32 0.0520000011 k_MtrCtrlVirualResQax_Ohm_f32 0.0529999994 k_MtrCurrQaxRefModifDsb_Cnt_lgc $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ Λ 18.4785004 k_MtrVoltDaxIntegHiLim_Volt_f32 k_MtrVoltDaxIntegLoLim_Volt_f32 -11.3000002 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 18.1450005 k_MtrVoltQaxIntegLoLim_Volt_f32 -22.4099998 k_MtrVoltVecuFiltEnable_Cnt_lgc $k_VoltSatDaxPolyCoeff_Uls_f32$ -22.6620007 k VoltSatQaxPolyCoeff Uls f32 -24.7110004 k_deadtimeVScale_Uls_f32 0.986000001 t CommOffsetTblX Uls u3p13[0] 459 t_CommOffsetTblX_Uls_u3p13[1] 5775 t CommOffsetTblY Cnt u16[0] 24 t_CommOffsetTblY_Cnt_u16[1] 47 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 $target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val$ $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 80.8180008 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 4523 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -126.640999 target_MtrCntrl_Read_SysState_Cnt_Enum_Val Actual Value **Expected Value** Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 4523 4523 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 0 0 ± 1 220 ± 7.81E-03 $MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)$ 220 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 19.8459988 19.8459988 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 23 2468662 23 2468662 + 4 88F-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 6009 ± 1.52588E-05 6009 -11 3000002 -11 3000002 $MtrCurrDaxPrevIntg_Volt_M_f32$

0.0477499962

0.0477499962 ± 0.0625

PICurrCntrl_DualEcuFailSclFac_Uls_M_f32



Test Step Call Trace	est Step Call Trace				
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	-	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	•	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	-	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~	
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~	

Name	Input Value	
-astDataAccessBufIndex_Cnt_M_u16	0	
/trCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	
ftrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	
ItrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
ItrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr	
ftrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
htrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-91.4420013	
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	133.692993	
MtrCtrl MtrDampTermDax Ohm M f32[0]	0.0970000029	
/trCtrl MtrDampTermDax Ohm M f32[1]	0.0270000007	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0120000001	
/trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0560000017	
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.412	
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.523	
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-834.685974	
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-788.218994	
htrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0120000001	
/trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0560000017	
/trCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994	
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009	
/trCtrl MtrQaxIntegralGain Ohm M f32[0]	0.0850000009	
/trCtrl MtrQaxIntegralGain Ohm M f32[1]	1.50899994	
MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	881.109009	
MtrCtrl MtrQaxPropotionalGain Ohm M f32[1]	1005.21997	
MtrCtrl MtrVoltDaxFF Volt M f32[0]	-23.6089993	
/trCtrl MtrVoltDaxFF Volt M f32[1]	-14.04	
/trCtrl MtrVoltQaxFF Volt M f32[0]	14.9390001	
/trCtrl MtrVoltQaxFF Volt M f32[1]	-29.4060001	
/trCtrl Vecu Volt M f32[0]	17.9899998	
/trCtrl_Vecu_Volt_M_f32[1]	20.3500004	
MtrCurrDaxPrevIntg Volt M f32	-14.0459995	
/trCurrDaxRef_Amp_M_f32[0]	-76.8769989	
MtrCurrDaxRef Amp M f32[1]	-153.238998	
/trCurrQaxCog_Amp_M_f32	-161.352005	
MtrCurrQaxPrevIntg Volt M f32	29.0646	
/trCurrQaxRef_Amp_M_f32[0]	-108.124001	
/trCurrQaxRef_Amp_M_f32[1]	178.639008	
/trCurrQaxRpl_Amp_M_f32	0	
htrPosComputationDelay_Rad_M_f32[0]	0.452699989	
/trPosComputationDelay_Rad_M_f32[1]	1.22019994	
PlCurrCntrl CurrSensFailSclFac Uls M f32	0.635999978	
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0590000004	
PICurrCntrl InverterFailSclFac Uls M f32	0.791999996	

PICurrCntrl_Per1

2016-09-15, 18:23:31+0530



Name	Input Value		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.847599983		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.493999988		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-784.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	25.7999992		
PICurrCntrl MtrVecuFilt M str.TermD Uls f32	0.400299996		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-784.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	25.7999992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.400299996		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3945.78003		
k_DualEcuSignalSclFacSlew_UlspS_f32	83.1999969		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	7691.68994		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.172999993		
k_MtrCtrlVirualResQax_Ohm_f32	0.0810000002		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	15.3655996		
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.3999996		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	0.514699996		
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-10.2639999		
k_VoltSatQaxPolyCoeff_Uls_f32	11.283		
k_deadtimeVScale_Uls_f32	0.968999982		
t_CommOffsetTbIX_Uls_u3p13[0]	2638		
t_CommOffsetTbIX_Uls_u3p13[1]	3628		
t_CommOffsetTblY_Cnt_u16[0]	310		
t_CommOffsetTblY_Cnt_u16[1]	1418		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-44.2579994		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	129		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	121.994003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1418	1418	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63504	63504 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	53.2280045	53.2280045 ± 7.81E-03	-
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-16.8014221	-16.8014221 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.64732885	-4.64732885 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	51059	51059 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	✓
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0693999976	0.0693999976 ± 0.0625	~

rest step out Trace	est Step Call Trace			
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	-
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	-
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	-
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	-
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	-
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	-
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	-
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	-
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	-
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	-
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	-
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	-
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	-
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	-



Test Step 2.64 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val) MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	target_MtrCntrl_Read_SysState_Cnt_Enum_Val 171.485992
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	163.787003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.093999968
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0939999968
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.087999995
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.30700004
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.85500002
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	620.015015
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	715.487
MtrCtrl_MtrlmpedDax_Ohm_M_f32[0]	0.093999968
MtrCtrl_MtrImpedOax_Ohm_M_f32[1]	0.0879999995
MtrCtrl_MtrImpedQax_Ohm_M_f32[0] MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.098999995 0.0170000009
MtrCtrl_MtrImpeoQax_Onm_M_r32[1] MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.987999976
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.42899999
MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	811.825012
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-796.757996
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-21.1959991
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-4.78100014
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-17.6959991
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-27.8540001
MtrCtrl_Vecu_Volt_M_f32[0]	26.6809998
MtrCtrl_Vecu_Volt_M_f32[1]	29.0410004
MtrCurrDaxPrevIntg_Volt_M_f32	9.64799976
MtrCurrDaxRef_Amp_M_f32[0]	191.369003
MtrCurrDaxRef_Amp_M_f32[1]	107.137001
MtrCurrQaxCog_Amp_M_f32 MtrCurrQaxPrevIntg_Volt_M_f32	-205.514999 2.89910007
MtrCurrQaxRef Amp M f32[0]	-76.8769989
MtrCurrQaxRef Amp M f32[1]	-153.238998
MtrCurrQaxRpl Amp M f32	0
MtrPosComputationDelay_Rad_M_f32[0]	0.906000018
MtrPosComputationDelay_Rad_M_f32[1]	1.87189996
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.713
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0599999987
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.726000011
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.289799988
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.129999995
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-38.7999992 047.73000
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_UIs_f32 PICurrCntrl_MtrVecuFilt_M_str.TermN_LIIs_f32	947.73999 70.1131973
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32 PICurrCntrl MtrVecuFilt M str.TermD UIs f32	0.631200016
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput Uls f32	-38.7999992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	947.73999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	70.1131973
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.631200016
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	1611.48999
k_DualEcuSignalSclFacSlew_UlspS_f32	84.4000015
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5394.18018
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.0710000023
k_MtrCtrlVirualResQax_Ohm_f32	0.101000004
k_MtrCurrQaxRefModifDsb_Cnt_lgc k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k MtrVoltDaxIntegHiLim Volt f32	2.98559999
k MtrVoltDaxIntegLoLim Volt f32	-11.6000004
k MtrVoltQaxFiltFFEnable Cnt Igc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	16.9648991
_	
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017

PICurrCntrl_Per1



Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	-22.1690006		
k_VoltSatQaxPolyCoeff_Uls_f32	-5.51499987		
k_deadtimeVScale_Uls_f32	0.995000005		
t_CommOffsetTblX_Uls_u3p13[0]	1212		
t_CommOffsetTblX_Uls_u3p13[1]	1704		
t_CommOffsetTblY_Cnt_u16[0]	1257		
t_CommOffsetTblY_Cnt_u16[1]	1842		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-40.9220009		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	662		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-41.5750008		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1842	1842	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	65208	65208 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	52.276001	52.276001 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	16.3759613	16.3759613 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-23.8074532	-23.8074532 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	46008	46008 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0494499989	0.0494499989 ± 0.0625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	~

Test Step 2.65 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	106.072998
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-112.455002
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0170000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0410000011
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0649999976
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.100000001
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.526000023
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.53100002
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	555.133972
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	919.028015
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0560000017
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.0130000003





Nome	Innut Value		
Name MtrCtrl MtrOovlatogralCain Ohm M #23[0]	Input Value 0.662		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] MtrCtrl MtrQaxIntegralGain Ohm M f32[1]	1.31599998		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-876.906982		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-215.744003		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-22.7129993		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-20.4500008		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-11.9399996		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	29.1739998		
MtrCtrl_Vecu_Volt_M_f32[0]	16.882		
MtrCtrl_Vecu_Volt_M_f32[1]	19.2420006		
MtrCurrDaxPrevIntg_Volt_M_f32	6.12300014		
MtrCurrDaxRef_Amp_M_f32[0]	-147.343002		
MtrCurrDaxRef_Amp_M_f32[1]	127.972		
MtrCurrQaxCog_Amp_M_f32	-118.848		
MtrCurrQaxPrevIntg_Volt_M_f32	24.7549992		
MtrCurrQaxRef_Amp_M_f32[0]	191.369003		
MtrCurrQaxRef_Amp_M_f32[1]	0 107.137001		
MtrCurrQaxRpl_Amp_M_f32 MtrPosComputationDelay Rad M f32[0]	2.77749991		
MtrPosComputationDelay_Rad_M_f32[1]	2.20070004		
PICurrCntrl CurrSensFailSclFac Uls M f32	0.47299999		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0610000007		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0089999961		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0659999996		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.665000021		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-657.099976		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	269.399994		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	8.62930012		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.434899986		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-657.099976		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	269.399994		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	8.62930012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.434899986		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2358.21997		
k_DualEcuSignalSclFacSlew_UlspS_f32	85.5999985 5388.91992		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.0729999989		
k_MtrCtrlVirualResQax_Ohm_f32	0.023		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	8.81120014		
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	25.0259991		
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	22.6620007		
k_VoltSatQaxPolyCoeff_Uls_f32	12.3109999		
k_deadtimeVScale_Uls_f32 t CommOffsetTblX Uls u3p13[0]	0.985000014 3808		
t_CommOffsetTblX_Uls_u3p13[1]	7298		
t_CommOffsetTbIY_Cnt_u16[0]	181		
t CommOffsetTblY Cnt u16[1]	812		
target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	75.0830002		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	917		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	48.8400002		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	917	917	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	1.70572281	1.70572269 ± 4.88E-04	V
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-30.487318	-30.4873199 ± 4.88E-04	✓
MtrCutrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	61155 0	61155 ± 1.52588E-05	*
MtrCurrDaxPrevIntg_Volt_M_f32 PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0716999993	0.0716999993 ± 0.0625	
	5.51 .000000	5.5. 10000000 ± 0.0020	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

lame	Input Value	
	0	
FastDataAccessBufIndex_Cnt_M_u16		
/trCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	
/trCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	
/trCntrl_Read_ModidxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
/trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
/trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
ftrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	24.6130009	
ItrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-20.9400005	
htrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0130000003	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0970000029	
htrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0549999997	
htrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0979999974	
ftrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.44099998	
ltrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.26900005	
ltrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-818.776001	
ltrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-274.428986	
ltrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009	
ltrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998	
ftrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0359999985	
ltrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.075000003	
ltrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.261000007	
htrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.976000011	
ltrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-737.580994	
ItrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	408.726013	
ltrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	18.2380009	
ItrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	27.3910007	
ItrCtrl MtrVoltQaxFF Volt M f32[0]	-12.816	
ItrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-15.0170002	
ItrCtrl_Vecu_Volt_M_f32[0]	5.70200014	
ItrCtrl Vecu Volt M f32[1]	8.06200027	
ItrCurrDaxPrevIntg_Volt_M_f32	15.0279999	
ItrCurrDaxRef Amp M f32[0]	6.18900013	
ItrCurrDaxRef Amp M f32[1]	83.0540009	
ItrCurrQaxCog_Amp_M_f32	-220	
ItrCurrQaxPrevIntg Volt M f32	1.02610004	
trCurrQaxRef Amp M f32[0]	-147.343002	
trCurrQaxRef Amp M f32[1]	127.972	
trCurrQaxRpl Amp M f32	0	
ItrPosComputationDelay_Rad_M_f32[0]	2.83299994	
trPosComputationDelay Rad M f32[1]	0.72420001	
CurrCntrl CurrSensFailSclFac Uls M f32	0.657000005	
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.061999999	

PICurrCntrl Per1

2016-09-15, 18:23:31+0530



Input Value PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.432999998 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.185100004 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.515999973 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32 570.700012 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -340.130005 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 12.6120005 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.790099978 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32$ 570.700012 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 -340.130005 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 12 6120005 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.790099978 k CLOAFdbackSignalSclFacSlew UlspS f32 3275 26001 k_DualEcuSignalSclFacSlew_UlspS_f32 86.8000031 k ILOAFdbackSignalSclFacSlew UlspS f32 2711 1499 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc k_MtrCtrlFeedbackControlDisable_Cnt_lgc k_MtrCtrlVirualResDax_Ohm_f32 0.165000007 k_MtrCtrlVirualResQax_Ohm_f32 0.20000003 $k_MtrCurrQaxRefModifDsb_Cnt_lgc$ 0 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 25.7908001 $k_MtrVoltDaxIntegHiLim_Volt_f32$ k_MtrVoltDaxIntegLoLim_Volt_f32 -10.5 $k_MtrVoltQaxFiltFFEnable_Cnt_lgc$ Λ k_MtrVoltQaxIntegHiLim_Volt_f32 19.1938992 k_MtrVoltQaxIntegLoLim_Volt_f32 -10.5 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 19.0259991 k_VoltSatQaxPolyCoeff_Uls_f32 1.51499999 k deadtimeVScale Uls f32 1 t_CommOffsetTblX_Uls_u3p13[0] 918 t_CommOffsetTblX_Uls_u3p13[1] 1679 t_CommOffsetTbIY_Cnt_u16[0] 174 t CommOffsetTblY Cnt u16[1] 589 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr 1 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val 0 $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 74.0660019 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 3897 107.702003 target MtrCntrl Read MtrCurrQax Amp f32 Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val Actual Value Name **Expected Value** Result MtrCntrl Write CommOffset Cnt u16(val) 589 589 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 65536 65536 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 72.6569977 72.6569977 ± 7.81E-03 4.84246635 ± 4.88E-04 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 4.84246731 -3.01053452 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -3.010535 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 51735 51735 ± 1.52588E-05

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

0.0511499979

0.0511499979 ± 0.0625

MtrCurrDaxPrevIntg_Volt_M_f32 PICurrCntrl_DualEcuFailSclFac_Uls_M_f32





Test Step 2.67 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val) MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-166.035004 183.065002
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.039000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.104000002
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.063000001
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.165999994
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.68499994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	41.1699982
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	456.949005
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.079999982
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.57700002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.83099997
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-771.507996 920.502991
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-17.2689991
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	15.2200003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	27.9379997
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	8.50300026
MtrCtrl_Vecu_Volt_M_f32[0]	28.3600006
MtrCtrl_Vecu_Volt_M_f32[1]	30.7199993
MtrCurrDaxPrevIntg_Volt_M_f32	-1.19400001
MtrCurrDaxRef_Amp_M_f32[0]	-105.246002
MtrCurrDaxRef_Amp_M_f32[1]	41.6290016
MtrCurrQaxCog_Amp_M_f32	220
MtrCurrQaxPrevIntg_Volt_M_f32	11.6451998
MtrCurrQaxRef_Amp_M_f32[0]	6.18900013
MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRpl Amp M f32	83.0540009
MtrPosComputationDelay Rad M f32[0]	0.90079999
MtrPosComputationDelay_Rad_M_f32[1]	2.43770003
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.93099994
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.063000001
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.893000007
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.76819998
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.670000017
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	0
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-784.130005
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	42.0777016
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.450700015
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	0 794 12000E
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-784.130005 42.0777016
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32	42.0777016 0.450700015
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	220.787994
k DualEcuSignalSclFacSlew UlspS f32	88
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5103.45996
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.0370000005
k_MtrCtrlVirualResQax_Ohm_f32	0.114
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1.
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	2.48790002
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.6000004
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1 18 0482002
k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32	18.9482002 -11.600004
k MtrVoltVecuFiltEnable Cnt lgc	1
K_IMIL VOIL VOOD IIILEHADIC_OTIL_IGO	<u>'</u>

PICurrCntrl_Per1



Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	7.15899992		
k_VoltSatQaxPolyCoeff_Uls_f32	6.94099998		
k_deadtimeVScale_Uls_f32	0.968999982		
t_CommOffsetTblX_Uls_u3p13[0]	1532		
t_CommOffsetTblX_Uls_u3p13[1]	2851		
t_CommOffsetTblY_Cnt_u16[0]	158		
t_CommOffsetTblY_Cnt_u16[1]	544		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-149.003006		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4983		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	5.72399998		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4983	4983	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-213.811005	-213.811005 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0.16656135	0.166561365 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.84213591	4.84213591 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	9754	9754 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.074000001	0.074000001 ± 0.0625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	~

Test Step 2.68 (Repeat Count = 1)		V
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-216.921997	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-184.923996	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0160000008	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.123000003	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.112999998	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.125	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.598999977	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.00399995	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	324.985992	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-932.651978	
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0989999995	
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0170000009	

PICurrCntrl_Per1



Name	Picurichun_Peri			i Citat
Marci J. Minosariega Com. M. (2011)	Name	Input Value		
Michael Mich	MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.075000003		
Micro Lab Coart Programme (1998)	MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0710000023		
MICHAEL PROMISSION MICHAEL	MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.528999984		
Michal (Michael Michael Michae	MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.03900003		
Micro Micr	MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	139.416		
MICOL MINISTRES P. VIII. 1920 MICOL MINISTRES P. VIII. 1921 MICOL MINISTRES P. VIII. 1922 MICOL MINISTRES P. VIII. 1923 MICOL MINISTRES P. VIII. 1924 MICOL MINISTRES P. VIII. 1924		-482.338013		
MICOL MYNOTED-FF Vol. M. (201) MICOL WATER (1904) MICOL WATER (1904)		-4.91699982		
MICHEL MANIGRAFY VML M. (2011) MICH VEW VML M. (3021) MICH VML VML M. (3				
MICH (MICH) (MIC				
Microl Vest Vest M. 1920				
Michil Pure Very Very M. D. (1)				
MR.Curchare-Private Mr. 1209				
MRCURDARRE Amp. M. 120 21.029993 MRCURDARRE Amp. M. 20 MRCURD				
Microard Ref. Purp. M. 1921				
Microritans/Profile Vest M. 182				
Michard Carlo May 1, 192 19.0 2.000.06 1				
Michaeling Amerikan M. 1920 100				
Microanseted Amery M. 1920				
Microaripal Amp M, 192				
MPPGCOMPURIDATION M. 1921				
MPPOROMONIA DISPORT M. M. 192 0.987999992	MtrCurrQaxRpl_Amp_M_f32			
Picturnorth_LowSenis-BillsGrain_Uis_M_152				
Picumorial DualeCarlaiSidaria Ulis M. 192 0.94900003	MtrPosComputationDelay_Rad_M_f32[1]	2.15840006		
Picturnord Inverter FailSelface Ute M, 192 0.95789988	PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.907999992		
Picurroit Mircurroassaffunkanio Lis M. 152 0 957899888 Picurroit Mircursossaffunkanio Lis M. 152 0 731899993 0 Picurroit Mircuriassaffunkanio Lis M. 152 0 731899993 0 Picurroit Mircuria M. 5th Previnput Uis J. 152 42 2845011 0 Picurroit Mircuria M. 5th Previnput Uis J. 152 42 2845011 0 Picurroit Mircuria M. 15 15 15 15 15 15 15 15 15 15 15 15 15	PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.064000003		
Picurciant Mircurciansataliuxianic Lis, M, 182 0 71999993	PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.222000003		
Picurcott Mirvesufit M. str Previoud Uis, 132 386,22001	PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.957899988		
Picturiont, MinVecuriii M, str. Previouput, Uis. 522 38.8220001	PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.731999993		
PiCurrContrt_MitVecuFill* M_str*TermD_Uis_102	PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	0		
PiCurrCntt, MMYecuFit, M, str TermD, Uis, 132	PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	386.220001		
PiCurrCort MrVorQasFFFIL M_str. FermD_Uls_182 0 870100021		42.2845001		
PiCurrCntt_Mrt/vilCaxFFFit_M_str Previput_Uis_132				
PiCurrCntrl_MrVolfQaxFFFill_M_str_PrevOutput_Uis_132 388.20001 PiCurrCntrl_MrVolfQaxFFFill_M_str_Ferm_D_uis_132 0.870100021				
PiCurrinti, MrtVoliCaxFFFit, M, shr. TermN, Uls. [32]				
PICUMENTIA MITAVORIDASE/FIEI, M. Jat. TermD, Ulas 132				
K. CLA AFchackSignal SciFacSlew UlspS_132 89.1999898 K. Dual EcuSignal SciFacSlew UlspS_132 89.1999898 K. LOAF drack Signal SciFacSlew UlspS_132 4901.47021 K. MitCill Curt. Loop Sec. Of Tran FeEnable, Cnt. Lgo 0 C. K. MitCirt Virual ResDax, Ohm_132 0.0680000037 K. MitCirt Virual ResDax, Ohm_132 0.029999993 K. MitCirt Virual ResDax Vir				
R. DualEuSignalSciFacSlew_UlspS_132				
K_UROAFdbackSignalSciFacSlew_UlspS_f32				
K_MICCHICurrLoopSecOrTranFcEnable_Cnt_lgc				
K_MrCtriFeedbackControlDisable_Cnt_lgc				
K_MrCtrVirualResDax_Ohm_f32				
k, MrCtrlvirualResQax_Ohm_f32 0.0299999993 k, MrCurraxRefModiffble_Cht_lgc 0 k, MrCurraxRefModiffble_Cht_lgc 0 k, MrtvOttDaxIntegHtLim_Volt_f32 12.7781 k, MrtvOttDaxIntegHtLim_Volt_f32 -30.2000008 k, MrtvOttQaxIntegHtLim_Volt_f32 -30.2000008 k, MrtvOttQaxIntegHtLim_Volt_f32 30.2348995 k, MrtvOttQaxIntegHtLim_Volt_f32 -30.2000008 k, MrtvOttQaxIntegHtLim_Volt_f32 -30.2000008 k, MrtvOttQaxIntegHtLim_Volt_f32 -30.20000008 k, MrtvOttQaxIntegHtLim_Volt_f32 -30.20000000 k, WrtvOttQaxIntegHtLim_Volt_f32 -40.20000000 k, MrtvOttQaxIntegHtLim_Volt_f32 -120.7000000000000000000000000				
k_MrCurrQaxRefModiffDsb_Cnt_lgc 0 k_MrCurrQaxRefModiffDsb_Cnt_lgc 0 k_MrVofbaxIntegLbiLim_Volt_f32 12.7781 k_MrVofbaxIntegLoLim_Volt_f32 -30.2000008 k_MrVofbCaxIntegLoLim_Volt_f32 -30.2000008 k_MrVofbCaxIntegLoLim_Volt_f32 30.2348995 k_MrVofbCaxIntegLoLim_Volt_f32 -30.2000008 k_MrVofbCavEnd_Lim_Volt_f32 -30.2000008 k_MrVofbCevEnd_Lim_S12 13.868 k_VolfsatCaxPolyCoeff_Uis_f32 24.8209991 k_deadtimeVScale_Uis_f32 0.996999999999 k_deadtimeVScale_Uis_f32 0.99699999999 L_CommOffsetTbX_Uis_usp13[0] 4162 L_CommOffsetTbY_Cnt_u16[0] 565 L_CommOffsetTbY_Cnt_u16[0] 1207 L_CommOffsetTbY_Cnt_u16[1] 1207 target_MrCntrl_Read_UrlLoaMtgInE_Cnt_lgc_ptr 0 target_MrCntrl_Read_ModidxSriComSvcDft_Cnt_lgc_ptr 1 target_MrCntrl_Read_ModifxCurrDax_Amp_f32_Val -124.758003 target_MrCntrl_Read_MrCurrDax_Amp_f32_Val -124.758003 target_MrCntrl_Read_MrCurrOax_Amp_f32_Val 59.304009 target_MrCntrl_Read_MrcurrQax_Amp_f32_Val	-			
k_MtrCurrQaxRefModifRpIEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLm_Volt_f32 12.7781 k_MtrVoltDaxIntegHiLm_Volt_f32 30.2000008 k_MtrVoltQaxIntegHiLm_Volt_f32 30.2048995 k_MtrVoltQaxIntegHiLm_Volt_f32 30.2000008 k_MtrVoltQaxIntegHiLm_Volt_f32 30.2000008 k_MtrVoltQaxIntegHiLm_Volt_f32 30.2000008 k_VoltSatDaxPolyCoeff_Uls_f32 13.868 k_VoltSatDaxPolyCoeff_Uls_f32 24.8209991 k_deatimeVScale_ulls_f32 0.996999979 k_deatimeVScale_ulls_f32 0.996999979 k_CommOffsetTblX_Uls_u3p13[0] 4162 t_CommOffsetTblY_Cnt_u16[0] 565 t_CommOffsetTblY_Cnt_u16[1] 1207 t_arget_MtrCntrl_Read_Dutl_coaltiginEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_Dutl_coaltiginEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModicurrLoaktiginEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_val -124.758003 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_val -124.758003 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_val -124.758003 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_val 9.9040009				
K_MtrVoltDaxIntegHiLim_Volt_f32				
k_MtrVoliDaxIntegLoLim_Volt_f32 -30.2000008 k_MtrVoliQaxFiltFERable_Cnt_Igc 0 k_MtrVoliDaxIntegLoLim_Volt_f32 30.248995 k_MtrVoliDaxIntegLoLim_Volt_f32 -30.2000008 k_VoltSaCaxIntegLoLim_Volt_f32 -30.2000008 k_VoltSaCaxPolyCofer_Uls_f32 13.868 k_VoltSaCaxPolyCofer_Uls_f32 24.8209991 k_CommOffsetTblX_Uls_u3p13(0) -4162 L_CommOffsetTblX_Uls_u3p13(1) 8053 t_CommOffsetTblY_Cnt_u16(0) 565 t_CommOffsetTblY_Cnt_u16(1) 1207 target_MtrCntrl_Read_UnalEcuMotCtrIMIgnEna_Cnt_Igc_ptr 1 target_MtrCntrl_Read_MtrCurrLoaMIgnEn_Cnt_Igc_ptr 0 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -124.758003 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -124.758003 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.3040009 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.3040009 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.3040009 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.3040009 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.3040009 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val				
MttVollQaxIntegritim_Volt_f32		12.7781		
k_Mtr/ollQaxIntegHiLim_Volt_f32	k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltQaxIntegLoLim_Volt_f32 -30.2000008 k_MtrVoltVecuFiltEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 13.868 k_VoltSatCaxPolyCoeff_Uls_f32 24.8209991 k_CommOffsetTblX_Uls_u3p13[0] 4162 t_CommOffsetTblY_Cnt_u16[0] 565 t_CommOffsetTblY_Cnt_u16[1] 1207 target_MtrCntrl_Read_IntrLoaMtigtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_MoldxSriComSvcDft_Cnt_lgc_Val 0 target_MtrCntrl_Read_MoldvCurrLoaMtigtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MicCurrLoaMtigtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MicCurrCoax_Amp_f32_Val -124.758003 target_MtrCntrl_Read_MtrCurrOax_Amp_f32_Val 59.304009 target_MtrCntrl_Read_MtrCurrOax_Amp_f32_Val 59.304009 target_MtrCntrl_Read_MtrCurrOax_Amp_f32_Val 59.304009 target_MtrCntrl_Read_MtrCurrOax_Amp_f32_Val 59.304009 target_MtrCntrl_Read_SysState_Cnt_unl6_ptr 30.29 target_MtrCntrl_Read_SysState_Cnt_enum_val 1 Name Actual Value Expected Value R MtrCntrl_Write_Modidx_Uls_unl6p16(val) 65339 65339 ± 1 <t< td=""><td>k_MtrVoltQaxFiltFFEnable_Cnt_lgc</td><td>0</td><td></td><td></td></t<>	k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltVecuFitEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 13.868 k_VoltSatDaxPolyCoeff_Uls_f32 24.8209991 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTbIX_Uls_u3p13[0] 4162 t_CommOffsetTbIX_Clls_u3p13[1] 8053 t_CommOffsetTbIY_Cnt_u16[0] 565 t_CommOffsetTbIY_Cnt_u16[1] 1207 target_MtrCntrl_Read_IvrLoaMtghEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_Mold\u00fd	k_MtrVoltQaxIntegHiLim_Volt_f32	30.2348995		
k_VollSatDaxPolyCoeff_Uls_f32 13.868 k_VollSatCaxPolyCoeff_Uls_f32 24.8209991 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTbIX_Uls_u3p13[0] 4162 t_CommOffsetTbIY_Cnt_u16[0] 565 t_CommOffsetTbIY_Cnt_u16[1] 1207 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr 1 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIxSrlComSvcDt, Cnt_lgc_Val 0 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 124.758003 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 3029 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.3040009 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.3040009 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 1 Name Actual Value Expected Value R MtrCntrl_Write_CommOffset_Cnt_u16(val) 1207 1207 1207 MtrCntrl_Write_MtrCurrQax_FinalRef_Amp_f32(val) 65399 65399 65399 65399 65399 65399 65399 65399 65399 65394	k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008		
k_deadtimeVScale_Uls_f32 k_deadtimeVScale_Uls_f32 t_CommOffsetTbIX_Uls_u3p13[0] t_CommOffsetTbIY_Cnt_u16[0] t_CommOffsetTbIY_Cnt_u16[0] t_CommOffsetTbIY_Cnt_u16[0] t_CommOffsetTbIY_Cnt_u16[1] target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr target_MtrCntrl_Read_Indidx_SrComSvcDft_Cnt_lgc_Val target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name Actual Value Expected Value Repeted Value Repter Value	k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_deadtimeVScale_UIs_f32 t_CommOffsetTbIX_UIs_u3p13[0] 4162 t_CommOffsetTbIX_UIs_u3p13[1] 4162 t_CommOffsetTbIX_UIs_u3p13[1] 4162 t_CommOffsetTbIX_UIs_u3p13[1] 4162 t_CommOffsetTbIY_Cnt_u16[0] 565 t_CommOffsetTbIY_Cnt_u16[1] 4167 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr 4167 target_MtrCntrl_Read_IndidxSrlComSvcDft_Cnt_lgc_ptr 417 target_MtrCntrl_Read_MotGxrlComSvcDft_Cnt_lgc_val 418 target_MtrCntrl_Read_MotGurrLoaMtgtnEn_Cnt_lgc_ptr 418 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 418 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 418 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 418 target_MtrCntrl_Read_MotCurrDax_Amp_f32_Val 419 target_MtrCntrl_Read_MotrCurrOax_Amp_f32_Val 419 target_MtrCntrl_Read_MtrCurrOax_Amp_f32_Val 419 target_MtrCntrl_Read_MotrCurrQax_Amp_f32_Val 419 419 4107 4107 4107 4107 4107 4107 4107 4107	k_VoltSatDaxPolyCoeff_Uls_f32	13.868		
t_CommOffsetTbIX_Uls_u3p13[0]	k VoltSatQaxPolyCoeff Uls f32	24.8209991		
t_CommOffsetTbIX_Uls_u3p13[0]		0.996999979		
t_CommOffsetTblX_Uls_u3p13[1] 8053 t_CommOffsetTblY_Cnt_u16[0] 565 t_CommOffsetTblY_Cnt_u16[1] 1207 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr 0 target_MtrCntrl_Read_NotQurrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 1-124.758003 target_MtrCntrl_Read_MtrCurrOax_Amp_f32_Val 2-124.758003 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 3-124.758003 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 3-124.758003 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 3-124.758009 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 3-124.758009 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 3-124.758009 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 3-124.758009 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 3-124.758009 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 3-1207 MtrCntrl_Write_CommOffset_Cnt_u16(val) 1207 1207 MtrCntrl_Write_Motldx_Uls_u16p16(val) 65339 65339 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 3-105.246002 1-105.24600				
t_CommOffsetTblY_Cnt_u16[0] 565 t_CommOffsetTblY_Cnt_u16[1] 1207 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr 1 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_val 0 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 1-124.758003 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 3029 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 3029 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.3040009 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 1 Name				
t_CommOffsetTblY_Cnt_u16[1] 1207 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr 1 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 1-124.758003 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 3029 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.3040009 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 1 Name Actual Value Expected Value R MtrCntrl_Write_CommOffset_Cnt_u16(val) 1207 1207 MtrCntrl_Write_Modldx_Uls_u16p16(val) 65339 65339 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 1-105.246002 1-105.24600				
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr 0 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -124.758003 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 3029 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.3040009 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 1 Name Actual Value Expected Value R MtrCntrl_Write_CommOffset_Cnt_u16(val) 1207 1207 MtrCntrl_Write_Modldx_Uls_u16p16(val) 65339 65339 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -105.246002 -105.246002 +7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 20.4891834 20.4891834 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -9.79861736 -9.79861736 +4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 43921 43921 ± 1.52588E-05				
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_Val 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -124.758003 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 3029 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.3040009 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 1 Name Actual Value Expected Value R MtrCntrl_Write_CommOffset_Cnt_u16(val) 1207 1207 1207 MtrCntrl_Write_Modldx_UIs_u16p16(val) 65339 65339 ± 1 65339 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -105.246002 -105.246002 ± 7.81E-03 105.246002 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 20.4891834 20.4891834 ± 4.88E-04 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 43921 43921 ± 1.52588E-05				
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -124.758003 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 3029 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.3040009 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 1 Name Actual Value Expected Value R MtrCntrl_Write_CommOffset_Cnt_u16(val) 1207 1207 1207 MtrCntrl_Write_Modldx_Uls_u16p16(val) 65339 65339 ± 1 65339 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -105.246002 -105.246002 ± 7.81E-03 105.246002 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 20.4891834 20.4891834 ± 4.88E-04 20.4891834 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 43921 43921 ± 1.52588E-05				
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -124.758003 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 3029 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.3040009 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 1 Name Actual Value Expected Value R MtrCntrl_Write_CommOffset_Cnt_u16(val) 1207 1207 1207 MtrCntrl_Write_Modldx_Uls_u16p16(val) 65339 65339 ± 1 65339 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -105.246002 -105.246002 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 20.4891834 20.4891834 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -9.79861736 -9.79861736 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 43921 43921 ± 1.52588E-05				
target_MtrCntrl_Read_MtrCurrDax_Amp_132_Val				
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 3029 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.3040009 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 1 Name Actual Value Expected Value R MtrCntrl_Write_CommOffset_Cnt_u16(val) 1207 1207 MtrCntrl_Write_Modldx_Uls_u16p16(val) 65339 65339 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -105.246002 -105.246002 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 20.4891834 20.4891834 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -9.79861736 -9.79861736 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 43921 43921 ± 1.52588E-05				
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.3040009 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 1 Name Actual Value Expected Value R MtrCntrl_Write_CommOffset_Cnt_u16(val) 1207 1207 MtrCntrl_Write_Modldx_Uls_u16p16(val) 65339 65339 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -105.246002 -105.246002 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 20.4891834 20.4891834 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -9.79861736 -9.79861736 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 43921 43921 ± 1.52588E-05				
Name Actual Value Expected Value R MtrCntrl_Write_CommOffset_Cnt_u16(val) 1207 1207 MtrCntrl_Write_Modldx_UIs_u16p16(val) 65339 65339 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -105.246002 -105.246002 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 20.4891834 20.4891834 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -9.79861736 -9.79861736 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 43921 43921 ± 1.52588E-05				
Name Actual Value Expected Value R MtrCntrl_Write_CommOffset_Cnt_u16(val) 1207 1207 MtrCntrl_Write_Modldx_UIs_u16p16(val) 65339 65339 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -105.246002 -105.246002 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 20.4891834 20.4891834 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -9.79861736 -9.79861736 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 43921 43921 ± 1.52588E-05				
MtrCntrl_Write_CommOffset_Cnt_u16(val) 1207 MtrCntrl_Write_Modldx_Uls_u16p16(val) 65339 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -105.246002 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 20.4891834 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -9.79861736 -9.79861736 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 43921 43921 ± 1.52588E-05	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
MtrCntrl_Write_Modldx_Uls_u16p16(val) 65339 65339 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -105.246002 -105.246002 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 20.4891834 20.4891834 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -9.79861736 -9.79861736 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 43921 43921 ± 1.52588E-05	Name	Actual Value	Expected Value	Resu
MtrCntrl_Write_ModIdx_UIs_u16p16(val) 65339 65339 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -105.246002 -105.246002 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 20.4891834 20.4891834 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -9.79861736 -9.79861736 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 43921 43921 ± 1.52588E-05	MtrCntrl_Write_CommOffset Cnt u16(val)	1207	·	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -105.246002 -105.246002 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 20.4891834 20.4891834 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -9.79861736 -9.79861736 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 43921 43921 ± 1.52588E-05				
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 20.4891834 20.4891834 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -9.79861736 -9.79861736 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 43921 43921 ± 1.52588E-05				
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -9.79861736 -9.79861736 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 43921 43921 ± 1.52588E-05				
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 43921 43921 ± 1.52588E-05				
MITCUITIDAXPTEVINIQ_VOIT_M_T32 0				
	witrGurrDaxPrevintg_voit_M_t32	U	U	





Name	Actual Value	Expected Value	Result
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0528500043	0.0528500043 ± 0.0625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

lame	Input Value	
astDataAccessBufIndex_Cnt_M_u16	1	
trCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	
trCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
trCntrl Read MtrCurrDax Amp f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
trCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val	
trCntrl Read SysState Cnt Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
trCtrl MtrCurrDaxMaxVal Amp M f32[0]	138.595001	
trCtrl MtrCurrDaxMaxVal Amp M f32[1]	-157.388	
trCtrl MtrDampTermDax Ohm M f32[0]	0.0759999976	
trCtrl MtrDampTermDax Ohm M f32[1]	0.0500000007	
trCtrl MtrDampTermQax Ohm M f32[0]	0.0850000009	
trCtrl MtrDampTermQax Ohm M f32[1]	0.112999998	
trCtrl MtrDaxIntegralGain Ohm M f32[0]	0.169	
trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.234999999	
trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	643.937012	
trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-774.807983	
rCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0560000017	
trCtrl_MtrlmpedDax_Ohm_M_f32[1]	0.0130000003	
trCtrl MtrlmpedQax Ohm M f32[0]	0.112999998	
trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0769999996	
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.34399998	
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.00300002	
trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	144.895996	
trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	675.440002	
trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-10.6440001	
trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-11.8400002	
trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-12.2150002	
trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	0.874000013	
trCtrl Vecu Volt M f32[0]	21.2910004	
trCtrl Vecu Volt M f32[1]	23.6509991	
trCurrDaxPrevIntg_Volt_M_f32	-6.90899992	
trCurrDaxRef Amp M f32[0]	-212.632996	
trCurrDaxRef_Amp_M_f32[1]	-205.085007	
trCurrQaxCog Amp M f32	70.6559982	
trCurrQaxPrevIntg_Volt_M_f32	24.0646	
trCurrQaxRef Amp M f32[0]	-213.026993	
trCurrQaxRef_Amp_M_f32[1]	-66.7229996	
trCurrQaxRpl_Amp_M_f32	0	
trPosComputationDelay_Rad_M_f32[0]	-1.39279997	
trPosComputationDelay_Rad_M_f32[1]	-1.38090003	
CurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.0250000004	
ICurrCntrl DualEcuFailSclFac Uls M f32	0.0649999976	
ICurrCntrl InverterFailSclFac Uls M f32	0.723999977	

PICurrCntrl Per1

2016-09-15, 18:23:31+0530



Input Value PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32 0.785399973 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.0450000018 PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32 -43.1699982 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -627.179993 PICurrCntrl MtrVecuFilt M str.TermN Uls f32 43.7542992 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.50029999 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 -43.1699982 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32$ -627.179993 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 43.7542992 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32$ 0.50029999 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 5655.9502 k_DualEcuSignalSclFacSlew_UlspS_f32 90 4000015 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 4407.62012 $k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc$ 1 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0.0579999983 k_MtrCtrlVirualResDax_Ohm_f32 k_MtrCtrlVirualResQax_Ohm_f32 0.191 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ 0 k_MtrVoltDaxIntegHiLim_Volt_f32 10.6548996 $k_MtrVoltDaxIntegLoLim_Volt_f32$ -9.64999962 k_MtrVoltQaxFiltFFEnable_Cnt_lgc $k_MtrVoltQaxIntegHiLim_Volt_f32$ 13 8830004 k_MtrVoltQaxIntegLoLim_Volt_f32 -9.64999962 k MtrVoltVecuFiltEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 -5.80900002 k VoltSatQaxPolyCoeff Uls f32 1.51699996 k_deadtimeVScale_Uls_f32 0.986000001 t CommOffsetTblX Uls u3p13[0] 5153 $t_CommOffsetTblX_Uls_u3p13[1]$ 8027 t CommOffsetTblY Cnt u16[0] 164 t_CommOffsetTblY_Cnt_u16[1] 921 target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr 0 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 0 $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ 0 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 50.0610008 $target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr$ 2959 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 20.6149998 $target_MtrCntrl_Read_SysState_Cnt_Enum_Val$ **Actual Value Expected Value** Name Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 921 921 64618 ± 1 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 64618 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -137.378998 -137.378998 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 11.8574018 11.8574009 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -20.0803146 -20.0803146 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 12801 12801 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	-
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	-
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	-

0.0762999952

0.0762999952 ± 0.0625

PICurrCntrl_DualEcuFailSclFac_Uls_M_f32





Test Step 2.70 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-100.282997
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-120.248001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0649999976
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0920000002 0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0280000009
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1] MtrCtrl MtrDaxIntegralGain Ohm M f32[0]	1.4179998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.275000006
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	747.85199
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-144.074005
MtrCtrl MtrImpedDax Ohm M f32[0]	0.035999985
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.05900002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.105999999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	675.771973
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	1006.70001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-12.7250004
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-6.00099993
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	22.1809998
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-11.0150003
MtrCtrl_Vecu_Volt_M_f32[0]	12.1129999
MtrCtrl_Vecu_Volt_M_f32[1]	14.4729996
MtrCurrDaxPrevIntg_Volt_M_f32	-14.8070002
MtrCurrDaxRef_Amp_M_f32[0]	205.820999
MtrCurrDaxRef_Amp_M_f32[1]	-206.792007
MtrCurrQaxCog_Amp_M_f32	-111.970001
MtrCurrQaxPrevIntg_Volt_M_f32	11.6198997
MtrCurrQaxRef_Amp_M_f32[0]	-212.632996
MtrCurrQaxRef_Amp_M_f32[1]	-205.085007
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	2.94420004
MtrPosComputationDelay_Rad_M_f32[1]	-2.26290011
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.0820000023
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0659999996
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.128999993
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.530900002
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.677999973
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-10.21
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	16.2423992
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.400099993
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	1118
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-10.21
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	16.2423992 0.400099993
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 k CLOAFdbackSignalSclFacSlew UlspS f32	7814.10986
k DualEcuSignalSclFacSlew UlspS f32	91.599985
k_Dualecusignalsciracsiew_disps_is2 k_ILOAFdbackSignalSciFacSiew_UlspS_f32	3404.45996
k_ILOAFdbacksignalscifacsiew_0isps_isz k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.0659999996
k_MtrCtrlVirualResQax_Ohm_f32	0.0670000017
k MtrCurrQaxRefModifDsb Cnt lgc	0
k MtrCurrQaxRefModifRplEn Cnt Igc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	23.7416
k MtrVoltDaxIntegLoLim Volt f32	-22.4099998
k_MtrVoltQaxFiltFFEnable Cnt lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	30.2787991
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998
k MtrVoltVecuFiltEnable Cnt lgc	0

PICurrCntrl_Per1



Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	-21.8419991		
k_VoltSatQaxPolyCoeff_Uls_f32	-0.100000001		
k_deadtimeVScale_UIs_f32	0.978999972		
t_CommOffsetTblX_Uls_u3p13[0]	2802		
t_CommOffsetTblX_Uls_u3p13[1]	3899		
t_CommOffsetTblY_Cnt_u16[0]	12		
t_CommOffsetTblY_Cnt_u16[1]	15		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1011		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	79.6729965		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	15	15	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	56799	56799 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-93.1150055	-93.1150055 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-6.00099993	-6.00099993 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-11.0150003	-11.0150003 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	14368	14368 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0545499995	0.0545499995 ± 0.0625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.71 (Repeat Count = 1)		V
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-68.6760025	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-96.776001	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.023	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0799999982	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.14400005	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.58899999	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	734.922974	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	615.338989	
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0099999978	
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.079999982	
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.123000003	
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0460000001	

PICurrCntrl_Per1





Name	Input Value		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.216999993		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.522000015		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-153.945007		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-664.44397		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	0.779999971		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-18.3120003		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-23.6089993		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-14.04		
MtrCtrl_Veeu_Volt_M_f32[0]	20.3600006 22.7199993		
MtrCtrl_Vecu_Volt_M_f32[1] MtrCurrDaxPrevIntg_Volt_M_f32	-19.6690006		
MtrCurrDaxRef_Amp_M_f32[0]	-69.0940018		
MtrCurrDaxRef Amp M f32[1]	161.973007		
MtrCurrQaxCog_Amp_M_f32	-72.4260025		
MtrCurrQaxPrevIntg_Volt_M_f32	28.9094009		
MtrCurrQaxRef Amp M f32[0]	-132.813004		
MtrCurrQaxRef_Amp_M_f32[1]	-9.14299965		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	1.75950003		
MtrPosComputationDelay_Rad_M_f32[1]	-2.1559		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.261000007		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0670000017		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.728999972		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.386200011		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-717.299988		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	570.700012		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	83.4807968 0.745700002		
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	-717.299988		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_UIs_f32	570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	83.4807968		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.745700002		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2013.56995		
k_DualEcuSignalSclFacSlew_UlspS_f32	92.8000031		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	944.638977		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.126000002		
k_MtrCtrlVirualResQax_Ohm_f32	0.0670000017		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	0.694800019		
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	27.6896992 -8.68999958		
k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltVecuFiltEnable_Cnt_lgc	-6.06999936		
k VoltSatDaxPolyCoeff Uls f32	-8.64500046		
k VoltSatQaxPolyCoeff Uls f32	-10.3520002		
k deadtimeVScale Uls f32	0.987999976		
t_CommOffsetTblX_Uls_u3p13[0]	4686		
t_CommOffsetTblX_Uls_u3p13[1]	6119		
t_CommOffsetTblY_Cnt_u16[0]	557		
t_CommOffsetTblY_Cnt_u16[1]	678		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-198.285995		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3142		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	0.486999989		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		1_
Name	Actual Value	Expected Value	Resul
MtrCntrl_Write_CommOffset_Cnt_u16(val)	678	678	•
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	64749	64749 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-60.387001 20.0507793	-60.387001 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	20.0007793	20.0507793 ± 4.88E-04	
MtrCntrl Write MtrOayVoltage Volt f32(val)	-1 61/157313		
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0n16(val)	-1.61457312 35574	-1.614573 ± 4.88E-04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) MtrCurrDaxPrevIntg_Volt_M_f32	-1.61457312 35574 0.694800019	-1.0 (45/3 ± 4.60E-04 35574 ± 1.52588E-05 0.694800019	



Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~		
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~		
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~		
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~		
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~		
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•		
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	•		
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•		
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•		
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~		
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~		
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓		
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~		
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~		
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~		
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~		
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~		

Name	Input Value
FastDataAccessBufIndex Cnt M u16	1
AtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc(ptr)	target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr
VirCntrl Read IvtrLoaMtgtnEn Cnt Igc(ptr)	target_MtrCntrl_Read_bualeconoloctrinitghena_cnt_igc_pti
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
AttCatal Read MtrCurrOpy App. (230/cl)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-139.906998
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	115.814003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0540000014
/trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0560000017
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.88999999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.33099997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-329.475006
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-304.359985
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.075000003
/trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0710000023
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0340000018
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.104999997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.384000003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.620999992
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	794.978027
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-414.11499
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-7.04099989
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-21.3549995
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-21.1959991
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-4.78100014
/trCtrl_Vecu_Volt_M_f32[0]	5.33099985
/trCtrl_Vecu_Volt_M_f32[1]	7.69099998
MtrCurrDaxPrevIntg_Volt_M_f32	24.066
/trCurrDaxRef_Amp_M_f32[0]	-132.813004
/trCurrDaxRef_Amp_M_f32[1]	-9.14299965
/ltrCurrQaxCog_Amp_M_f32	83.9489975
MtrCurrQaxPrevIntg_Volt_M_f32	19.3868999
/trCurrQaxRef_Amp_M_f32[0]	-146.173996
/trCurrQaxRef_Amp_M_f32[1]	-213.335007
/trCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	2.70140004
htrPosComputationDelay_Rad_M_f32[1]	1.77929997
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.479000002
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0680000037
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.882000029
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.258100003
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	1

2016-09-15, 18:23:31+0530



PICurrCntrl_Per1

MtrCntrl_Write_CommOffset_Cnt_u16(val) 1844 1844 1844 MtrCntrl_Write_ModIdx_UIs_u16p16(val) 64552 64552 ± 1 46552 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -220 -220 ± 7.81E-03 -220 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 5.92555666 5.92555618 ± 4.88E-04 471996021 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 27927 27927 ± 1.52588E-05 47192672 MtrCurrDaxPrevIntg_Volt_M_f32 0 0 4719672			•	
Picurcint Mivesuifit M. at Prevolute U. Us. 122 55.44948	Name	Input Value		
PiCurrCort MiVvocialTit, M. st. TremN, Uls, 12	PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118		
Picumorin Mivocularit M, str. tem. D, Us. 192 0.843-00002 1118 118 1188 1118 11	PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-38.7999992		
PiCurrCntt_MrVolCaarFFII_M_str Pervinut_Uis_T2	PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	65.649498		
PiCurrCnit_MirVolCaarFFII_M_etr_TemN_UIs_132 38799992 PiCurrCnit_MirVolCaarFFII_M_etr_TemN_UIs_132 38799992 PiCurrCnit_MirVolCaarFFII_M_etr_TemN_UIs_132 38799992 PiCurrCnit_MirVolCaarFFII_M_etr_TemN_UIs_132 38440002	PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.843400002		
PiCurrOnti, MirVoliQaxFFFII, M_str.TemM, Uis, 132 0.843400002 N. CLOAF chaokSignalsGFacSiew, UispS_132 7558.6001 N. CLOAF chaokSignalsGFacSiew, UispS_132 348.41992 N. LOAF chaokSignalsGFacSiew, UispS_132 348.41992 N. LOAF chaokSignalsGFacSiew, UispS_132 348.41992 N. LOAF chaokSignalsGFacSiew, UispS_132 348.41992 N. MirCitli/Curl.copsSecOrtInnelErable, Crit.lgc 0 N. Kircitli/VisalResDax, Chinr 1052 0.128000002 N. MirCitli/VisalResDax, Chinr 1052 0.1280000003 N. MirCitli/VisalResDax, Chinr 1052 0.1280000003 N. MirCitli/VisalResDax, Chinr 1052 0.1280000000 N. MirCitli/VisalResDax, Chinr 1052 0.1280000000 N. MirCitli/VisalResDax, Chinr 1052 0.128000000 N. MirCitli/VisalResDax Novella, Chinr 1052 0.128000000 N. MirCitli/VisalResDax Novella, Chinr 1052 0.12800000 N. MirCitli/VisalResDax Novella, Chinr 1052 0.1280000 N. MirCitli/VisalResDax Novella, Chinr 1052 0.12800000 N. MirCitli/VisalResDax Novella, Chinr 1052 0.1280000 N. MirCitli/VisalResDax Novella, Chinr 1052 0.128000000 N. MirCitli/VisalResDax Novella, Chinr 1052 N. MirCitli/VisalResD	PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	1118		
PICUROTHIE MINVOIRCAMPFEET, M. str. Temp. Uls. [32]	PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-38.7999992		
K_CLOAF-backSignalSclFacSiew_UlspS_132 94 K_DualEcuSignalSclFacSiew_UlspS_132 94 K_DOAF-backSignalSclFacSiew_UlspS_132 35441992 K_MICCIFCUSAT-CORPSCOTTRAIF-ERABLE_CR1_Uge 0 K_MICCIFCUSATE-CR1_USE	PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	65.649498		
K	PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.843400002		
k_IMCOAFdbackSignalSciFacSiew_UispS_132	k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7558.6001		
K_MRCHICOURL.copSec0TrianFcEnable_Cnt_lgc	k_DualEcuSignalSclFacSlew_UlspS_f32	94		
k, MrCrifeedbackControlDisable_Cnt_ige 0.126000002 k, MrCrivirualResDax_Ohm_152 0.126000005 k, MrCruvirualResDax_Ohm_152 0.179000005 k, MrCruvirualResDax_Ohm_152 0. k, MrCruvirualResRoddiffixeEn_Cnt_ige 0. k, MrVoriDaxIntegHillim_Volt_152 24.4853992 k, MrVoriDaxIntegHillim_Volt_152 4.57000017 k, MrVoriDaxIntegHillim_Volt_152 9.72780037 k, MrVoriDaxIntegHillim_Volt_152 4.57000017 k, MrVoriDaxIntegHillim_Volt_152 4.57000017 k, MrVoriDaxIntegHillim_Volt_152 4.57000017 k, MrVoriDaxIntegLolim_Volt_152 4.57000017 k, MrVoriDaxIntegLolim_Volt_152 4.57000017 k, VoliSatiDaxPolyCoeff_Uis_132 4.57000017 k, VoliSatiDaxPolyCoeff_Uis_132 4.80100012 k, VoliSatiDaxPolyCoeff_Uis_152 19.4750004 L, CommOffsetTDX_Uis_usp13(1) 1139 L_CommOffsetTDX_Uis_usp13(1) 1434 L_CommOffsetTDY_Cnt_u16(1) 1844 L_CommOffsetTDY_Cnt_u16(1) 1844 L_CommOffsetTDY_Cnt_u16(1) 19.475000000000000000000000000000000000000	k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3548.41992		
K, MrCritvirualResDax, Ohm _132 0.126000002 K, MrCritvirualResDax, Ohm _132 0.179000005 K, MrCritvirualResDax, Ohm _132 0.179000005 K, MrCritrialResDax, Ohm _132 0.0 K, MrCritrialResDax, Ohm _132 24.4853992 K, MrVoltDaxIntegl-Illim, Volt_132 4.57000017 K, MrVoltDaxIntegl-Illim, Volt_132 4.57000017 K, MrVoltDaxIntegl-Illim, Volt_132 9.72780037 K, MrVoltCavIntegl-Illim, Volt_132 4.57000017 K, MrVoltCavIntegl-Illim, Volt_132 4.57000017 K, MrVoltCavIntegl-Illim, Volt_132 4.57000017 K, MrVoltCavIntegl-Illim, Volt_132 4.57000017 K, MrVoltGavIntegl-Illim, Volt_132 4.57000017 <td>k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc</td> <td>0</td> <td></td> <td></td>	k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k, MrCtrVirualResQax_Ohm_f32 0.179000005 k, McCurdXexRetModifSpEn_Cnt_Ige 0 k, MCurdXexRetModifSpEn_Cnt_Ige 0 k, MrVoltDaxIntegHiLim_Volt_f32 24.4853992 k, MrVoltDaxIntegHiLim_Volt_f32 4.57000017 k, MrVoltDaxIntegHiLim_Volt_f32 4.57000017 k, MrVoltDaxIntegHiLim_Volt_f32 9.72780037 k, MrVoltDaxIntegHiLim_Volt_f32 4.57000017 k, MrVoltDaxIntegHiLim_Volt_f32 4.57000017 k, MrVoltDaxIntegHiLim_Volt_f32 4.57000017 k, MrVoltDaxIntegHiLim_Volt_f32 4.57000017 k, VoltSatQaxPolyCoef_Uls_f32 4.57000017 k, VoltSatQaxPolyCoef_Uls_f32 19.4750004 k, deadtimeVScale_Uls_f32 19.4750004 k, deadtimeVScale_Uls_f32 19.4750004 k, deadtimeVScale_Uls_f32 19.4750004 k, deadtimeVScale_Uls_f32 19.4750004 k, deadtimeVScale_Uls_f317 7438 t_CommOffsetTblX_Uls_usp13(1) 7438 t_CommOffsetTblX_Uls_usp13(1) 1844 t_CommOffsetTblY_Cnt_u16(0) 1844 t_CommOffsetTblY_Cnt_u16(1) 19.475000000000000000000000000000000000000	k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k, MrCurraxReRModifDisb, Cnt. Igc 0 k, MrCurraxReRModifDisb, Cnt. Igc 0 k, MrVolDaxIntegl.Luin_Volt_i32 24.4853992 k, MrVolDaxIntegl.Luin_Volt_i32 4.57000017 k, MrVolDaxIntegl.Luin_Volt_i32 9.72780037 k, MrVoltGaxIntegl.Luin_Volt_i32 4.57000017 k, MrVoltGaxIntegl.Luin_Volt_i32 4.57000017 k, MrVoltSatDoyCoeff_Uls_i52 4.80100012 k, VoltSatDaxPolyCoeff_Uls_i532 4.80100012 k, OstaBatDaxB, Volt_is_i530 19.4750004 k, GeadimevScale_Uls_i732 0.985000014 t, CommOffsetDix, Uls_us_j13[1] 7438 t, CommOffsetDix, Uls_us_j13[1] 7438 t, CommOffsetDix, Ont_ut6[1] 1844 target_MrCntri_Read_DualEcuMotCtriMtgnEna_Cnt_lgc_ptr 1 target_MrCntri_Read_DualEcuMotCtriMtgnEna_Cnt_lgc_ptr 1 target_MrCntri_Read_MotCurrLoaMtgnEn_Cnt_ut6[1] 1 target_MrCntri_Read_MrCurrDax_Amp_j32_Val 1 target_MrCntri_Read_MrCurrCax_Amp_j32_Val 1 target_MrCntri_Read_MrCurrCax_Amp_j32_Val 1 target_MrCntri_Read_MrCurrCax_Amp_j32_Val 1 target_MrCntr	k_MtrCtrlVirualResDax_Ohm_f32	0.126000002		
K. MrCurrQaxRefModifRpIEn_Cnt_lgc 0 k. MrVolibaxinlegHiLim_Volt_f32 24.4853992 k. MrVolibaxinlegHiLim_Volt_f32 4.57000017 k. MrVolidaxinlegHiLim_Volt_f32 9.72780037 k. MrVolidaxinlegHiLim_Volt_f32 9.72780037 k. MrVolidaxinlegHiLim_Volt_f32 4.57000017 k. MrVolidaxinlegLoLim_Volt_f32 4.57000017 k. MrVollequerillenable_Cnt_lgc 0 k. VoltSatDaxPolyCoeff_Uls_f32 4.80100012 k. VoltSatDaxPolyCoeff_Uls_f32 19.4750004 k. deadtimeVScale_Uls_f32 0.985000014 L. CommOffsetTbX_Uls_usp13[0] 1139 L. CommOffsetTbY_Cnt_u16[0] 288 L. CommOffsetTbY_Cnt_u16[1] 1844 Larget_MtrCntrl_Read_DualecuMofCtrlMtgnEna_Cnt_lgc_ptr 1 Larget_MtrCntrl_Read_ModidxSriComSvcDft_Cnt_lgc_Val 0 Larget_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 136.341003 Larget_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 136.341003 Larget_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 136.341003 Larget_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 14704 Larget_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 190.440994	k_MtrCtrlVirualResQax_Ohm_f32	0.179000005		
k_Mitr/oltDaxinteghiLim_Volt_f32	k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
KmtrvoltDaxintegLoLim_Volf_f32	k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
K_Mtr/oltQaxIntegHiLIm_Voit_f32	k_MtrVoltDaxIntegHiLim_Volt_f32	24.4853992		
k_Mtr/oltQaxInteg/ILim_Volt_f32	k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017		
k_Mtr/ollQaxIntegLoLim_volt_f32 4.57000017 k_Mtr/ollVecuFillEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 4.80100012 k_VoltSatDaxPolyCoeff_Uls_f32 19.4750004 k_deadtimeVScale_Uls_f32 0.985000014 t_CommOffsetTbIX_Uls_u3p13[0] 1139 t_CommOffsetTbIX_Uls_u3p13[1] 7438 t_CommOffsetTbIY_Cnt_u16[0] 268 t_CommOffsetTbIY_Cnt_u16[1] 1844 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_igc_ptr 1 target_MtrCntrl_Read_MotCurt_coaMtgnEn_Cnt_lgc_val 0 target_MtrCntrl_Read_MotCurt_coaMtgnEn_Cnt_lgc_val 0 target_MtrCntrl_Read_MtrCurrDax_Amp_132_Val 136.341003 target_MtrCntrl_Read_MtrCurrDax_Amp_132_Val 136.341003 target_MtrCntrl_Read_MtrCurrDax_Amp_132_Val 190.440994 target_MtrCntrl_Read_MtrCurrOax_Amp_132_Val 190.440994 target_MtrCntrl_Read_MtrCurrOax_Amp_132_Val 1844 MtrCntrl_Write_CommOffset_Cnt_u16(val) 1844 MtrCntrl_Write_Mcordox_Lgc_val 1844 MtrCntrl_Write_Mcordox_Lgc_val 46552 64552 ± 1 MtrCntrl_Write_MitrDaxVoltage_Volt_(32(val)	k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
K_MtrVoltVecuFiltEnable_Cn_lgc O	k_MtrVoltQaxIntegHiLim_Volt_f32	9.72780037		
k_VollSatDaxPolyCoeff_Uls_f32	k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017		
k_VoltSatQaxPolyCoeff_Uls_f32	k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_deadtimeVScale_Uls_f32	k_VoltSatDaxPolyCoeff_Uls_f32	-4.80100012		
t_CommOffsetTbIX_Uls_u3p13[0]	k_VoltSatQaxPolyCoeff_Uls_f32	19.4750004		
t_CommOffsetTbIX_UIs_u3p13[1] 7438 t_CommOffsetTbIY_Cnt_u16[0] 268 t_CommOffsetTbIY_Cnt_u16[1] 1844 target_MtrCntrl_Read_DualEcuMotctrlMtgnEna_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ivir_LoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ivir_LoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_MotldxSriComSvcDft_Cnt_lgc_ptr 0 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 136.341003 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 136.341003 target_MtrCntrl_Read_MtrCurrOffcomOffset_Cnt_u16_ptr 4704 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 190.440994 target_MtrCntrl_Read_SysState_Cnt_u16_ptr 4704 Name Actual Value Expected Value Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1844 1844 1844	k_deadtimeVScale_Uls_f32	0.985000014		
t_CommOffsetTbIY_Cnt_u16[0] 268 t_CommOffsetTbIY_Cnt_u16[1] 1844 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_igc_ptr 1 target_MtrCntrl_Read_JNutLoaMtgnEn_Cnt_igc_ptr 1 target_MtrCntrl_Read_MtnCurrLoaMtgnEn_Cnt_igc_ptr 1 target_MtrCntrl_Read_MtnCurrLoaMtgnEn_Cnt_igc_ptr 0 target_MtrCntrl_Read_MtnCurrLoaMtgnEn_Cnt_igc_ptr 0 target_MtrCntrl_Read_MtnCurrDax_Amp_f32_Val 136.341003 target_MtrCntrl_Read_MtrCurrOax_Amp_f32_Val 136.341003 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 190.440994 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 190.440994 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 4 Name	t_CommOffsetTblX_Uls_u3p13[0]	1139		
t_CommOffsetTbIY_Cnt_u16[1] 1844 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr 1 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModldxSrlComsvcDft_Cnt_lgc_Val 0 target_MtrCntrl_Read_ModlcwrloaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 136.341003 target_MtrCntrl_Read_MtrCurrOffComoffset_Cnt_u16_ptr 4704 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -190.440994 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 4 Name Actual Value Expected Value Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 844 1844 1844 1844 1844 1844 1844 1844	t_CommOffsetTblX_Uls_u3p13[1]	7438		
target_MtrCntrl_Read_UualEcuMotCtrlMtgnEna_Cnt_lgc_ptr 1 target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_val 0 target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_ptr 0 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 136.341003 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 4704 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -190.440994 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 4 Name Actual Value Expected Value Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1844 1844 1844 1844 1844 1844 1844 184	t_CommOffsetTblY_Cnt_u16[0]	268		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_Val 1 target_MtrCntrl_Read_ModIdx5rlComSvcDft_Cnt_Igc_Val 0 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr 0 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 136.341003 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 4704 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -190.440994 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 4 Name Actual Value Expected Value Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1844 1844 ✓ MtrCntrl_Write_ModIdx_UIs_u16p16(val) 64552 64552 ± 1 ✓ MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -220 -20 ± 7.81E-03 ✓ MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 5.92555666 5.92555618 ± 4.88E-04 ✓ MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 27927 27927 ± 1.52588E-05 ✓ MtrCurrDaxPrevIntg_Volt_M_f32 0 0 ✓	t_CommOffsetTblY_Cnt_u16[1]	1844		
target_MtrCntrl_Read_ModIdxSrIComSvcDft_Cnt_Igc_Val 0 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr 0 target_MtrCntrl_Read_MtrCurrDax_Amp_i32_Val 136.341003 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 4704 target_MtrCntrl_Read_MtrCurrQax_Amp_i32_Val -190.440994 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 4 Name Actual Value Expected Value Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1844 1844 ✓ MtrCntrl_Write_ModIdx_UIs_u16p16(val) 64552 64552 ± 1 ✓ MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_i32(val) -220 -220 ± 7.81E-03 ✓ MtrCntrl_Write_MtrDaxVoltage_Volt_i32(val) 5.92555666 5.92555618 ± 4.88E-04 ✓ MtrCntrl_Write_MtrQaxVoltage_Volt_i32(val) 4.71996021 4.71996021 ± 4.88E-04 ✓ MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 27927 27927 ± 1.52588E-05 ✓ MtrCurrDaxPrevIntg_Volt_M_i32 0 0 ✓	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_MtrCurrDax_Amp_32_Val 136.341003 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 4704 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -190.440994 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 4 Name Actual Value Expected Value Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1844 1844 ✓ MtrCntrl_Write_Modldx_Uls_u16p16(val) 64552 64552 ± 1 ✓ MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -220 -220 ± 7.81E-03 ✓ MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 5.92555666 5.92555618 ± 4.88E-04 ✓ MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 4.71996021 4.71996021 ± 4.88E-04 ✓ MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 27927 27927 ± 1.52588E-05 ✓ MtrCurrDaxPrevIntg_Volt_M_f32 0 0 ✓	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 136.341003 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 4704 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -190.440994 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 4 Name Actual Value Expected Value Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1844 1844 ✓ MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 64552 64552 ± 1 ✓ MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 5.92555666 5.92555618 ± 4.88E-04 ✓ MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 4.71996021 4.71996021 ± 4.88E-04 ✓ MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 27927 27927 ± 1.52588E-05 ✓ MtrCurrDaxPrevIntg_Volt_M_f32 0 0 ✓	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 4704 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -190.440994 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 4 Name Actual Value Expected Value Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1844 1844 4 MtrCntrl_Write_ModIdx_UIs_u16p16(val) 64552 64552 ± 1 4 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -220 -220 ± 7.81E-03 4 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 5.92555666 5.92555618 ± 4.88E-04 4 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 4.71996021 4.71996021 ± 4.88E-04 4 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 27927 27927 ± 1.52588E-05 4 MtrCurrDaxPrevIntg_Volt_M_f32 0 0 4	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -190.440994 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 4 Name Actual Value Expected Value Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1844 1844 4 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 64552 64552 ± 1 4 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -220 -220 ± 7.81E-03 4 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 5.92555666 5.92555618 ± 4.88E-04 4 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 4.71996021 4.71996021 ± 4.88E-04 4 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 27927 27927 ± 1.52588E-05 4 MtrCurrDaxPrevIntg_Volt_M_f32 0 0 4	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val 4 Name Actual Value Expected Value Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1844 1844 4 MtrCntrl_Write_ModIdx_UIs_u16p16(val) 64552 64552 ± 1 4 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -220 -220 ± 7.81E-03 4 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 5.92555666 5.92555618 ± 4.88E-04 4 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 4.71996021 4.71996021 ± 4.88E-04 4 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 27927 27927 ± 1.52588E-05 4 MtrCurrDaxPrevIntg_Volt_M_f32 0 0 4	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4704		
Name Actual Value Expected Value Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1844 1844 4 MtrCntrl_Write_ModIdx_UIs_u16p16(val) 64552 64552 ± 1 4 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -220 -220 ± 7.81E-03 4 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 5.92555666 5.92555618 ± 4.88E-04 4 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 4.71996021 4.71996021 ± 4.88E-04 4 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 27927 27927 ± 1.52588E-05 4 MtrCurrDaxPrevIntg_Volt_M_f32 0 0 4	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-190.440994		
MtrCntrl_Write_CommOffset_Cnt_u16(val) 1844 1844 1844 MtrCntrl_Write_ModIdx_UIs_u16p16(val) 64552 64552 ± 1 46552 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -220 -220 ± 7.81E-03 76666 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 5.92555666 5.92555618 ± 4.88E-04 76666 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 4.71996021 4.71996021 ± 4.88E-04 76666 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 27927 27927 ± 1.52588E-05 76666 MtrCurrDaxPrevIntg_Volt_M_f32 0 0 76666	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
MtrCntrl_Write_ModIdx_Uls_u16p16(val) 64552 64552 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -220 -220 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 5.92555666 5.92555618 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 4.71996021 4.71996021 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 27927 27927 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32 0 0	Name	Actual Value	Expected Value	Result
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -220 -220 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 5.92555666 5.92555618 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 4.71996021 4.71996021 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 27927 27927 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32 0 0	MtrCntrl_Write_CommOffset_Cnt_u16(val)	1844	1844	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 5.92555666 5.92555618 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 4.71996021 4.71996021 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 27927 27927 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32 0 0	MtrCntrl_Write_ModIdx_Uls_u16p16(val)	64552	64552 ± 1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 4.71996021 4.71996021 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 27927 27927 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32 0 0	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 27927 27927 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32 0 0	MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	5.92555666	5.92555618 ± 4.88E-04	~
MtrCurrDaxPrevIntg_Volt_M_f32 0	MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.71996021	4.71996021 ± 4.88E-04	•
	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	27927	27927 ± 1.52588E-05	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32 0.0562500022 0.0562500022 ± 0.0625 ✓	MtrCurrDaxPrevIntg_Volt_M_f32	0	0	✓
	PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0562500022	0.0562500022 ± 0.0625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	-
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	-
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	-
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	-
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	-
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	-
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	-
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	-
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	-
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	-
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	-
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	-





Test Step 2.73 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val -82.2979965
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	46.8180008
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1] MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0410000011
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.104999997
MtrCtrl MtrDampTermQax Ohm M f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl MtrDaxIntegralGain Ohm M f32[0]	1.03900003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.2699998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	964.854004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-233.382004
MtrCtrl MtrImpedDax Ohm M f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0949999988
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.0179999992
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.765999973
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.43099999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	887.062988
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	250.690994
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	6.24700022
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	3.82500005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-22.7129993
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-20.4500008
MtrCtrl_Vecu_Volt_M_f32[0]	18.2229996
MtrCtrl_Vecu_Volt_M_f32[1]	20.5830002
MtrCurrDaxPrevIntg_Volt_M_f32	-21.9330006
MtrCurrDaxRef_Amp_M_f32[0]	-146.173996
MtrCurrDaxRef_Amp_M_f32[1]	-213.335007
MtrCurrQaxCog_Amp_M_f32	-144.667007
MtrCurrQaxPrevIntg_Volt_M_f32	19.6061993
MtrCurrQaxRef_Amp_M_f32[0]	-91.4420013
MtrCurrQaxRef_Amp_M_f32[1]	133.692993
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	-0.779100001
MtrPosComputationDelay_Rad_M_f32[1]	-2.89840007
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.324000001 0.068999983
PICurrCntrl InverterFailScIFac Uls M f32	
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.54000021 0.15399999
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.93000007
PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32	-657.130005
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	-194.190002
PICurrCntrl MtrVecuFilt M str.TermN Uls f32	95.354599
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.241500005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	-657.130005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	95.354599
PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.241500005
k CLOAFdbackSignalSclFacSlew UlspS f32	2668.02002
k_DualEcuSignalSclFacSlew_UlspS_f32	95.1999969
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	791.747986
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.0839999989
k_MtrCtrlVirualResQax_Ohm_f32	0.128999993
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	19.6718006
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	13.4462996
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004
k MtrVoltVecuFiltEnable Cnt lgc	1

PICurrCntrl_Per1



Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	1.19500005		
k_VoltSatQaxPolyCoeff_Uls_f32	-18.6650009		
k_deadtimeVScale_Uls_f32	0.963999987		
t_CommOffsetTblX_Uls_u3p13[0]	2449		
t_CommOffsetTblX_Uls_u3p13[1]	2875		
t_CommOffsetTblY_Cnt_u16[0]	135		
t_CommOffsetTblY_Cnt_u16[1]	1455		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	654		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	83.9489975		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	654	654	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	1.69637311	1.69637322 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.51162004	4.51162052 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	39056	39056 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0808999985	0.0808999985 ± 0.0625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	~

Test Step 2.74 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	160.044006
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	165.242004
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.967999995
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.12600005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-115.790001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	183.574997
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.00899999961

2016-09-15, 18:23:31+0530





Name	Input Value		
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0939999968		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.62800002		
MtrCtrl MtrQaxIntegralGain Ohm M f32[1]	1.66900003		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-689.698975		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	753.629028		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	18.1280003		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-29.3299999		
MtrCtrl MtrVoltQaxFF Volt M f32[0]	18.2380009		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	27.3910007		
MtrCtrl Vecu Volt M f32[0]	25.7329998		
MtrCtrl_Vecu_Volt_M_f32[1]	28.0930004		
MtrCurrDaxPrevIntg_Volt_M_f32	-14.8500004		
MtrCurrDaxRef_Amp_M_f32[0]	-91.4420013		
MtrCurrDaxRef_Amp_M_f32[1]	133.692993		
MtrCurrQaxCog_Amp_M_f32	80.8180008		
MtrCurrQaxPrevIntg_Volt_M_f32	3.95619988		
MtrCurrQaxRef_Amp_M_f32[0]	171.485992		
MtrCurrQaxRef_Amp_M_f32[1]	163.787003		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	2.79419994		
MtrPosComputationDelay_Rad_M_f32[1]	-2.36879992		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.538999975		
PICurrCntrl DualEcuFailSclFac Uls M f32	0.070000003		
PICurrCntrl InverterFailSclFac Uls M f32	0.671000004		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.321999997		
	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32			
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	68.892601		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.91109997		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-194.190002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	68.892601		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.91109997		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6474.75		
k_DualEcuSignalSclFacSlew_UlspS_f32	96.4000015		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5821.56006		
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k MtrCtrlVirualResDax Ohm f32	0.187999994		
k_MtrCtrlVirualResQax_Ohm_f32	0.0480000004		
k MtrCurrQaxRefModifDsb Cnt Igc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	28.8398991		
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	16.0121994		
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	21.8379993		
k_VoltSatQaxPolyCoeff_Uls_f32	-10.9659996		
k_deadtimeVScale_Uls_f32	0.972000003		
t_CommOffsetTblX_Uls_u3p13[0]	4153		
t_CommOffsetTbIX_UIs_u3p13[1]	8176		
t_CommOffsetTblY_Cnt_u16[0]	434		
t_CommOffsetTblY_Cnt_u16[1]	1438		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target MtrCntrl Read ModldxSrlComSvcDft Cnt Igc Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	1.62199998		
target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	1246		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-144.667007		
terret MacOutal Deed OverOtate Out 5	2		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val		Expected Value	
target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name	Actual Value	P. C.	Result
	Actual Value 1246	1246	Kesuit
Name		•	Result
Name MtrCntrl_Write_CommOffset_Cnt_u16(val)	1246	1246	•
Name MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_ModIdx_Uls_u16p16(val)	1246 0	1246 0 ± 1	Result
Name MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_ModIdx_UIs_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	1246 0 82.9690018	1246 0 ± 1 82.9690018 ± 7.81E-03	•
Name MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_ModIdx_Uls_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	1246 0 82.9690018 -3.55194187	1246 0 ± 1 82.9690018 ± 7.81E-03 -3.55194139 ± 4.88E-04	





Name	Actual Value	Expected Value	Result
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0579499975	0.0579499975 ± 0.0625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

lame	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
/trCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc(ptr)	target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr
ItrCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr
ItrCntrl Read ModIdxSrlComSvcDft Cnt Igc(Val)	target MtrCntrl Read ModidxSrlComSvcDft Cnt lgc Val
ItrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
ItrCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val
ItrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr
ItrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
ItrCntrl_Read_SysState_Cnt_Enum(Val)	target MtrCntrl Read SysState Cnt Enum Val
/trCtrl MtrCurrDaxMaxVal Amp M f32[0]	-65.1900024
/trCtrl MtrCurrDaxMaxVal Amp M f32[1]	-216.972
/trCtrl MtrDampTermDax Ohm M f32[0]	0.115999997
/trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.115999997
/trCtrl MtrDampTermQax Ohm M f32[0]	0.075000003
/trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0710000023
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.938000023
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.98699999
/trCtrl MtrDaxPropotionalGain Ohm M f32[0]	-751.672974
htrCtrl MtrDaxPropotionalGain Ohm M f32[1]	758.984985
ItrCtrl MtrImpedDax Ohm M f32[0]	0.123000003
htrCtrl MtrImpedDax Ohm M f32[1]	0.046000001
/trCtrl MtrImpedQax Ohm M f32[0]	0.0850000009
/trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.112999998
/trCtrl MtrQaxIntegralGain Ohm M f32[0]	1.87199998
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.86800003
/trCtrl MtrQaxPropotionalGain Ohm M f32[0]	881.539001
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	971.434998
trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-1.92999995
htrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	0.432000011
// htrCtrl MtrVoltQaxFF Volt M f32[0]	-17.2689991
/trCtrl MtrVoltQaxFF Volt M f32[1]	15.2200003
// MtrCtrl Vecu Volt M f32[0]	17.7010002
/trCtrl Vecu Volt M f32[1]	20.0610008
1trCurrDaxPrevIntg Volt M f32	-21.5599995
/trCurrDaxRef Amp M f32[0]	171.485992
/trCurrDaxRef_Amp_M_f32[1]	163.787003
1trCurrQaxCog_Amp_M_f32	-44.2579994
ItrCurrQaxPrevIntg_Volt_M_f32	15.5335999
htrCurrQaxRef_Amp_M_f32[0]	106.072998
ItrCurrQaxRef_Amp_M_f32[1]	-112.455002
htrCurrQaxRpl_Amp_M_f32	0
/trPosComputationDelay Rad M f32[0]	0.463400006
/trPosComputationDelay Rad M f32[1]	1.54390001
PICurrCntrl CurrSensFailSclFac Uls M f32	0.47299999

PICurrCntrl_Per1

2016-09-15, 18:23:31+0530



Name	Input Value		
PICurrCntrl DualEcuFailSclFac Uls M f32	0.0710000023		
PICurrCntrl InverterFailSclFac UIs M f32	0.0410000011		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	1		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.142000005		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	386.220001		
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	20.7000008		
PICurrCntrl MtrVecuFilt M str.TermN Uls f32	82.1283035		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.396600008		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	386.220001		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	20.7000008		
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	82.1283035		
	0.396600008		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32			
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6937.75977		
k_DualEcuSignalSclFacSlew_UlspS_f32	97.5999985		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3932.20996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.0869999975		
k_MtrCtrlVirualResQax_Ohm_f32	0.175999999		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	2.12940001		
k_MtrVoltDaxIntegLoLim_Volt_f32	-12.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	28.5435009		
k_MtrVoltQaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	14.7320004		
k_VoltSatQaxPolyCoeff_Uls_f32	9.23999977		
k_deadtimeVScale_Uls_f32	0.967999995		
t_CommOffsetTblX_Uls_u3p13[0]	4776		
t_CommOffsetTblX_Uls_u3p13[1]	7741		
t_CommOffsetTblY_Cnt_u16[0]	1756		
t_CommOffsetTbIY_Cnt_u16[1]	1670		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1865		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-44.2579994		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1715	1715	-
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	49741	49741 ± 1	•
MtrCntrl Write MtrCurrQaxFinalRef Amp f32(val)	-68.1970062	-68.1970062 ± 7.81E-03	-
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0.432000011	0.432000011 ± 4.88E-04	•
MtrCntrl Write MtrQaxVoltage Volt f32(val)	15.2200003	15.2200003 ± 4.88E-04	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	16399	16399 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	
PICurrCntrl DualEcuFailSclFac Uls M f32	0.0832000002	0.0832000002 ± 0.0625	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~



Test Step 2.76 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr) MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl Read MtrCurrQax Amp f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl Read SysState Cnt Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-146.723007
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-121.943001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.125
MtrCtrl MtrDampTermQax Ohm M f32[0]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.68200004
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.48800004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-133.520004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-739.294006
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0340000018
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.104999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.337000012
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.964999974
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	481.321014
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	577.322998
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-8.4989996
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	13.1389999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-4.91699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-13.9359999
MtrCtrl_Vecu_Volt_M_f32[0]	18.5559998
MtrCtrl_Vecu_Volt_M_f32[1]	20.9160004
MtrCurrDaxPrevIntg_Volt_M_f32	106.072998
MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1]	-112.455002
MtrCurrQaxCog_Amp_M_f32	-40.9220009
MtrCurrQaxPrevIntg_Volt_M_f32	28.4825993
MtrCurrQaxRef_Amp_M_f32[0]	24.6130009
MtrCurrQaxRef_Amp_M_f32[1]	-20.9400005
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay Rad M f32[0]	-2.37339997
MtrPosComputationDelay_Rad_M_f32[1]	-2.12700009
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.134000003
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0719999969
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0390000008
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.5352
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.524999976
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-784.130005
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-340.130005
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	79.8788986
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.45570001
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-784.130005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-340.130005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	79.8788986
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.45570001
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	1146.66003
k_DualEcuSignalSclFacSlew_UlspS_f32	98.8000031
k_ILOAFdbackSignalScIFacSlew_UlspS_f32	4987.56982
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.10999999
k_MtrCtrlVirualResQax_Ohm_f32	0.0340000018
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0 9.01119995
k_MtrVoltDaxIntegHiLim_Volt_f32	-30.2000008
k MtrVolti Jayintedi oli im Volt 132	-00.2000000
k_MtrVoltDaxIntegLoLim_Volt_f32	1
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32	1 20.6303005





Name	Innut Value		
	Input Value		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	20.9039993		
k_VoltSatQaxPolyCoeff_Uls_f32	-4.77099991		
k_deadtimeVScale_Uls_f32	0.985000014		
t_CommOffsetTblX_Uls_u3p13[0]	492		
t_CommOffsetTblX_Uls_u3p13[1]	7840		
t_CommOffsetTblY_Cnt_u16[0]	589		
t_CommOffsetTblY_Cnt_u16[1]	1202		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	121.994003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	812		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-40.9220009		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	812	812	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	65.5350037	65.5350037 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-4.26297998	-4.26297998 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-2.46629858	-2.46629858 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	18926	18926 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0596499965	0.0596499965 ± 0.0625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.77 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.04700005
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.331999987
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	965.18103
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	180.692001
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0270000007





Name	Input Value		
MtrCtrl_MtrlmpedDax_Ohm_M_f32[1]	0.0149999997		
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0209999997		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0659999996		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.88900006		
	0.802999973		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]			
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-940.226013		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-976.195007		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-17.1070004		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	15.9390001		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.1280003		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-29.3299999		
MtrCtrl_Vecu_Volt_M_f32[0]	21.3729992		
MtrCtrl_Vecu_Volt_M_f32[1]	23.7329998		
MtrCurrDaxPrevIntg_Volt_M_f32	-22.2029991		
MtrCurrDaxRef_Amp_M_f32[0]	24.6130009		
MtrCurrDaxRef_Amp_M_f32[1]	-20.9400005		
MtrCurrQaxCog_Amp_M_f32	-207.917999		
MtrCurrQaxPrevIntg_Volt_M_f32	-31		
MtrCurrQaxRef_Amp_M_f32[0]	-166.035004		
MtrCurrQaxRef_Amp_M_f32[1]	183.065002		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	-1.03180003		
MtrPosComputationDelay_Rad_M_f32[1]	2.37590003		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.216000006		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0729999989		
PICurrCntrl_InverterFailSclFac_UIs_M_f32	0.023		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.945299983		
PICurrCntrl_MtrCurrQaxSatFluxRatio_UIs_M_f32	0.112999998		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	947.73999		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-717.299988		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	94.2040024		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.323000014		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	947.73999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-717.299988		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	94.2040024		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.323000014		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7138.00977		
k_DualEcuSignalSclFacSlew_UlspS_f32	100		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	7847.91016		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.0930000022		
k_MtrCtrlVirualResQax_Ohm_f32	0.050999999		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	3.55049992		
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962		
k MtrVoltQaxFiltFFEnable Cnt lgc	1		
k MtrVoltQaxIntegHiLim Volt f32	5.07770014		
k MtrVoltQaxIntegLoLim Volt f32	-9.64999962		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
	-5.18900013		
k_VoltSatDaxPolyCoeff_Uls_f32			
k_VoltSatQaxPolyCoeff_Uls_f32	-7.39099979		
k_deadtimeVScale_UIs_f32	0.986000001		
t_CommOffsetTblX_Uls_u3p13[0]	2834		
t_CommOffsetTblX_Uls_u3p13[1]	3595		
t_CommOffsetTbIY_Cnt_u16[0]	1165		
t_CommOffsetTblY_Cnt_u16[1]	1651		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0 1 1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	0 1 1 -41.5750008		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	0 1 1 -41.5750008 744		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	0 1 1 -41.5750008 744 75.0830002		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0 1 1 -41.5750008 744 75.0830002 2		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	0 1 1 -41.5750008 744 75.0830002	Expected Value	Result
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0 1 1 -41.5750008 744 75.0830002 2	Expected Value	Result
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name	0 1 1 -41.5750008 744 75.0830002 2 Actual Value	· ·	~
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0 1 1 -41.5750008 744 75.0830002 2 Actual Value 744	744 0 ± 1	→
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_ModIdx_Uls_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	0 1 1 -41.5750008 744 75.0830002 2 Actual Value 744 0 41.8829956	744 0 ± 1 41.8829956 ± 7.81E-03	•
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_ModIdx_Uls_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0 1 1 -41.5750008 744 75.0830002 2 Actual Value 744 0 41.8829956 -3.38360167	744 0 ± 1 41.8829956 ± 7.81E-03 -3.38360167 ± 4.88E-04	Result
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_ModIdx_Uls_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	0 1 1 -41.5750008 744 75.0830002 2 Actual Value 744 0 41.8829956	744 0 ± 1 41.8829956 ± 7.81E-03	•





Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	3.55049992	3.55049992	~
PICurrCntrl DualEcuFailSclFac Uls M f32	0.0855000019	0.0855000019 ± 0.0625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.78 (Repeat Count = 1) Name	Input Value
FastDataAccessBufIndex Cnt M u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr
	0 = = = = 0 = = = = = = = = = = = = = =
VtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
VtrCntrl_Read_MtrCurrOffCorrOffcot. Cot. v15(ntr)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
VtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
VtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	37.4550018
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-2.84500003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0160000008
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0109999999
VtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.039000008
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.41499996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.84899977
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-824.46698
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	454.670013
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0769999996
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0299999993
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0719999969
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0289999992
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.196999997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.954
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-729.622009
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	640.599976
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	0.908999979
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.0249996
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-1.92999995
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	0.432000011
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	26.6639996
MtrCurrDaxRef_Amp_M_f32[0]	-166.035004
MtrCurrDaxRef_Amp_M_f32[1]	183.065002
MtrCurrQaxCog_Amp_M_f32	-198.285995
MtrCurrQaxPrevIntg_Volt_M_f32	31
MtrCurrQaxRef_Amp_M_f32[0]	140.289001
MtrCurrQaxRef_Amp_M_f32[1]	178.235992
MtrCurrQaxRpI_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	0.108199999
MtrPosComputationDelay_Rad_M_f32[1]	2.61420012

PICurrCntrl_Per1



Name	Input Value		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.810000002		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.074000001		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0489999987		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.932299972		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0469999984		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	269.399994		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-453.029999		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	6.96400023		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.331699997		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	269.399994		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-453.029999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	6.96400023		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.331699997		
k CLOAFdbackSignalSclFacSlew UlspS f32	1416.70996		
k_DualEcuSignalSclFacSlew_UlspS_f32	101.199997		
k ILOAFdbackSignalSclFacSlew UlspS f32	5107.18018		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.057		
k_MtrCtrlVirualResQax_Ohm_f32	0.179000005		
k MtrCurrQaxRefModifDsb Cnt lqc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	8.02270031		
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.409998		
k MtrVoltQaxFiltFFEnable Cnt lgc	0		
k MtrVoltQaxIntegHiLim Volt f32	24.8586998		
k MtrVoltQaxIntegLoLim Volt f32	-22.4099998		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-11.7589998		
k_VoltSatQaxPolyCoeff_Uls_f32	-7.63000011		
k deadtimeVScale Uls f32	0.984000027		
t_CommOffsetTblX_Uls_u3p13[0]	2154		
t_CommOffsetTbIX_UIs_u3p13[1]	6783		
t_CommOffsetTblY_Cnt_u16[0]	297		
t_CommOffsetTblY_Cnt_u16[1]	1110		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target MtrCntrl Read MtrCurrDax Amp f32 Val	-190.440994		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	253		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-190.440994		
target MtrCntrl Read SysState Cnt Enum Val	3		
Name	Actual Value	Expected Value	Result
		·	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl Write ModIdx Uls u16p16(val)	1110 64487	1110 64487 ± 1	•
, , ,	220	220 ± 7.81E-03	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)		7.35975122 ± 4.88E-04	-
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	7.3597517		•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	0.15122059	0.151220575 ± 4.88E-04	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	43437	43437 ± 1.52588E-05	· ·
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	•
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0613500029	0.0613500029 ± 0.0625	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~



Test Step 2.79 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
$MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)$	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val) MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	94.3150024
MtrCtrl MtrCurrDaxMaxVal Amp M f32[1]	37.4959984
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.119000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0820000023
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.896000028
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.76800001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-286.584991
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	831.651001
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.633000016
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.927999973 -140.283005
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	380.562012
MtrCtrl MtrVoltDaxFF Volt M f32[0]	-28.2420006
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	12.6160002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-8.4989996
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	13.1389999
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	-18.7919998
MtrCurrDaxRef_Amp_M_f32[0]	140.289001
MtrCurrDaxRef_Amp_M_f32[1]	178.235992
MtrCurrQaxCog_Amp_M_f32	136.341003
MtrCurrQaxPrevIntg_Volt_M_f32	0
MtrCurrQaxRef_Amp_M_f32[0]	91.8850021
MtrCurrQaxRef_Amp_M_f32[1]	182.261002
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0] MtrPosComputationDelay_Rad_M_f32[1]	-1.93439996 -1.87769997
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.93599999
PICurrCntrl DualEcuFailSclFac Uls M f32	0.075000003
PICurrCntrl InverterFailSclFac Uls M f32	0.967000008
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.99089998
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.30399999
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-340.130005
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-826.23999
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	12.8741999
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.75819999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-340.130005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-826.23999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	12.8741999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.75819999
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	5043.06982
k_DualEcuSignalSclFacSlew_UlspS_f32	102.400002
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	1949.64001
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.181999996
k MtrCtrlVirualResQax Ohm f32	0.018999994
k MtrCurrQaxRefModifDsb Cnt lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	21.3099003
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.68999958
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
K_IVILI VOILQAXFIILFFEITADIE CITLIGC	

PICurrCntrl_Per1



Name	Input Value		
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	19.0599995		
k_VoltSatQaxPolyCoeff_Uls_f32	14.7340002		
k_deadtimeVScale_Uls_f32	0.970000029		
t_CommOffsetTblX_Uls_u3p13[0]	418		
t_CommOffsetTblX_Uls_u3p13[1]	4570		
t_CommOffsetTblY_Cnt_u16[0]	23		
t_CommOffsetTblY_Cnt_u16[1]	212		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	83.9489975		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4760		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	83.9489975		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4760	4760	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-44.4560013	-44.4560013 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-2.51786757	-2.51786757 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.14521933	4.14521933 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	39666	39666 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0878000036	0.0878000036 ± 0.0625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	-
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.80 (Repeat Count = 1)		V
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	1	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	212.455994	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	89.8619995	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0850000009	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0270000007	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0149999997	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.20599997	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.90699995	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-304.572998	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	299.334991	

PICurrCntrl_Per1



Picuricilli_Peri			ACTUAL
Name	Input Value		
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009		
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998		
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.093999968		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.85399997		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.3999998		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	778.853027 -658.843994		
MtrCtrl MtrVoltDaxFF Volt M f32[0]	18.6380005		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-23.1870003		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-9.60900021		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	26.3260002		
MtrCtrl_Vecu_Volt_M_f32[0]	14.243		
MtrCtrl_Vecu_Volt_M_f32[1]	16.6030006		
MtrCurrDaxPrevIntg_Volt_M_f32	-9.86299992		
MtrCurrDaxRef_Amp_M_f32[0]	91.8850021		
MtrCurrDaxRef_Amp_M_f32[1]	182.261002		
MtrCurrQaxCog_Amp_M_f32	59.7319984		
MtrCurrQaxPrevIntg_Volt_M_f32	13.4132004		
MtrCurrQaxRef_Amp_M_f32[0]	-218.035004		
MtrCurrQaxRef_Amp_M_f32[1]	11.6370001		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	2.10890007		
MtrPosComputationDelay_Rad_M_f32[1]	0.785000026		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.144999996		
PICurrCotrl DualEcuFailSelFac_Uls_M_f32	0.0759999976		
PICurrCotrl_InverterFailSclFac_Uls_M_f32	0.995999992		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.702400029 0.208000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-784.130005		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	5.05210018		
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.2227		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	-784.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	5.05210018		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.2227		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3857.37988		
k_DualEcuSignalSclFacSlew_UlspS_f32	103.599998		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2438.91992		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.0970000029		
k_MtrCtrlVirualResQax_Ohm_f32	0.0659999996		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	1.79429996		
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc	-4.57000017 0		
	8.95400047		
k_MtrVoltQaxIntegHiLim_Volt_f32 k MtrVoltQaxIntegLoLim Volt f32	-4.57000017		
k_MtrVoltVecuFiltEnable_Cnt_lgc	-4.57000017		
k_VoltSatDaxPolyCoeff_Uls_f32	-12.4820004		
k_VoltSatQaxPolyCoeff_Uls_f32	10.7770004		
k_deadtimeVScale_Uls_f32	0.971000016		
t_CommOffsetTbIX_UIs_u3p13[0]	2163		
t_CommOffsetTbIX_UIs_u3p13[1]	5439		
t_CommOffsetTblY_Cnt_u16[0]	237		
t_CommOffsetTblY_Cnt_u16[1]	383		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-144.667007		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1062		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-144.667007		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	383	383	•
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63635	63635 ± 1	•
AN OUT THE STATE OF THE STATE O	-48.0949974	-48.0949974 ± 7.81E-03	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)			
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-10.6555309 12.0980501	-10.6555309 ± 4.88E-04 12.0980501 ± 4.88E-04	





Name	Actual Value	Expected Value	Result
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	656	656 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl DualEcuFailSclFac Uls M f32	0.0630499944	0.0630499944 ± 0.0625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
//trCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read lytrLoaMtgtnEn Cnt lgc ptr
/trCntrl Read ModIdxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModidxSrlComSvcDft Cnt Igc Val
AtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr
MtrCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val
/trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
/trCntrl Read SysState Cnt Enum(Val)	target MtrCntrl Read SysState Cnt Enum Val
/trCtrl MtrCurrDaxMaxVal Amp M f32[0]	-108.124001
MtrCtrl MtrCurrDaxMaxVal Amp M f32[1]	178.639008
MtrCtrl MtrDampTermDax Ohm M f32[0]	0.041999994
/trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl MtrDampTermQax Ohm M f32[0]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.029999993
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.880999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.5470005
MtrCtrl MtrDaxPropotionalGain Ohm M f32[0]	-747.278992
MtrCtrl MtrDaxPropotionalGain Ohm M f32[1]	-161.845993
MtrCtrl MtrImpedDax Ohm M f32[0]	0.041999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl MtrImpedQax Ohm M f32[0]	0.0170000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0410000011
/trCtrl MtrQaxIntegralGain Ohm M f32[0]	0.050999999
MtrCtrl MtrQaxIntegralGain Ohm M f32[1]	0.836000025
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	1011.37
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	886.40802
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-8.61900043
/trCtrl MtrVoltDaxFF Volt M f32[1]	-13.1560001
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	23.4519997
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-14,2340002
/trCtrl_Vecu_Volt_M_f32[0]	13.3629999
/trCtrl_Vecu_Volt_M_f32[1]	15.7229996
/trCurrDaxPrevIntg Volt M f32	-27.6060009
MtrCurrDaxRef Amp M f32[0]	-218.035004
/trCurrDaxRef Amp M f32[1]	11.6370001
/trCurrQaxCog Amp M f32	1.62199998
/trCurrQaxPrevIntg_Volt_M_f32	-5.69140005
/trCurrQaxRef Amp M f32[0]	-216.921997
/trCurrQaxRef_Amp_M_f32[1]	-184.923996
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	-0.556900024
MtrPosComputationDelay_Rad_M_f32[1]	-2.69639993
PICurrCntrl CurrSensFailSclFac Uls M f32	0.354999989

PICurrCntrl_Per1



Name	Input Value		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0769999996		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.851999998		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.345699996		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0850000009		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	386.220001		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	69.3029022		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.383899987		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	386.220001		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	69.3029022		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.383899987		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3045.54004		
k_DualEcuSignalSclFacSlew_UlspS_f32	104.800003		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4138.33984		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.063000001		
k_MtrCtrlVirualResQax_Ohm_f32	0.101999998		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	18.9549007		
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	16.6681995		
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-2.83899999		
k_VoltSatQaxPolyCoeff_Uls_f32	-14.1759996		
k_deadtimeVScale_Uls_f32	0.990999997		
t_CommOffsetTblX_Uls_u3p13[0]	1162		
t_CommOffsetTblX_Uls_u3p13[1]	1932		
t_CommOffsetTblY_Cnt_u16[0]	71		
t_CommOffsetTblY_Cnt_u16[1]	676		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.3040009		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	50.0610008		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4	4	-
MtrCntrl Write ModIdx Uls u16p16(val)	0	0 ± 1	•
MtrCntrl Write MtrCurrQaxFinalRef Amp f32(val)	-218.543991	-218.543991 ± 7.81E-03	-
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	2.26559305	2.26559305 ± 4.88E-04	•
MtrCntrl Write MtrQaxVoltage Volt f32(val)	-4.40671206	-4.40671206 ± 4.88E-04	•
MtrCntrl Write PhaseAdvanceFinal Rev u0p16(val)	22006	22006 ± 1.52588E-05	•
MtrCurrDaxPrevIntg Volt M f32	0	0	
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.090099978	0.0900999978 ± 0.0625	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32 1	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32 1	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc 1	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum 1	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc 1	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc 1	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc 1	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut 1	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut 1	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState 1	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac 1	1	LoaMtgtnSclFac	1	~
CalLowPassFiltBilinearOut 1	1	CalLowPassFiltBilinearOut	1	~
IntplVarXY_u16_u16Xu16Y_Cnt 1	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16 1	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32 1	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32 1	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16 1	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Name	Input Value	
FastDataAccessBufIndex Cnt M u16	0	
MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc(ptr)	target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr	
MtrCntrl Read IvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr	
MtrCntrl Read ModIdxSrlComSvcDft Cnt Igc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val	
MtrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	
MtrCntrl Read MtrCurrQax Amp f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl MtrCurrDaxMaxVal Amp M f32[0]	-76.8769989	
MtrCtrl MtrCurrDaxMaxVal Amp M f32[1]	-153.238998	
MtrCtrl MtrDampTermDax Ohm M f32[0]	0.098999995	
MtrCtrl MtrDampTermDax Ohm M f32[1]	0.0170000009	
MtrCtrl MtrDampTermQax Ohm M f32[0]	0.112999998	
VtrCtrl MtrDampTermQax Ohm M f32[1]	0.125	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.70299995	
MtrCtrl MtrDaxIntegralGain Ohm M f32[1]	1.4400006	
MtrCtrl MtrDaxPropotionalGain Ohm M f32[0]	317.347992	
VtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-582.065002	
VtrCtrl MtrImpedDax Ohm M f32[0]	0.098999995	
VtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0170000009	
VtrCtrl MtrImpedQax Ohm M f32[0]	0.0130000003	
VtrCtrl MtrImpedQax Ohm M f32[1]	0.0970000029	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.21300006	
MtrCtrl MtrQaxIntegralGain Ohm M f32[1]	1.16400003	
MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	72.6969986	
VtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-560.289978	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-27.0669994	
VtrCtrl MtrVoltDaxFF Volt M f32[1]	28.1070004	
VtrCtrl MtrVoltQaxFF Volt M f32[0]	26.3199997	
VtrCtrl MtrVoltQaxFF Volt M f32[1]	18.0170002	
VtrCtrl Vecu Volt M f32[0]	24.8479996	
VtrCtrl Vecu Volt M f32[1]	27.2080002	
MtrCurrDaxPrevIntg Volt M f32	-31	
MtrCurrDaxRef_Amp_M_f32[0]	-216.921997	
MtrCurrDaxRef_Amp_M_f32[1]	-184.923996	
MtrCurrQaxCog Amp M f32	-126.640999	
MtrCurrQaxPrevIntg Volt M f32	0.20000003	
MtrCurrQaxRef Amp M f32[0]	-82,2979965	
MtrCurrQaxRef_Amp_M_f32[1]	46.8180008	
MtrCurrQaxRpl Amp M f32	0	
MtrPosComputationDelay Rad M f32[0]	1.93110001	
MtrPosComputationDelay_Rad_M_f32[1]	2.75889993	
PICurrCntrl CurrSensFailSclFac Uls M f32	0.661000013	
PICurrCntrl DualEcuFailSclFac Uls M f32	0.078000016	

PICurrCntrl Per1

2016-09-15, 18:23:31+0530



Input Value PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.606999993 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.201399997 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.564999998 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32 -627.179993 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -43.1699982 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 56.7700005 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.200399995 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32$ -627.179993 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 -43.1699982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 56 7700005 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.200399995 k CLOAFdbackSignalSclFacSlew UlspS f32 874 497986 k_DualEcuSignalSclFacSlew_UlspS_f32 106 k ILOAFdbackSignalSclFacSlew UlspS f32 1758 53003 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc k_MtrCtrlFeedbackControlDisable_Cnt_lgc k_MtrCtrlVirualResDax_Ohm_f32 0.156000003 k_MtrCtrlVirualResQax_Ohm_f32 0.0890000015 $k_MtrCurrQaxRefModifDsb_Cnt_lgc$ 0 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 6.24860001 $k_MtrVoltDaxIntegHiLim_Volt_f32$ k_MtrVoltDaxIntegLoLim_Volt_f32 -10.5 $k_MtrVoltQaxFiltFFEnable_Cnt_lgc$ Λ k_MtrVoltQaxIntegHiLim_Volt_f32 18.9195995 -10.5 k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltVecuFiltEnable_Cnt_lgc 0 k VoltSatDaxPolyCoeff Uls f32 13.8400002 k_VoltSatQaxPolyCoeff_Uls_f32 7 34399986 k deadtimeVScale Uls f32 0.964999974 t_CommOffsetTblX_Uls_u3p13[0] 1892 t_CommOffsetTblX_Uls_u3p13[1] 4832 t_CommOffsetTblY_Cnt_u16[0] 912 t CommOffsetTblY Cnt u16[1] 1211 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr 1 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val 0 $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 20.6149998 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 2780 target MtrCntrl Read MtrCurrQax Amp f32 Val -207.917999 target_MtrCntrl_Read_SysState_Cnt_Enum_Val Actual Value **Expected Value** Name Result MtrCntrl Write CommOffset Cnt u16(val) 1211 1211 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 63242 63242 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 44.3430023 44.3430023 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -7.7531743 -7.75317383 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 22.6902637 22.6902637 ± 4.88E-04

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	-
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	•

16708

0.0647500008

16708 ± 1.52588E-05

0.0647500008 ± 0.0625

MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)

MtrCurrDaxPrevIntg_Volt_M_f32

PICurrCntrl_DualEcuFailSclFac_Uls_M_f32





Test Step 2.83 (Repeat Count = 1)	→
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val) MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	target_MtrCntrl_Read_SysState_Cnt_Enum_Val 191.369003
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	107.137001
MtrCtrl MtrDampTermDax Ohm M f32[0]	0.0560000017
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.174999997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.270999998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	317.493011
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	653.375977
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0560000017
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[0] MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.107000001 0.0109999999
MtrCtrl_MtrImpedQax_Ohm_M_f32[1] MtrCtrl MtrQaxIntegralGain Ohm M f32[0]	0.0109999999 1.89100003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.17499995
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	345.561005
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	325.127991
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-24.052
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.3250008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-8.35299969
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-29.7590008
MtrCtrl_Vecu_Volt_M_f32[0]	29.0240002
MtrCtrl_Vecu_Volt_M_f32[1]	30.2299995
MtrCurrDaxPrevIntg_Volt_M_f32	31
MtrCurrDaxRef_Amp_M_f32[0]	138.595001
MtrCurrDaxRef_Amp_M_f32[1]	-157.388
MtrCurrQaxCog_Amp_M_f32 MtrCurrQaxPrevIntg Volt M f32	121.994003 27.8124008
MtrCurrQaxRef_Amp_M_f32[0]	160.044006
MtrCurrQaxRef_Amp_M_f32[1]	165.242004
MtrCurrQaxRpl Amp M f32	0
MtrPosComputationDelay_Rad_M_f32[0]	2.59809995
MtrPosComputationDelay_Rad_M_f32[1]	-0.516900003
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.261999995
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0790000036
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.382999986
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.419499993
PICurrCotrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.226999998
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-10.21 -304.940002
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	95.180397
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.65170002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	-10.21
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-304.940002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	95.180397
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.65170002
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6833.22998
k_DualEcuSignalSclFacSlew_UlspS_f32	107.199997
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3531.6499
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrl/feedbackControlDisable_Cnt_lgc	1 0.030999995
k_MtrCtrlVirualResDax_Ohm_f32 k_MtrCtrlVirualResQax_Ohm_f32	0.0309999995
k_MtrCurrQaxRefModifDsb Cnt lgc	0.131999999
k MtrCurrQaxRefModifRplEn Cnt lgc	0
k MtrVoltDaxIntegHiLim Volt f32	23.7777004
	-11.6000004
k_MtrVoltDaxIntegLoLim_Volt_f32	
·	0
k_MtrVoltDaxIntegLoLim_Volt_f32	
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0

PICurrCntrl_Per1



Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	-19.1399994		
k_VoltSatQaxPolyCoeff_Uls_f32	-21.7269993		
k_deadtimeVScale_Uls_f32	0.981999993		
t_CommOffsetTblX_Uls_u3p13[0]	6349		
t_CommOffsetTblX_Uls_u3p13[1]	7225		
t_CommOffsetTblY_Cnt_u16[0]	49		
t_CommOffsetTblY_Cnt_u16[1]	735		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-190.440994		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3088		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-198.285995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	735	735	-
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	64356	64356 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	38.0500031	38.0500031 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	26.9765892	26.9765873 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	9.19799137	9.19799137 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	40056	40056 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	23.7777004	23.7777004	-
PICurrCntrl DualEcuFailSclFac Uls M f32	0.0924000069	0.0924000069 ± 0.0625	✓

Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.84 (Repeat Count = 1)	<u> </u>
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-147.343002
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	127.972
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.26699996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.0820000023
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-645.427979
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	733.924988
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0120000001
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0560000017

PICurrCntrl Per1

2016-09-15, 18:23:31+0530



Input Value MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 1.03900003 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] 1.528 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] 643.85498 MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] 55.9690018 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -7.66699982 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] 2.61400008 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -29.3959999 $MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]$ -1.94000006 MtrCtrl_Vecu_Volt_M_f32[0] 21.2989998 MtrCtrl_Vecu_Volt_M_f32[1] 23 6590004 MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrDaxRef_Amp_M_f32[0] -100 282997 MtrCurrDaxRef_Amp_M_f32[1] -120.248001 -41 5750008 MtrCurrQaxCog_Amp_M_f32 MtrCurrQaxPrevIntg_Volt_M_f32 22.0902996 MtrCurrQaxRef_Amp_M_f32[0] -65.1900024 MtrCurrQaxRef_Amp_M_f32[1] -216.972 MtrCurrQaxRpl_Amp_M_f32 $MtrPosComputationDelay_Rad_M_f32[0]$ 2.90019989 MtrPosComputationDelay_Rad_M_f32[1] 0.400099993 $PICurrCntrl_CurrSensFailSclFac_Uls_M_f32$ 0.181999996 PICurrCntrl_DualEcuFailSclFac_Uls_M_f32 0.0799999982 $PICurrCntrl_InverterFailSclFac_Uls_M_f32$ 0.349999994 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.615199983 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.521000028 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 570.700012 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -38.7999992 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 78.1542969 PICurrCntrl MtrVecuFilt M str.TermD Uls f32 0.47240001 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32 570.700012 PICurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32 -38.7999992 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32 78 1542969 PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32 0.47240001 k CLOAFdbackSignalSclFacSlew UlspS f32 7388 58984 k_DualEcuSignalSclFacSlew_UlspS_f32 108.400002 k ILOAFdbackSignalSclFacSlew_UlspS_f32 4638.1499 $k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc$ k MtrCtrlFeedbackControlDisable_Cnt_lgc k_MtrCtrlVirualResDax_Ohm_f32 0.123000003 k MtrCtrlVirualResQax Ohm f32 0.0130000003 k_MtrCurrQaxRefModifDsb_Cnt_lgc k MtrCurrQaxRefModifRplEn Cnt lgc 0 9.58860016 $k_MtrVoltDaxIntegHiLim_Volt_f32$ k_MtrVoltDaxIntegLoLim_Volt_f32 -30.2000008 $k_MtrVoltQaxFiltFFEnable_Cnt_lgc$ k_MtrVoltQaxIntegHiLim_Volt_f32 10.2995996 -30 2000008 k MtrVoltQaxIntegLoLim Volt f32 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 -11 0579996 k_VoltSatQaxPolyCoeff_Uls_f32 14.974 k_deadtimeVScale_Uls_f32 0.981999993 t_CommOffsetTblX_Uls_u3p13[0] 3351 t_CommOffsetTblX_Uls_u3p13[1] 5291 t_CommOffsetTblY_Cnt_u16[0] 63 t_CommOffsetTblY_Cnt_u16[1] 327 $target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr$ 1 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr 0 $target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val$ target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 83.9489975 $target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr$ 4554 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 136.341003 $target_MtrCntrl_Read_SysState_Cnt_Enum_Val$ Name **Actual Value Expected Value** Result MtrCntrl Write CommOffset Cnt u16(val) 4554 4554 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 0 -23.6150017 -23.6150017 ± 7.81E-03 MtrCntrl Write MtrCurrQaxFinalRef Amp f32(val) MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 2 6695857 2.66958594 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -4 12085009 -4.12085056 ± 4.88E-04 57022 ± 1.52588E-05 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 57022 MtrCurrDaxPrevIntg_Volt_M_f32

0.0664499998

PICurrCntrl_DualEcuFailSclFac_Uls_M_f32

0.0664499998 ± 0.0625



Test Step Call Trace	est Step Call Trace					
Actual Function	Count	Expected Function	Count	Result		
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~		
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~		
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~		
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~		
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•		
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~		
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~		
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•		
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~		
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•		
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~		
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~		
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~		
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~		
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~		
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓		
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~		
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~		
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~		

Name	Input Value	
-astDataAccessBufIndex_Cnt_M_u16	0	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	6.18900013	
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	83.0540009	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0099999978	
/trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0799999982	
/trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995	
/trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.582000017	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.196999997	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	847.179993	
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-586.309021	
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0099999978	
/trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0799999982	
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0939999968	
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0879999995	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.351999998	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.83099997	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	808.513977	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	75.0500031	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-3.59500003	
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-28.4209995	
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-17.1070004	
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	15.9390001	
MtrCtrl_Vecu_Volt_M_f32[0]	21.3600006	
/trCtrl_Vecu_Volt_M_f32[1]	23.7199993	
MtrCurrDaxPrevIntg_Volt_M_f32	17.9769993	
MtrCurrDaxRef_Amp_M_f32[0]	-68.6760025	
MtrCurrDaxRef_Amp_M_f32[1]	-96.776001	
MtrCurrQaxCog_Amp_M_f32	48.8400002	
MtrCurrQaxPrevIntg_Volt_M_f32	24.0972004	
/trCurrQaxRef_Amp_M_f32[0]	-146.723007	
htrCurrQaxRef_Amp_M_f32[1]	-121.943001	
/trCurrQaxRpl_Amp_M_f32	0	
htrPosComputationDelay_Rad_M_f32[0]	-0.764100015	
/trPosComputationDelay_Rad_M_f32[1]	0.142299995	
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.512000024	
PlCurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0810000002	
PlCurrCntrl InverterFailSclFac Uls M f32	0.275000006	

2016-09-15, 18:23:31+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.342900008		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.686999977		
PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32	-38.7999992		
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	68.4229965		
PICurrCntrl MtrVecuFilt M str.TermD Uls f32	0.445100009		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput Uls f32	-38.7999992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	68.4229965		
PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.445100009		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	329.425995		
k_DualEcuSignalSclFacSlew_UlspS_f32	109.599998		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4506.12012		
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc	1		
k MtrCtrlFeedbackControlDisable Cnt Igc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.018999994		
k MtrCtrlVirualResQax Ohm f32	0.189999998		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k MtrCurrQaxRefModifRplEn Cnt lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	29.8101997		
k MtrVoltDaxIntegLoLim Volt f32	-9.64999962		
k MtrVoltQaxFiltFFEnable Cnt lgc	0		
k MtrVoltQaxInterHiLim Volt f32	30.8836002		
k MtrVoltQaxIntegLoLim Volt_132	-9.64999962		
k MtrVoltVecuFiltEnable Cnt lgc	0		
k VoltSatDaxPolyCoeff Uls f32	17.5830002		
k VoltSatQaxPolyCoeff Uls f32	3.3670001		
k_deadtimeVScale_Uls_f32	0.98699989		
t CommOffsetTbIX UIs u3p13[0]	1450		
t_CommOffsetTbIX_UIs_u3p13[1]	4529		
t_CommOffsetTblY_Cnt_u16[0]	889		
t CommOffsetTblY Cnt u16[1]	1543		
target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr	0		
target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target MtrCntrl Read MtrCurrDax Amp f32 Val	-144.667007		
target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	3203		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
	4		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val		Expected Value	Daguit
Name	Actual Value	· ·	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1543	1543	
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	64684	64684 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-195.563004	-195.563004 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	12.3367414	12.3367414 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-17.0958786	-17.0958767 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	18278	18278 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0947000012	0.0947000012 ± 0.0625	•

est Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	-
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~





Test Step 2.86 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val -105.246002
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0] MtrCtrl MtrCurrDaxMaxVal Amp M f32[1]	41.6290016
MtrCtrl MtrDampTermDax Ohm M f32[0]	0.075000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.071000003
MtrCtrl MtrDampTermQax Ohm M f32[0]	0.0560000017
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0130000003
MtrCtrl MtrDaxIntegralGain Ohm M f32[0]	0.23999995
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.128999993
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-873.200012
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-251.832993
MtrCtrl MtrImpedDax Ohm M f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.070000003
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.69299996
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.763999999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-639.518982
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	659.557007
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	0.908999979
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	21.0249996
MtrCtrl_Vecu_Volt_M_f32[0]	22.3600006
MtrCtrl_Vecu_Volt_M_f32[1]	24.7199993
MtrCurrDaxPrevIntg_Volt_M_f32	-22.4580002
MtrCurrDaxRef_Amp_M_f32[0]	-139.906998
MtrCurrDaxRef_Amp_M_f32[1]	115.814003
MtrCurrQaxCog_Amp_M_f32	107.702003
MtrCurrQaxPrevIntg_Volt_M_f32	25.1226997
MtrCurrQaxRef_Amp_M_f32[0]	-208.287994
MtrCurrQaxRef_Amp_M_f32[1]	-27.9839993
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	-1.32959998
MtrPosComputationDelay_Rad_M_f32[1]	1.80569994
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.84799999
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32 PICurrCntrl InverterFailSclFac Uls M f32	0.0820000023 0.22400007
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.133100003
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.143000007
PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32	-194.190002
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	-194.790002
PICurrCntrl MtrVecuFilt M str.TermN Uls f32	0.987200022
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.676100016
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	-194.190002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-1048.76001
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	0.987200022
PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.676100016
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6782.70996
k_DualEcuSignalSclFacSlew_UlspS_f32	110.800003
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	1518.89001
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.180999994
k_MtrCtrlVirualResQax_Ohm_f32	0.171000004
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	24.4330006
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	24.4650993
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998
k MtrVoltVecuFiltEnable Cnt lgc	0

PICurrCntrl_Per1



Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	10.5150003		
k_VoltSatQaxPolyCoeff_Uls_f32	-6.57200003		
k_deadtimeVScale_Uls_f32	0.972000003		
t_CommOffsetTblX_Uls_u3p13[0]	1663		
t_CommOffsetTblX_Uls_u3p13[1]	5979		
t_CommOffsetTblY_Cnt_u16[0]	771		
t_CommOffsetTblY_Cnt_u16[1]	1636		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-144.667007		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1472		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	1.62199998		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1636	1636	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63700	63700 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-135.686005	-135.686005 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-12.5343781	-12.5343781 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-20.4994259	-20.4994259 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	57326	57326 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	✓
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0681499988	0.0681499988 ± 0.0625	•

Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.87 (Repeat Count = 1)	√
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.25300002
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.0610000007
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-237.227005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	186.412003
MtrCtrl_MtrlmpedDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007

PICurrCntrl_Per1



Picuricilui_Peri			ACTUAL
Name	Input Value		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.61699998		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.86600006		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	896.210999		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-75.5360031		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002		
MtrCtrl_Vecu_Volt_M_f32[0]	20.3600006 22.7199993		
MtrCtrl_Vecu_Volt_M_f32[1]	-22.4869995		
MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrDaxRef_Amp_M_f32[0]	-82.2979965		
MtrCurrDaxRef_Amp_M_f32[1]	46.8180008		
MtrCurrQaxCog_Amp_M_f32	5.72399998		
MtrCurrQaxPrevIntg_Volt_M_f32	25.5816994		
MtrCurrQaxRef_Amp_M_f32[0]	31.5869999		
MtrCurrQaxRef_Amp_M_f32[1]	-186.395996		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	1.33749998		
MtrPosComputationDelay_Rad_M_f32[1]	-1.13859999		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.768999994		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0829999968		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.68599999		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.566999972		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.098999995		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	412.23999		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	64.4531021		
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.970099986 -194.190002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	412.23999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	64.4531021		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.970099986		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2259.41992		
k_DualEcuSignalSclFacSlew_UlspS_f32	112		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	6958.5498		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.0219999999		
k_MtrCtrlVirualResQax_Ohm_f32	0.0149999997		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	14.9607		
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	0.550499976		
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958 0		
k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32	-24.7080002		
k_VoltSatQaxPolyCoeff_Uls_f32	7.73099995		
k deadtimeVScale Uls f32	0.963		
t_CommOffsetTblX_Uls_u3p13[0]	2195		
t_CommOffsetTblX_Uls_u3p13[1]	6013		
t_CommOffsetTblY_Cnt_u16[0]	365		
t_CommOffsetTblY_Cnt_u16[1]	1530		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-144.667007		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1694		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Resu
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1371	1371	•
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	43962	43962 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-192.119995	-192.119995 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	8.55099964	8.55099964 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	12.6160002	12.6160002 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	59873	59873 ± 1.52588E-05	
MtrCurrDaxPrevIntg_Volt_M_f32	0 0000000054	0 0060000064 + 0 0635	
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0969999954	0.0969999954 ± 0.0625	



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	•
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Fest Step 2.88 (Repeat Count = 1)	Input Value
FastDataAccessBufIndex Cnt M u16	0
/trCntrl Read DualEcuMotCtrlMtgnEna Cnt Igc(ptr)	target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr
//trCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr
/trCntrl_Read_NotIdxSrlComSvcDft_Cnt_lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val
	target_MtrCntrl_Read_MotCurrLoaMtgtnEn Cnt lgc ptr
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr) MtrCntrl Read MtrCurrDax Amp f32(Val)	
,	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr) MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target MtrCntrl Read MtrCurrQax Amp f32 Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0089999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.079999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.10000002
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.16100001
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	600.401001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-510.458008
/trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
/trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
/trCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.0939999968
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.709999979
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.37
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-807.60199
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-536.44397
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
/trCtrl_Vecu_Volt_M_f32[0]	14.243
/trCtrl_Vecu_Volt_M_f32[1]	16.6030006
MtrCurrDaxPrevIntg Volt M f32	29.1970005
MtrCurrDaxRef Amp M f32[0]	160.044006
MtrCurrDaxRef Amp M f32[1]	165.242004
/trCurrQaxCog Amp M f32	59.3040009
MtrCurrQaxPrevIntg Volt M f32	3.13030005
MtrCurrQaxRef Amp M f32[0]	-133.947006
/trCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay Rad M f32[0]	-2.95729995
MtrPosComputationDelay_Rad_M_i32[i]	1.63189995
PICurrCntrl CurrSensFailSclFac Uls M f32	0.602999985
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0839999989
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0450000018
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.0560999997 0.046999984

PICurrCntrl_Per1



Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	20.7000008		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	4.45230007		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.590499997		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	20.7000008		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	4.45230007		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.590499997		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3135.04004		
k_DualEcuSignalSclFacSlew_UlspS_f32	113.199997		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	6729.4502		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.046000001		
k_MtrCtrlVirualResQax_Ohm_f32	0.196999997		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	18.1431999		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	30.7143993		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-15.9820004		
k_VoltSatQaxPolyCoeff_Uls_f32	-19.8069992		
k_deadtimeVScale_Uls_f32	0.981000006		
t_CommOffsetTblX_Uls_u3p13[0]	3023		
t_CommOffsetTblX_Uls_u3p13[1]	3703		
t_CommOffsetTblY_Cnt_u16[0]	163		
t_CommOffsetTblY_Cnt_u16[1]	1236		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4486		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1236	1236	-
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	64290	64290 ± 1	-
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-193.251007	-193.251007 ± 7.81E-03	-
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	8.86743355	8.86743259 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	10.7979689	10.7979679 ± 4.88E-04	-
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	41862	41862 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	-
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0698499978	0.0698499978 ± 0.0625	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓	
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	•	
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~	



Test Step 2.89 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-132.813004
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-9.14299965
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.123000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0460000001 0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.123000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1] MtrCtrl MtrDaxIntegralGain Ohm M f32[0]	1.09300005
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.82000005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	2.95000005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-590.848999
MtrCtrl MtrImpedDax Ohm M f32[0]	0.0850000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0170000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0410000011
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.216999993
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.446999997
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	20.6189995
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-802.844971
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-4.55999994
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-20.8330002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-8.61900043
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-13.1560001
MtrCtrl_Vecu_Volt_M_f32[0]	13.3629999
MtrCtrl_Vecu_Volt_M_f32[1]	15.7229996
MtrCurrDaxPrevIntg_Volt_M_f32	-23.1609993
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrDaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxCog_Amp_M_f32	20.6149998
MtrCurrQaxPrevIntg_Volt_M_f32	1.44630003
MtrCurrQaxRef_Amp_M_f32[0]	-105.246002
MtrCurrQaxRef_Amp_M_f32[1]	41.6290016
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	0.698000014
MtrPosComputationDelay_Rad_M_f32[1]	1.64339995
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.0179999992
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0850000009
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0599999987
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0817999989
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-340.130005
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1048.76001
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	64.245903
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.0513000004
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 PICurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32	-340.130005 -1048.76001
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uis_132 PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	64.245903
	0.0513000004
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 k CLOAFdbackSignalSclFacSlew UlspS f32	3946.5
k DualEcuSignalSclFacSlew UlspS f32	114.40002
k_DualecusignalsciFacslew_disp5_i32 k_ILOAFdbackSignalSciFacSlew_UlspS_f32	3683.88989
k_ILOAFdbacksignalsciracsiew_0isps_isz k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	3003.00969
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.0040000019
k_MtrCtrlVirualResQax_Ohm_f32	0.145999998
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k MtrCurrQaxRefModifRplEn Cnt Igc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	5.50430012
k MtrVoltDaxIntegLoLim Volt f32	-25.6000004
k MtrVoltQaxFiltFFEnable Cnt lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	23.5613003
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004
k MtrVoltVecuFiltEnable Cnt Igc	0

PICurrCntrl_Per1



Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	-1.58000004		
k_VoltSatQaxPolyCoeff_Uls_f32	24.8470001		
k_deadtimeVScale_Uls_f32	0.962000012		
t_CommOffsetTblX_Uls_u3p13[0]	6128		
t_CommOffsetTblX_Uls_u3p13[1]	7397		
t_CommOffsetTblY_Cnt_u16[0]	1081		
t_CommOffsetTblY_Cnt_u16[1]	1779		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	177.046997		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4823		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	177.046997		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1779	1779	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63045	63045 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	21.0140018	21.0140018 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-12.7889309	-12.7889309 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-8.07618523	-8.07618523 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	60418	60418 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl DualEcuFailSclFac Uls M f32	0.0993000045	0.0993000045 ± 0.0625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	~

Test Step 2.90 (Repeat Count = 1)		✓
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	1	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	31.5869999	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-186.395996	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0340000018	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.104999997	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.115999997	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.115999997	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.47399998	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.90199995	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-146.214005	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-942.195007	
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994	
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009	
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994	
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.123000003	





Name	Input Value		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.49000001		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.959999979		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-53.862999		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	75.7020035		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-30.2169991		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	19.2049999		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-27.0669994		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	28.1070004		
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002		
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008		
	-18.5370007		
MtrCurrDayPer Amp. M. 63/01			
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007		
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001		
MtrCurrQaxCog_Amp_M_f32	79.6729965		
MtrCurrQaxPrevIntg_Volt_M_f32	21.3169994		
MtrCurrQaxRef_Amp_M_f32[0]	-146.173996		
MtrCurrQaxRef_Amp_M_f32[1]	-213.335007		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	0.28639999		
MtrPosComputationDelay_Rad_M_f32[1]	-0.813000023		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.40000006		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0860000029		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0590000004		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.231900007		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.337000012		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	22.2399998		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	16.5851002		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.887899995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	22.2399998		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
	16.5851002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	0.887899995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32			
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2663.65991		
k_DualEcuSignalSclFacSlew_UlspS_f32	115.599998		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5194.8999		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.112000003		
k_MtrCtrlVirualResQax_Ohm_f32	0.0219999999		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	23.7327003		
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	19.5590992		
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5		
k MtrVoltVecuFiltEnable Cnt lgc	1		
k VoltSatDaxPolyCoeff Uls f32	-2.4230001		
k VoltSatQaxPolyCoeff Uls f32	-21.368		
k deadtimeVScale Uls f32	0.958999991		
t_CommOffsetTbIX_Uls_u3p13[0]	6528		
	8192		
t_CommOffsetTbIX_UIs_u3p13[1]			
t_CommOffsetTblY_Cnt_u16[0]	76		
t_CommOffsetTblY_Cnt_u16[1]	211		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-9.31999969		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	65		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-9.31999969		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	65	65	
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)		-220 ± 7.81E-03	
	-220	2E0 ± 1.01E-00	
	-220 2 70515442	2 70515442 + 4 885 04	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	2.70515442	2.70515442 ± 4.88E-04	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	2.70515442 3.95906138	3.95906138 ± 4.88E-04	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	2.70515442 3.95906138 63308	3.95906138 ± 4.88E-04 63308 ± 1.52588E-05	·
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	2.70515442 3.95906138	3.95906138 ± 4.88E-04	•



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	✓
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	-
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	-
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	-
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓

lame	Input Value
FastDataAccessBufIndex Cnt M u16	1
/trCntrl Read DualEcuMotCtrlMtgnEna Cnt Igc(ptr)	target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr
trCntrl Read IvtrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read lytrLoaMtgtnEn Cnt lgc ptr
ItrCntrl Read ModIdxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModidxSrlComSvcDft Cnt Igc Val
ItrCntrl Read MotCurrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc ptr
/trCntrl Read MtrCurrDax Amp f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
htrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr
/trCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
ItrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
htrCtrl MtrCurrDaxMaxVal Amp M f32[0]	-133.947006
htrCtrl MtrCurrDaxMaxVal Amp M f32[1]	75.7020035
ItrCtrl MtrDampTermDax Ohm M f32[0]	0.0949999988
htrCtrl MtrDampTermDax_Ohm M f32[1]	0.0179999992
ItrCtrl MtrDampTermQax Ohm M f32[0]	0.112999998
ttrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.125
NtrCtrl MtrDaxIntegralGain Ohm M f32[0]	0.558000028
ItrCtrl MtrDaxIntegralGain Ohm M f32[1]	1.505
htrCtrl MtrDaxPropotionalGain Ohm M f32[0]	-763.603027
htrCtrl MtrDaxPropotionalGain Ohm M f32[1]	830.864014
trCtrl MtrImpedDax Ohm M f32[0]	0.098999995
trCtrl MtrImpedDax_Ohm M f32[1]	0.0170000009
ItrCtrl MtrImpedQax Ohm M f32[0]	0.115999997
/trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.115999997
htrCtrl MtrQaxIntegralGain Ohm M f32[0]	0.0810000002
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.354999989
htrCtrl MtrQaxPropotionalGain Ohm M f32[0]	657.155029
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-284.454987
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	22.7639999
trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	9.54300022
trCtrl MtrVoltQaxFF Volt M f32[0]	-24.052
ItrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-25.3250008
htrCtrl_Vecu_Volt_M_f32[0]	14.243
/trCtrl_Vecu_Volt_M_f32[1]	16.6030006
trCurrDaxPrevIntg_Volt_M_f32	-10.9969997
htrCurrDaxRef_Amp_M_f32[0]	-208.287994
/trCurrDaxRef_Amp_M_f32[1]	-27.9839993
htrCurrQaxCog_Amp_M_f32	0.486999989
/trCurrQaxPrevIntg_Volt_M_f32	26.5330009
ItrCurrQaxRef Amp M f32[0]	-91.4420013
ItrCurrQaxRef Amp M f32[1]	133.692993
ItrCurrQaxRpl Amp M f32	0
ItrPosComputationDelay Rad M f32[0]	0.907299995
ItrPosComputationDelay Rad M f32[1]	-1.30149996
PlCurrCntrl CurrSensFailSclFac Uls M f32	0.533999979
PlCurrCntrl DualEcuFailSclFac Uls M f32	0.0869999975
PICurrCntrl InverterFailSclFac UIs M f32	0.0460000001

PICurrCntrl Per1

2016-09-15, 18:23:31+0530



Input Value PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32 0.339700013 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.492000014 PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32 -43.1699982 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -453.029999 PICurrCntrl MtrVecuFilt M str.TermN Uls f32 51.8735008 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.139899999 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 -43.1699982 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32$ -453.029999 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 51.8735008 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32$ 0 139899999 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 k_DualEcuSignalSclFacSlew_UlspS_f32 116 800003 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 4664.1001 $k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc$ 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0.0250000004 k_MtrCtrlVirualResDax_Ohm_f32 k_MtrCtrlVirualResQax_Ohm_f32 0.151999995 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ 0 k_MtrVoltDaxIntegHiLim_Volt_f32 26.3267994 $k_MtrVoltDaxIntegLoLim_Volt_f32$ -11.6000004 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 16 5105991 $k_MtrVoltQaxIntegHiLim_Volt_f32$ k_MtrVoltQaxIntegLoLim_Volt_f32 -11.6000004 k MtrVoltVecuFiltEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 11.9359999 k VoltSatQaxPolyCoeff Uls f32 -16.2380009 k_deadtimeVScale_Uls_f32 0.950999975 t CommOffsetTblX Uls u3p13[0] 1106 $t_CommOffsetTblX_Uls_u3p13[1]$ 4701 t CommOffsetTblY Cnt u16[0] 363 t_CommOffsetTblY_Cnt_u16[1] 989 target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr 0 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 0 $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -161.352005 $target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr$ 3229 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -161.352005 target_MtrCntrl_Read_SysState_Cnt_Enum_Val **Actual Value Expected Value** Name Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 989 989 62324 62324 ± 1 MtrCntrl_Write_ModIdx_Uls_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 133.205994 133.205994 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 14.7205381 14.7205372 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -5 71074629 -5.71074581 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 6669 6669 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

0.101599999

0.101599999 ± 0.0625

PICurrCntrl_DualEcuFailSclFac_Uls_M_f32





Test Step 2.92 (Repeat Count = 1)	√
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val) MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	target_MtrCntrl_Read_SysState_Cnt_Enum_Val 209.052002
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-124.994003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0939999968
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.03999996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.82000005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-632.612
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-39.875
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0560000017
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[0] MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.112999998 0.125
MtrCtrl_MtrImpedQax_Onm_M_rs2[1] MtrCtrl MtrQaxIntegralGain Ohm M f32[0]	0.125
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.62100005
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-201.291
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-817.749023
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	13.3629999
MtrCtrl_Vecu_Volt_M_f32[1]	15.7229996
MtrCurrDaxPrevIntg_Volt_M_f32	-17.9279995
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxCog_Amp_M_f32	-190.440994
MtrCurrQaxPrevIntg_Volt_M_f32 MtrCurrQaxRef_Amp_M_f32[0]	26.2782993 171.485992
MtrCurrQaxRef Amp M f32[1]	163.787003
MtrCurrQaxRpl Amp M f32	0
MtrPosComputationDelay Rad M f32[0]	1.91369998
MtrPosComputationDelay_Rad_M_f32[1]	1.35399997
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.090000036
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0879999995
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.88499999
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.660399973
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.535000026
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-826.23999 45.0001006
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32 PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	15.0881996 0.349099994
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput Uls f32	-43.1699982
PICurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32	-826.23999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	15.0881996
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.349099994
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	8000
k_DualEcuSignalSclFacSlew_UlspS_f32	118
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5272.3999
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.179000005
k_MtrCtrlVirualResQax_Ohm_f32	0.0209999997
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	24.5879993
k_MtrVoltDaxIntegLoLim_Volt_f32 k MtrVoltQaxFiltFFEnable Cnt lgc	-30.2000008 1
·	20.5517998
k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32	20.5517998 -30.2000008

PICurrCntrl_Per1



Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	24.6410007		
k_VoltSatQaxPolyCoeff_Uls_f32	20.0030003		
k_deadtimeVScale_Uls_f32	0.967999995		
t_CommOffsetTblX_Uls_u3p13[0]	687		
t_CommOffsetTblX_Uls_u3p13[1]	7234		
t_CommOffsetTblY_Cnt_u16[0]	341		
t_CommOffsetTblY_Cnt_u16[1]	370		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-205.514999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	389		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-205.514999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	389	389	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-0.118355408	-0.118355393 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.83855247	-4.83855247 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	47146	47146 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl DualEcuFailSclFac Uls M f32	0.0732499957	0.0732499957 ± 0.0625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	~

Test Step 2.93 (Repeat Count = 1)		✓
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-200.556	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4449997	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0850000009	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.495	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.398999989	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	184.223999	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-915.817017	
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009	
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998	

2016-09-15, 18:23:31+0530





Name	Input Value		
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0850000009		
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.112999998		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.58099997		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.776000023		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-189.419998		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	896.187988		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-28.4209995		
MtrCtrl_Vecu_Volt_M_f32[0]	24.8479996		
MtrCtrl_Vecu_Volt_M_f32[1]	27.2080002		
MtrCurrDaxPrevIntg_Volt_M_f32	-9.66300011		
MtrCurrDaxRef_Amp_M_f32[0]	-133.947006		
MtrCurrDaxRef_Amp_M_f32[1]	75.7020035		
MtrCurrQaxCog_Amp_M_f32	83.9489975		
MtrCurrQaxPrevIntg_Volt_M_f32	9.36159992		
MtrCurrQaxRef_Amp_M_f32[0]	106.072998		
MtrCurrQaxRef_Amp_M_f32[1]	-112.455002		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	-2.32159996		
MtrPosComputationDelay_Rad_M_f32[1]	0.166500002		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.155000001		
PICurrCntrl DualEcuFailSclFac Uls M f32	0.0890000015		
PICurrCntrl InverterFailSclFac UIs M f32	0.158000007		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.768999994		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.565999985		
PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32	267.119995		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	58.6325989		
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.559199989		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	267.119995		
	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	58.6325989		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	0.559199989		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32			
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	1848.06995		
k_DualEcuSignalSclFacSlew_UlspS_f32	119.199997		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	6831.5		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0.021999999		
k_MtrCtrlVirualResDax_Ohm_f32			
k_MtrCtrlVirualResQax_Ohm_f32	0.041999994		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRpIEn_Cnt_lgc			
k_MtrVoltDaxIntegHiLim_Volt_f32	2.43009996		
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	24.5324001		
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	2.3499999		
k_VoltSatQaxPolyCoeff_Uls_f32	21.7280006		
k_deadtimeVScale_UIs_f32	0.957000017		
t_CommOffsetTblX_Uls_u3p13[0]	474		
t_CommOffsetTblX_Uls_u3p13[1]	6954		
t_CommOffsetTblY_Cnt_u16[0]	434		
t_CommOffsetTblY_Cnt_u16[1]	1438		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-118.848		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2090		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-118.848		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	2090	2090	-
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	22.1240005	22.1240005 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-4.73769617	-4.73769665 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-0.671159625	-0.671159685 ± 4.88E-04	-
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	23469	23469 ± 1.52588E-05	•
MtrCurrDaxPrevIntg Volt M f32	0	0	_

MtrCurrDaxPrevIntg_Volt_M_f32





Name	Actual Value	Expected Value	Result
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.1039	0.1039 ± 0.0625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

lawa.	Innut Value	
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	
/trCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	
/trCntrl_Read_ModidxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
htrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
htrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-200.556	
htrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4449997	
ItrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0850000009	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.495	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.398999989	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	184.223999	
ItrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-915.817017	
ltrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009	
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998	
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0850000009	
/trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.112999998	
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.58099997	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.776000023	
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-189.419998	
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	896.187988	
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008	
1trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005	
/ltrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003	
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-28.4209995	
/ltrCtrl_Vecu_Volt_M_f32[0]	24.8479996	
/ltrCtrl_Vecu_Volt_M_f32[1]	27.2080002	
/ltrCurrDaxPrevIntg_Volt_M_f32	-9.66300011	
/trCurrDaxRef_Amp_M_f32[0]	-133.947006	
/trCurrDaxRef_Amp_M_f32[1]	75.7020035	
/ltrCurrQaxCog_Amp_M_f32	83.9489975	
/trCurrQaxPrevIntg_Volt_M_f32	15.9664001	
/trCurrQaxRef_Amp_M_f32[0]	106.072998	
/trCurrQaxRef_Amp_M_f32[1]	-112.455002	
/trCurrQaxRpl_Amp_M_f32	0	
MtrPosComputationDelay_Rad_M_f32[0]	1.36259997	
MtrPosComputationDelay_Rad_M_f32[1]	1.81219995	
PICurrCntrl CurrSensFailSclFac Uls M f32	0.155000001	

PICurrCntrl Per1

2016-09-15, 18:23:31+0530



Input Value PICurrCntrl DualEcuFailSclFac Uls M f32 0.0900000036 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.158000007 PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32 0.147200003 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.565999985 PICurrCntrl MtrVecuFilt_M_str.PrevInput_UIs_f32 267.119995 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 947.73999 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 0.442999989 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.579999983 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32 267.119995 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32$ 947 73999 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 0.442999989 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32$ 0.579999983 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 100 k_DualEcuSignalSclFacSlew_UlspS_f32 120.400002 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 6831.5 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 1 $k_MtrCtrlFeedbackControlDisable_Cnt_lgc$ k_MtrCtrlVirualResDax_Ohm_f32 0.0219999999 k_MtrCtrlVirualResQax_Ohm_f32 0.0419999994 k_MtrCurrQaxRefModifDsb_Cnt_lgc $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ Λ k_MtrVoltDaxIntegHiLim_Volt_f32 0.578599989 k_MtrVoltDaxIntegLoLim_Volt_f32 -22.4099998 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 9.73509979 k_MtrVoltQaxIntegLoLim_Volt_f32 -22.4099998 k_MtrVoltVecuFiltEnable_Cnt_lgc $k_VoltSatDaxPolyCoeff_Uls_f32$ 2.3499999 k VoltSatQaxPolyCoeff Uls f32 21.7280006 k_deadtimeVScale_Uls_f32 0.957000017 t CommOffsetTblX Uls u3p13[0] t_CommOffsetTblX_Uls_u3p13[1] 6954 t CommOffsetTblY Cnt u16[0] 434 t_CommOffsetTblY_Cnt_u16[1] 1438 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 1 $target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val$ target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -118.848 $target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr$ 2090 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -118.848 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 0 **Actual Value Expected Value** Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 2090 2090 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 0 ± 1 22 1240005 22.1240005 ± 7.81E-03 $MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)$ MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -25.3770008 -25.3770008 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -3 59500003 -3 59500003 + 4 88F-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 61897 61897 ± 1.52588E-05 $MtrCurrDaxPrevIntg_Volt_M_f32$ 0 PICurrCntrl_DualEcuFailSclFac_Uls_M_f32 0.0749500021 0.0749500021 ± 0.0625



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	-
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

lame	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
/trCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	
ItrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
ItrCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val	
ItrCntrl Read MotCurrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc ptr	
ItrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
ItrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	
ItrCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val	
ItrCntrl Read SysState Cnt Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
ItrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979	
ItrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001	
ltrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999	
trCtrl MtrDampTermDax Ohm M f32[1]	0.039000008	
trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995	
trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009	
ltrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.56099999	
trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.21300006	
ItrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	907.228027	
ltrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-851.888	
trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994	
trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009	
trCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994	
ltrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009	
ItrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.0930000022	
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.467999995	
ltrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-286.428009	
ltrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	784.336975	
ItrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302	
trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964	
trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-25.3770008	
trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	21.3880005	
ltrCtrl_Vecu_Volt_M_f32[0]	25.3600006	
trCtrl_Vecu_Volt_M_f32[1]	27.7199993	
ltrCurrDaxPrevIntg_Volt_M_f32	-27.3339996	
trCurrDaxRef_Amp_M_f32[0]	209.052002	
ltrCurrDaxRef_Amp_M_f32[1]	-124.994003	
ltrCurrQaxCog_Amp_M_f32	-144.667007	
trCurrQaxPrevIntg_Volt_M_f32	21.0373001	
trCurrQaxRef_Amp_M_f32[0]	24.6130009	
trCurrQaxRef_Amp_M_f32[1]	-20.9400005	
trCurrQaxRpl_Amp_M_f32	0	
ltrPosComputationDelay_Rad_M_f32[0]	-2.93910003	
ltrPosComputationDelay_Rad_M_f32[1]	2.14949989	
ICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0	
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0909999982	
ICurrCntrl InverterFailSclFac Uls M f32	0.178000003	

PICurrCntrl_Per1

2016-09-15, 18:23:31+0530



0.106199995 ± 0.0625

Name	Input Value		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0496999994		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.833000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	404.899994		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	267.119995		
PICurrCntrl MtrVecuFilt M str.TermN Uls f32	17.1812992		
PICurrCntrl MtrVecuFilt M str.TermD Uls f32	0.386400014		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput Uls f32	404.899994		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	267.119995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	17.1812992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.386400014		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	4653.20996		
k_DualEcuSignalSclFacSlew_UlspS_f32	121.599998		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	1635.59998		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.171000004		
k_MtrCtrlVirualResQax_Ohm_f32	0.180000007		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	18.0632		
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	1.61609995		
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-22.6819992		
k_VoltSatQaxPolyCoeff_Uls_f32	2.70700002		
k_deadtimeVScale_Uls_f32	0.970000029		
t_CommOffsetTblX_Uls_u3p13[0]	4506		
t_CommOffsetTblX_Uls_u3p13[1]	5381		
t_CommOffsetTblY_Cnt_u16[0]	156		
t_CommOffsetTblY_Cnt_u16[1]	1570		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.3040009		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4809		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	50.0610008		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4809	4809	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	169.280014	169.280014 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	10.8069401	10.8069391 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-28.0609188	-28.060915 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	63814	63814 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•

0.106199995

PICurrCntrl_DualEcuFailSclFac_Uls_M_f32



Test Step 2.96 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrOffCorrOffcot Ont (16(ntr))	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val) MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	127.972
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.075000003
MtrCtrl MtrDampTermQax Ohm M f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.841000021
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.24800003
MtrCtrl MtrDaxPropotionalGain Ohm M f32[0]	-552.150024
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-568.89502
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.075000003
MtrCtrl MtrImpedQax Ohm M f32[0]	0.112999998
MtrCtrl MtrlmpedQax Ohm M f32[1]	0.125
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.921000004
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.173999995
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	1018.71997
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-471.221985
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-29.3959999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-1.9400006
MtrCtrl_Vecu_Volt_M_f32[0]	17.7010002
MtrCtrl_Vecu_Volt_M_f32[1]	20.0610008
MtrCurrDaxPrevIntg_Volt_M_f32	20.9669991
MtrCurrDaxRef_Amp_M_f32[0]	-139.906998
MtrCurrDaxRef_Amp_M_f32[1]	115.814003
MtrCurrQaxCog_Amp_M_f32	-41.5750008
MtrCurrQaxPrevIntg_Volt_M_f32	23.1735001
MtrCurrQaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrQaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	-0.864000022
MtrPosComputationDelay_Rad_M_f32[1]	-2.79839993
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1.
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0920000002
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.662999988
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.952600002
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.686999977
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	865.320007
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	67.9733963
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.439500004
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	865.320007
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	67.9733963
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.439500004
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	1265.93005
k_DualEcuSignalSclFacSlew_UlspS_f32	122.800003
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5888.85986
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.151999995
k_MtrCtrlVirualResQax_Ohm_f32	0.0329999998
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	11.7454004
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	7.04580021
k MtrVoltQaxIntegLoLim Volt f32	-4.57000017

PICurrCntrl_Per1



Name	Input Value		
k MtrVoltVecuFiltEnable Cnt lqc	0		
k VoltSatDaxPolyCoeff Uls f32	-24.3470001		
k VoltSatQaxPolyCoeff Uls f32	8.97500038		
k deadtimeVScale Uls f32	0.970000029		
t_CommOffsetTblX_Uls_u3p13[0]	3030		
t_CommOffsetTblX_Uls_u3p13[1]	5366		
t_CommOffsetTblY_Cnt_u16[0]	589		
t_CommOffsetTblY_Cnt_u16[1]	1202		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	50.0610008		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4196		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	589	589	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	10634	10634 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-175.397003	-175.397003 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	2.61400008	2.61400008 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-1.94000006	-1.94000006 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	59391	59391 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	11.7454004	11.7454004	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0766500011	0.0766500011 ± 0.0625	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	-
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	•
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.97 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	6.18900013
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	83.0540009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.70099998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.287999988
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-1014.57001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	639.960022
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0939999968

2016-09-15, 18:23:31+0530





Name	Input Value		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0879999995		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.42200005		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.612999976		
MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	-811.013		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-317.71701		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-3.59500003		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-28.4209995		
	-17.1070004		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]			
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	15.9390001		
MtrCtrl_Vecu_Volt_M_f32[0]	18.5559998		
MtrCtrl_Vecu_Volt_M_f32[1]	20.9160004		
MtrCurrDaxPrevIntg_Volt_M_f32	10.2959995		
MtrCurrDaxRef_Amp_M_f32[0]	-82.2979965		
MtrCurrDaxRef_Amp_M_f32[1]	46.8180008		
MtrCurrQaxCog_Amp_M_f32	48.8400002		
MtrCurrQaxPrevIntg_Volt_M_f32	12.8893003		
MtrCurrQaxRef_Amp_M_f32[0]	-146.723007		
MtrCurrQaxRef_Amp_M_f32[1]	-121.943001		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay Rad M f32[0]	3.05150008		
MtrPosComputationDelay_Rad_M_f32[1]	2.39380002		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.640999973		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0930000022		
PICurrCntrl InverterFailScIFac Uls M f32	0.88499999		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.120399997		
PICurrCntrl_MtrCurrQaxSatFluxRatio_UIs_M_f32	0.120399997		
	267.119995		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32			
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-38.7999992		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	74.1108017		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.654399991		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	267.119995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-38.7999992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	74.1108017		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.654399991		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7251.52002		
k_DualEcuSignalSclFacSlew_UlspS_f32	124		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5272.3999		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k MtrCtrlVirualResDax Ohm f32	0.126000002		
k_MtrCtrlVirualResQax_Ohm_f32	0.128999993		
k_MtrCurrQaxRefModifDsb_Cnt_Igc	1		
k MtrCurrQaxRefModifRplEn Cnt lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	20.9356995		
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	9.26469994		
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	12.3450003		
k_VoltSatQaxPolyCoeff_Uls_f32	-21.0529995		
k_deadtimeVScale_Uls_f32	0.967999995		
t_CommOffsetTbIX_UIs_u3p13[0]	4850		
t_CommOffsetTblX_Uls_u3p13[1]	6241		
t_CommOffsetTblY_Cnt_u16[0]	165		
t_CommOffsetTblY_Cnt_u16[1]	651		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3061		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
		Pour esta de la	J
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	3061	3061	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-195.563004	-195.563004 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-0.995374382	-0.995374382 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.73654222	-4.73654222 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	1221	1221 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
DICurrental DualFauFailCalFaa IIIa M 622	0.400500004	0.400500004 + 0.0005	

0.108500004

PICurrCntrl_DualEcuFailSclFac_Uls_M_f32

0.108500004 ± 0.0625



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.98 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-105.246002
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	41.6290016
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.075000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0710000023
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0560000017
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.74000001
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.391000003
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	382.878998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-891.598022
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl MtrlmpedDax Ohm M f32[1]	0.123000003
MtrCtrl MtrlmpedQax Ohm M f32[0]	0.070000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.207000002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.145999998
MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	-192.985992
MtrCtrl MtrQaxPropotionalGain Ohm M f32[1]	708.689026
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl MtrVoltQaxFF Volt M f32[0]	0.908999979
MtrCtrl MtrVoltQaxFF Volt M f32[1]	21.0249996
MtrCtrl_Vecu_Volt_M_f32[0]	21.3729992
MtrCtrl_Vecu_Volt_M_f32[1]	23.7329998
MtrCurrDaxPrevIntg_Volt_M_f32	28.4400005
MtrCurrDaxRef_Amp_M_f32[0]	160.044006
MtrCurrDaxRef_Amp_M_f32[1]	165.242004
MtrCurrQaxCog_Amp_M_f32	107.702003
MtrCurrQaxPrevIntg Volt M f32	9.35029984
MtrCurrQaxRef_Amp_M_f32[0]	-208.287994
MtrCurrQaxRef_Amp_M_f32[1]	-27.9839993
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	1.90050006
MtrPosComputationDelay_Rad_M_f32[1]	2.34050012
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.986999989
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0939999968
PICurrCntrl InverterFailSclFac Uls M f32	0.370000005

PICurrCntrl Per1

2016-09-15, 18:23:31+0530



Input Value PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32 0.382499993 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.0989999995 PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32 -657.099976 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -657.130005 PICurrCntrl MtrVecuFilt M str.TermN Uls f32 79.4266968 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.757300019 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 -657.099976 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32$ -657.130005 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 79.4266968 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32$ 0.757300019 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 5252.41016 k_DualEcuSignalSclFacSlew_UlspS_f32 125.199997 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 10 $k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc$ 1 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0 0.192000002 k_MtrCtrlVirualResDax_Ohm_f32 k_MtrCtrlVirualResQax_Ohm_f32 0.145999998 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ 0 k_MtrVoltDaxIntegHiLim_Volt_f32 9.65880013 $k_MtrVoltDaxIntegLoLim_Volt_f32$ -10.5 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 23.7485008 $k_MtrVoltQaxIntegHiLim_Volt_f32$ k_MtrVoltQaxIntegLoLim_Volt_f32 -10.5 k MtrVoltVecuFiltEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 8.97799969 k VoltSatQaxPolyCoeff Uls f32 18.2439995 k_deadtimeVScale_Uls_f32 0.975000024 t CommOffsetTblX Uls u3p13[0] 2114 $t_CommOffsetTblX_Uls_u3p13[1]$ 4735 t CommOffsetTblY Cnt u16[0] 297 t_CommOffsetTblY_Cnt_u16[1] 1110 target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr 1 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 0 $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -198.285995 $target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr$ 2412 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 1.62199998 $target_MtrCntrl_Read_SysState_Cnt_Enum_Val$ **Actual Value Expected Value** Name Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1110 1110 63897 63897 ± 1 MtrCntrl_Write_ModIdx_Uls_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -220 -220 ± 7.81E-03 19.7092075 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 19.7092075 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 6.76738214 6.76738167 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 32757 32757 ± 1.52588E-05

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	-
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

0.0783499926

0.0783499926 ± 0.0625

MtrCurrDaxPrevIntg_Volt_M_f32 PICurrCntrl_DualEcuFailSclFac_Uls_M_f32





Test Step 2.99 (Repeat Count = 1)	v v
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993 -66.7229996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1] MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.076999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl MtrDampTermQax Ohm M f32[1]	0.075000003
MtrCtrl MtrDaxIntegralGain Ohm M f32[0]	1.15499997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.71800005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	953.320984
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-556.945007
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.34200001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.0759999976
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	41.5550003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	935.856018 -16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl Vecu Volt M f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	1.82700002
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrDaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxCog_Amp_M_f32	5.72399998
MtrCurrQaxPrevIntg_Volt_M_f32	7.8980999
MtrCurrQaxRef_Amp_M_f32[0]	31.5869999
MtrCurrQaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	-2.64689994 -1.53659999
MtrPosComputationDelay_Rad_M_f32[1] PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.818000019
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0949999988
PICurrCntrl InverterFailSclFac Uls M f32	0.61500001
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.0132999998
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.0469999984
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	97.2235031
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.703100026
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	97.2235031
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.703100026
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	18.2099991
k_DualEcuSignalSclFacSlew_UlspS_f32	126.400002 8000
k_ILOAFdbackSignalSclFacSlew_UlspS_f32 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_Igc	8000
k MtrCtrlFeedbackControlDisable Cnt lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.0500000007
k_MtrCtrlVirualResQax_Ohm_f32	0.0469999984
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	16.8875999
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.6000004
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	29.7059002
k_MtrVoltQaxIntegLoLim_Volt_f32	-11.6000004
k MtrVoltVecuFiltEnable Cnt lgc	1

PICurrCntrl_Per1

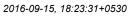


Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	-14.7080002		
k_VoltSatQaxPolyCoeff_Uls_f32	3.90300012		
k_deadtimeVScale_Uls_f32	0.957000017		
t_CommOffsetTblX_Uls_u3p13[0]	1498		
t_CommOffsetTblX_Uls_u3p13[1]	4940		
t_CommOffsetTblY_Cnt_u16[0]	623		
t_CommOffsetTblY_Cnt_u16[1]	1212		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1116		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1116	1116	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	25.862999	25.862999 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-2.39211178	-2.39211178 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.1441555	-4.1441555 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	10620	10620 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	✓
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.110799998	0.110799998 ± 0.0625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	~

Test Step 2.100 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.335000008
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.61000001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	676.015015
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	949.322021
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125

PICurrCntrl_Per1





Name	Input Value		
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.093999968		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.521000028		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.65699995		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	40.1660004		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	649.921021		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003		
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002		
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008		
MtrCurrDaxPrevIntg_Volt_M_f32	-11.698		
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007		
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001		
MtrCurrQaxCog_Amp_M_f32	59.3040009		
MtrCurrQaxPrevIntg_Volt_M_f32	3.4605999		
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006		
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	-1.73670006		
MtrPosComputationDelay_Rad_M_f32[1]	0.894200027		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.810000002		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0960000008		
PICurrCntrl InverterFailSclFac Uls M f32	0.551999986		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.397899985		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.725000024		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-38.7999992		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	18.5506001		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.689499974		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-38.7999992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	18.5506001		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.689499974		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	5675.16992		
k_DualEcuSignalSclFacSlew_UlspS_f32	127.599998		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	7779.18994		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0.174999997		
k_MtrCtrlVirualResDax_Ohm_f32			
k_MtrCtrlVirualResQax_Ohm_f32	0.0280000009		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	-		
k_MtrVoltDaxIntegHiLim_Volt_f32	4.64909983		
k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	9.52950001		
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-2.73900008		
k_VoltSatQaxPolyCoeff_Uls_f32	-7.03700018		
k_deadtimeVScale_UIs_f32	0.958000004		
t_CommOffsetTbIX_UIs_u3p13[0]	6110		
t_CommOffsetTbIX_UIs_u3p13[1]	7324		
t_CommOffsetTblY_Cnt_u16[0]	237		
t_CommOffsetTblY_Cnt_u16[1]	383		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2039		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Resu
MtrCntrl_Write_CommOffset_Cnt_u16(val)	383	383	•
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	62783	62783 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	16.3980026	16.3980026 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-10.3380842	-10.3380833 ± 4.88E-04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-17.6049614	-17.6049595 ± 4.88E-04	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	47633	47633 ± 1.52588E-05	
Mt-Our-Deviler-Math. Math. M. 600	30 3000008	20 2000000	

-30.2000008

-30.2000008

MtrCurrDaxPrevIntg_Volt_M_f32





Name	Actual Value	Expected Value	Result
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0800499991	0.0800499991 ± 0.0625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

lame	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	1	
/trCntrl Read DualEcuMotCtrlMtgnEna Cnt Igc(ptr)	target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr	
trCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr	
htrCntrl Read ModidxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModidxSrlComSvcDft Cnt lgc Val	
ItrCntrl Read MotCurrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc ptr	
htrCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val	
ItrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
ltrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996	
ltrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007	
trCtrl MtrDampTermDax Ohm M f32[0]	0.079999982	
ItrCtrl MtrDampTermDax Ohm M f32[1]	0.00899999961	
ItrCtrl MtrDampTermQax Ohm M f32[0]	0.0099999978	
ItrCtrl MtrDampTermQax Ohm M f32[1]	0.0799999982	
trCtrl MtrDaxIntegralGain Ohm M f32[0]	0.335000008	
trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.61000001	
ltrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	676.015015	
ltrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	949.322021	
trCtrl MtrImpedDax Ohm M f32[0]	0.112999998	
trCtrl MtrImpedDax Ohm M f32[1]	0.125	
ItrCtrl MtrImpedQax Ohm M f32[0]	0.0529999994	
ltrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968	
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.521000028	
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.65699995	
ltrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	40.1660004	
ltrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	649.921021	
trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001	
trCtrl MtrVoltDaxFF Volt M f32[1]	-13.6160002	
trCtrl MtrVoltQaxFF Volt M f32[0]	18.6380005	
trCtrl MtrVoltQaxFF Volt M f32[1]	-23.1870003	
trCtrl_Vecu_Volt_M_f32[0]	18.9510002	
trCtrl Vecu Volt M f32[1]	21.3110008	
ItrCurrDaxPrevIntg_Volt_M_f32	-11.698	
trCurrDaxRef_Amp_M_f32[0]	-146.723007	
ItrCurrDaxRef_Amp_M_f32[1]	-121.943001	
ItrCurrQaxCog Amp M f32	59.3040009	
trCurrQaxPrevIntg Volt M f32	10.2080002	
trCurrQaxRef Amp M f32[0]	-133.947006	
trCurrQaxRef_Amp_M_f32[1]	75.7020035	
trCurrQaxRpl_Amp_M_f32	0	
trPosComputationDelay_Rad_M_f32[0]	2.8204	
trPosComputationDelay_Rad_M_f32[1]	2.93499994	
CurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.810000002	
CICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0970000029	
ICurrCntrl InverterFailSclFac Uls M f32	0.551999986	

PICurrCntrl Per1

2016-09-15, 18:23:31+0530



Input Value PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32 0.338800013 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.725000024 PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32 -627.179993 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -304.940002 PICurrCntrl MtrVecuFilt M str.TermN Uls f32 21.2028008 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.0865999982 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 -627.179993 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32$ -304.940002 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 21.2028008 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32$ 0.0865999982 5675.16992 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 k_DualEcuSignalSclFacSlew_UlspS_f32 128 800003 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 100 $k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc$ 1 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.174999997 k_MtrCtrlVirualResQax_Ohm_f32 0.0280000009 k_MtrCurrQaxRefModifDsb_Cnt_lgc $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ k_MtrVoltDaxIntegHiLim_Volt_f32 0.178399995 k_MtrVoltDaxIntegLoLim_Volt_f32 -9.64999962 k_MtrVoltQaxFiltFFEnable_Cnt_lgc $k_MtrVoltQaxIntegHiLim_Volt_f32$ 14 8720999 k_MtrVoltQaxIntegLoLim_Volt_f32 -9.64999962 k MtrVoltVecuFiltEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 -2.73900008 k VoltSatQaxPolyCoeff Uls f32 -7.03700018 k_deadtimeVScale_Uls_f32 0.958000004 t CommOffsetTblX Uls u3p13[0] 6110 $t_CommOffsetTblX_Uls_u3p13[1]$ 7324 t CommOffsetTblY Cnt u16[0] 237 t_CommOffsetTblY_Cnt_u16[1] 383 target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr 0 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 0 $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 59.7319984 2039 $target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr$ target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -34.6189995 $target_MtrCntrl_Read_SysState_Cnt_Enum_Val$ 2 **Actual Value Expected Value** Name Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 383 62783 ± 1 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 62783 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 16.3980026 16.3980026 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -19.7145958 -19.7145939 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 5.30521631 5.30521584 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 16971 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32 -9 64999962 -9 64999962

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	-
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

0.113100007

0.113100007 ± 0.0625

PICurrCntrl_DualEcuFailSclFac_Uls_M_f32





Test Step 2.102 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModidxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val) MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	target_MtrCntrl_Read_SysState_Cnt_Enum_Val 31.5869999
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-186.395996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0340000018
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.104999997
MtrCtrl MtrDampTermQax Ohm M f32[0]	0.115999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.115999997
MtrCtrl MtrDaxIntegralGain Ohm M f32[0]	1.47399998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.90199995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-146.214005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-942.195007
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.49000001
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.959999979
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-53.862999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	75.7020035
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-30.2169991
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	19.2049999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-27.0669994
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	28.1070004
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	-18.5370007
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	79.6729965
MtrCurrQaxPrevIntg_Volt_M_f32	0.771099985
MtrCurrQaxRef_Amp_M_f32[0]	-146.173996
MtrCurrQaxRef_Amp_M_f32[1]	-213.335007
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	1.01540005
MtrPosComputationDelay_Rad_M_f32[1]	-2.31789994
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.40000006 0.097999974
PICurrCntrl InverterFailScIFac Uls M f32	0.0979999974
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	
PICurrCntrl MtrCurrQaxSatFluxRatio_UIs_M_132	0.375200003 0.337000012
PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32	22.2399998
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_OIs_132 PICurrCntrl MtrVecuFilt M str.PrevOutput UIs f32	-43.1699982
PICurrCntrl MtrVecuFilt M str.TermN Uls f32	16.5851002
PICurrCntrl_MtrVecuFilt_M_str.TermN_Ois_132 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.887899995
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	22.2399998
PICurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32	-43.1699982
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	16.5851002
PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.887899995
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2663.65991
k DualEcuSignalSclFacSlew UlspS f32	130
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5194.8999
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.112000003
k_MtrCtrlVirualResQax_Ohm_f32	0.0219999999
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	19.6758003
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	8.2833004
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998
k MtrVoltVecuFiltEnable Cnt lgc	1

PICurrCntrl_Per1



Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	-2.4230001		
k_VoltSatQaxPolyCoeff_Uls_f32	-21.368		
k_deadtimeVScale_Uls_f32	0.958999991		
t_CommOffsetTblX_Uls_u3p13[0]	6528		
t_CommOffsetTblX_Uls_u3p13[1]	8192		
t_CommOffsetTblY_Cnt_u16[0]	76		
t_CommOffsetTblY_Cnt_u16[1]	211		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-9.31999969		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	65		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-9.31999969		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	65	65	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	2.70515442	2.70515442 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	3.95906138	3.95906138 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	47612	47612 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.081749998	0.081749998 ± 0.0625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	~

Test Step 2.103 (Repeat Count = 1)		V
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.73699999	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.397000015	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	692.312988	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	147.145996	
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997	
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997	

2016-09-15, 18:23:31+0530





Name	Input Value		
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.29700005		
MtrCtrl MtrQaxIntegralGain Ohm M f32[1]	0.0260000005		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	381.019012		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-514.21698		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964		
MtrCtrl MtrVoltQaxFF Volt M f32[0]	-28.2420006		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002		
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981		
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995		
MtrCurrDaxPrevIntg_Volt_M_f32	-1.17400002		
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024		
MtrCurrDaxRef_Amp_M_f32[1]	-216.972		
MtrCurrQaxCog Amp M f32	5.72399998		
MtrCurrQaxPrevIntg_Volt_M_f32	23.9027004		
MtrCurrQaxRef_Amp_M_f32[0]	31.5869999		
MtrCurrQaxRef_Amp_M_f32[1]	-186.395996		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	1.8154		
MtrPosComputationDelay_Rad_M_f32[1]	-1.37559998		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.109999999		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.098999995		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.289799988		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0469999984		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-38.7999992		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	39.4047012		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.869400024		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-38.7999992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	39.4047012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.869400024		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3550.11011		
k_DualEcuSignalSclFacSlew_UlspS_f32	131.199997		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5873.56006		
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k MtrCtrlVirualResDax Ohm f32	0.162		
k_MtrCtrlVirualResQax_Ohm_f32	0.0790000036		
k MtrCurrQaxRefModifDsb Cnt lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	26.7257996		
k MtrVoltDaxIntegLoLim Volt f32	-8.68999958		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k MtrVoltQaxIntegHiLim Volt f32	15.8303003		
k MtrVoltQaxIntegLoLim Volt f32	-8.68999958		
k MtrVoltVecuFiltEnable Cnt lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	24.2010002		
k_VoltSatQaxPolyCoeff_Uls_f32	-9.57699966		
k deadtimeVScale Uls f32	0.963999987		
t_CommOffsetTblX_Uls_u3p13[0]	573		
	7569		
t_CommOffsetTbIX_UIs_u3p13[1]			
t_CommOffsetTblY_Cnt_u16[0]	912		
t_CommOffsetTblY_Cnt_u16[1]	1211		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	410		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	410	410	
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	·
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	25.862999	25.862999 ± 7.81E-03	-
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-2.40960884	-2.40960884 ± 4.88E-04	·
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.17446756	-4.17446756 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	57164	57164 ± 1.52588E-05	·
MtrCurrDaxPrevIntg Volt M f32	0	0	✓

MtrCurrDaxPrevIntg_Volt_M_f32

PICurrCntrl_Per1



Name	Actual Value	Expected Value	Result
PICurrCntrl DualEcuFailSclFac Uls M f32	0.115400001	0.115400001 ± 0.0625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Input Value 1 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val -212.632996 -205.085007 0.0799999982 0.00899999961
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val -212.632996 -205.085007 0.0799999982
target_MtrCntrl_Read_IvtrLoaMtgtnEn_cnt_lgc_ptr target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val target_MtrCntrl_Read_MotCurrLoaMtgtnEn_cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val -212.632996 -205.085007 0.0799999982
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val -212.632996 -205.085007 0.0799999982
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val -212.632996 -205.085007 0.0799999982
target_MtrCntrl_Read_MtrCurrDax_mp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val -212.632996 -205.085007 0.0799999982
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val -212.632996 -205.085007 0.0799999982
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val -212.632996 -205.085007 0.0799999982
target_MtrCntrl_Read_SysState_Cnt_Enum_Val -212.632996 -205.085007 0.0799999982
-212.632996 -205.085007 0.0799999982
-205.085007 0.0799999982
0.0799999982
0.0089999961
0.00033333301
0.0099999978
0.079999982
0.878000021
1.29400003
-295.479004
-442.687988
0.112999998
0.125
0.0529999994
0.0939999968
0.896000028
1.91700006
1015.31
-261.230011
-0.736000001
-13.6160002
18.6380005
-23.1870003
18.9510002
21.3110008
-16.7709999
-146.723007
-121.943001
59.3040009
17.1947002
-133.947006
75.7020035
0
-0.675599992
-0.35800001
0.65899979

PICurrCntrl Per1

2016-09-15, 18:23:31+0530



Input Value PICurrCntrl DualEcuFailSclFac Uls M f32 0.100000001 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.203999996 PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32 0.49939999 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.725000024 PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32 570.700012 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 96.5500031 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 56.0906982 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.676299989 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32 570.700012 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 96 5500031 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 56.0906982 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32$ 0.676299989 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 6301.31982 k_DualEcuSignalSclFacSlew_UlspS_f32 132 399994 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 3999.36011 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 1 $k_MtrCtrlFeedbackControlDisable_Cnt_lgc$ k_MtrCtrlVirualResDax_Ohm_f32 0.129999995 k_MtrCtrlVirualResQax_Ohm_f32 0.182999998 k_MtrCurrQaxRefModifDsb_Cnt_lgc $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ Λ k_MtrVoltDaxIntegHiLim_Volt_f32 24.9897995 k_MtrVoltDaxIntegLoLim_Volt_f32 -4.57000017 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 17.6005001 k_MtrVoltQaxIntegLoLim_Volt_f32 -4.57000017 k MtrVoltVecuFiltEnable Cnt lgc $k_VoltSatDaxPolyCoeff_Uls_f32$ -3.84200001 k VoltSatQaxPolyCoeff Uls f32 18.5489998 k_deadtimeVScale_Uls_f32 0.963999987 t CommOffsetTblX Uls u3p13[0] 1154 t_CommOffsetTblX_Uls_u3p13[1] 5284 t CommOffsetTblY Cnt u16[0] 49 t_CommOffsetTblY_Cnt_u16[1] 735 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 $target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val$ 0 $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 59.7319984 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 1520 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -34.6189995 target_MtrCntrl_Read_SysState_Cnt_Enum_Val **Actual Value Expected Value** Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 735 735 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 63176 63176 ± 1 16 3980026 16 3980026 + 7 81F-03 $MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)$ MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -10.4028311 -10.4028301 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -17 7152214 -17 7152195 + 4 88F-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 34572 ± 1.52588E-05 34572 -4 57000017 -4 57000017 MtrCurrDaxPrevIntg_Volt_M_f32

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	•
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•

0.0834500045

0.0834500045 ± 0.0625

PICurrCntrl_DualEcuFailSclFac_Uls_M_f32



Test Step 2.105 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val) MtrCtrl MtrCurrDaxMaxVal Amp M f32[0]	target_MtrCntrl_Read_SysState_Cnt_Enum_Val 67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.010999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl MtrDampTermQax Ohm M f32[0]	0.098999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.76499999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.986999989
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-48.4529991
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-844.020996
MtrCtrl_MtrlmpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrlmpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.22599995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.58099997
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	17.7269993
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-203.524002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	18.5559998
MtrCtrl_Vecu_Volt_M_f32[1]	20.9160004
MtrCurrDaxPrevIntg_Volt_M_f32	-1.39499998 31.5869999
MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxCog_Amp_M_f32	-144.667007
MtrCurrQaxPrevIntg_Volt_M_f32	7.91989994
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.787003
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	2.22340012
MtrPosComputationDelay_Rad_M_f32[1]	2.74799991
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.944000006
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.101000004
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.294999987
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.936500013
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	0
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	5.45760012
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.833899975
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	0
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	5.45760012
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.833899975
k_CLOAFdbackSignalSclFacSlew_UlspS_f32 k_DualEcuSignalSclFacSlew_UlspS_f32	5388.75 133.600006
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3076.13989
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k MtrCtrlVirualResDax Ohm f32	0.123999998
k_MtrCtrlVirualResQax_Ohm_f32	0.163000003
k MtrCurrQaxRefModifDsb Cnt lgc	1
k MtrCurrQaxRefModifRplEn Cnt lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	3.2125001
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	9.89789963
k MtrVoltQaxIntegLoLim Volt f32	-25.6000004

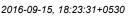
PICurrCntrl_Per1

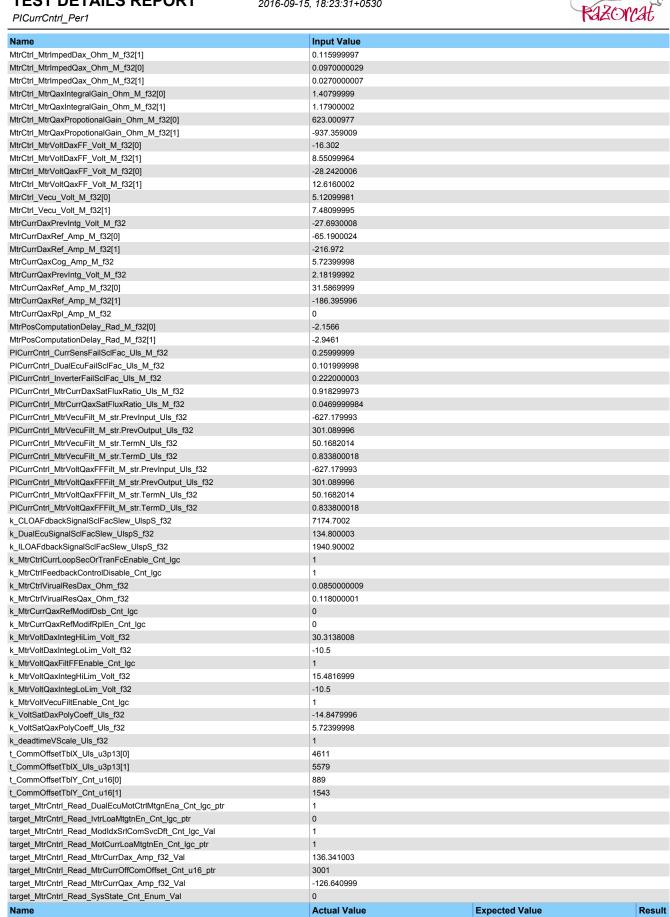


Name	Input Value		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-23.9650002		
k_VoltSatQaxPolyCoeff_Uls_f32	-16.2169991		
k_deadtimeVScale_Uls_f32	0.949999988		
t_CommOffsetTblX_Uls_u3p13[0]	5022		
t_CommOffsetTblX_Uls_u3p13[1]	7003		
t_CommOffsetTblY_Cnt_u16[0]	63		
t_CommOffsetTblY_Cnt_u16[1]	327		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3921		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	3921	3921	~
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-4.21121168	-4.21121168 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-2.19731569	-2.19731569 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	1791	1791 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.117700003	0.117700003 ± 0.0625	~

Actual Function	Count	Expected Function	Count	Result
MtrCntrl Read MtrCurrQax Amp f32	1	MtrCntrl Read MtrCurrQax Amp f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
ntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	_

Test Step 2.106 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.08099997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.26699999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	484.062988
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	62.8199997
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997





Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	3001	3001	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-192.119995	-192.119995 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	8.55099964	8.55099964 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	12.6160002	12.6160002 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	41020	41020 ± 1.52588E-05	~





Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl DualEcuFailSclFac Uls M f32	0.085149996	0.085149996 ± 0.0625	✓

Test Step Call Trace	Test Step Call Trace			V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	-
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Name	Input Value
FastDataAccessBufIndex Cnt M u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr
MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr
MtrCntrl Read ModIdxSrlComSvcDft Cnt Igc(Val)	target_MtrCntrl_Read_ModidxSrlComSvcDft Cnt lgc Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_var
MtrCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val
MtrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr
MtrCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target MtrCntrl Read SysState Cnt Enum Val
WtrCtrl MtrCurrDaxMaxVal Amp M f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
VtrCtrl MtrDampTermDax Ohm M f32[0]	0.079999982
WtrCtrl MtrDampTermDax_Ohm M f32[1]	0.00899999961
VtrCtrl_MtrDampTermDax_Offin_iv_i32[1]	0.0099999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982
WtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.42499995
MtrCtrl MtrDaxIntegralGain Ohm M f32[1]	1.64400005
WtrOtrl_MtrDaxPropotionalGain_Ohm_M_i32[0]	980.661987
MtrCtrl MtrDaxPropotionalGain Ohm M f32[1]	771.224976
MtrCtrl MtrImpedDax Ohm M f32[0]	0.112999998
WtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968
MtrCtrl MtrQaxIntegralGain Ohm M f32[0]	1.26199996
MtrCtrl MtrQaxIntegralGain Ohm M f32[1]	1.75600004
VtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	424.487
VtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	866.411987
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
VtrCtrl MtrVoltDaxFF Volt M f32[1]	-13.6160002
VtrCtrl MtrVoltQaxFF Volt M f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	20.066
MtrCurrDaxRef Amp M f32[0]	-146.723007
MtrCurrDaxRef Amp M f32[1]	-121.943001
MtrCurrQaxCog Amp M f32	59.3040009
MtrCurrQaxPrevIntg Volt M f32	13.4927998
MtrCurrQaxRef Amp M f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl Amp M f32	0
MtrPosComputationDelay Rad M f32[0]	0.737800002
MtrPosComputationDelay Rad M f32[1]	1.74370003

2016-09-15, 18:23:31+0530



PICurrCntrl_Per1

FIGUITORIUI_FELT		10	in Citodo
Name	Input Value		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.185000002		
PICurrCntrl DualEcuFailSclFac Uls M f32	0.103		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.737999976		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.543500006		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.725000024		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-200.740005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	85.9597015		
PICurrCntrl MtrVecuFilt M str.TermD Uls f32	0.842700005		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput UIs f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-200.740005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	85.9597015		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32	0.842700005		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3789.18994		
k_DualEcuSignalSclFacSlew_UlspS_f32	136		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3865.98999		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k MtrCtrlVirualResDax Ohm f32	0.00700000022		
k_MtrCtrlVirualResQax_Ohm_f32	0.149000004		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	19.9689007		
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.60000038		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k MtrVoltQaxIntegHiLim Volt f32	16.1835003		
k_MtrVoltQaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-10.0129995		
k_VoltSatQaxPolyCoeff_Uls_f32	-13.5450001		
k deadtimeVScale Uls f32	0.995000005		
t_CommOffsetTbIX_UIs_u3p13[0]	459		
t_CommOffsetTbIX_UIs_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	771		
t_CommOffsetTbIY_Cnt_u16[1]	1636		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val	0		
target MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4728		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
		•	Kesuii
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1636	1636	
MtrCntrl_Write_ModIdx_UIs_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	65208 16.3980026	65208 ± 1 16.3980026 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-10.7373629	-10.7373619 ± 4.88E-04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-18.2849026	-18.2849007 ± 4.88E-04	
MtrCutrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	56494	56494 ± 1.52588E-05	
MtrCurrDaxPrevIntg_Volt_M_f32	-9.60000038	-9.60000038	
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.120000005	0.120000005 ± 0.0625	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~



Test Step 2.108 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.039000008 0.098999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0] MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0363939393
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.247999996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.372999996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	875.137024
MtrCtrl MtrDaxPropotionalGain Ohm M f32[1]	-484.88501
MtrCtrl MtrImpedDax Ohm M f32[0]	0.041999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl MtrImpedQax Ohm M f32[0]	0.041999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl MtrQaxIntegralGain Ohm M f32[0]	1.29700005
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.26699999
MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	-49.7849998
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	739.11499
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	18.5559998
MtrCtrl_Vecu_Volt_M_f32[1]	20.9160004
MtrCurrDaxPrevIntg_Volt_M_f32	-7.71299982
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxCog_Amp_M_f32	-144.667007
MtrCurrQaxPrevIntg_Volt_M_f32	2.60570002
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.787003
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	3.01139998
MtrPosComputationDelay_Rad_M_f32[1]	-2.0072999
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.723999977
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.104000002
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.80099999
PICurrCotrl_MtrCurrOavSatFluxRatio_Uls_M_f32	0.335799992
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32 PICurrCntrl MtrVecuFilt M str.PrevOutput UIs f32	1118 -194.190002
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uis_r32 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	19.6403008
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.13330006
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput Uls f32	1118
PICurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32	-194.190002
PICurrCntrl MtrVoltQaxFFFiit M str.TermN Uls f32	19.6403008
PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32	0.133300006
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3827.27002
k DualEcuSignalSclFacSlew UlspS f32	137.199997
k ILOAFdbackSignalSclFacSlew UlspS f32	2156.63989
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.112000003
k_MtrCtrlVirualResQax_Ohm_f32	0.147
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	3.43280005
k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	15.2376003

PICurrCntrl_Per1



Name	Input Value		
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-20.5009995		
k_VoltSatQaxPolyCoeff_Uls_f32	6.51900005		
k_deadtimeVScale_Uls_f32	0.975000024		
t_CommOffsetTbIX_UIs_u3p13[0]	2638		
t_CommOffsetTbIX_UIs_u3p13[1]	3628		
t_CommOffsetTblY_Cnt_u16[0]	297		
t_CommOffsetTblY_Cnt_u16[1]	1110		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	121.994003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	34		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-40.9220009		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	34	34	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-4.75500202	-4.75500202 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-1.07498026	-1.07498026 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	12707	12707 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0868500024	0.0868500024 ± 0.0625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	-
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.109 (Repeat Count = 1)		✓
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.474999994	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.837000012	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	673.796997	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-165.348999	





Name	Input Value		
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997		
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997		
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029		
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.027000007		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.0149999997		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.88800001		
MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	-32.8989983		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	814.530029		
MtrCtrl MtrVoltDaxFF Volt M f32[0]	-16.302		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002		
MtrCtrl_Vecu_Volt_M_f32[0]	21.3729992		
	23.7329998		
MtrCtrl_Vecu_Volt_M_f32[1]	-7.6500001		
MtrCurrDavPrevIntg_Volt_M_f32			
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024		
MtrCurrDaxRef_Amp_M_f32[1]	-216.972		
MtrCurrQaxCog_Amp_M_f32	5.72399998		
MtrCurrQaxPrevIntg_Volt_M_f32	6.07289982		
MtrCurrQaxRef_Amp_M_f32[0]	31.5869999		
MtrCurrQaxRef_Amp_M_f32[1]	-186.395996		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	-1.93190002		
MtrPosComputationDelay_Rad_M_f32[1]	-1.18069994		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.848999977		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.104999997		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.296000004		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.281399995		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0469999984		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-717.299988		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-861.580017		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	88.4244995		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.322100013		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-717.299988		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-861.580017		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	88.4244995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.322100013		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	208.033005		
k_DualEcuSignalSclFacSlew_UlspS_f32	138.399994		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5517.5		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k MtrCtrlVirualResDax Ohm f32	0.061999999		
k_MtrCtrlVirualResQax_Ohm_f32	0.098999995		
	1		
k_MtrCurrQaxRefModifDsb_Cnt_lgc			
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	0.790099978		
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	25.3572006		
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-6.70300007		
k_VoltSatQaxPolyCoeff_Uls_f32	-3.44300008		
k_deadtimeVScale_Uls_f32	0.977999985		
t_CommOffsetTbIX_Uls_u3p13[0]	1212		
t_CommOffsetTblX_Uls_u3p13[1]	1704		
t_CommOffsetTblY_Cnt_u16[0]	23		
t_CommOffsetTblY_Cnt_u16[1]	212		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-41.5750008		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1147		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	75.0830002		
	75.0830002		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val		e c tvi	la .
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1147	1147	
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	0 25.862999	25.862999 ± 7.81E-03	V
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0		





Name	Actual Value	Expected Value	Result
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	60899	60899 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0.790099978	0.790099978	~
PICurrCntrl DualEcuFailSclFac Uls M f32	0.122299999	0.122299999 ± 0.0625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.110 (Repeat Count = 1)	▼
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.22500002
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.88600004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	800.210022
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-622.848022
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.82700002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.74600005
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-301.071014
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	368.852997
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	-9.05200005
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	59.3040009
MtrCurrQaxPrevIntg_Volt_M_f32	11.2777004
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	1.29970002

2016-09-15, 18:23:31+0530



PICurrCntrl_Per1

Name	Input Value		
MtrPosComputationDelay_Rad_M_f32[1]	-2.23920012		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.231999993		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.105999999		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.448000014		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.565900028		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	947.700012		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	75.3476028		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.841799974		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	947.700012		
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	75.3476028		
PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.841799974		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6145.56982		
k DualEcuSignalSclFacSlew UlspS f32	139.600006		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4019.20996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.046000001		
k_MtrCtrlVirualResQax_Ohm_f32	0.101000004		
k MtrCurrQaxRefModifDsb Cnt lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k MtrVoltDaxIntegHiLim Volt f32	0.3583		
k MtrVoltDaxIntegLoLim Volt f32	-22.4099998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	0.993200004		
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.409998		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	23.7310009		
k VoltSatQaxPolyCoeff Uls f32	10.2309999		
k_deadtimeVScale_Uls_f32	0.986999989		
t_CommOffsetTbIX_UIs_u3p13[0]	3808		
t_CommOffsetTbIX_UIs_u3p13[1]	7298		
t_CommOffsetTblY_Cnt_u16[0]	1237		
t_CommOffsetTblY_Cnt_u16[1]	383		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr	0		
· · · · · · · · · · · · · · · · · · ·			
target_MtrCntrl_Read_MtrCurrOffComOffcot_Cnt_v16_ntr	48.8400002 2022		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr			
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-220 3		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val			1=
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	383	383	
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	64684	64684 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	16.3980026	16.3980026 ± 7.81E-03	_
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-3.73893094	-3.73893094 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-6.36711168	-6.36711168 ± 4.88E-04	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	14951	14951 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0885500014	0.0885500014 ± 0.0625	✓



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Name	Input Value
FastDataAccessBufIndex Cnt M u16	0
VtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc(ptr)	target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr
/trCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read lytrLoaMtgtnEn Cnt lgc ptr
	target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	
<pre>htrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr) htrCntrl Read MtrCurrDax Amp f32(Val)</pre>	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
/trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr) /trCntrl Read MtrCurrQax Amp f32(Val)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
, ,	
MtrCntrl_Read_SysState_Cnt_Enum(Val) MtrCtrl MtrCurrDaxMaxVal Amp M f32[0]	target_MtrCntrl_Read_SysState_Cnt_Enum_Val 67.4899979
	119.721001
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
htrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.039000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995
/trCtrl_MtrDawpTermQax_Ohm_M_f32[1]	0.0170000009
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.62300003
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.23300004
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	810.853027
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-988.492981
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.041999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.041999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.263000011
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.324999988
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-344.360992
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	396.108002
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
/trCtrl_Vecu_Volt_M_f32[0]	18.9510002
/trCtrl_Vecu_Volt_M_f32[1]	21.3110008
/trCurrDaxPrevIntg_Volt_M_f32	18.9990005
/trCurrDaxRef_Amp_M_f32[0]	31.5869999
/ltrCurrDaxRef_Amp_M_f32[1]	-186.395996
/ltrCurrQaxCog_Amp_M_f32	-144.667007
/trCurrQaxPrevIntg_Volt_M_f32	3.09949994
/trCurrQaxRef_Amp_M_f32[0]	171.485992
/trCurrQaxRef_Amp_M_f32[1]	163.787003
/ltrCurrQaxRpl_Amp_M_f32	0
/ltrPosComputationDelay_Rad_M_f32[0]	1.56770003
/trPosComputationDelay_Rad_M_f32[1]	0.73360002
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.777999997
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.107000001
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0450000018
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.95569998
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004

PICurrCntrl_Per1

2016-09-15, 18:23:31+0530



Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-38.7999992		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	269.399994		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	77.2248001		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.411900014		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-38.7999992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	269.399994		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	77.2248001		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.411900014		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6667.54004		
k_DualEcuSignalSclFacSlew_UlspS_f32	140.800003		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	7823.27002		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.0049999989		
k_MtrCtrlVirualResQax_Ohm_f32	0.098999995		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	30.7106991		
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	1.35650003		
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	12.0270004		
k_VoltSatQaxPolyCoeff_Uls_f32	19.8290005		
k_deadtimeVScale_Uls_f32	0.970000029		
t_CommOffsetTblX_Uls_u3p13[0]	918		
t_CommOffsetTblX_Uls_u3p13[1]	1679		
t_CommOffsetTblY_Cnt_u16[0]	71		
t_CommOffsetTblY_Cnt_u16[1]	676		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	107.702003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	233		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	220		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	233	233	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-19.898735	-19.8987331 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	22.5442982	22.5442944 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	8809	8809 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.124600001	0.124600001 ± 0.0625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•



Test Step 2.112 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val) MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	target_MtrCntrl_Read_SysState_Cnt_Enum_Val 67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.010999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.039000008
MtrCtrl MtrDampTermQax Ohm M f32[0]	0.098999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl MtrDaxIntegralGain Ohm M f32[0]	0.859000027
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.911000013
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-309.057007
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-788.815002
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.00399995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.43400002
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-19.7409992
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-1021.15997
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	14.243
MtrCtrl_Vecu_Volt_M_f32[1]	16.6030006
MtrCurrDaxPrevIntg_Volt_M_f32	-3.78200006
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	-144.667007
MtrCurrQaxPrevIntg_Volt_M_f32	23.843399
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	-2.87389994
MtrPosComputationDelay_Rad_M_f32[1]	-2.28410006
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.851999998
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32 PICurrCntrl InverterFailSclFac Uls M f32	0.108000003
PICUITCHUI_INVERTEIFAIISCIFAC_OIS_M_I32 PICUITCHUI MtrCurrDaxSatFluxRatio Uls M f32	0.39899989
PICurrCntrl MtrCurrQaxSatFluxRatio_UIs_M_132 PICurrCntrl MtrCurrQaxSatFluxRatio UIs M f32	0.705799997 0.833000004
PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32	-657.099976
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	-340.130005
PICurrCntrl MtrVecuFilt M str.TermN Uls f32	49.1376991
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0364000015
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput Uls f32	-657.099976
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-340.130005
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	49.1376991
PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.0364000015
k CLOAFdbackSignalSclFacSlew UlspS f32	7980.1499
k DualEcuSignalSclFacSlew UlspS f32	142
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	6489.7002
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.0280000009
k_MtrCtrlVirualResQax_Ohm_f32	0.129999995
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRpIEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	14.1104002
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	13.7721004
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017
k MtrVoltVecuFiltEnable Cnt lgc	0

PICurrCntrl_Per1



Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	-0.893999994		
k_VoltSatQaxPolyCoeff_Uls_f32	-24.9239998		
k_deadtimeVScale_Uls_f32	0.966000021		
t_CommOffsetTbIX_UIs_u3p13[0]	1532		
t_CommOffsetTbIX_UIs_u3p13[1]	2851		
t_CommOffsetTblY_Cnt_u16[0]	912		
t_CommOffsetTblY_Cnt_u16[1]	1211		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	5.72399998		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2264		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	0		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1211	1211	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63307	63307 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-15.956131	-15.956131 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	1.62337315	1.62337315 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	26386	26386 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0902500004	0.0902500004 ± 0.0625	~

Test Step Call Trace				V
Actual Function	Count	expected Function		Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	-
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.113 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-147.343002
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	127.972
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.624000013
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.05799997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	609.603027
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-912.517029
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0120000001
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0560000017





MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] MtrCtrl_Vecu_Volt_M_f32[0] MtrCtrl_Vecu_Volt_M_f32[1] MtrCtrl_Vecu_Volt_M_f32[0] MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1] MtrCurrDaxRef_Amp_M_f32[1] MtrCurrDaxRef_Amp_M_f32[1] MtrCurrDaxCog_Amp_M_f32	Input Value 1.47599995 0.801999986 -292.941986 762.052002 -7.66699982 2.61400008 -29.3959999 -1.9400006 13.3629999 15.7229996 7.36499977		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] MtrCtrl_Vecu_Volt_M_f32[0] MtrCtrl_Vecu_Volt_M_f32[1] MtrCtrl_vecu_Volt_M_f32[1] MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1]	1.47599995 0.801999986 -292.941986 762.052002 -7.66699982 2.61400008 -29.3959999 -1.94000006 13.3629999 15.7229996		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] MtrCtrl_Vecu_Volt_M_f32[0] MtrCtrl_Vecu_Volt_M_f32[1] MtrCtrl_Vecu_Volt_M_f32[1] MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1]	0.801999986 -292.941986 762.052002 -7.66699982 2.61400008 -29.3959999 -1.94000006 13.3629999 15.7229996		
MtrCttr_MtrQaxPropotionalGain_Ohm_M_f32[0] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] MtrCtrl_Vecu_Volt_M_f32[0] MtrCtrl_Vecu_Volt_M_f32[1] MtrCtrl_Vecu_Volt_M_f32[1] MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1]	762.052002 -7.66699982 2.61400008 -29.3959999 -1.9400006 13.3629999 15.7229996		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] MtrCtrl_Vecu_Volt_M_f32[0] MtrCtrl_Vecu_Volt_M_f32[1] MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1]	-7.66699982 2.61400008 -29.3959999 -1.9400006 13.3629999 15.7229996		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] MtrCtrl_Vecu_Volt_M_f32[0] MtrCtrl_Vecu_Volt_M_f32[1] MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1]	-7.66699982 2.61400008 -29.3959999 -1.9400006 13.3629999 15.7229996		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] MtrCtrl_Vecu_Volt_M_f32[0] MtrCtrl_Vecu_Volt_M_f32[1] MtrCtrl_Vecu_Volt_M_f32[1] MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1]	2.61400008 -29.3959999 -1.9400006 13.3629999 15.7229996		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] MtrCtrl_Vecu_Volt_M_f32[0] MtrCtrl_Vecu_Volt_M_f32[1] MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1]	-1.9400006 13.3629999 15.7229996		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] MtrCtrl_Vecu_Volt_M_f32[0] MtrCtrl_Vecu_Volt_M_f32[1] MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1]	-1.9400006 13.3629999 15.7229996		
MtrCtrl_Vecu_Volt_M_f32[0] MtrCtrl_Vecu_Volt_M_f32[1] MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1]	13.3629999 15.7229996		
MtrCtrl_Vecu_Volt_M_f32[1] MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1]	15.7229996		
MtrCurrDaxPrevintg_Volt_M_f32 MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1]			
MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1]			
MtrCurrDaxRef_Amp_M_f32[1]	-100.282997		
	-120.248001		
	-41.5750008		
MtrCurrQaxPrevIntg_Volt_M_f32	8.32960033		
MtrCurrQaxRef Amp M f32[0]	-65.1900024		
MtrCurrQaxRef_Amp_M_f32[1]	-216.972		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	-0.0883999988		
MtrPosComputationDelay_Rad_M_f32[1]	-0.0131999999		
PICurrCntrl CurrSensFailSclFac Uls M f32	0.349000007		
PICurrCntrl DualEcuFailSclFac Uls M f32	0.108999997		
PICurrCottl_InverterFailSclFac_UIs_M_f32	0.111000001		
PICurrCottl_MtrCurrOaxSatFluxRatio_Uls_M_f32	0.824800014		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.521000028		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	570.700012		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-657.099976		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	82.828598		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.797699988		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-657.099976		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	82.828598		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.797699988		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	1293.53003		
k_DualEcuSignalSclFacSlew_UlspS_f32	143.199997		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	7361.14014		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.193000004		
k_MtrCtrlVirualResQax_Ohm_f32	0.103		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	24.9596996		
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	17.6800003		
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	8.96500015		
k_VoltSatQaxPolyCoeff_Uls_f32	-13.8369999		
k_deadtimeVScale_Uls_f32	0.968999982		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	771		
t_CommOffsetTblY_Cnt_u16[1]	1636		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.3040009		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3317		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	50.0610008		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	3317	3317	Result
	0	0 ± 1	
MtrCntrl_Write_ModIdx_Uls_u16p16(val)			
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-23.6150017	-23.6150017 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-0.269776285	-0.269776255 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.83748341	-4.83748341 ± 4.88E-04	· ·
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	32427	32427 ± 1.52588E-05	· ·
	0	0	
MtrCurrDaxPrevIntg_Volt_M_f32 PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.126900002	0.126900002 ± 0.0625	→



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Name	Input Value
FastDataAccessBufIndex Cnt M u16	0
/trCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr
/trCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr
/trCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val
AtrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
/trCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
/trCntrl Read SysState Cnt Enum(Val)	target MtrCntrl Read SysState Cnt Enum Val
MtrCtrl MtrCurrDaxMaxVal Amp M f32[0]	6.18900013
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	83.0540009
/trCtrl MtrDampTermDax Ohm M f32[0]	0.0099999978
/trCtrl MtrDampTermDax Ohm M f32[1]	0.0799999982
VtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995
VtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.591000021
VtrCtrl MtrDaxIntegralGain Ohm M f32[1]	0.0130000003
MtrCtrl MtrDaxPropotionalGain Ohm M f32[0]	248.214996
MtrCtrl MtrDaxPropotionalGain Ohm M f32[1]	-29.2189999
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0939999968
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0879999995
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.46300006
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.03900003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	126.671997
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-963.362976
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-3.59500003
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-28.4209995
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-17.1070004
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	15.9390001
MtrCtrl_Vecu_Volt_M_f32[0]	24.8479996
MtrCtrl_Vecu_Volt_M_f32[1]	27.2080002
MtrCurrDaxPrevIntg_Volt_M_f32	0.851999998
MtrCurrDaxRef_Amp_M_f32[0]	-68.6760025
MtrCurrDaxRef_Amp_M_f32[1]	-96.776001
/trCurrQaxCog_Amp_M_f32	48.8400002
MtrCurrQaxPrevIntg_Volt_M_f32	28.9717007
/trCurrQaxRef_Amp_M_f32[0]	-146.723007
/trCurrQaxRef_Amp_M_f32[1]	-121.943001
/trCurrQaxRpl_Amp_M_f32	0
/trPosComputationDelay_Rad_M_f32[0]	-0.787299991
MtrPosComputationDelay_Rad_M_f32[1]	-1.41530001
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.911000013
PICurrCntrl DualEcuFailSclFac Uls M f32	0.109999999



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.816999972		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.916199982		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.686999977		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-44.2799988		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	95.5231018		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.219099998		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	0		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-44.2799988		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	95.5231018		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.219099998		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6616.02002		
k_DualEcuSignalSclFacSlew_UlspS_f32	144.399994		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5777.70996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.0810000002		
k_MtrCtrlVirualResQax_Ohm_f32	0.0829999968		
k MtrCurrQaxRefModifDsb Cnt Igc	0		
k MtrCurrQaxRefModifRplEn Cnt lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	18.9941998		
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.5		
k MtrVoltQaxFiltFFEnable Cnt lqc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	1.51160002		
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	19.1259995		
k_VoltSatQaxPolyCoeff_Uls_f32	1.05900002		
k deadtimeVScale Uls f32	0.977999985		
t CommOffsetTbIX UIs u3p13[0]	2638		
t_CommOffsetTbIX_UIs_u3p13[1]	3628		
t_CommOffsetTbIY_Cnt_u16[0]	297		
t_CommOffsetTblY_Cnt_u16[1]	1110		
target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr	1		
·	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val			
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	20.6149998		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	70		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Resu
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1110	1110	•
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	64094	64094 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-195.563004	-195.563004 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-17.846735	-17.8467331 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	16.4939213	16.4939194 ± 4.88E-04	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	48721	48721 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	-10.5	-10.5	•
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0919499993	0.0919499993 ± 0.0625	•

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~	
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~	
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	_	





Test Step 2.115 (Repeat Count = 1)	🗸
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrChtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-105.246002 41.6290016
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1] MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.075000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0710000023
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0560000017
MtrCtrl MtrDampTermQax Ohm M f32[1]	0.0130000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.0710000023
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.72399998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	376.216003
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-802.426025
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.070000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.60300004
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.42400002
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	18.6140003 -320.81601
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	0.908999979
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	21.0249996
MtrCtrl_Vecu_Volt_M_f32[0]	28.3600006
MtrCtrl_Vecu_Volt_M_f32[1]	30.7199993
MtrCurrDaxPrevIntg_Volt_M_f32	1.579
MtrCurrDaxRef_Amp_M_f32[0]	-139.906998
MtrCurrDaxRef_Amp_M_f32[1]	115.814003
MtrCurrQaxCog_Amp_M_f32	107.702003
MtrCurrQaxPrevIntg_Volt_M_f32	0.0671999976
MtrCurrQaxRef_Amp_M_f32[0]	-208.287994 -27.9839993
MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRpl Amp M f32	-27.9039993
MtrPosComputationDelay Rad M f32[0]	-1.79719996
MtrPosComputationDelay_Rad_M_f32[1]	3.08010006
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.702000022
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.111000001
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.657000005
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.99180001
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.143000007
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	0
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-627.179993
PICurrCotal_MtrVecuFilt_M_str.TermN_UIs_f32	44.7025986
PICurrCotrl_MtrVoltOayEFEilt_M_str.TermD_UIs_f32	0.123199999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32 PICurrCntrl MtrVoltQaxFFFilt M str.PrevOutput UIs f32	-627.179993
PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermN_Uls_f32	-627.179993 44.7025986
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.123199999
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	5911.31982
k_DualEcuSignalSclFacSlew_UlspS_f32	145.600006
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2332.93994
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.0179999992
k_MtrCtrlVirualResQax_Ohm_f32	0.0500000007
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	5.83920002 8.60000038
k_MtrVoltDaxIntegLoLim_Volt_f32 k MtrVoltQaxFiltFFEnable Cnt lgc	-8.60000038 0
k_MtrVoltQaxIntegHiLim_Volt_f32	19.530899
k_MtrVoltQaxIntegLoLim_Volt_f32	-11.6000004

PICurrCntrl_Per1



Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	-9.26000023		
k_VoltSatQaxPolyCoeff_Uls_f32	12.5810003		
k_deadtimeVScale_Uls_f32	0.996999979		
t_CommOffsetTblX_Uls_u3p13[0]	1212		
t_CommOffsetTblX_Uls_u3p13[1]	1704		
t_CommOffsetTblY_Cnt_u16[0]	23		
t_CommOffsetTblY_Cnt_u16[1]	212		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-220		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3905		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-198.285995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	212	212	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	65339	65339 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	28.2720394	28.2720451 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	0.403350025	0.403350085 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	63026	63026 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	5.83920002	5.83920002	~
PICurrCntrl DualEcuFailSclFac Uls M f32	0.129199997	0.129199997 ± 0.0625	✓

Actual Function	Count	Expected Function	Count	Result
MtrCntrl Read MtrCurrQax Amp f32	1	MtrCntrl Read MtrCurrQax Amp f32	1	, 100 U.
MtrCntrl Read MtrCurrDax Amp f32	1	MtrCntrl Read MtrCurrDax Amp f32	1	-
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	-
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	-
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	-
ntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	-
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	-
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	-
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	-
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	-
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	-
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	-
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	-
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	-

Test Step 2.116 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.898000002
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.30599999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	806.749023
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-34.0489998
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007

PICurrCntrl Per1

2016-09-15, 18:23:31+0530



Input Value MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] 0.221000001 MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] 0.49000001 737.367004 MtrCtrl MtrQaxPropotionalGain Ohm M f32[0] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] 253.417999 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -16.302 MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] 8.55099964 MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -28.2420006 $MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]$ 12.6160002 MtrCtrl_Vecu_Volt_M_f32[0] 21.2989998 MtrCtrl_Vecu_Volt_M_f32[1] 23 6590004 MtrCurrDaxPrevIntg_Volt_M_f32 -8.56599998 MtrCurrDaxRef_Amp_M_f32[0] -82 2979965 MtrCurrDaxRef_Amp_M_f32[1] 46.8180008 5 72399998 MtrCurrQaxCog_Amp_M_f32 MtrCurrQaxPrevIntg_Volt_M_f32 2.58669996 MtrCurrQaxRef_Amp_M_f32[0] 31.5869999 MtrCurrQaxRef_Amp_M_f32[1] -186.395996 MtrCurrQaxRpl_Amp_M_f32 $MtrPosComputationDelay_Rad_M_f32[0]$ -2.94300008 MtrPosComputationDelay_Rad_M_f32[1] 0.898000002 $PICurrCntrl_CurrSensFailSclFac_Uls_M_f32$ 0.662 PICurrCntrl_DualEcuFailSclFac_Uls_M_f32 0.112000003 $PICurrCntrl_InverterFailSclFac_Uls_M_f32$ 0.499000013 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.098999995 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 -43.1699982 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -717.299988 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 1.35469997 PICurrCntrl MtrVecuFilt M str.TermD Uls f32 0.649999976 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32 -43.1699982 PICurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32 -717.299988 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32 1 35469997 PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32 0.649999976 k CLOAFdbackSignalSclFacSlew UlspS f32 7083 27002 k_DualEcuSignalSclFacSlew_UlspS_f32 146.800003 k ILOAFdbackSignalSclFacSlew_UlspS_f32 947.890015 $k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc$ 0 k MtrCtrlFeedbackControlDisable_Cnt_lgc k_MtrCtrlVirualResDax_Ohm_f32 0.0540000014 k MtrCtrlVirualResQax Ohm f32 0.172000006 k_MtrCurrQaxRefModifDsb_Cnt_lgc k MtrCurrQaxRefModifRplEn Cnt lgc $k_MtrVoltDaxIntegHiLim_Volt_f32$ 27.2376995 k_MtrVoltDaxIntegLoLim_Volt_f32 -30.2000008 $k_MtrVoltQaxFiltFFEnable_Cnt_lgc$ k_MtrVoltQaxIntegHiLim_Volt_f32 20.0447998 -30 2000008 k MtrVoltQaxIntegLoLim Volt f32 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 -13 0120001 k_VoltSatQaxPolyCoeff_Uls_f32 6 00699997 k_deadtimeVScale_Uls_f32 0.967999995 t_CommOffsetTblX_Uls_u3p13[0] 3808 t_CommOffsetTblX_Uls_u3p13[1] 7298 t_CommOffsetTblY_Cnt_u16[0] 1237 t_CommOffsetTblY_Cnt_u16[1] 383 $target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr$ target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr 0 $target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val$ 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 220 $target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr$ 794 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 136.341003 $target_MtrCntrl_Read_SysState_Cnt_Enum_Val$ Name **Actual Value Expected Value** Result MtrCntrl Write CommOffset Cnt u16(val) 794 794 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 0 25.862999 25.862999 ± 7.81E-03 MtrCntrl Write MtrCurrQaxFinalRef Amp f32(val) MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -4.18785191 -4.18785143 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -2 42641664 -2.42641664 ± 4.88E-04 12978 ± 1.52588E-05 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 12978 MtrCurrDaxPrevIntg_Volt_M_f32

0.0936499983

PICurrCntrl_DualEcuFailSclFac_Uls_M_f32

0.0936499983 ± 0.0625



Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~		
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~		
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~		
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~		
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~		
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~		
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~		
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•		
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~		
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•		
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~		
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~		
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~		
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~		
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~		
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓		
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~		
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~		
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~		

Test Step 2.117 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.65499997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.88900006
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	392.079987
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	734.911987
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl MtrlmpedDax Ohm M f32[1]	0.125
MtrCtrl MtrlmpedQax Ohm M f32[0]	0.0529999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.22399998
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-333.980988
MtrCtrl MtrQaxPropotionalGain Ohm M f32[1]	-661.781006
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
MtrCtrl MtrVoltQaxFF Volt M f32[0]	18.6380005
MtrCtrl MtrVoltQaxFF Volt M f32[1]	-23.1870003
MtrCtrl_Vecu_Volt_M_f32[0]	21.3600006
MtrCtrl_Vecu_Volt_M_f32[1]	23.7199993
MtrCurrDaxPrevIntg_Volt_M_f32	7.60099983
MtrCurrDaxRef_Amp_M_f32[0]	160.044006
MtrCurrDaxRef_Amp_M_f32[1]	165.242004
MtrCurrQaxCog_Amp_M_f32	59.3040009
MtrCurrQaxPrevIntg_Volt_M_f32	22.5144005
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	-3.61800003
MtrPosComputationDelay_Rad_M_f32[1]	-4.93400002
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.851000011
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.112999998
PICurrCntrl InverterFailSclFac Uls M f32	0.757000029

2016-09-15, 18:23:31+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.070600003		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.0469999984		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	-826.23999		
PICurrCntrl MtrVecuFilt M str.TermN Uls f32	53.2509003		
PICurrCntrl MtrVecuFilt M str.TermD Uls f32	0.3134		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput Uls f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-826.23999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	53.2509003		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.3134		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2409.94995		
k_DualEcuSignalSclFacSlew_UlspS_f32	148		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4854.70996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.0359999985		
k_MtrCtrlVirualResQax_Ohm_f32	0.163000003		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	20.7133999		
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	12.3408003		
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-10.0080004		
k_VoltSatQaxPolyCoeff_Uls_f32	-22.6299992		
k_deadtimeVScale_Uls_f32	0.97299999		
t_CommOffsetTbIX_UIs_u3p13[0]	4611		
t_CommOffsetTbIX_Uls_u3p13[1]	5579		
t_CommOffsetTbIY_Cnt_u16[0]	2000		
t_CommOffsetTbIY_Cnt_u16[1]	2000		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	0		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2515		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Resul
MtrCntrl_Write_CommOffset_Cnt_u16(val)	2000	2000	
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63766	63766 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-193.251007	-193.251007 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-0.00127269397	-0.00127269374 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	20.7832813	20.7832794 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	27798	27798 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	•
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.131500006	0.131500006 ± 0.0625	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	-
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	-
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	-





Test Step 2.118 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrDox_Amp_f32(/cl)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val) MtrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl Read MtrCurrQax Amp f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target MtrCntrl Read SysState Cnt Enum Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-132.813004
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-9.14299965
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.123000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0460000001
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.703999996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.129999995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-685.018005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-140.973999
MtrCtrl_MtrlmpedDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.017000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[1] MtrCtrl MtrQaxIntegralGain Ohm M f32[0]	0.0410000011 1.24399996
MtrCtrl_MtrQaxIntegralGain_Onm_M_132[u] MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.83899999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	584.664978
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	935.218018
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-4.5599994
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-20.8330002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-8.61900043
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-13.1560001
MtrCtrl_Vecu_Volt_M_f32[0]	17.2600002
MtrCtrl_Vecu_Volt_M_f32[1]	19.6200008
MtrCurrDaxPrevIntg_Volt_M_f32	14.2440004
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrDaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxCog_Amp_M_f32	20.6149998
MtrCurrQaxPrevIntg_Volt_M_f32	30.4113998
MtrCurrQaxRef_Amp_M_f32[0]	-105.246002
MtrCurrQaxRef_Amp_M_f32[1]	41.6290016
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0] MtrPosComputationDelay_Rad_M_f32[1]	6.09100008 3.83599997
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.237000003
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.114
PICurrCntrl InverterFailSclFac UIs M f32	0.150000006
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.483900011
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.725000024
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-717.299988
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-657.099976
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	50.986599
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.889199972
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-717.299988
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-657.099976
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	50.986599
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.889199972
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6110.83008
k_DualEcuSignalSclFacSlew_UlspS_f32	149.199997
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	7208.8501
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc k_MtrCtrlVirualResDax_Ohm_f32	0.172999993
k MtrCtrlVirualResQax Ohm f32	0.18999998
k MtrCurrQaxRefModifDsb Cnt lgc	0
k MtrCurrQaxRefModifRplEn Cnt Igc	0
k MtrVoltDaxIntegHiLim Volt f32	23.9330006
k MtrVoltDaxIntegLoLim Volt_132	-22.409998
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	23.8327999
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998
k MtrVoltVecuFiltEnable Cnt lgc	0

2016-09-15, 18:23:31+0530



PICurrCntrl_Per1

Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	17.1749992		
k_VoltSatQaxPolyCoeff_Uls_f32	8.79699993		
k_deadtimeVScale_Uls_f32	0.990999997		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	49		
t_CommOffsetTblY_Cnt_u16[1]	735		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	83.9489975		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3557		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	1.62199998		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	735	735	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	64946	64946 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	21.0140018	21.0140018 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-16.4397964	-16.4397964 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-10.3817005	-10.3817005 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	17752	17752 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	✓
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0953499973	0.0953499973 ± 0.0625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	~

Test Step 2.119 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.131999999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.051
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	781.679016
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	1013.59998
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009





Name	Input Value		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.495000005		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.11000001		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-786.575989		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	755.47699		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002		
MtrCtrl MtrVoltDaxFF Volt M f32[1]	-25.6930008		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008		
MtrCtrl_Vecu_Volt_M_f32[0]	16.4099998		
MtrCtrl_Vecu_Volt_M_f32[1]	18.7700005		
MtrCurrDaxPrevIntg_Volt_M_f32	26.7269993		
	31.5869999		
MtrCurrDayRef_Amp_M_f32[0]			
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996		
MtrCurrQaxCog_Amp_M_f32	-144.667007		
MtrCurrQaxPrevIntg_Volt_M_f32	30.6539001		
MtrCurrQaxRef_Amp_M_f32[0]	171.485992		
MtrCurrQaxRef_Amp_M_f32[1]	163.787003		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	4.46799994		
MtrPosComputationDelay_Rad_M_f32[1]	2.6400001		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.980000019		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.115000002		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.513000011		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.840300024		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.833000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	51.9599991		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.70389998		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32			
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	51.9599991		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.70389998		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	754.531982		
k_DualEcuSignalSclFacSlew_UlspS_f32	150.399994		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	248.589005		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.179000005		
k_MtrCtrlVirualResQax_Ohm_f32	0.158999994		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k MtrVoltDaxIntegHiLim Volt f32	30.8127003		
k MtrVoltDaxIntegLoLim Volt f32	-8.68999958		
k MtrVoltQaxFiltFFEnable Cnt lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	17.2922001		
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958		
k MtrVoltVecuFiltEnable Cnt lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-23.691		
k_VoltSatQaxPolyCoeff_Uls_f32	-14.6350002		
k_deadtimeVScale_UIs_f32	0.971000016		
t_CommOffsetTblX_Uls_u3p13[0]	220		
t_CommOffsetTblX_Uls_u3p13[1]	5037		
t_CommOffsetTblY_Cnt_u16[0]	671		
t_CommOffsetTblY_Cnt_u16[1]	876		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-144.667007		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	256		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	876	876	Result
MtrCntrl_Write_Modldx_Uls_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	63635	63635 ± 1	
NAME AND ANGLE MAD THE PAYED SINGLE AND TRAINED	220	220 ± 7.81E-03	
	220		•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	8.88619614	8.88619804 ± 4.88E-04	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	8.88619614 15.9125919	8.88619804 ± 4.88E-04 15.9125929 ± 4.88E-04	*
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	8.88619614 15.9125919 32848	8.88619804 ± 4.88E-04 15.9125929 ± 4.88E-04 32848 ± 1.52588E-05	· · ·
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	8.88619614 15.9125919	8.88619804 ± 4.88E-04 15.9125929 ± 4.88E-04	y



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

lame	Input Value	
FastDataAccessBufIndex Cnt M u16	0	
/trCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc(ptr)	target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr	
/trCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr	
MtrCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModIdxSrlComSvcDft Cnt Igc Val	
AtrCntrl Read MotCurrLoaMtgtnEn Cnt Iqc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
/trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993	
trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996	
MtrCtrl MtrDampTermDax Ohm M f32[0]	0.112999998	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.076999996	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.77100003	
htrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.31400001	
htrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	4.21999979	
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	387.277008	
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997	
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997	
ftrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029	
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007	
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.25800002	
htrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.36399996	
htrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	860.961975	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-245.580002	
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302	
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964	
htrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006	
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002	
ItrCtrl_Vecu_Volt_M_f32[0]	5	
htrCtrl_Vecu_Volt_M_f32[1]	5	
ltrCurrDaxPrevIntg_Volt_M_f32	-1.17400002	
htrCurrDaxRef_Amp_M_f32[0]	-65.1900024	
ItrCurrDaxRef_Amp_M_f32[1]	-216.972	
ltrCurrQaxCog_Amp_M_f32	5.72399998	
ftrCurrQaxPrevIntg_Volt_M_f32	23.6063004	
ItrCurrQaxRef_Amp_M_f32[0]	31.5869999	
htrCurrQaxRef_Amp_M_f32[1]	-186.395996	
ItrCurrQaxRpl_Amp_M_f32	0	
ItrPosComputationDelay_Rad_M_f32[0]	4.70499992	
htrPosComputationDelay_Rad_M_f32[1]	-2.8670001	
ICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.10999999	
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.115999997	
PICurrCntrl InverterFailSclFac UIs M f32	0.0040000019	
PlCurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.703999996	
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.0469999984	

PICurrCntrl Per1

2016-09-15, 18:23:31+0530



Input Value PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32 -657.130005 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 107.129997 PICurrCntrl MtrVecuFilt M str.TermN Uls f32 71.2244034 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.953999996 PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput Uls f32 -657.130005 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32$ 107.129997 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 71.2244034 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32$ 0.953999996 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 3550.11011 k_DualEcuSignalSclFacSlew_UlspS_f32 151 600006 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 5873.56006 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 1 k_MtrCtrlFeedbackControlDisable_Cnt_lgc k_MtrCtrlVirualResDax_Ohm_f32 0.0240000002 k_MtrCtrlVirualResQax_Ohm_f32 0.0109999999 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ k_MtrVoltDaxIntegHiLim_Volt_f32 16.5436993 $k_MtrVoltDaxIntegLoLim_Volt_f32$ -4.57000017 k_MtrVoltQaxFiltFFEnable_Cnt_lgc $k_MtrVoltQaxIntegHiLim_Volt_f32$ 14.1534996 k_MtrVoltQaxIntegLoLim_Volt_f32 -4.57000017 $k_MtrVoltVecuFiltEnable_Cnt_lgc$ k_VoltSatDaxPolyCoeff_Uls_f32 17.4050007 k VoltSatQaxPolyCoeff Uls f32 -2.23099995 k_deadtimeVScale_Uls_f32 0.963 t CommOffsetTblX Uls u3p13[0] 573 t_CommOffsetTblX_Uls_u3p13[1] 7569 t CommOffsetTblY Cnt u16[0] 912 t_CommOffsetTblY_Cnt_u16[1] 1211 target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val 0 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -34.6189995 $target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr$ 4516 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -34.6189995 target_MtrCntrl_Read_SysState_Cnt_Enum_Val **Expected Value** Name **Actual Value** Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1211 1211 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 63111 63111 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 25.862999 25.862999 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -0.046955727 -0.046955727 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 4.8147707 4.8147707 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 48973 48973 ± 1.52588E-05 $MtrCurrDaxPrevIntg_Volt_M_f32$

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	•
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	

0.0970499963

0.0970499963 ± 0.0625

PICurrCntrl_DualEcuFailSclFac_Uls_M_f32





Test Step 2.121 (Repeat Count = 1)	→
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996 -205.085007
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1] MtrCtrl MtrDampTermDax Ohm M f32[0]	0.079999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.079999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.975000024
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.69599998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	585.619019
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-791.551025
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.052999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.093999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.456999987
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.693000019
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	1016.78003 915.791992
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_Vecu_Volt_M_f32[0]	31
MtrCtrl_Vecu_Volt_M_f32[1]	31
MtrCurrDaxPrevIntg_Volt_M_f32	-16.7709999
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	59.3040009
MtrCurrQaxPrevIntg_Volt_M_f32	4.80830002
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006 75.7020035
MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRpl Amp M f32	0
MtrPosComputationDelay Rad M f32[0]	-2.35100007
MtrPosComputationDelay_Rad_M_f32[1]	-1.33299994
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.658999979
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.116999999
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.481000006
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.824599981
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-194.190002
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	96.5500031
PICurrCotrl_MtrVeouFilt_M_str.TermN_UIs_f32	27.4986992
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput Uls f32	0.75029999 -194.190002
PICurrCntrl_MtrVoltQaxFFFiit_M_str.PrevInput_Uis_f32 PICurrCntrl_MtrVoltQaxFFFiit_M_str.PrevOutput_Uls_f32	96.5500031
PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermN_Uls_f32	27.4986992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.75029999
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6301.31982
k_DualEcuSignalSclFacSlew_UlspS_f32	152.800003
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3999.36011
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.00899999961
k_MtrCtrlVirualResQax_Ohm_f32	0.0469999984
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	23.9073009
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004 0
k MtrVoltQaxFiltFFFnable Cnt Inc	
k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxInteqHiLim_Volt_f32	
k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32	21.2607994 -25.6000004

PICurrCntrl_Per1



Name	Input Value		
k VoltSatDaxPolyCoeff Uls f32	19.5340004		
k VoltSatQaxPolyCoeff Uls f32	10.6110001		
k deadtimeVScale UIs f32	0.987999976		
t_CommOffsetTbIX_UIs_u3p13[0]	1154		
t CommOffsetTblX Uls u3p13[1]	5284		
t CommOffsetTblY Cnt u16[0]	49		
t CommOffsetTblY Cnt u16[1]	735		
target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	1		
target MtrCntrl Read ModldxSrlComSvcDft Cnt Igc Val	0		
target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr	0		
0 = = = 0 = = = = = = = = = = = = = = =	177.046997		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val			
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	153		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	177.046997		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	735	735	✓
MtrCntrl Write Modldx Uls u16p16(val)	56845	56845 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	16.3980026	16.3980026 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-13.6160002	-13.6160002 ± 4.88E-04	✓
MtrCntrl Write MtrQaxVoltage Volt f32(val)	-23.1870003	-23.1870003 ± 4.88E-04	
MtrCntrl Write PhaseAdvanceFinal Rev u0p16(val)	24403	24403 ± 1.52588E-05	_
MtrCurrDaxPrevIntg Volt M f32	0	0	_
PICurrCntrl DualEcuFailSclFac Uls M f32	0.136099994	0.136099994 ± 0.0625	~

Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.122 (Repeat Count = 1)	√
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.533999979
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.231999993
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	322.946991
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	998.633972
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009





Name	Input Value		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.869000018		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.70599997		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	53.6450005		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-178.399002		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008		
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002		
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008		
MtrCurrDaxPrevIntg_Volt_M_f32	-1.39499998		
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999		
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996		
MtrCurrQaxCog_Amp_M_f32	-144.667007		
MtrCurrQaxPrevIntg_Volt_M_f32	18.9440002		
MtrCurrQaxRef Amp M f32[0]	171.485992		
MtrCurrQaxRef_Amp_M_f32[1]	163.787003		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	3.41799998		
MtrPosComputationDelay_Rad_M_f32[1]	3.54200006		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.944000006		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.118000001		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.465999991		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.403899997		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	386.220001		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	13.2960997		
	0.640799999		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32			
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	386.220001		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	0		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	13.2960997		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.640799999		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	5388.75		
k_DualEcuSignalSclFacSlew_UlspS_f32	154		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3076.13989		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.178000003		
k_MtrCtrlVirualResQax_Ohm_f32	0.181999996		
	1		
k_MtrCurrQaxRefModifDsb_Cnt_lgc			
k_MtrCurrQaxRefModifRpIEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	19.5997009		
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	6.4369998		
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k VoltSatDaxPolyCoeff Uls f32	16.1879997		
k VoltSatQaxPolyCoeff Uls f32	0.165000007		
k deadtimeVScale Uls f32	0.954999983		
t_CommOffsetTblX_Uls_u3p13[0]	5022		
t_CommOffsetTbIX_UIs_u3p13[1]	7003		
t CommOffsetTblY Cnt u16[0]	63		
t_CommOffsetTblY_Cnt_u16[1]	327		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-9.31999969		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	998		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-9.31999969		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
		•	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	998	998	•
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	•
		220 ± 7.81E-03	· ·
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220		
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	220 -1.85992479	-1.85992479 ± 4.88E-04	•
		-1.85992479 ± 4.88E-04 -29.5465164 ± 4.88E-04	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-1.85992479		
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-1.85992479 -29.5465164	-29.5465164 ± 4.88E-04	•



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•

Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
/trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996
/trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
/trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.317999989
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.53199995
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-952.169983
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-35.3190002
ftrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997
ftrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029
ftrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007
htrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.0149999997
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.439999998
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-452.992004
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	297.122009
NtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002
/trCtrl_Vecu_Volt_M_f32[0]	14.243
/trCtrl_Vecu_Volt_M_f32[1]	16.6030006
/trCurrDaxPrevIntg_Volt_M_f32	-27.6930008
htrCurrDaxRef_Amp_M_f32[0]	-65.1900024
/trCurrDaxRef_Amp_M_f32[1]	-216.972
/trCurrQaxCog_Amp_M_f32	5.72399998
//trCurrQaxPrevIntg_Volt_M_f32	2.83229995
htrCurrQaxRef_Amp_M_f32[0]	31.5869999
ItrCurrQaxRef_Amp_M_f32[1]	-186.395996
ItrCurrQaxRpl_Amp_M_f32	0
htrPosComputationDelay_Rad_M_f32[0]	-5.51200008
ItrPosComputationDelay Rad M f32[1]	3.42700005
PlCurrCntrl CurrSensFailSclFac Uls M f32	0.25999999
PICurrCntrl DualEcuFailSclFac Uls M f32	0.119000003
PICurrCntrl InverterFailSclFac UIs M f32	0.0450000018

PICurrCntrl_Per1

2016-09-15, 18:23:31+0530



Name	Input Value		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.464599997		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0469999984		
PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32	-784.130005		
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	99.8274994		
PICurrCntrl MtrVecuFilt M str.TermD Uls f32	0.052099999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	-784.130005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	99.8274994		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.052099999		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7174.7002		
k_DualEcuSignalSclFacSlew_UlspS_f32	155.199997		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	1940.90002		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.0810000002		
k_MtrCtrlVirualResQax_Ohm_f32	0.159999996		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	2.89389992		
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	6.79160023		
k_MtrVoltQaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-22.066		
k_VoltSatQaxPolyCoeff_Uls_f32	-1.36500001		
k_deadtimeVScale_Uls_f32	0.970000029		
t_CommOffsetTblX_Uls_u3p13[0]	4611		
t_CommOffsetTblX_Uls_u3p13[1]	5579		
t_CommOffsetTblY_Cnt_u16[0]	889		
t_CommOffsetTblY_Cnt_u16[1]	1543		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-161.352005		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	926		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-161.352005		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1543	1543	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	60159	60159 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-192.119995	-192.119995 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	8.55099964	8.55099964 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	12.6160002	12.6160002 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	41958	41958 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.138400003	0.138400003 ± 0.0625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	-
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~



Test Step 2.124 (Repeat Count = 1)	→
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrChtrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996 -205.085007
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1] MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978
MtrCtrl MtrDampTermQax Ohm M f32[1]	0.079999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.507000029
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.53999996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	330.04599
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-179.259003
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.093999968
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.230000004 1.53799999
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	-818.869995
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	353.450989
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003
MtrCtrl_Vecu_Volt_M_f32[0]	13.3629999
MtrCtrl_Vecu_Volt_M_f32[1]	15.7229996
MtrCurrDaxPrevIntg_Volt_M_f32	20.066
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	59.3040009
MtrCurrQaxPrevIntg_Volt_M_f32	25.5028
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006 75.7020035
MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRpl Amp M f32	0
MtrPosComputationDelay Rad M f32[0]	2.39700007
MtrPosComputationDelay_Rad_M_f32[1]	2.73799992
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.185000002
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.119999997
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.398999989
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0604999997
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	947.73999
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_UIs_f32	1118
PICurrCotrl_MtrVecuFilt_M_str.TermN_UIs_f32	1.48909998
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32 PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput UIs f32	0.176699996 947.73999
PICurrCntrl_MtrVoitQaxFFFilt_M_str.PrevInput_Uis_r32 PICurrCntrl MtrVoitQaxFFFilt M str.PrevOutput Uls f32	1118
PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermN_Uls_f32	1.48909998
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.176699996
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3789.18994
k_DualEcuSignalSclFacSlew_UlspS_f32	156.399994
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3865.98999
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.0710000023
k_MtrCtrlVirualResQax_Ohm_f32	0.0489999987
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1
k_MtrVoltDaxIntegHiLim_Volt_f32	0.557200015
k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008 1
k MtrVoltQaxFiltFFEnable Cnt Inc	
k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32	
k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 k_MtrVoltQaxIntegLoLim_Volt_f32	30.7143993 -30.2000008

PICurrCntrl_Per1



Name	Input Value		
	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	-9.73099995		
k_VoltSatQaxPolyCoeff_Uls_f32	-24.3579998		
k_deadtimeVScale_Uls_f32	0.966000021		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	771		
t_CommOffsetTblY_Cnt_u16[1]	1636		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-205.514999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	474		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-205.514999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	474	474	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	16.3980026	16.3980026 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-13.062192	-13.062191 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	26.9470234	26.9470234 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	23850	23850 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.100449994	0.100449994 ± 0.0625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	~

Test Step 2.125 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.822000027
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.41999996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-292.269989
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-754.054993
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009

2016-09-15, 18:23:31+0530





Name	Input Value		
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.041999994		
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.0280000009		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.871999979		
MtrCtrl MtrQaxIntegralGain Ohm M f32[1]	1.22000003		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	688.346985		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-854.249023		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008		
MtrCtrl MtrVoltQaxFF Volt M f32[0]	-7.66699982		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008		
MtrCtrl_Vecu_Volt_M_f32[0]	24.8479996		
MtrCtrl_Vecu_Volt_M_f32[1]	27.2080002		
MtrCurrDaxPrevIntg_Volt_M_f32	-7.71299982		
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999		
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996		
MtrCurrQaxCog_Amp_M_f32	-144.667007		
MtrCurrQaxPrevIntg_Volt_M_f32	17.9473		
MtrCurrQaxRef_Amp_M_f32[0]	171.485992		
MtrCurrQaxRef_Amp_M_f32[1]	163.787003		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	-1.56599998		
MtrPosComputationDelay_Rad_M_f32[1]	1.21399999		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.723999977		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.120999999		
PICurrCntrl InverterFailSclFac Uls M f32	0.111000001		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0421999991		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.833000004		
PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32	269.399994		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-717.299988		
	75.4738007		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32			
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.306199998		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	269.399994		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-717.299988		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	75.4738007		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.306199998		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3827.27002		
k_DualEcuSignalSclFacSlew_UlspS_f32	157.600006		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2156.63989		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.185000002		
k_MtrCtrlVirualResQax_Ohm_f32	0.079999982		
k MtrCurrQaxRefModifDsb Cnt lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	2.9461		
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
	19.4281006		
k_MtrVoltQaxIntegHiLim_Volt_f32			
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	17.6229992		
k_VoltSatQaxPolyCoeff_Uls_f32	-20.6590004		
k_deadtimeVScale_Uls_f32	0.968999982		
t_CommOffsetTbIX_UIs_u3p13[0]	2638		
t_CommOffsetTbIX_Uls_u3p13[1]	3628		
t_CommOffsetTbIY_Cnt_u16[0]	297		
t_CommOffsetTblY_Cnt_u16[1]	1110		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-118.848		
target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	2994		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-118.848		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
		e	
Name	Actual Value	Expected Value	Resul
MtrCntrl_Write_CommOffset_Cnt_u16(val)	2994	2994	•
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	,
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-4.29543591	-4.29543591 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-2.24126196	-2.24126196 ± 4.88E-04	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	27802	27802 ± 1.52588E-05	•
MtrCurrDaxPrevIntg Volt M f32	0	0	

MtrCurrDaxPrevIntg_Volt_M_f32





Name	Actual Value	Expected Value	Result
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.140699998	0.140699998 ± 0.0625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	-
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Name	Input Value	
	0	
FastDataAccessBufIndex_Cnt_M_u16		
/trCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	
/trCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	
/trCntrl_Read_ModidxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
/trCntrl_Read_MtrCurrOffCorrOffcot Cot (446/ptr)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
/trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993	
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.076999996	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985	
/trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.90799999	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.08500004	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-899.770996	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	697.859009	
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997	
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997	
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029	
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.48399997	
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.526000023	
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-99.262001	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	138.542007	
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302	
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964	
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006	
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002	
/trCtrl_Vecu_Volt_M_f32[0]	28.3600006	
/ltrCtrl_Vecu_Volt_M_f32[1]	30.7199993	
/ltrCurrDaxPrevIntg_Volt_M_f32	-7.6500001	
/trCurrDaxRef_Amp_M_f32[0]	-65.1900024	
//trCurrDaxRef_Amp_M_f32[1]	-216.972	
/trCurrQaxCog_Amp_M_f32	5.72399998	
/trCurrQaxPrevIntg_Volt_M_f32	7.55770016	
/trCurrQaxRef_Amp_M_f32[0]	31.5869999	
MtrCurrQaxRef_Amp_M_f32[1]	-186.395996	
/trCurrQaxRpl_Amp_M_f32	0	
MtrPosComputationDelay_Rad_M_f32[0]	1.403	
MtrPosComputationDelay_Rad_M_f32[1]	-4.98400021	
PICurrCntrl CurrSensFailSclFac Uls M f32	0.848999977	

2016-09-15, 18:23:31+0530



PICurrCntrl Per1 Input Value PICurrCntrl DualEcuFailSclFac Uls M f32 0.122000001 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.816999972 PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32 0.848399997 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.0469999984 PICurrCntrl MtrVecuFilt_M_str.PrevInput_Uls_f32 -340.130005 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 1118 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 52.3392982 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.404900014 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32 -340.130005 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32$ 1118 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 52.3392982 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32$ 0.404900014 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 208.033005 k_DualEcuSignalSclFacSlew_UlspS_f32 158.800003 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 5517.5 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 1 $k_MtrCtrlFeedbackControlDisable_Cnt_lgc$ k_MtrCtrlVirualResDax_Ohm_f32 0.0909999982 k_MtrCtrlVirualResQax_Ohm_f32 0.0199999996 k_MtrCurrQaxRefModifDsb_Cnt_lgc $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ k_MtrVoltDaxIntegHiLim_Volt_f32 9.36629963 k_MtrVoltDaxIntegLoLim_Volt_f32 -22.4099998 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 10.1091003 k_MtrVoltQaxIntegLoLim_Volt_f32 -22.4099998 k_MtrVoltVecuFiltEnable_Cnt_lgc $k_VoltSatDaxPolyCoeff_Uls_f32$ 2.421 k VoltSatQaxPolyCoeff Uls f32 -11.9060001 k_deadtimeVScale_Uls_f32 0.977999985 t CommOffsetTblX Uls u3p13[0] 1212 t_CommOffsetTblX_Uls_u3p13[1] 1704 t CommOffsetTblY Cnt u16[0] 23 t_CommOffsetTblY_Cnt_u16[1] 212 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 $target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val$ target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 59.3040009 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 3747 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 50.0610008 target_MtrCntrl_Read_SysState_Cnt_Enum_Val Actual Value **Expected Value** Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 3747 3747 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 0 0 ± 1 25 862999 25.862999 ± 7.81E-03 $MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)$ MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 30.2094536 30.2094536 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 2 56321192 2 56321192 + 4 88F-04

30135

0.102150001

30135 ± 1.52588E-05

0.102150001 ± 0.0625

n

MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)

 $MtrCurrDaxPrevIntg_Volt_M_f32$

PICurrCntrl_DualEcuFailSclFac_Uls_M_f32



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓	
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~	
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~	

lame	Input Value	
astDataAccessBufIndex_Cnt_M_u16	1	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	
ltrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
htrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
ItrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007	
htrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.079999982	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0089999961	
/trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.079999982	
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.98000002	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.097	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-313.263	
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	882.630981	
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998	
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125	
/trCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994	
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968	
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.958000004	
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.437000006	
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	89.8040009	
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	673.749023	
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001	
/trCtrl MtrVoltDaxFF Volt M f32[1]	-13.6160002	
MtrCtrl MtrVoltQaxFF Volt M f32[0]	18.6380005	
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003	
/trCtrl_Vecu_Volt_M_f32[0]	17.7010002	
/trCtrl_Vecu_Volt_M_f32[1]	20.0610008	
AtrCurrDaxPrevIntg Volt M f32	-9.05200005	
/trCurrDaxRef_Amp_M_f32[0]	-146.723007	
/trCurrDaxRef Amp M f32[1]	-121.943001	
/trCurrQaxCog_Amp_M_f32	59.3040009	
/trCurrQaxPrevIntg Volt M f32	-22.7238007	
/trCurrQaxRef_Amp_M_f32[0]	-133.947006	
/trCurrQaxRef_Amp_M_f32[1]	75.7020035	
trCurrQaxRpl_Amp_M_f32	0	
htrPosComputationDelay_Rad_M_f32[0]	-2.704	
htrPosComputationDelay_Rad_M_f32[1]	-2.66799998	
CurrCntrl CurrSensFailSclFac Uls M f32	0.231999993	
PlCurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.123000003	
PICurrCntrl InverterFailSclFac Uls M f32	0.657000005	

 $target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val\\target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$

 $target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr$

target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val

target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val

target_MtrCntrl_Read_SysState_Cnt_Enum_Val

PICurrCntrl_Per1

2016-09-15, 18:23:31+0530



Name	Input Value
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.657599986
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-784.130005
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-657.130005
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	84.4263992
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.404500008
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-784.130005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-657.130005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	84.4263992
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.404500008
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6145.56982
k_DualEcuSignalSclFacSlew_UlspS_f32	160
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4019.20996
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.050999999
k_MtrCtrlVirualResQax_Ohm_f32	0.0270000007
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1
k_MtrVoltDaxIntegHiLim_Volt_f32	15.1113005
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.68999958
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	15.5658998
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958
k_MtrVoltVecuFiltEnable_Cnt_lgc	0
k_VoltSatDaxPolyCoeff_Uls_f32	24.6089993
k_VoltSatQaxPolyCoeff_Uls_f32	1.55499995
k_deadtimeVScale_Uls_f32	0.996999979
t_CommOffsetTblX_Uls_u3p13[0]	3808
t_CommOffsetTbIX_Uls_u3p13[1]	7298
t_CommOffsetTblY_Cnt_u16[0]	1237
t_CommOffsetTblY_Cnt_u16[1]	383
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1

Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	383	383	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	65339	65339 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	16.3980026	16.3980026 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-9.33170986	-9.33170986 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-17.6904488	-17.6904488 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	10003	10003 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	-8.68999958	-8.68999958	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.143000007	0.143000007 ± 0.0625	~

0

4791

50.0610008

136.341003

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	-
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•





	Test Step 2.128 (Repeat Count = 1)	v v v v v v v v v v v v v v v v v v v
Facilitations Column Col		Input Value
Michael Basel Content (·
Microtin Read Modison Compression (Light) (by 1)		target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
Microst Pearl Modern Confugery Isage Microst Read Modern Confugery Isage Microst Read Modern Confugery Isage Microst Read Read Systate Conf. Enum. Val Microst Mi	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
Michael Read_Michael Description Energy Michael Read_Michael Amp_52(2) at Michael Read_Michael Control Condition Control (Control	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
McCord Read_McCurd Clore Offeet Cet_u16(pt)	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
Michael Read, Micharlose, Amp. 1202/ast Michael Read, Michael Read, Michael Read, Michael Read, System, Cert. Enum., Val Michael Read, Read, Michael Read, System, Cert. Enum., Val Michael Read, Re		
MICHAEL Planel Systilate Coll Enum Val MICHAEL MICHAEL STATE (
MicCli MicCurbasAvovi Agra, M. 1920 MicCli MicCurbas MicCurbas, Chin, M. 1920 MicCli MicCurbas Microbas, Chin, M. 1920 MicCli MicCurbas Microbas, Chin, M. 1920 MicCli MicCurbas Microbas, Chin, M. 1920 MicCli MicCurbas, Chin, M. 1920 MicCli Microbas, Chin, M. 1920 MicCli Microbas Microbas, Chin, M. 1920 MicCli Miccobas, Chin, M. 1920 Miccobas, Miccobas, Ch		
MicCl, MicParay FermBax, Ohm M, 1921 MicCl, Micray Fer		
MacCing Michagn Fermbax, Onm. M. 12(9)		
MoCint Micham Termbax, Ohm M. 1231 0.030000008		
MicCl, MicClampTermax, Chim, M. (201)		
MicCl_MicRaeIntegralGan_Orm_M_12[1]		0.098999995
MicCl, Michaelregan Gan, Dim, M. (2011) 1.8880004 MicCl, Michaelregan Gan, Dim, M. (2011) 848 53387 MicCl, MiningedDax, Chm, M. (2021) 0.41899994 MicCl, MiningedDax, Chm, M. (2021) 0.28800009 MicCl, MiningedDax, Chm, M. (2021) 0.58909079 MicCl, MiningedDax, Chm, M. (2021) 0.589000011 MicCl, MiningedDax, Chm, M. (2021) 0.58900001 0.589000001 0.58900001 0.589000001 0.589000001 0.589000001 0.589000001 0.589000001 0.589000001 0.5890000001 0.589000	MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MicClu Michae/PropionasCain. Ohm M. 1521)	MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.24300003
Microl InfrinceProportionsGan_One_M_B2[1]	MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.98800004
MicCir MirripedDaz, Ohm M, 15210 0.041999994 MicCir MirripedDaz, Ohm M, 15211 0.028000009 MicCir MirchanitegraGain, Ohm M, 15211 0.038000011 MicCir MirchanitegraGain, Ohm M, 15211 0.038000011 MicCir MirchanitegraGain, Ohm M, 15211 0.03800011 MicCir MirchanitegraGain, Ohm M, 15211 0.0380011 MicCir MirchanitegraGain, Ohm M, 15211 0.03800011 Mirchanitegrafic MirchanitegraGain, Ohm M, 15211 0.03800011 Mirchanitegrafic MirchanitegraGain, Ohm M, 15211 0.03800011 Mirchanitegrafic Mirchanitegrafic Mirchanitegrafic Mirchanitegrafic Mirch		
MicCiri, MirringedDax, Ohm, M. 132(1) MicCiri, MirringedDax, Ohm, M. 132(9) MicCiri, MirchagnilGain, Ohm, M. 132(9) MicCiri, Mirc		
MicCit MirringedQax, Ohm, M. (52(1)) 0.0419999991 MicCit MirringedQax, Ohm, M. (52(1)) 0.28099979 MicCit MirringedQax, Ohm, M. (52(1)) 0.58090011 Mircit MirchashitegralGain, Ohm, M. (52(1)) 3.83999012 Mircit MirchashitegralGain, Ohm, M. (52(1)) 3.83999012 Mircit MirchashitegralGain, Ohm, M. (52(1)) 3.815 (1)7971 Mircit MirchashitegralGain, Ohm, M. (52(1)) 1.4690002 Mircit MirchashitegralGain, Ohm, M. (52(1)) 1.4690002 Mircit MirchashitegralGain, Ohm, M. (52(1)) 2.56930008 Mircit MirchashitegralGain, Ohm, M. (52(1)) 2.56930008 Mircit MirchashitegralGain, M. (52(1)) 2.6699982 Mircit MirchashitegralGain, M. (52(1)) 1.8599998 Mircit MirchashitegralGain, M. (52(1)) 2.9160004 Mircit Veou, Voll, M. (52(1)) 3.1599999 Mircit Veou, Voll, M. (52(1)) 3.1599999 Mircit Lang, M. (52(1)) 1.9839990005 Mircit Lang, M. (52(1)) 1.9839990005 Mircit Lang, M. (52(1)) 1.9839990001 Mircit Lang, M. (52(1)) 1.983990001 Mircit Lang, M. (52(1)) 1.937990001 Mircit Lang, M. (52(1		
MicCiri_MircQacingCac_Ohm_M_CS2(1)		
MicCli MicColingra(Cain, Ohm, M. 152(1) 0.58499979 MicCli MicColingra(Cain, Ohm, M. 152(1) 338.384012 MicCli MicColingra(Cain, Ohm, M. 152(1) 315.107971 MicCli MicColingra(Cain, Ohm, M. 152(1) 14.694002 MicCli MicColingra(F. Volt, M. 152(1) 2.5630008 MicCli MicColingra(F. Volt, M. 152(1) 7.66699892 MicCli MicColingra(F. Volt, M. 152(1) 2.5630008 MicCli MicColingra(F. Volt, M. 152(1) 1.6699982 MicCli MicColingra(F. Volt, M. 152(1) 2.61400008 MicCli MicColingra(F. Volt, M. 152(1) 2.51400008 MicCul Veu, Volt, M. 152(1) 2.0160004 MicCul DaxRef, Amp. M. 152(1) 1.8559988 MicCul DaxRef, Amp. M. 152(1) 1.80990005 MicCul DaxRef, Amp. M. 152(1) 1.80990005 MicCul DaxRef, Amp. M. 152(1) 1.96.35996 MicCul DaxRef, Amp. M. 152(1) 1.96.35996 MicCul DaxRef, Amp. M. 152(1) 1.877903		
MicCit J. MirCoarFropotionalGain, Ohm, M. (32(1) 3.38.394012 MicCit J. MirCoarFropotionalGain, Ohm, M. (32(1) 3.83.394012 MicCit J. MirCoarPropotionalGain, Ohm, M. (32(1) 14.6940002 MicCit J. MirVollDaxFF_Volt, M. (32(1) 2.56.30008 MicCit J. MirVollCaxFF_Volt, M. (32(1) 2.56.30008 MicCit J. MirVollCaxFF_Volt, M. (32(1) 2.61400008 MicCit J. MirVollCaxFF_Volt, M. (32(1) 2.61400008 MicCit J. Vecu, Volt, M. (32(1) 2.61600008 MicCit J. Vecu, Volt, M. (32(1) 2.0160004 MicCit J. Vecu, Volt, M. (32(1) 2.0160004 MicCurl DaxRell, Amp. M. (32(1) 1.68.309906 MicCurl DaxRell, Amp. M. (32(1) 1.68.37003 MicCurl DaxRell, Amp. M. (32(1) 1.69.37003 MicCurl DaxRell, Amp. M. (32(1) 1.42.499906 MicCurl DaxRell, Amp. M. (32(1) 1.42.499906 MicCurl DaxRell, Amp. M. (32(1) 1.42.499906 MicCurl DaxRell,		
MicCurl MiCoaxPropotionalGain, Ohm M, 132(1) 815 107971	_	
MicCirt MirOsuPropolionaGian, Ohm, M. J32(1) 415.107971 MitCirt MirVollbasFF, Volt, M. J32(0) -14.6940002 MitCirt MirVollbasFF, Volt, M. J32(0) -7.66699982 MitCirt, MirVollbasFF, Volt, M. J32(0) -7.66699982 MitCirt, MirVollbasFF, Volt, M. J32(0) 18.5559998 MitCirt, Veau, Volt, M. J32(0) 18.5559998 MitCirt, Veau, Volt, M. J32(1) 20.9180004 MitCurDasRef, Amp, M. J32(1) 18.999005 MitCurDasRef, Amp, M. J32(1) -18.999006 MitCurDasRef, Amp, M. J32(1) -18.999006 MitCurDasRef, Amp, M. J32(1) -18.999006 MitCurDasRef, Amp, M. J32(1) -18.999906 MitCurDasRef, Amp, M. J32(1) 163.787003 MitCurDasRef, Amp, M. J32(1) 163.787003 MitCurDasRef, Amp, M. J32(1) 144.99996 MitPosComputationDelay, Rad, M. J32(1) 142.99996 MitPosComputationDelay, Rad, M. J32(1) 142.99996 PlCurCnit, DualEura IsiSefac, Uis, M. J32 0.77799997 PlCurCnit, MitVourDasSaffluxRato, Uis, M. J32 0.77999997 PlCurCnit, MitVourDasSaffluxRato, Uis, M. J32 0.38999999 PlCurCnit, MitVourDasSaffluxRato, Uis,		
MirCitr, MirVoliDaxFF_Volt, M_132[1]		815.107971
MrtCrt MrVoltQaxFF_Volt M_132[0] -7.66699982 MrtCrt MrVoltQaxFF_Volt M_132[1] 2.6140008 MrtCrt MrVoltQaxFF_Volt M_132[1] 2.6140008 MrtCrt MrQavCt M_162[0] 18.559998 MrtCrurDaxRef_Amp_M_132[1] 20.9160004 MrtCrurDaxRef_Amp_M_132[0] 31.5669999 MrtCrurDaxRef_Amp_M_132[1] -186.39996 MrtCrurDaxRef_Amp_M_132[1] -186.39996 MrtCrurDaxRef_Amp_M_132[1] 185.39996 MrtCrurDaxRef_Amp_M_132[0] 171.485992 MrtCrurDaxRef_Amp_M_132[0] 174.485992 MrtCrurDaxRef_Amp_M_132[0] 4.8499996 MrtDaxRef_Amp_M_132[0] 4.8499996 MrtPosComputationDelay_Rad_M_132[0] 4.2439996 MrtPosComputationDelay_Rad_M_132[0] 4.2439996 MrtPosComputationDelay_Rad_M_132[0] 4.2439996 PlCurrCntr_Crut_Crut_Crut_Crut_Crut_Crut_Crut_Cr	MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] 2,61400008 MtrCtrl_Veuv_Volt_M_f32[0] 18,5559998 MtrCtrl_Veuv_Volt_M_f32[1] 20,9160004 MtrCumPaxPrevintg_Volt_M_f32 18,9990005 MtrCumPaxRef_Amp_M_f32[0] 31,5869999 MtrCumPaxRef_Amp_M_f32[1] -188,395996 MtrCumCaxCog_Amp_M_f32 -144,667007 MtrCumCaxRef_Amp_M_f32[0] 171,485992 MtrCumCaxRef_Amp_M_f32[0] 171,485992 MtrCumCaxRef_Amp_M_f32[0] 163,787003 MtrCumCaxRef_Amp_M_f32[0] 42439996 MtrCumCaxRef_Amp_M_f32[0] 42439996 MtrPosComputationDelay_Rad_M_f32[0] 42439996 MtrPosComputationDelay_Rad_M_f32[0] 42439996 MtrPosComputationDelay_Rad_M_f32[0] 42439996 PCurrCott_DusEcuFaisCyFac_Us_M_f32 0.72399998 PCurrCott_DusEcuFaisCyFac_Us_M_f32 0.723999998 PCurrCott_InverterFaisCrFac_Us_M_f32 0.489900013 PCurrCott_M_ftrCurrCaxSaffuxRato_Us_M_f32 0.833000004 PCurrCott_M_ftrCurrCaxSaffuxRato_Us_M_f32 0.833000004 PCurrCott_M_ftrVolcurSaffuxRato_Us_M_f32 194,100002 PCurrCott_M_ftrVolcu	MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MttCtrl_Vecu_Volt_M_[32](0) 18.5559998 MttCtrl_Vecu_Volt_M_[32](1) 20.9160004 MttCurnDaxPerLynlig_Volt_M_[32] 18.9990005 MttCurnDaxPerLynlig_Volt_M_[32] 1.86.999999 MttCurnDaxPerLynlig_Volt_M_[32] -186.999998 MttCurnDaxPerLynlig_Volt_M_[32] -144.667007 MttCurnDaxPerLynlin_M_[32] -144.667007 MttCurnDaxPerLynlin_M_[32](1) 163.787003 MttCurnDaxRef_Amp_M_[32](1) 163.787003 MttPostComputationDelay_Rad_M_[32](1) 163.787003 MttPostComputationDelay_Rad_M_[32](1) 1.44299996 MttPostComputationDelay_Rad_M_[32](1) 1.44299996 PlCurrCottl_CurrSensFallsCiFac_Uls_M_[32] 0.777999997 PlCurrCottl_DualEcuFallsCiFac_Uls_M_[32] 0.777999997 PlCurrCottl_UnitCurrSensFallsCiFac_Uls_M_[32] 0.49900013 PlCurrCottl_MitCurrDaxSafTiuxRatio_Uls_M_[32] 0.389899999 PlCurrCottl_MitCurrDaxSafTiuxRatio_Uls_M_[32] 0.389899999999 PlCurrCottl_MitVecuFill_M_str_TermD_Uls_[32] 194.190002 PlCurrCottl_MitVecuFill_M_str_TermD_Uls_[32] 76.5533861 PlCurrCottl_MitVolCaxFFFill_M_str_TermD_Uls_[32] 76.5533861	MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MitCirt_Veou_Volt_M_[32[1] 20.9160004 MitCurbaxPrevintg_Volt_M_[32] 18.999005 MitCurbaxRef_Amp_M_[32[1] -186.39596 MitCurDaxRef_Amp_M_[32[1] -186.39596 MitCurDaxRef_Amp_M_[32[1] -186.39596 MitCurDaxRef_Amp_M_[32[0] 171.485992 MitCurDaxRef_Amp_M_[32[1] 163.78003 MitCurDaxRef_Amp_M_[32[1] 163.78003 MitCurDaxRef_Amp_M_[32[1] 163.78003 MitCurDaxRef_Amp_M_[32[1] 163.78003 MitPosComputationDelay_Rad_M_[32[1] 4.2439996 MitPosComputationDelay_Rad_M_[32[1] 1.4429996 PICurrCntl_CurSensFailSciFac_Uis_M_52 0.77799997 PICurrCntl_CurSensFailSciFac_Uis_M_52 0.77799997 PICurrCntl_MitCurDaxSaiFluxRatio_Uis_M_52 0.499900013 PICurrCntl_MitCurDaxSaiFluxRatio_Uis_M_52 0.38089999 PICurrCntl_MitrVecuFit_M_str-revolupt_Uis_f32 0.83000004 PICurrCntl_MitrVecuFit_M_str-revolupt_Uis_f32 194.190002 PICurrCntl_MitrVecuFit_M_str-revolupt_Uis_f32 194.190002 PICurrCntl_MitrVecuFit_M_str-revolupt_Uis_f32 306.20001 PICurrCntl_MitrVelQaxFFFit_M_str-revolupt_Uis_f32 10		
MtrCurrDaxPrevintg_Volt_M_f32 18.9990005 MtrCurrDaxRef_Amp_M_f32(I) 31.569999 MtrCurrDaxRef_Amp_M_f32(I) 196.39996 MtrCurrQaxCog_Amp_M_f32 -144.667007 MtrCurrQaxPrevintg_Volt_M_f32 27.7875996 MtrCurrQaxRef_Amp_M_f32(I) 171.485992 MtrCurrQaxRef_Amp_M_f32(I) 163.787003 MtrCurrQaxRef_Amp_M_f32(I) 163.787003 MtrPosComputationDelay_Rad_M_f32(I) 4.2439996 MtrPosComputationDelay_Rad_M_f32(I) 1.44299996 PICurrCntrl_CurrSensFailsCirac_Uls_M_f32 0.77799997 PICurrCntrl_DualEcurFailsCirac_Uls_M_f32 0.12399998 PICurrCntrl_InverterFailsCirac_Uls_M_f32 0.498000013 PICurrCntrl_MtrCurrCaxSatFluxRatio_Uls_M_f32 0.38899999 PICurrCntrl_MtrCurrCaxSatFluxRatio_Uls_M_f32 0.838000004 PICurrCntrl_MtrVecuriit_M_str.PrevOutput_Uls_f32 386.220001 PICurrCntrl_MtrVecuriit_M_str.PrevOutput_Uls_f32 176.5533981 PICurrCntrl_MtrVecuriit_M_str.FremD_Uls_f32 76.5533981 PICurrCntrl_MtrVecuriit_M_str.PrevOutput_Uls_f32 194.190002 PICurrCntrl_MtrVecuriit_M_str.FremD_Uls_f32 0.099799981 PI		
MtrCurrDaxRef_Amp_M_f32(0) 31.5869999 MtrCurrDaxRef_Amp_M_f32(1) -186.395986 MtrCurrQaxCp_Amp_M_f32 -144.667007 MtrCurrQaxPevintg_Volt_M_f32 27.7875996 MtrCurrQaxRef_Amp_M_f32(0) 171.485992 MtrCurrQaxRef_Amp_M_f32(1) 183.787003 MtrCurrQaxRef_Amp_M_f32(1) 0 MtrPosComputationDelay_Rad_M_f32(1) 4.4399996 MtrPosComputationDelay_Rad_M_f32(1) 1.4429996 PICurrCntr_CurrSensFailScriac_Uis_M_f32 0.77799997 PICurrCntr_LurrSensFailScriac_Uis_M_f32 0.77799997 PICurrCntr_InverterFailScriac_Uis_M_f32 0.499000013 PICurrCntr_InverterFailScriac_Uis_M_f32 0.38989999 PICurrCntr_MtrCurrDaxSaitFluxRatio_Uis_M_f32 0.38989999 PICurrCntr_MtrVecuFiil_M_str_Previoput_Uis_f32 386.220001 PICurrCntr_MtrVecuFiil_M_str_Previoput_Uis_f32 194.190002 PICurrCntr_MtrVecuFiil_M_str_Previoput_Uis_f32 0.997999981 PICurrCntr_MtrVecuFiil_M_str_Previoput_Uis_f32 386.220001 PICurrCntr_MtrVolicaxFFFiil_M_str_Previoput_Uis_f32 386.220001 PICurrCntr_MtrVolicaxFFFiil_M_str_Previoput_Uis_f32 366.533981		
MtrCurrDaxRef_Amp_M_[32[1] -186.395996 MtrCurrQaxCog_Amp_M_[32 1-144.667007 MtrCurrQaxRef_Amp_M_[32] 27.7875996 MtrCurrQaxRef_Amp_M_[32[0] 171.485992 MtrCurrQaxRef_Amp_M_[32[1] 163.787003 MtrCurrQaxRef_Amp_M_[32[1] 163.787003 MtrPosComputationDelay_Rad_M_[32[0] 4.24399996 MtrPosComputationDelay_Rad_M_[32[1] 1.44299996 MtrPosComputationDelay_Rad_M_[32[1] 1.44299996 PICurrCnt_GrovenseralisClFac_Uls_M_[32] 0.777999997 PICurrCnttd_DualEcuFailSclFac_Uls_M_[32] 0.123999998 PICurrCnttd_MicurrDaxSaftFluxRatio_Uls_M_[32] 0.3899900013 PICurrCnttd_MircurrQaxSaftFluxRatio_Uls_M_[42] 0.38999999 PICurrCnttd_MircurrQaxSaftFluxRatio_Uls_M_[42] 0.833000004 PICurrCnttd_MirvecuFill_M_str_Prevloupt_Uls_[42] 386.220001 PICurrCnttd_MirvecuFill_M_str_TermN_Uls_[32] 76.5533981 PICurrCnttd_MirveloiLaxFFFill_M_str_Prevloupt_Uls_[32] 194.190002 PICurrCnttd_MirveloiLaxFFFill_M_str_Prevloupt_Uls_[32] 194.190002 PICurrCnttd_MirveloiLaxFFFill_M_str_Prevloupt_Uls_[32] 195.5533981 PICurrCnttd_MirveloiLaxFFFill_M_str_Prevloupt_Uls_[32		
MtrCurrQaxCog_Amp_M_[32 .144.667007 MtrCurrQaxFevIntg_Voit_M_[52 .27.7875996 MtrCurrQaxRef_Amp_M_[32[0] MtrCurrQaxRef_Amp_M_[32[1] MtrCurrQaxRef_Amp_M_[32[1] MtrCurrQaxRef_Amp_M_[32[1] MtrCurrQaxRef_Amp_M_[32[1] MtrCurrQaxRef_Amp_M_[32[1] MtrPosComputationDelay_Rad_M_[32[0] 4.24399996 MtrPosComputationDelay_Rad_M_[32[1] 1.44299996 PICurrCntt_CurrSensFailSofFac_Uls_M_[32] PICurrCntt_DualEcuFailSofFac_Uls_M_[32] PICurrCntt_DualEcuFailSofFac_Uls_M_[32] PICurrCntt_DualEcuFailSofFac_Uls_M_[32] 0.43999998 PICurrCntt_DualEcuFailSofFac_Uls_M_[32] 0.43999999 PICurrCntt_MrCurrDaxSafFluxRatio_Uls_M_[32] 0.43999999 PICurrCntt_MrCurrDaxSafFluxRatio_Uls_M_[32] 0.43999999 PICurrCntt_MrVecuFilt_M_str.Previnput_Uls_[32] 0.499900013 PICurrCntt_MrVecuFilt_M_str.Previnput_Uls_[32] 194.190002 PICurrCntt_MrVecuFilt_M_str.Previnput_Uls_[32] 194.190002 PICurrCntt_MrVecuFilt_M_str.Previnput_Uls_[32] 194.190002 PICurrCntt_MrVecuFilt_M_str.Previnput_Uls_[32] 194.190002 PICurrCntt_MrVecuFilt_M_str.Previnput_Uls_[32] 194.190002 PICurrCntt_MrVecuFilt_M_str.Previnput_Uls_[32] 194.190002 PICurrCntt_MrVoliQaxFFFilt_M_str.Previnput_Uls_[32] 194.190002 PICurrCntt_MrVoliQaxFFFilt_M_str.Previnput_Uls_[32] 194.190002 PICurrCntt_MrVoliQaxFFFilt_M_str.Previnput_Uls_[32] 194.190002 PICurrCntt_MrVoliQaxFFFilt_M_str.Previnput_Uls_[32] 194.190002 PICurrCntt_MrVoliQaxFFFilt_M_str.Previnput_Uls_[32] 194.190002 PICurrCntt_MrVoliQaxFFFilt_M_str.Previnput_Uls_[32] 194.190002 194.190002 194.190002 194.190002 194.190002 194.190002		
MtrCurrQaxPrevintg_Volt_M_f32 27.7875996 MtrCurrQaxRef_Amp_M_f32[0] 171.485992 MtrCurrQaxRef_Amp_M_f32[1] 163.787003 MtrCurQaxRef_Amp_M_f32[1] 163.787003 MtrOscComputationDelay_Rad_M_f32[0] 4.24399996 MtrPosComputationDelay_Rad_M_f32[0] 4.24399996 MtrPosComputationDelay_Rad_M_f32[0] 4.24399996 PlCurrCntr_CurrSensFallSclFac_Uls_M_f32 0.77799997 PlCurrCntr_DualEcuFailSclFac_Uls_M_f32 0.12399998 PlCurrCntr_McTurDaxSatFluxRatio_Uls_M_f32 0.499000013 PlCurrCntr_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.38989999 PlCurrCntr_MtrVcouFlit_M_str_Previnput_Uls_f32 0.833000004 PlCurrCntr_MtrVcouFilt_M_str_Previnput_Uls_f32 194.19002 PlCurrCntr_MtrVcouFilt_M_str_TermD_Uls_f32 194.19002 PlCurrCntr_MtrVcouFilt_M_str_TermD_Uls_f32 366.220001 PlCurrCntr_MtrVclQaxFFFilt_M_str_TermD_Uls_f32 386.220001 PlCurrCntr_MtrVclQaxFFFilt_M_str_TermD_Uls_f32 194.190002 PlCurrCntr_MtrVclQaxFFFilt_M_str_TermD_Uls_f32 194.190002 PlCurrCntr_MtrVclQaxFFFilt_M_str_TermD_Uls_f32 76.5533981 PlCurrCntr_MtrVclQaxFFFilt_M_str_TermD_Uls_f32		
MtrCurrQaxRef_Amp_M_f32[1] 163.787003 MtrCurrQaxRej_Amp_M_f32 0 MtrPosComputationDelay_Rad_M_f32[0] 4.24399996 MtrPosComputationDelay_Rad_M_f32[1] 1.4429996 PICurrCntrl_CurrSensFailSciFac_Uls_M_f32 0.777999997 PICurrCntrl_DualEcuFac_Uls_M_f32 0.123999998 PICurrCntrl_MrtCurrGaxSatFluxRatio_Uls_M_f32 0.499000013 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.833000004 PICurrCntrl_MtrVecuFilt_M_str.Previnput_Uls_f32 386.220001 PICurrCntrl_MtrVecuFilt_M_str.Previnput_Uls_f32 194.90002 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 76.5533981 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.0997999991 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 386.220001 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 76.5533981 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 194.190002 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 194.190002 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.0997999991 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.0997999991 R_CLOAFdbackSignalSciFacSlew_UlspS_f32 161.19997 k_ILO		27.7875996
MtrCurrQaxRp]_Amp_M_f32 0 MtrPosComputationDelay_Rad_M_f32[0] 4_2439996 MtrPosComputationDelay_Rad_M_f32[1] 1_44299996 PICurrCntrl_OurSensFailSolFac_Uls_M_f32 0_777999997 PICurrCntrl_DualEccFailSolFac_Uls_M_f32 0_123999998 PICurrCntrl_InverterFailSolFac_Uls_M_f32 0_499000013 PICurrCntrl_MtrCurrQaxSatrFluxRatio_Uls_M_f32 0_389899999 PICurrCntrl_MtrVecuFilt_M_str_PrevOutput_Uls_f32 0_833000004 PICurrCntrl_MtrVecuFilt_M_str_PrevOutput_Uls_f32 386.220001 PICurrCntrl_MtrVecuFilt_M_str_TermN_Uls_f32 76.5533981 PICurrCntrl_MtrVecuFilt_M_str_TermD_Uls_f32 0_0997999981 PICurrCntrl_MtrVoltGaxFFFilt_M_str_PrevOutput_Uls_f32 396.220001 PICurrCntrl_MtrVoltGaxFFFilt_M_str_TermN_Uls_f32 396.220001 PICurrCntrl_MtrVoltGaxFFFilt_M_str_TermN_Uls_f32 194.190002 PICurrCntrl_MtrVoltGaxFFFilt_M_str_TermN_Uls_f32 76.5533981 PICurrCntrl_MtrVoltGaxFFFilt_M_str_TermN_Uls_f32 16.5533981 PICurrCntrl_MtrVoltGaxFFFilt_M_str_TermN_Uls_f32 0_9997999981 K_CLOAFdbackSignalSclFacSlew_UlspS_f32 6667.54004 K_DualEcusSignalSclFacSlew_UlspS_f32 161.199997 <	MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrPosComputationDelay_Rad_M_[32[0] 4.2439996 MtrPosComputationDelay_Rad_M_[32[1] 1.4429996 PICurrCntrl_DualEcuFailsClFac_Uls_M_[32] 0.77799997 PICurrCntrl_DualEcuFailsClFac_Uls_M_[32] 0.12399998 PICurrCntrl_InverterFailsClFac_Uls_M_[32] 0.49900013 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_[32] 0.838099999 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_[32] 0.833000004 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_[32] 386.220001 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_[32] 194.190002 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_[32] 76.5533981 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_[32] 0.0997999981 PICurrCntrl_MtrVoltQaxFFFilt_M_str.Prevloutput_Uls_[32] -194.190002 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_[32] -194.190002 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_[32] 0.997999981 k_CLOAFdbackSignalSclFacSlew_UlspS_[32] 6667.54004 k_DualEcuSignalSclFacSlew_UlspS_[32] 6667.54004 k_DualEcuSignalSclFacSlew_UlspS_[32] 7823.27002 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0 <t< td=""><td>MtrCurrQaxRef_Amp_M_f32[1]</td><td>163.787003</td></t<>	MtrCurrQaxRef_Amp_M_f32[1]	163.787003
MtrPosComputationDelay_Rad_M_[32[1] 1.44299996 PICurrCntrL_CurrSensFailSclFac_Uis_M_[32] 0.777999997 PICurrCntrl_DualEcuFailSclFac_Uis_M_[32] 0.123999998 PICurrCntrl_InverterFailSclFac_Uis_M_[32] 0.499000013 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uis_M_[32] 0.389899999 PICurrCntrl_MtrVecuFiit_M_str.Previnput_Uis_[32] 0.833000004 PICurrCntrl_MtrVecuFiit_M_str.Previnput_Uis_[32] 386.220001 PICurrCntrl_MtrVecuFiit_M_str.TermD_Uis_[32] 194.190002 PICurrCntrl_MtrVecuFiit_M_str.TermD_Uis_[32] 76.5533981 PICurrCntrl_MtrVelcuFiit_M_str.TermD_Uis_[32] 0.099799981 PICurrCntrl_MtrVoltQaxFFFiit_M_str.PrevOutput_Uis_[32] 386.220001 PICurrCntrl_MtrVoltQaxFFFiit_M_str.PrevOutput_Uis_[32] 194.190002 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uis_[32] 196.5533981 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uis_[32] 0.0997999981 k_CLOAFdbackSignalSclFacSlew_UispS_[32] 6667.54004 k_DualEcuSignalSclFacSlew_UispS_[32] 161.199997 k_ILOAFdbackSignalSclFacSlew_UispS_[32] 161.199997 k_ILOAFdbackSignalSclFacSlew_UispS_[32] 0 k_MtrCtriVirualResDax_Ohm_[32] 0.19499993	MtrCurrQaxRpl_Amp_M_f32	0
PICurrCntrl_CurrSensFallSclFac_Uls_M_f32 0.777999997 PICurrCntrl_DualEcuFailSclFac_Uls_M_f32 0.123999998 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.499000013 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.389899999 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.839000004 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 386.220001 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 194.190002 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 76.5533981 PICurrCntrl_MtrVetuFilt_M_str.TermD_Uls_f32 0.0997999981 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 386.220001 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 386.220001 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 194.190002 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 76.5533981 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.0997999981 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 6667.54004 k_DualEcuSignalSclFacSlew_UlspS_f32 181.19997 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 7823.27002 k_MtrCtrlCurrLoopSecOrtranFcEnable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.14200005		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32 0.123999998 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.499000013 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.389899999 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.833000004 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 386.220001 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 194.190002 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 76.5533981 PICurrCntrl_MtrVoliQaxFFFilt_M_str.PrevPoutput_Uls_f32 0.9997999981 PICurrCntrl_MtrVoliQaxFFFilt_M_str.PrevPoutput_Uls_f32 386.220001 PICurrCntrl_MtrVoliQaxFFFilt_M_str.PrevDutput_Uls_f32 194.190002 PICurrCntrl_MtrVoliQaxFFFilt_M_str.TermN_Uls_f32 76.5533981 PICurrCntrl_MtrVoliQaxFFFilt_M_str.TermD_Uls_f32 76.5533981 PICurrCntrl_MtrVoliQaxFFFilt_M_str.TermD_Uls_f32 0.0997999981 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 6667.54004 k_DualEcuSignalSclFacSlew_UlspS_f32 161.199997 k_LIOAFdbackSignalSclFacSlew_UlspS_f32 7823.27002 k_MtrCtrlCrucLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.194999993 k_MtrCtrlVirualResDax_Ohm_f32 0.142000005		11111
PICurrCntrI_InverterFailSclFac_Uls_M_f32 0.499000013 PICurrCntrI_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.389899999 PICurrCntrI_MtrVecurFit_M_str.PrevInput_Uls_f32 0.833000004 PICurrCntrI_MtrVecuFit_M_str.PrevInput_Uls_f32 386.220001 PICurrCntrI_MtrVecuFit_M_str.PrevOutput_Uls_f32 194.190002 PICurrCntrI_MtrVecuFit_M_str.TermD_Uls_f32 0.0997999981 PICurrCntrI_MtrVoltQaxFFFit_M_str.PrevInput_Uls_f32 0.0997999981 PICurrCntrI_MtrVoltQaxFFFit_M_str.PrevOutput_Uls_f32 386.220001 PICurrCntrI_MtrVoltQaxFFFit_M_str.PrevOutput_Uls_f32 194.190002 PICurrCntrI_MtrVoltQaxFFFit_M_str.TermD_uls_f32 76.5533981 PICurrCntrI_MtrVoltQaxFFFit_M_str.TermD_uls_f32 76.5533981 PICurrCntrI_MtrVoltQaxFFFit_M_str.TermD_uls_f32 76.5533981 PICurrCntrI_MtrVoltQaxFFFit_M_str.TermD_uls_f32 76.5533981 PICurrCntrI_MtrVoltQaxFFFit_M_str.TermD_uls_f32 76.5533981 PICurrCntrI_MtrVoltQaxFFFit_M_str.TermD_uls_f32 76.5533981 PICurrCntrI_MtrVoltQaxFFit_M_str.TermD_uls_f32 7823.27002 k_MtrCtrIGreedbackCignalSclFacSlew_UlspS_f32 7823.27002 k_MtrCtrIVirualResDax_Ohm_f32 0.142000005 k_MtrCurrQaxRefModifRpl		
PICurrCnttl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.389899999 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.833000004 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 386.220001 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -194.190002 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 76.5533981 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 0.9997999981 PICurrCntrl_MtrVoltQaxFFFIIt_M_str.PrevInput_Uls_f32 386.220001 PICurrCntrl_MtrVoltQaxFFFIIt_M_str.PrevInput_Uls_f32 -194.190002 PICurrCntrl_MtrVoltQaxFFFIIt_M_str.TrermM_Uls_f32 76.5533981 PICurrCntrl_MtrVoltQaxFFFIIt_M_str.TrermD_Uls_f32 76.5533981 PICurrCntrl_MtrVoltQaxFFFIIt_M_str.TrermD_Uls_f32 0.9997999981 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 6667.54004 k_DualEcuSignalSclFacSlew_UlspS_f32 6667.54004 k_DualEcuSignalSclFacSlew_UlspS_f32 7823.27002 k_MtrCtrICurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrIVirualResDax_Ohm_f32 0.194999993 k_MtrCtrIVirualResDax_Ohm_f32 0.142000005 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrOu		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 0.833000004 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 386.220001 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -194.190002 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 76.5533981 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 0.0997999981 PICurrCntrl_MtrVoltQaxFFFiit_M_str.PrevInput_Uls_f32 386.220001 PICurrCntrl_MtrVoltQaxFFFiit_M_str.PrevOutput_Uls_f32 -194.190002 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 76.5533981 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 76.5533981 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 0.0997999981 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 6667.54004 k_DualEcuSignalSclFacSlew_UlspS_f32 161.199997 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 7823.27002 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.194999993 k_MtrCtrlVirualResDax_Ohm_f32 0.142000005 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_V		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32 386.220001 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_UIs_f32 -194.190002 PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32 76.5533981 PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32 0.0997999981 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32 386.220001 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_UIs_f32 -194.190002 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32 76.5533981 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32 -194.190002 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32 76.5533981 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32 0.0997999981 k_CLOAFdbackSignalSclFacSlew_UIspS_f32 6667.54004 k_DualEcuSignalSclFacSlew_UIspS_f32 161.199997 k_ILOAFdbackSignalSclFacSlew_UIspS_f32 7823.27002 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.194999993 k_MtrCtrlVirualResDax_Ohm_f32 0.142000005 k_MtrCurrQaxRefModifBsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrCurrQ		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 76.5533981 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.0997999981 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 386.220001 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 -194.190002 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 76.5533981 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.0997999981 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 6667.54004 k_DualEcuSignalSclFacSlew_UlspS_f32 161.199997 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 7823.27002 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.194999993 k_MtrCtrlVirualResQax_Ohm_f32 0.142000005 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_volt_f32 6.06680012		111
PICurrCntr_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32 386.220001 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_UIs_f32 -194.190002 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32 76.5533981 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32 0.0997999981 k_CLOAFdbackSignalSclFacSlew_UIspS_f32 6667.54004 k_DualEcuSignalSclFacSlew_UIspS_f32 161.199997 k_ILOAFdbackSignalSclFacSlew_UIspS_f32 7823.27002 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.194999993 k_MtrCtrlVirualResQax_Ohm_f32 0.142000005 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRpIEn_Cnt_lgc 0 k_MtrCurrQaxRefModifRpIEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 6.06680012		76.5533981
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 -194.190002 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 76.5533981 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.0997999981 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 6667.54004 k_DualEcuSignalSclFacSlew_UlspS_f32 161.199997 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 7823.27002 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.194999993 k_MtrCtrlVirualResQax_Ohm_f32 0.142000005 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 6.06680012	PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0997999981
PICurrCntrl_MtrVoltQaxFFFilt_N_str.TermN_UIs_f32 76.5533981 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32 0.0997999981 k_CLOAFdbackSignalSclFacSlew_UIspS_f32 6667.54004 k_DualEcuSignalSclFacSlew_UIspS_f32 161.199997 k_ILOAFdbackSignalSclFacSlew_UIspS_f32 7823.27002 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.194999993 k_MtrCtrlVirualResQax_Ohm_f32 0.142000005 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRpIEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 6.06680012	PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	386.220001
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.0997999981 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 6667.54004 k_DualEcuSignalSclFacSlew_UlspS_f32 161.199997 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 7823.27002 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.194999993 k_MtrCtrlVirualResQax_Ohm_f32 0.142000005 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRpIEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 6.06680012		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32 6667.54004 k_DualEcuSignalSclFacSlew_UlspS_f32 161.199997 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 7823.27002 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlVirualResDax_OntrolDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.194999993 k_MtrCtrlVirualResQax_Ohm_f32 0.142000005 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRpIEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 6.06680012		
k_DualEcuSignalSclFacSlew_UlspS_f32 161.199997 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 7823.27002 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.194999993 k_MtrCtrlVirualResQax_Ohm_f32 0.142000005 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 6.06680012		
k_ILOAFdbackSignalScIFacSlew_UIspS_f32 7823.27002 k_MtrCtrICurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrIFeedbackControlDisable_Cnt_lgc 0 k_MtrCtrIVirualResDax_Ohm_f32 0.194999993 k_MtrCtrIVirualResQax_Ohm_f32 0.142000005 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRpIEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 6.06680012		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.194999993 k_MtrCtrlVirualResQax_Ohm_f32 0.142000005 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 6.06680012		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.194999993 k_MtrCtrlVirualResQax_Ohm_f32 0.142000005 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 6.06680012		
k_MtrCtrlVirualResDax_Ohm_f32 0.194999993 k_MtrCtrlVirualResQax_Ohm_f32 0.142000005 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 6.06680012		
k_MtrCtrlVirualResQax_Ohm_f32 0.142000005 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 6.06680012		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 6.06680012	k_MtrCtrlVirualResQax_Ohm_f32	0.142000005
k_MtrVoltDaxIntegHiLim_Volt_f32 6.06680012	k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
	k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k Mtr)/altDayIntagl al im Valt f32	- · · · · · · · · · · · · · · · · · · ·	
	k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017
k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1 1 24 2020002		
k_MtrVoltQaxIntegHiLim_Volt_f32 24.2038002 k_MtrVoltQaxIntegLoLim_Volt_f32 -4.57000017		
k_MtrVoltVecuFiltEnable Cnt lgc 1		

PICurrCntrl_Per1



Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	-23.5930004		
k_VoltSatQaxPolyCoeff_Uls_f32	22.8640003		
k_deadtimeVScale_Uls_f32	0.967999995		
t_CommOffsetTblX_Uls_u3p13[0]	918		
t_CommOffsetTblX_Uls_u3p13[1]	1679		
t_CommOffsetTblY_Cnt_u16[0]	71		
t_CommOffsetTblY_Cnt_u16[1]	676		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	190		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	190	190	-
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	-
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-4.29100323	-4.29100323 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-2.2389493	-2.2389493 ± 4.88E-04	-
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	22866	22866 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	-
PICurrCntrl DualEcuFailSclFac Uls M f32	0.10385	0.10385 ± 0.0625	•

Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	-
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	-
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	-
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	-
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	-

Test Step 2.129 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.76400006
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.99300003
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	176.421997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-310.208008
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009





Name	Input Value		
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.71200001		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.60000002		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	392.343994		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	136.852005		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008		
MtrCtrl_Vecu_Volt_M_f32[0]	21.3729992		
MtrCtrl_Vecu_Volt_M_f32[1]	23.7329998		
MtrCurrDaxPrevIntg_Volt_M_f32	-3.78200006		
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007		
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001		
MtrCurrQaxCog_Amp_M_f32	-144.667007		
MtrCurrQaxPrevIntg_Volt_M_f32	4.63049984		
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006		
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	5.38999987		
MtrPosComputationDelay_Rad_M_f32[1]	2.17600012		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.851999998		
PICurrCntrl DualEcuFailSclFac Uls M f32	0.125		
PICurrCntrl InverterFailSclFac Uls M f32	0.757000029		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.754400015		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.83300004		
PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32	-627.179993		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	386.220001		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	50.1534996		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.319000006		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-627.179993		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	386.220001		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	50.1534996		
	0.319000006		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32			
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7980.1499		
k_DualEcuSignalSclFacSlew_UlspS_f32	162.399994		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	6489.7002		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.0040000019		
k_MtrCtrlVirualResQax_Ohm_f32	0.108999997		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	7.02110004		
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	18.0783997		
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	21.5779991		
k_VoltSatQaxPolyCoeff_Uls_f32	19.1660004		
k_deadtimeVScale_Uls_f32	0.97299999		
t_CommOffsetTblX_Uls_u3p13[0]	5022		
t_CommOffsetTblX_Uls_u3p13[1]	7003		
t_CommOffsetTblY_Cnt_u16[0]	63		
t_CommOffsetTblY_Cnt_u16[1]	327		
target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr	0		
target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr	0		
target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-198.285995		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3399		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	1.62199998		
	1.62199998		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val		Province d Male	
Name	Actual Value	Expected Value	Resu
MtrCntrl_Write_CommOffset_Cnt_u16(val)	240	240	•
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	50820	50820 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	10.7200012	10.7200012 ± 7.81E-03	•
MtrCntrl Write MtrDaxVoltage Volt f32(val)	-14.6940002	-14.6940002 ± 4.88E-04	•
		7.00000000 . 4.005.04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-7.66699982	-7.66699982 ± 4.88E-04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	-7.66699982 34820	-7.66699982 ± 4.88E-04 34820 ± 1.52588E-05	





Name	Actual Value	Expected Value	Result
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.145300001	0.145300001 ± 0.0625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

lame	Input Value	
rastDataAccessBufIndex_Cnt_M_u16	0	
/trCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	
trCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	
ItrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	
trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
ItrCntrl Read MtrCurrDax Amp f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
ltrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
trCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val	
trCntrl Read SysState Cnt Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
trCtrl MtrCurrDaxMaxVal Amp M f32[0]	-147.343002	
trCtrl MtrCurrDaxMaxVal Amp M f32[1]	127.972	
trCtrl MtrDampTermDax Ohm M f32[0]	0.0359999985	
trCtrl MtrDampTermDax_Ohm M f32[1]	0.075000003	
trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994	
trCtrl_MtrDampTermQax_Onm_M_i32[0]	0.0280000009	
trCtrl_MtrDaxIntegralGain Ohm M f32[0]	1.28900003	
trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.70599997	
trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	383.354004	
trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-411.454987	
	0.035999985	
trCtrl_MtrlmpedDax_Ohm_M_f32[0]	0.075000003	
trCtrl_MtrImpedDax_Ohm_M_f32[1]		
trCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0120000001	
trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0560000017	
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.43299998	
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.112999998	
trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	39.0110016	
trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-717.330017	
trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-7.66699982	
trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	2.61400008	
trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-29.3959999	
trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-1.9400006	
trCtrl_Vecu_Volt_M_f32[0]	5.12099981	
trCtrl_Vecu_Volt_M_f32[1]	7.48099995	
trCurrDaxPrevIntg_Volt_M_f32	7.36499977	
trCurrDaxRef_Amp_M_f32[0]	-100.282997	
trCurrDaxRef_Amp_M_f32[1]	-120.248001	
trCurrQaxCog_Amp_M_f32	-41.5750008	
trCurrQaxPrevIntg_Volt_M_f32	9.1906004	
trCurrQaxRef_Amp_M_f32[0]	-65.1900024	
trCurrQaxRef_Amp_M_f32[1]	-216.972	
trCurrQaxRpl_Amp_M_f32	0	
trPosComputationDelay_Rad_M_f32[0]	-0.195999995	
trPosComputationDelay_Rad_M_f32[1]	-0.303000003	
ICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.349000007	
ICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.126000002	
ClCurrCntrl_InverterFailSclFac_Uls_M_f32	0.150000006	

PICurrCntrl_Per1



Name	Input Value		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.203999996		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.521000028		
PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32	-10.21		
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	-784.130005		
PICurrCntrl MtrVecuFilt M str.TermN Uls f32	51.3003006		
PICurrCntrl MtrVecuFilt M str.TermD Uls f32	0.881699979		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-10.21		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32	-784.130005		
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	51.3003006		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.881699979		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	1293.53003		
k DualEcuSignalSclFacSlew UlspS f32	163.600006		
k ILOAFdbackSignalSclFacSlew UlspS f32	7361.14014		
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc	1		
k MtrCtrlFeedbackControlDisable Cnt lgc	0		
k MtrCtrlVirualResDax Ohm f32	0.101000004		
k_MtrCtrlVirualResQax_Ohm_f32	0.107000001		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k MtrCurrQaxRefModifRplEn Cnt lgc	0		
k MtrVoltDaxIntegHiLim Volt f32	15.2988997		
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.5		
k MtrVoltQaxFiltFFEnable Cnt lgc	1		
k MtrVoltQaxIntegHiLim Volt f32	14.7958002		
k MtrVoltQaxIntegLoLim Volt f32	-10.5		
k MtrVoltVecuFiltEnable Cnt lqc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	6.31400013		
k_VoltSatQaxPolyCoeff_Uls_f32	16.4209995		
k deadtimeVScale Uls f32	0.990999997		
t CommOffsetTblX Uls u3p13[0]	4611		
t CommOffsetTblX Uls u3p13[1]	5579		
t_CommOffsetTblY_Cnt_u16[0]	889		
t CommOffsetTblY Cnt u16[1]	1543		
target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr	1		
target MtrCntrl Read IvtrLoaMtgtnEn Cnt lgc ptr	1		
target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4458		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Resul
MtrCntrl Write CommOffset Cnt u16(val)	4458	4458	
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	
MtrCntrl Write MtrCurrQaxFinalRef Amp f32(val)	-23.6150017	-23.6150017 ± 7.81E-03	
MtrCntrl Write MtrDaxVoltage Volt f32(val)	-1.25051808	-1.25051808 ± 4.88E-04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.7946043	-4.7946043 ± 4.88E-04	
MtrCntrl Write PhaseAdvanceFinal Rev u0p16(val)	33385	33385 ± 1.52588E-05	
MtrCurrDaxPrevIntg Volt M f32	0	0	
PICurrCntrl DualEcuFailSclFac Uls M f32	0.105549999	0.105549999 ± 0.0625	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	-
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	-
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	-
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓



Test Step 2.131 (Repeat Count = 1)		*
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	1	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrOffComOffcet_Cnt_u16(ntr)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr) MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl Read SysState Cnt Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl MtrCurrDaxMaxVal Amp M f32[0]	6.18900013	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	83.0540009	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0099999978	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.079999982	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.0099999978	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.111000001	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	757.447021	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-505.596985	
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0099999978	
MtrCtrl_MtrlmpedDax_Ohm_M_f32[1]	0.079999982	
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0939999968	
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.0879999995	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.73800004	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.727 841.114014	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	815.677979	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-3.59500003	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-28.4209995	
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-17.1070004	
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	15.9390001	
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002	
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008	
MtrCurrDaxPrevIntg_Volt_M_f32	0.851999998	
MtrCurrDaxRef_Amp_M_f32[0]	-68.6760025	
MtrCurrDaxRef_Amp_M_f32[1]	-96.776001	
MtrCurrQaxCog_Amp_M_f32	48.8400002	
MtrCurrQaxPrevIntg_Volt_M_f32	17.5212994	
MtrCurrQaxRef_Amp_M_f32[0]	-146.723007	
MtrCurrQaxRef_Amp_M_f32[1]	-121.943001	
MtrCurrQaxRpl_Amp_M_f32	0	
MtrPosComputationDelay_Rad_M_f32[0]	-4.36899996	
MtrPosComputationDelay_Rad_M_f32[1]	-4.83900023	
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.911000013	
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32 PICurrCntrl InverterFailSclFac Uls M f32	0.127000004 0.513000011	
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.68629998	
PICurrCntrl MtrCurrQaxSatFluxRatio_Uls_M_132	0.686999977	
PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32	570.700012	
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	947.73999	
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	32.4859009	
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.175400004	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	570.700012	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	947.73999	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	32.4859009	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.175400004	
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6616.02002	
k_DualEcuSignalSclFacSlew_UlspS_f32	164.800003	
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5777.70996	
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1	
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1	
k_MtrCtrlVirualResDax_Ohm_f32	0.00200000009	
k_MtrCtrlVirualResQax_Ohm_f32	0.0410000011	
k_MtrCurrQaxRefModifDsb_Cnt_lgc k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0	
k_MtrVoltDaxIntegHiLim Volt f32	28.8642998	
k_MtrVoltDaxIntegLoLim_Volt_f32	-2.400001	
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0	
k MtrVoltQaxIntegHiLim Volt f32	6.41629982	

PICurrCntrl_Per1



Name	Input Value		
k MtrVoltVecuFiltEnable Cnt lgc	0		
k VoltSatDaxPolyCoeff Uls f32	16.5690002		
k VoltSatQaxPolyCoeff Uls f32	18.1630001		
k deadtimeVScale Uls f32	0.971000016		
t_CommOffsetTbIX_UIs_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	771		
t_CommOffsetTblY_Cnt_u16[1]	1636		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1441		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1636	1636	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63635	63635 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-170.783005	-170.783005 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-18.0484562	-18.0484562 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	10.1218939	10.1218939 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	4010	4010 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	-2.4000001	-2.4000001	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.14760001	0.14760001 ± 0.0625	~

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	-
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	-
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	_

Test Step 2.132 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-105.246002
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	41.6290016
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.075000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0710000023
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0560000017
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0130000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.029999993
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.186000004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	161.147995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	308.806
MtrCtrl_MtrlmpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0700000003

2016-09-15, 18:23:31+0530





Name	Input Value		
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.0130000003		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.43900001		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.87399995		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	259.894012		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	103.217003		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	0.908999979		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	21.0249996		
MtrCtrl_Vecu_Volt_M_f32[0]	18.5559998		
MtrCtrl_Vecu_Volt_M_f32[1]	20.9160004		
	1.579		
MtrCurrDayPer Amp M 63(0)			
MtrCurrDayRef_Amp_M_f32[0]	-139.906998		
MtrCurrDaxRef_Amp_M_f32[1]	115.814003		
MtrCurrQaxCog_Amp_M_f32	107.702003		
MtrCurrQaxPrevIntg_Volt_M_f32	13.7006998		
MtrCurrQaxRef_Amp_M_f32[0]	-208.287994		
MtrCurrQaxRef_Amp_M_f32[1]	-27.9839993		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	-5.46899986		
MtrPosComputationDelay_Rad_M_f32[1]	-3.09299994		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.702000022		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.128000006		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.00400000019		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.35589999		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.143000007		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-38.7999992		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	269.399994		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	79.1921005		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.763000011		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-38.7999992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	269.399994		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	79.1921005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.763000011		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	5911.31982		
k_DualEcuSignalSclFacSlew_UlspS_f32	166		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2332.93994		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k MtrCtrlFeedbackControlDisable Cnt Igc	0		
k MtrCtrlVirualResDax Ohm f32	0.169		
k_MtrCtrlVirualResQax_Ohm_f32	0.194000006		
	1		
k_MtrCurrQaxRefModifDsb_Cnt_lgc			
k_MtrCurrQaxRefModifRpIEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	15.7931995		
k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	26.1371994		
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	4.79199982		
k_VoltSatQaxPolyCoeff_Uls_f32	19.2709999		
k_deadtimeVScale_Uls_f32	0.963		
t_CommOffsetTbIX_UIs_u3p13[0]	2638		
t_CommOffsetTbIX_Uls_u3p13[1]	3628		
t_CommOffsetTblY_Cnt_u16[0]	297		
t_CommOffsetTblY_Cnt_u16[1]	1110		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3172		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
		·	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	3172	3172	
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0±1	V
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	16.0734272	16.0734253 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-25.1564445	-25.1564407 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	35330	35330 ± 1.52588E-05	~
	<u>-</u>		
MtrCurrDaxPrevIntg_Volt_M_f32 PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0 0.107250005	0 0.107250005 ± 0.0625	· ·



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

lame	Input Value
astDataAccessBufIndex Cnt M u16	0
/trCntrl Read DualEcuMotCtrlMtgnEna Cnt Igc(ptr)	target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr
ItrCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr
ItrCntrl Read ModIdxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModidxSrlComSvcDft Cnt lgc Val
ItrCntrl Read MotCurrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc ptr
/trCntrl Read MtrCurrDax Amp f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
/trCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr
ItrCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
ItrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
ItrCtrl MtrCurrDaxMaxVal Amp M f32[0]	-213.026993
htrCtrl MtrCurrDaxMaxVal Amp M f32[1]	-66.7229996
trCtrl MtrDampTermDax Ohm M f32[0]	0.112999998
ItrCtrl MtrDampTermDax Ohm M f32[1]	0.0769999996
htrCtrl MtrDampTermQax Ohm M f32[0]	0.0359999985
/trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
trCtrl MtrDaxIntegralGain Ohm M f32[0]	1.94200003
ItrCtrl MtrDaxIntegralGain Ohm M f32[1]	1.00600004
htrCtrl MtrDaxPropotionalGain Ohm M f32[0]	-80.9769974
trCtrl MtrDaxPropotionalGain Ohm M f32[1]	-53.5390015
ItrCtrl MtrImpedDax Ohm M f32[0]	0.115999997
ItrCtrl MtrImpedDax Ohm M f32[1]	0.115999997
ItrCtrl MtrImpedQax Ohm M f32[0]	0.0970000029
trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007
ItrCtrl MtrQaxIntegralGain Ohm M f32[0]	0.352999985
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.35800004
ItrCtrl MtrQaxPropotionalGain Ohm M f32[0]	-261.626007
trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-882.955017
trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
ltrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006
ltrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002
htrCtrl_Vecu_Volt_M_f32[0]	5.12099981
htrCtrl_Vecu_Volt_M_f32[1]	7.48099995
trCurrDaxPrevIntg_Volt_M_f32	-8.56599998
ItrCurrDaxRef_Amp_M_f32[0]	-82.2979965
ItrCurrDaxRef_Amp_M_f32[1]	46.8180008
htrCurrQaxCog_Amp_M_f32	5.72399998
htrCurrQaxPrevIntg_Volt_M_f32	26.4118996
ItrCurrQaxRef Amp M f32[0]	31.5869999
trCurrQaxRef Amp M f32[1]	-186.395996
ItrCurrQaxRpl Amp M f32	0
ItrPosComputationDelay Rad M f32[0]	2.9749999
ItrPosComputationDelay Rad M f32[1]	0.486999989
PlCurrCntrl CurrSensFailSclFac Uls M f32	0.662
PlCurrCntrl DualEcuFailSclFac Uls M f32	0.128999993
PICurrCntrl InverterFailScIFac UIs M f32	0.481000006

PICurrCntrl Per1

2016-09-15, 18:23:31+0530



Input Value PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32 0.116499998 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.0989999995 PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32 -194.190002 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -340.130005 PICurrCntrl MtrVecuFilt M str.TermN Uls f32 3.26889992 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.282200009 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 -194.190002 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32$ -340.130005 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 3.26889992 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32$ 0.282200009 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 7083.27002 k_DualEcuSignalSclFacSlew_UlspS_f32 167 199997 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 947.890015 $k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc$ 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0.133000001 k_MtrCtrlVirualResDax_Ohm_f32 k_MtrCtrlVirualResQax_Ohm_f32 0.131999999 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ k_MtrVoltDaxIntegHiLim_Volt_f32 12.9096003 $k_MtrVoltDaxIntegLoLim_Volt_f32$ -9.64999962 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 29 3528004 $k_MtrVoltQaxIntegHiLim_Volt_f32$ k_MtrVoltQaxIntegLoLim_Volt_f32 -9.64999962 k MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 -6.11899996 k VoltSatQaxPolyCoeff Uls f32 19.4669991 k_deadtimeVScale_Uls_f32 0.987999976 t CommOffsetTblX Uls u3p13[0] 1212 $t_CommOffsetTblX_Uls_u3p13[1]$ 1704 t CommOffsetTblY Cnt u16[0] 23 t_CommOffsetTblY_Cnt_u16[1] 212 target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr 0 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 136.341003 $target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr$ 479 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -126.640999 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 0 **Actual Value Expected Value** Name Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 479 479 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 0 0 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 25.862999 25.862999 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -2.46959925 -2.46959901 ± 4.88E-04 -4.27839613 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -4 27839661 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 3723 3723 ± 1.52588E-05

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

0.149899989

0.149899989 ± 0.0625

MtrCurrDaxPrevIntg_Volt_M_f32 PICurrCntrl_DualEcuFailSclFac_Uls_M_f32



Test Step 2.134 (Repeat Count = 1)		*
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	1	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val) MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MitCriti_Read_MitCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target MtrCntrl Read SysState Cnt Enum Val	
MtrCtrl MtrCurrDaxMaxVal Amp M f32[0]	-212.632996	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.079999982	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.20099998	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.342000008	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	808.778992	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-903.747009	
MtrCtrl_MtrlmpedDax_Ohm_M_f32[0]	0.112999998	
MtrCtrl_MtrImpedOax_Ohm_M_f32[1] MtrCtrl_MtrImpedOax_Ohm_M_f32[0]	0.125 0.0529999994	
MtrCtrl_MtrImpedQax_Ohm_M_f32[0] MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.101999998	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.838	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	827.307007	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	398.522003	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002	
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005	
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003	
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002	
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008	
MtrCurrDaxPrevIntg_Volt_M_f32	7.60099983	
MtrCurrDavRef_Amp_M_f32[0]	160.044006	
MtrCurrDaxRef_Amp_M_f32[1] MtrCurrQaxCog_Amp_M_f32	165.242004 59.3040009	
MtrCurrQaxPrevIntg_Volt_M_f32	8.14280033	
MtrCurrQaxRef Amp M f32[0]	-133.947006	
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035	
MtrCurrQaxRpl_Amp_M_f32	0	
MtrPosComputationDelay_Rad_M_f32[0]	4.70800018	
MtrPosComputationDelay_Rad_M_f32[1]	-2.68499994	
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.851000011	
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.129999995	
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.465999991	
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.591700017	
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0469999984	
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-194.190002 704.130005	
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_UIs_f32 PICurrCntrl MtrVecuFilt M str.TermN UIs f32	-784.130005 12.2650003	
PICurrCntrl MtrVecuFilt M str.TermD Uls f32	0.361299992	
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput Uls f32	-194.190002	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-784.130005	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	12.2650003	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.361299992	
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2409.94995	
k_DualEcuSignalSclFacSlew_UlspS_f32	168.399994	
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4854.70996	
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1	
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1	
k_MtrCtrlVirualResDax_Ohm_f32	0.039000008	
k_MtrCtrlVirualResQax_Ohm_f32	0.158000007	
k_MtrCurrQaxRefModifDsb_Cnt_lgc k_MtrCurrQaxRefModifRpIEn_Cnt_lgc	0	
k_MtrVoltDaxIntegHiLim Volt f32	18.4475994	
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.409998	
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0	
k MtrVoltQaxIntegHiLim Volt f32	24.7383003	





Name	Input Value		
k MtrVoltVecuFiltEnable Cnt lqc	0		
k VoltSatDaxPolyCoeff Uls f32	20.7450008		
k VoltSatQaxPolyCoeff Uls f32	23.7810001		
k deadtimeVScale Uls f32	0.954999983		
t CommOffsetTblX Uls u3p13[0]	3808		
t CommOffsetTblX Uls u3p13[1]	7298		
t CommOffsetTblY Cnt u16[0]	1237		
t CommOffsetTblY Cnt u16[1]	383		
target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr	1		
target MtrCntrl Read IvtrLoaMtgtnEn Cnt lgc ptr	0		
·			
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2036		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	383	383	✓
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	62586	62586 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	16.3980026	16.3980026 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	19.8985023	19.8985004 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.27244139	-4.27244043 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	56120	56120 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	-8.02652168	-8.02652168	✓
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.108949997	0.108949997 ± 0.0625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	-
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	•
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.135 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-132.813004
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-9.14299965
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.123000003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0460000001
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.123000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.296
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.25800002
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-261.467987
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-308.463013
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0170000009

2016-09-15, 18:23:31+0530





MitCirl MirrogerGox, Ohm, M, 152(1)	
MicCir MicQasPropotionalGain, Ohm M, 132(1) 0.814000022 MitCir MicQasPropotionalGain, Ohm M, 132(1) 115,543999 MitCir MicQasPropotionalGain, Ohm M, 132(1) 115,543999 MitCir MicQasPropotionalGain, Ohm M, 132(1) 2.08,330002 4.5599994 MitCir MicVoli M, 132(1) 2.08,330002 8.61900043 MitCir MicVoli M, 132(1) 1.5599998 1.5590998 MitCir MicVoli M, 132(1) 1.5599998 1.5590998 1.5590998 1.5590998 MitCir MicCurdosPer Volt M, 132(1) 1.5599998 1.55909998 1.5590	
MicCir MiCoxPropotionalGain_Ohm_M_32(I)	
MrCri_MrCaxPropotionalGain_Ohm_M_52[1] 115.543999 MrCri_MrVorlbaxFr_Vorl_M_52[1] 4.55999984 MrCri_MrVorlbaxFr_Vorl_M_52[1] 20.8330002 MrCri_MrVorlbaxFr_Vorl_M_52[1] 1.31590001 MrCri_MrVorlbaxFr_Vorl_M_52[1] 1.31590001 MrCri_Vecu_Vorl_M_52[1] 1.5559998 MrCri_Vecu_Vorl_M_52[1] 20.9160004 MrCurDaxRef_Amp_M_52[0] 45.190024 MrCurDaxRef_Amp_M_52[0] 46.190024 MrCurDaxRef_Amp_M_52[0] 46.190024 MrCurDaxRef_Amp_M_52[0] 46.190024 MrCurDaxRef_Amp_M_52[0] 46.190024 MrCurDaxRef_Amp_M_52[0] 105.24002 MrCurDaxRef_Amp_M_52[0] 105.24002 MrCurDaxRef_Amp_M_52[0] 1.65.24002 MrCurDaxRef_Amp_M_52[1] 41.629016 MrCurDaxRef_Amp_M_52[1] 41.629016 MrCurDaxRef_Amp_M_52[2] 0.0000000 MrPosComputationDelay_Rad_M_52[0] 0.15700005 MrPosComputationDelay_Rad_M_52[0] 0.15700005 MrCurDaxRef_Amp_M_52[1] 0.00000000000000000000000000000000000	
MicCir MirVoliDarF Volt M 732(0)	
MicCirt MirVolRoarF, Volt, M.	
MirCirt MirVolRaxFF, Volt, M.	
MirCir MirVoliQaxFF_Voli_M_[32[1] -13.1560001 MirCir Vecu_Voit_M_[32[1] 18.5559988 MirCir Jevac_Voit_M_[32[1] 20.9160004 MirCurrDaxRerIng_Voli_M_[32] 14.2440004 MirCurrDaxRerI_Amp_M_[32[0] -65.1900024 MirCurrDaxRef_Amp_M_[32[1] -216.972 MirCurrDaxRef_Amp_M_[32[1] -216.972 MirCurrDaxRef_Amp_M_[32[0] -105.246002 MirCurrQaxRef_Amp_M_[32[0] -105.246002 MirCurrQaxRef_Amp_M_[32[1] 41.629016 MirCurrQaxRef_Amp_M_[32[1] 41.629016 MirCurrQaxRef_Amp_M_[32[1] -105.246002 MirCurrQaxRef_Amp_M_[32[1] 0 MirCurrQaxRef_Amp_M_[32[1] -105.246002 MirCurrQaxRef_Amp_M_[32[1] -105.246002 MirCurrQaxRef_Amp_M_[32[1] 0 MirCurrQaxRef_Amp_M_[32[1] -5100005 MirCurrQaxRef_Amp_M_[32[1] -51000005 MirCurrQaxRef_Amp_M_[32[1] -51000006 MirCurrQaxRef_Amp_M_[32[2] -0.17000005 MirCurrQaxRef_Amp_M_[32[2] -0.27000003 PlCurrCntr_QurrSer_Fall_M_End_M_[32] 0.36000003 PlCurrCntr_MirCurrQax	
MirCtrl Veau_Volt_M_[32](0) 18.559998 MirCtrl Veau_Volt_M_[32](1) 20.9160004 MirCurrDaxPerf_Amp_M_[32](1) 18.2440004 MirCurrDaxPerf_Amp_M_[32](1) -65.1900024 MirCurrDaxPerf_Amp_M_[32](1) -216.972 MirCurrDaxPerlot_M_D_M_[32](1) -216.972 MirCurrDaxPerlot_M_D_M_[32](1) -105.246002 MirCurrDaxPerlot_M_D_M_[32](1) -105.246002 MirCurrDaxRef_Amp_M_[32](1) -105.00006 MirCurrDaxRef_Amp_M_[32](1) -107.00006 MirCurrDaxRef_Amp_M_[32](1) -107.00006 MirCurrDaxRef_Amp_M_[32](1) -5.0110016 PlCurrCntrl_UniverterFailSelf-ac_Uis_M_[32] 0.48100006 PlCurrCntrl_InverterFailSelf-ac_Uis_M_[32] 0.48100006 PlCurrCntrl_MirVerUrrDaxSelf-InvRatio_Uis_M_[32] 0.70000000001 PlCurrCntrl_MirVerUr	
MITCIT_Vecu_Volt_M_R32[1] 20.9160004 MITCUTDARPRevining_Volt_M_R32 14.2440004 MITCUTDARPRE_Amp_M_R32[1] -65.1900024 MITCUTDARRE_Amp_M_R32[1] -216.972 MITCUTDARRE_Amp_M_R32[1] -20.6149998 MITCUTDARPREVINITQ_VOLT_M_R32 20.6149998 MITCUTDARPREVINITQ_VOLT_M_R32[1] -105.246002 MITCUTDARPRE_Amp_M_R32[1] 41.6290016 MITCUTDARPRE_Amp_M_R32[1] 41.6290016 MITCUTDARPRE_Amp_M_R32[1] -0 MITCUTDARPRE_Amp_M_R32[0] 0.157000005 MITPOSCOmputationDelay_Rad_M_R32[0] 0.157000005 MITPOSCOMPUtationDelay_Rad_M_R32[1] -5.01100016 PICUTCTCH_INTERSERSIBLEGA_UIS_M_R32 0.237000003 PICUTCTCH_INTERSERSIBLEGA_UIS_M_R32 0.130999997 PICUTCTCH_INTERSERSIBLEGA_UIS_M_R32 0.0850000013 PICUTCTCH_INTERVECUELIL_M_ST.Pervinput_UIS_R32 0.725000024 PICUTCTCH_INTERVECUELIL_M_ST.Pervinput_UIS_R32 20.7000008 PICUTCTCH_INTERVECUELIL_M_ST.Pervinput_UIS_R32 66.3365021 PICUTCTCH_INTERVECUELIL_M_ST.Pervinput_UIS_R32 0.444699991 PICUTCTCH_INTERVELORERSIBL_M_ST.Pervinput_UIS_R32 0.64469	
MrCurrDaxPrevinig_Volt_M_f32 14.2440004 MrCurrDaxRef_Amp_M_f32(1) -65.1900024 MrCurrDaxRef_Amp_M_f32(1) 216.972 MrCurrQaxCog_Amp_M_f32 20.6149988 MrCurrQaxPrevinig_Volt_M_f32 9.3899975 MrCurrQaxRef_Amp_M_f32(1) -105.246002 MrCurrQaxRef_Amp_M_f32(1) 41.629016 MrCurrQaxRef_Amp_M_f32(1) 0 MrPosComputationDelay_Rad_M_f32(1) -5.01100016 MrPosComputationDelay_Rad_M_f32(1) -5.01100016 MrPosComputationDelay_Rad_M_f32(1) -5.01100016 PlCurrCntrl_CurrSensFailSofFac_Uls_M_f32 0.13099997 PlCurrCntrl_UniterterFailSofFac_Uls_M_f32 0.481000006 PlCurrCntrl_MrtrcurrQaxSaffLwxRatio_Uls_M_f32 0.0650000013 PlCurrCntrl_MrtrcurGaxSaffLwxRatio_Uls_M_f32 0.725000024 PlCurrCntrl_Mrtrvecuriit_M_sir_Previoupt_Uls_f32 20.7000008 PlCurrCntrl_Mrtrvecuriit_M_sir_FremM_Uls_f32 66.3365021 PlCurrCntrl_Mrtrvecuriit_M_sir_FremD_Uls_f32 0.44699991 PlCurrCntrl_MrtrvelQaxFFFiit_M_sir_FremD_Uls_f32 0.64469991 PlCurrCntrl_MrtrvelQaxFFFiit_M_sir_FremD_Uls_f32 66.3365021 PlCurrCntrl_MrtrvelQa	
MITCurrDaxRef_Amp_M_[52]0] -65.1900024 MITCurrDaxRef_Amp_M_[52]1 -216.972 MITCurrDaxRef_Amp_M_[52]1 20.6149998 MITCurrQaxRef_Amp_M_[52]0 9.33899975 MITCurrQaxRef_Amp_M_[52]0 -105.246002 MITCurrQaxRef_Amp_M_[52]1 41.6290016 MITCurrQaxRef_Amp_M_[52]1 0 MITCurrQaxRef_Amp_M_[52]1 0.157000005 MITPOSCOmputationDelay, Rad_M_[52]0 0.157000005 MITPOSCOmputationDelay, Rad_M_[52]1 -5.01100016 PICurrChit_CurrSensFailsGiFac_Uls_M_[52] 0.237000003 PICurrChit_DualEcurFailsGiFac_Uls_M_[52] 0.13999997 PICurrChit_InverterFailsGiFac_Uls_M_[52] 0.48100006 PICurrChit_InverterFailsGiFac_Uls_M_[52] 0.0050000013 PICurrChit_InverterFailsGiFac_Uls_M_[52] 0.0050000013 PICurrChit_MITVecurFilt_M_str.Previoput_Uls_[52] 0.725000024 PICurrChit_MITVecurFilt_M_str.Previoput_Uls_[52] 0.725000024 PICurrChit_MITVecurFilt_M_str.Previoput_Uls_[52] 0.644699991 PICurrChit_MITVecurFilt_M_str.Previoput_Uls_[52] 20.7000008 PICurrChit_MITVelCaxFFFilt_M_str.Previoput_Uls_[52] 0.644699991 PICurr	
MtrCurrDaxRef_Amp_M_[32[1] 216.972 MtrCurrQaxCog_Amp_M_[32] 20.614998 MtrCurrQaxPrevintg_Volt_M_[32] 9.3389975 MtrCurrQaxRef_Amp_M_[32[0] -105.246002 MtrCurrQaxRef_Amp_M_[32[1] 41.6290016 MtrCurrQaxRef_Amp_M_[32] 0 MtrCurrQaxRef_Amp_M_[32] 0 MtrPosComputationDelay_Rad_M_[32[0] 0.15700005 MtrPosComputationDelay_Rad_M_[32[1] 5.01100016 PlCurrCntf_CurrSensFailsdFac_Uls_M_[32] 0.23700003 PlCurrCntf_UnderCarefailsdFac_Uls_M_[32] 0.30999997 PlCurrCntf_UnverterFailSclFac_Uls_M_[32] 0.48100006 PlCurrCntf_MircurrQaxSelf-LiveRatio_Uls_M_[32] 0.725000024 PlCurrCntf_MircurrQaxSelf-LiveRatio_Uls_M_[32] 0.725000024 PlCurrCntf_MirvecuFilt_M_str.Previput_Uls_[32] 20.7000008 PlCurrCntf_MirvecuFilt_M_str.Previput_Uls_[32] 366.220001 PlCurrCntf_MirvecuFilt_M_str.Previput_Uls_[32] 0.64469991 PlCurrCntf_MirvelClaxFFFilt_M_str.Previput_Uls_[32] 366.220001 PlCurrCntf_MirvelClaxFFFilt_M_str.TermD_Uls_[32] 0.64469991 PlCurrCntf_MirvelClaxFFFilt_M_str.TermD_Uls_[32] 66.3365021 <	
MITCUrrQaxCog_Amp_M_[32] 20.6149998 MITCUrrQaxPrelving_Voil_M_[32] 9.33899975 MITCUrrQaxRef_Amp_M_[32[0] -16.246002 MITCUrrQaxRef_Amp_M_[32[1] 41.6290016 MITCURQaxRef_Amp_M_[32[1] 41.6290016 MITCORQaxRef_Amp_M_[32] 0. MITPOSCOmputationDelay_Rad_M_[52[1] 5.51100016 PICUrrCntt_CurrSensFailSciFac_UIs_M_[32] 0.237000003 PICUrrCntt_DualEcuFailSciFac_UIs_M_[32] 0.481000006 PICUrrCntt_InverterFailSciFac_UIs_M_[32] 0.481000006 PICUrrCntt_INVEGURFAILSCHALD_UIS_M_[32] 0.725000024 PICUrrCntt_MITVecuFill_M_Str_Previoput_UIS_IS2 0.7000003 PICUrrCntt_MITVecuFill_M_str_Previoput_UIS_IS2 20.7000003 PICUrrCntt_MITVecuFill_M_str_Previoput_UIS_IS2 0.725000024 PICUrrCntt_MITVecuFill_M_str_Previoput_UIS_IS2 366.220001 PICUrrCntt_MITVecuFill_M_str_TermD_UIS_IS2 0.64489991 PICUrrCntt_MITVecuFill_M_str_TermD_UIS_IS2 0.64489991 PICUrrCntt_MITVolIQaxFFFill_M_str_TermD_UIS_IS2 0.64489991 PICUrrCntt_MITVolIQaxFFFill_M_str_TermD_UIS_IS2 0.64489991 R_UCLOAFdbackSignalSciFacSiew_UISpS_IS2 610.84659991 <td></td>	
MtrCurrQaxPrevintg_Volt_M_[32] 9.33899975 MtrCurrQaxRef_Amp_M_[32]0 -105.246002 MtrCurrQaxRef_Amp_M_[32]1 41.6290016 MtrCurrQaxRef_Amp_M_[32]2 0 MtrDsComputationDelay_Rad_M_[32]0] 0.157000005 MtrPosComputationDelay_Rad_M_[32]0] 5.01100016 PlCurrCntt_CurrSensPailsGlFac_Uls_M_[32] 0.237000003 PlCurrCntt_DvalEcuFailsClFac_Uls_M_[32] 0.130999997 PlCurrCntt_InverterFailsClFac_Uls_M_[32] 0.481000006 PlCurrCntt_MtrCurrDaxSatFluxRatio_Uls_M_[32] 0.0650000013 PlCurrCntt_MtrCurrDaxSatFluxRatio_Uls_M_[32] 0.725000024 PlCurrCntt_MtrVecuFilt_M_str.Previoput_Uls_[32] 0.725000024 PlCurrCntt_MtrVecuFilt_M_str.Previoput_Uls_[32] 0.644699991 PlCurrCntt_MtrVecuFilt_M_str.Previoput_Uls_[32] 0.644699991 PlCurrCntt_MtrVoltQaxFFFilt_M_str.Previoput_Uls_[32] 0.644699991 PlCurrCntt_MtrVoltQaxFFFilt_M_str.Previoput_Uls_[32] 0.64469991 PlCurrCntt_MtrVoltQaxFFFilt_M_str.FremD_Uls_[32] 0.64469991 PlCurrCntt_MtrVoltQaxFFFilt_M_str.FremD_Uls_[32] 0.64469991 PlCurrCntt_MtrVoltQaxFFFilt_M_str.FremD_Uls_[32] 0.64469991 R_ULOAFdbackSig	
MtrCurrQaxRef_Amp_M_632[0] -105.246002 MtrCurrQaxRef_Amp_M_632[1] 41.6290016 MtrCurrQaxRef_Amp_M_632 0 MtrPosComputationDelay_Rad_M_632[0] 0.157000005 MtrPosComputationDelay_Rad_M_632[1] -5.01100016 PlCurrCntr_CurrSensFailSciFac_Uls_M_632 0.237000003 PlCurrCntr_DualEcuFailSciFac_Uls_M_632 0.13099997 PlCurrCntr_InverterFailSciFac_Uls_M_632 0.48100006 PlCurrCntr_MtrCurrDaxSatFluxRatio_Uls_M_632 0.0065000013 PlCurrCntr_MtrCurrDaxSatFluxRatio_Uls_M_632 0.725000024 PlCurrCntr_MtrVecuFilt_M_str.Prevloupt_Uls_632 20.7000008 PlCurrCntr_MtrVecuFilt_M_str.PrevOutput_Uls_632 386.220001 PlCurrCntr_MtrVecuFilt_M_str.TermD_Uls_f32 0.644699991 PlCurrCntr_MtrVolQaxFFFilt_M_str.PrevOutput_Uls_f32 386.220001 PlCurrCntr_MtrVolQaxFFFilt_M_str.PrevOutput_Uls_f32 386.220001 PlCurrCntr_MtrVolQaxFFFilt_M_str.PrevOutput_Uls_f32 386.220001 PlCurrCntr_MtrVolQaxFFFilt_M_str.FremD_Uls_f32 66.3365021 PlCurrCntr_MtrVolQaxFFFilt_M_str.TermD_Uls_f32 66.3365021 PlCurrCntr_MtrVolQaxFFFilt_M_str.TermD_Uls_f32 67.086609991 k_CLOAFcbackSi	
MtrCurrQaxRef_Amp_M_f32[1] 41.6290016 MtrCurrQaxRef_Amp_M_f82 0 MtrDosComputationDelay_Rad_M_f32[0] 0.15700005 MtrPosComputationDelay_Rad_M_f32[1] 5.51100016 PICurrCntt_CurrSensFailSclFac_Uls_M_f32 0.23700003 PICurrCntt_DualEcuFailSclFac_Uls_M_f32 0.130999997 PICurrCntt_InverterFailSclFac_Uls_M_f32 0.48100006 PICurrCntt_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.0065000013 PICurrCntt_MtrCurrBaxSatFluxRatio_Uls_M_f32 0.725000024 PICurrCntt_MtrVecuFilt_M_str.Previnput_Uls_f32 2.0700008 PICurrCntt_MtrVecuFilt_M_str.TermD_Uls_f32 366.220001 PICurrCntt_MtrVecuFilt_M_str.TermD_Uls_f32 0.644699991 PICurrCntt_MtrVoliQaxFFFilt_M_str.Previnput_Uls_f32 366.220001 PICurrCntt_MtrVoliQaxFFFilt_M_str.TermD_Uls_f32 366.220001 PICurrCntt_MtrVoliQaxFFFilt_M_str.TermD_Uls_f32 66.3365021 PICurrCntt_MtrVoliQaxFFFilt_M_str.TermD_Uls_f32 66.3365021 PICurrCntt_MtrVoliQaxFFFilt_M_str.TermD_Uls_f32 66.3365021 PICurrCntt_MtrVoliQaxFFFilt_M_str.TermD_Uls_f32 67.44699991 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 6110.83008 k_DualEcusignalSc	
MtrCurrQaxRp_Amp_M_f32 0 MtrPosComputationDelay_Rad_M_f32[0] 0.157000005 MtrPosComputationDelay_Rad_M_f32[1] -5.01100016 PiCurrCntr_CurrSensFailsOfFac_Uls_M_f32 0.237000003 PiCurrCntr_DualEcuFailsOfFac_Uls_M_f32 0.130999997 PiCurrCntr_InverterFailsOfFac_Uls_M_f32 0.481000006 PiCurrCntr_MtrCurrDaxSatFuxRatio_Uls_M_f32 0.00650000013 PiCurrCntr_MtrCurrDaxSatFuxRatio_Uls_M_f32 0.725000024 PiCurrCntr_MtrVecuFiit_M_str_Previnput_Uls_f32 20.7000008 PiCurrCntr_MtrVecuFiit_M_str_Previnput_Uls_f32 386.220001 PiCurrCntr_MtrVecuFiit_M_str_TermD_Uls_f32 66.3365021 PiCurrCntr_MtrVecuFiit_M_str_TermD_Uls_f32 0.644699991 PiCurrCntr_MtrVoliQaxFFFiit_M_str_Previnput_Uls_f32 386.220001 PiCurrCntr_MtrVoliQaxFFFiit_M_str_TermD_Uls_f32 66.3365021 PiCurrCntr_MtrVoliQaxFFFiit_M_str_TermD_Uls_f32 66.3365021 PiCurrCntr_MtrVoliQaxFFFiit_M_str_TermD_Uls_f32 66.3365021 PiCurrCntr_MtrVoliQaxFFFiit_M_str_TermD_Uls_f32 66.3365021 PiCurrCntr_MtrVoliQaxFFFiit_M_str_TermD_Uls_f32 6110.83008 k_CLOAF-dbackSignalSciFacSlew_UlspS_f32 6110.83008	
MtrPosComputationDelay_Rad_M_f32[0] 0.157000005 MtrPosComputationDelay_Rad_M_f32[1] -5.01100016 PlCurrCntrl_CurSensFailSclFac_Uls_M_f32 0.2370000003 PlCurrCntrl_DvateSatFisclFac_Uls_M_f32 0.13099997 PlCurrCntrl_InverterFailsClFac_Uls_M_f32 0.481000006 PlCurrCntrl_MtrCurrDaxSatFiuxRatio_Uls_M_f32 0.00650000013 PlCurrCntrl_MtrVecurFit_Mstr_Previput_Uls_f32 0.725000024 PlCurrCntrl_MtrVecuFit_M_str_Previput_Uls_f32 20.7000008 PlCurrCntrl_MtrVecuFit_M_str_Previput_Uls_f32 386.220001 PlCurrCntrl_MtrVecuFit_M_str_TermD_Uls_f32 66.3365021 PlCurrCntrl_MtrVecuFit_M_str_Previput_Uls_f32 0.644699991 PlCurrCntrl_MtrVoltQaxFFFit_M_str_Previput_Uls_f32 386.220001 PlCurrCntrl_MtrVolQaxFFFit_M_str_TermD_Uls_f32 386.220001 PlCurrCntrl_MtrVolQaxFFFit_M_str_TermD_Uls_f32 66.3365021 PlCurrCntrl_MtrVolQaxFFFit_M_str_TermD_Uls_f32 66.3365021 PlCurrCntrl_MtrVolQaxFFFit_M_str_TermD_Uls_f32 66.3365021 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 6110.83008 k_DualEcusSignalSclFacSlew_UlspS_f32 6110.83008 k_DualEcusSignalSclFacSlew_UlspS_f32 69.600006	
MtrPosComputationDelay_Rad_M_f32[1] -5.01100016 PICurrCntrt_CurrSensFailSclFac_Uis_M_f32 0.237000003 PICurrCntrt_InverterFailSclFac_Uis_M_f32 0.13099997 PICurrCntrt_InverterFailSclFac_Uis_M_f32 0.481000006 PICurrCntrt_MtrCurrDaxSatFluxRatio_Uis_M_f32 0.00650000013 PICurrCntrt_MtrCurrQaxSatFluxRatio_Uis_M_f32 0.725000024 PICurrCntrt_MtrVecuFilt_M_str.Previnput_Uis_f32 20.7000008 PICurrCntrt_MtrVecuFilt_M_str.Prevoutput_Uis_f32 386.220001 PICurrCntrt_MtrVecuFilt_M_str.TermD_Uis_f32 66.3365021 PICurrCntrt_MtrVecuFilt_M_str.TermD_Uis_f32 0.644699991 PICurrCntrt_MtrVoltQaxFFFilt_M_str.Prevolupt_Uis_f32 20.7000008 PICurrCntrt_MtrVoltQaxFFFilt_M_str.Prevolupt_Uis_f32 386.220001 PICurrCntrt_MtrVoltQaxFFFilt_M_str.TermD_Uis_f32 66.3365021 PICurrCntrt_MtrVoltQaxFFFilt_M_str.TermD_Uis_f32 66.3365021 PICurrCntrt_MtrVoltQaxFFFilt_M_str.TermD_Uis_f32 66.3365021 k_CLOAFdbackSignalSclFacSlew_UispS_f32 6110.83008 k_DualEcuSignalSclFacSlew_UispS_f32 619.600006 k_LLOAFdbackSignalSclFacSlew_UispS_f32 7008.8501 k_MtrCtrUfvirualResDax_Ohm_f32 0.18700006	
PICurrCntrl_CurrSensFailScIFac_Uls_M_f32 0.237000003 PICurrCntrl_DualEcuFailScIFac_Uls_M_f32 0.130999997 PICurrCntrl_InverterFailScIFac_Uls_M_f32 0.481000006 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.00650000013 PICurrCntrl_MtrVecuFiit_M_str.PrevInput_Uls_f32 0.725000024 PICurrCntrl_MtrVecuFiit_M_str.PrevInput_Uls_f32 20.7000008 PICurrCntrl_MtrVecuFiit_M_str.PrevOutput_Uls_f32 386.220001 PICurrCntrl_MtrVecuFiit_M_str.TermD_Uls_f32 63.3365021 PICurrCntrl_MtrVelotitaxFFFiit_M_str.PrevInput_Uls_f32 0.644699991 PICurrCntrl_MtrVoltQaxFFFiit_M_str.PrevInput_Uls_f32 20.7000008 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 386.220001 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 66.3365021 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 66.3365021 PICurrCntrl_MtrVoltQaxFFFiit_M_str.TermD_Uls_f32 6110.83008 k_CLOAFdbackSignalScIFacSlew_UlspS_f32 6110.83008 k_DualEcuSignalScIFacSlew_UlspS_f32 7208.8501 k_MtrCtrlGreedbackControlDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.11299998 k_MtrCurrQaxRefModiffpls_Cnt_lgc 0	
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32 0.130999997 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.48100006 PICurrCntrl_MtrCurrDaxSattFluxRatio_Uls_M_f32 0.00650000013 PICurrCntrl_MtrCurrQaxSattFluxRatio_Uls_M_f32 0.725000024 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 20.7000008 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 386.220001 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 66.3365021 PICurrCntrl_MtrVeliQxFFFilt_M_str.PrevInput_Uls_f32 0.644699991 PICurrCntrl_MtrVollQaxFFFilt_M_str.PrevOutput_Uls_f32 20.7000008 PICurrCntrl_MtrVollQaxFFFilt_M_str.PrevOutput_Uls_f32 20.7000008 PICurrCntrl_MtrVollQaxFFFilt_M_str.TermN_Uls_f32 386.220001 PICurrCntrl_MtrVollQaxFFFilt_M_str.TermD_Uls_f32 66.3365021 PICurrCntrl_MtrVollQaxFFFilt_M_str.TermD_Uls_f32 66.3365021 PICurrCntrl_MtrVollQaxFFFilt_M_str.TermD_Uls_f32 66.3365021 PiCurrCntrl_MtrVollQaxFFFilt_M_str.TermD_Uls_f32 66.3365021 PiCurrCntrl_MtrVollQaxFFFilt_M_str.TermD_Uls_f32 610.83008 k_DualEcuSignalSclFacSlew_UlspS_f32 619.600006 k_LlOAFdbackSignalSclFacSlew_UlspS_f32 670.800006 k_MtrCtrlGrudResQax_Oh	
PICurrCntrI_InverterFailSclFac_Uls_M_f32 0.481000006 PICurrCntrI_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.00650000013 PICurrCntrI_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.725000024 PICurrCntrI_MtrVecuFilt_M_str.PrevInput_Uls_f32 20.7000008 PICurrCntrI_MtrVecuFilt_M_str.PrevOutput_Uls_f32 386.220001 PICurrCntrI_MtrVecuFilt_M_str.TermN_Uls_f32 66.3365021 PICurrCntrI_MtrVelQaxFFFilt_M_str.PrevInput_Uls_f32 0.644699991 PICurrCntrI_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 20.7000008 PICurrCntrI_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 386.220001 PICurrCntrI_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 66.3365021 PICurrCntrI_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 66.3365021 PICurrCntrI_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 66.3365021 PICurrCntrI_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 66.3365021 PICurrCntrI_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 66.3365021 PICurrCntrI_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 66.3365021 R_UcurrCntrI_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 66.3365021 R_UcurrCntrI_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 66.3365021 R_UcurrCntrI_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 66.3365021	
PICurrCntrI_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.00650000013 PICurrCntrI_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.725000024 PICurrCntrI_MtrVecuFilt_M_str.PrevInput_Uls_f32 20.7000008 PICurrCntrI_MtrVecuFilt_M_str.PrevOutput_Uls_f32 386.220001 PICurrCntrI_MtrVecuFilt_M_str.TermN_Uls_f32 66.3365021 PICurrCntrI_MtrVecuFilt_M_str.PrevInput_Uls_f32 0.644699991 PICurrCntrI_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 20.7000008 PICurrCntrI_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 386.220001 PICurrCntrI_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 366.3365021 PICurrCntrI_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 66.3365021 PICurrCntrI_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 66.3365021 PICurrCntrI_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.644699991 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 6110.83008 k_DualEcuSignalSclFacSlew_UlspS_f32 169.600006 k_MtrCtrICurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrIVirualResDax_Ohm_f32 0.18700006 k_MtrCtrIVirualResQax_Ohm_f32 0.18700006 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_M	
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.725000024 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 20.7000008 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 386.220001 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 66.3365021 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.64469991 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 20.7000008 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 386.220001 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 386.220001 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 66.3365021 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 66.3365021 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.64469991 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 6110.83008 k_DualEcuSignalSclFacSlew_UlspS_f32 6110.83008 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 7208.8501 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.18700006 k_MtrCtrlVirualResQax_Ohm_f32 0.112999998 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrCvirtQax	
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32 20.7000008 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_UIs_f32 386.220001 PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32 66.3365021 PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32 0.644699991 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_UIs_f32 20.7000008 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_UIs_f32 386.220001 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32 66.3365021 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32 66.3365021 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32 0.644699991 k_CLOAFdbackSignalSclFacSlew_UIspS_f32 6110.83008 k_DualEcuSignalSclFacSlew_UIspS_f32 619.600006 k_ILOAFdbackSignalSclFacSlew_UIspS_f32 7208.8501 k_MtrCtrIVcurLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrIVirualResDax_Ohm_f32 0.187000006 k_MtrCtrIVirualResQax_Ohm_f32 0.112999998 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 19.2907009	
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 386.220001 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 66.3365021 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.644699991 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 20.7000008 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 386.220001 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 66.3365021 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 66.3365021 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.644699991 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 6110.83008 k_DualEcuSignalSclFacSlew_UlspS_f32 619.600006 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 7208.8501 k_MtrCtrOurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrIVirualResDax_Ohm_f32 0.187000006 k_MtrCtrIVirualResDax_Ohm_f32 0.187000006 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 19.2907009	
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32 66.3365021 PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32 0.644699991 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32 20.7000008 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_UIs_f32 386.220001 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32 66.3365021 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32 0.644699991 k_CLOAFdbackSignalSclFacSlew_UIspS_f32 6110.83008 k_DualEcuSignalSclFacSlew_UIspS_f32 169.600006 k_ILOAFdbackSignalSclFacSlew_UIspS_f32 7208.8501 k_MtrCtrICurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrIVirualResDax_Ohm_f32 0.18700006 k_MtrCtrIVirualResDax_Ohm_f32 0.112999998 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 19.2907009	
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32 0.644699991 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32 20.7000008 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_UIs_f32 386.220001 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32 66.3365021 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32 0.644699991 k_CLOAFdbackSignalSclFacSlew_UIspS_f32 6110.83008 k_DualEcuSignalSclFacSlew_UIspS_f32 169.600006 k_ILOAFdbackSignalSclFacSlew_UIspS_f32 7208.8501 k_MtrCtrICurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrICurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrIVirualResDax_Ohm_f32 0.18700006 k_MtrCtrIVirualResQax_Ohm_f32 0.112999998 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 19.2907009	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32 20.7000008 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_UIs_f32 386.220001 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32 66.3365021 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32 0.644699991 k_CLOAFdbackSignalSclFacSlew_UIspS_f32 6110.83008 k_DualEcuSignalSclFacSlew_UIspS_f32 169.600006 k_ILOAFdbackSignalSclFacSlew_UIspS_f32 7208.8501 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.18700006 k_MtrCtrlVirualResQax_Ohm_f32 0.112999998 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 19.2907009	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uis_f32 386.220001 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uis_f32 66.3365021 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uis_f32 0.644699991 k_CLOAFdbackSignalSclFacSlew_UispS_f32 6110.83008 k_DualEcuSignalSclFacSlew_UispS_f32 169.600006 k_ILOAFdbackSignalSclFacSlew_UispS_f32 7208.8501 k_MtrCtrICurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrIFeedbackControlDisable_Cnt_lgc 0 k_MtrCtrIVirualResDax_Ohm_f32 0.18700006 k_MtrCtrIVirualResQax_Ohm_f32 0.112999998 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 19.2907009	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32 66.3365021 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32 0.644699991 k_CLOAFdbackSignalSclFacSlew_UIspS_f32 6110.83008 k_DualEcuSignalSclFacSlew_UIspS_f32 169.600006 k_ILOAFdbackSignalSclFacSlew_UIspS_f32 7208.8501 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.187000006 k_MtrCtrlVirualResQax_Ohm_f32 0.112999998 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 19.2907009	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32 0.644699991 k_CLOAFdbackSignalSclFacSlew_UIspS_f32 6110.83008 k_DualEcuSignalSclFacSlew_UIspS_f32 169.600006 k_ILOAFdbackSignalSclFacSlew_UIspS_f32 7208.8501 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.187000006 k_MtrCtrlVirualResQax_Ohm_f32 0.112999998 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 19.2907009	
k_CLOAFdbackSignalSclFacSlew_UlspS_f32 6110.83008 k_DualEcuSignalSclFacSlew_UlspS_f32 169.600006 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 7208.8501 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.187000006 k_MtrCtrlVirualResQax_Ohm_f32 0.112999998 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRpIEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 19.2907009	
k_DualEcuSignalScIFacSlew_UlspS_f32 169.600006 k_ILOAFdbackSignalScIFacSlew_UlspS_f32 7208.8501 k_MtrCtrICurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrIVirualResDax_OntrolDisable_Cnt_lgc 0 k_MtrCtrIVirualResDax_Ohm_f32 0.187000006 k_MtrCtrIVirualResQax_Ohm_f32 0.112999998 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRpIEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 19.2907009	
k_ILOAFdbackSignalScIFacSlew_UlspS_f32 7208.8501 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.187000006 k_MtrCtrlVirualResQax_Ohm_f32 0.112999998 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRpIEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 19.2907009	
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.187000006 k_MtrCtrlVirualResQax_Ohm_f32 0.112999998 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRpIEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 19.2907009	
k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.187000006 k_MtrCtrlVirualResQax_Ohm_f32 0.112999998 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 19.2907009	
k_MtrCtrlVirualResDax_Ohm_f32 0.187000006 k_MtrCtrlVirualResQax_Ohm_f32 0.112999998 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRpIEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 19.2907009	
k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 19.2907009	
k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 19.2907009	
k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0 k_MtrVoltDaxIntegHiLim_Volt_f32 19.2907009	
- 0 - 1	
k MtrVoltDaxIntegl of im Volt f32	
k_MtrVoltQaxFiltFFEnable_Cnt_lgc 1	
k_MtrVoltQaxIntegHiLim_Volt_f32 11.4712	
k_MtrVoltQaxIntegLoLim_Volt_f32 -8.68999958	
k_MtrVoltVecuFiltEnable_Cnt_lgc 1	
k_VoltSatDaxPolyCoeff_Uls_f32 19.9039993	
k_VoltSatQaxPolyCoeff_Uls_f32 -19.9039993	
k_deadtime\Scale_Uls_f32	
t_CommOffsetTbIX_UIs_u3p13[0] 918	
t_CommOffsetTbIX_UIs_u3p13[1] 1679	
t_CommOffsetTbIY_Cnt_u16[0] 71	
t_CommOffsetTblY_Cnt_u16[1] 676	
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr 0	
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 1	
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val 1	
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 0	
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -207.917999	
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 1357	
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.7319984	
target_MtrCntrl_Read_SysState_Cnt_Enum_Val 1	
Name Actual Value Expected Value	
MtrCntrl_Write_CommOffset_Cnt_u16(val) 1357 1357	Resu
MtrCntrl_Write_ModIdx_UIs_u16p16(val) 0 ± 1	Resu
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) -125.861 -125.861 -125.861	Resu
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -4.55999994 ± 4.88E-04	Resu
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -8.61900043 -8.61900043 -8.61900043 -8.61900043	Resu
MtrCntrl Write PhoneAdvanceFinal Pay u0n16(val)	Resu
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 39481 39481 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32 0 0	Resu

0.152199998

PICurrCntrl_DualEcuFailSclFac_Uls_M_f32

0.152199998 ± 0.0625



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	•	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	•	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•	
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~	
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•	

1	Innue Malus
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
/trCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
/trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
/trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995
/trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.51800001
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.48300004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-99.1699982
ItrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-632.38501
ltrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
ltrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
ltrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
ltrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
ltrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.074000001
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.166999996
htrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-1003.84003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	15.3310003
ItrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
ItrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
ItrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
ItrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
htrCtrl_Vecu_Volt_M_f32[0]	17.7010002
ltrCtrl_Vecu_Volt_M_f32[1]	20.0610008
htrCurrDaxPrevIntg_Volt_M_f32	-1.39499998
ltrCurrDaxRef_Amp_M_f32[0]	31.5869999
/trCurrDaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxCog_Amp_M_f32	-144.667007
ItrCurrQaxPrevIntg_Volt_M_f32	6.73180008
ItrCurrQaxRef_Amp_M_f32[0]	171.485992
ItrCurrQaxRef_Amp_M_f32[1]	163.787003
ItrCurrQaxRpl_Amp_M_f32	0
ItrPosComputationDelay_Rad_M_f32[0]	-2.15700006
ItrPosComputationDelay Rad M f32[1]	4.67700005
PlCurrCntrl CurrSensFailSclFac Uls M f32	0.94400006
PICurrCntrl DualEcuFailSclFac Uls M f32	0.131999999
PICurrCntrl InverterFailSclFac UIs M f32	0.294999987

PICurrCntrl Per1

2016-09-15, 18:23:31+0530



Input Value PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32 0.167500004 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.833000004 PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32 -340.130005 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -627.179993 PICurrCntrl MtrVecuFilt M str.TermN Uls f32 71.040802 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.209299996 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 -340.130005 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32$ -627.179993 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 71.040802 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32$ 0.209299996 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 5388.75 k_DualEcuSignalSclFacSlew_UlspS_f32 170.800003 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 3076.13989 $k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc$ 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0.145999998 k_MtrCtrlVirualResDax_Ohm_f32 k_MtrCtrlVirualResQax_Ohm_f32 0.0710000023 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ k_MtrVoltDaxIntegHiLim_Volt_f32 20.0139999 k_MtrVoltDaxIntegLoLim_Volt_f32 -4.57000017 k_MtrVoltQaxFiltFFEnable_Cnt_lgc $k_MtrVoltQaxIntegHiLim_Volt_f32$ 13 2535 k_MtrVoltQaxIntegLoLim_Volt_f32 -4.57000017 k MtrVoltVecuFiltEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 -4.86499977 k VoltSatQaxPolyCoeff Uls f32 -7.41699982 k_deadtimeVScale_Uls_f32 0.970000029 t CommOffsetTblX Uls u3p13[0] 918 $t_CommOffsetTblX_Uls_u3p13[1]$ 1679 t CommOffsetTblY Cnt u16[0] 71 t_CommOffsetTblY_Cnt_u16[1] 676 target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr 1 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 0 $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ 0 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 50.0610008 $target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr$ 826 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 136.341003 $target_MtrCntrl_Read_SysState_Cnt_Enum_Val$ **Actual Value Expected Value** Name Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 676 676 63569 ± 1 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 63569 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 220 220 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -19.3592358 -19.3592358 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 1.96960425 1.96960425 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 33456 33456 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32 20 0139999 20 0139999

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	-
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

0.110649996

0.110649996 ± 0.0625

PICurrCntrl_DualEcuFailSclFac_Uls_M_f32





Test Step 2.137 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val) MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	target_MtrCntrl_Read_SysState_Cnt_Enum_Val -213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.675999999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.14600003
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	63.882
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-0.995000005
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1] MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.115999997 0.097000029
MtrCtrl_MtrImpedQax_Onm_M_f32[0] MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000029
MtrCtrl MtrQaxIntegralGain Ohm M f32[0]	0.638999999
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.104000002
MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	-406.304993
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-469.421997
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002
MtrCtrl_Vecu_Volt_M_f32[0]	18.5559998
MtrCtrl_Vecu_Volt_M_f32[1]	20.9160004
MtrCurrDaxPrevIntg_Volt_M_f32	-27.6930008 -65.1900024
MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxCog_Amp_M_f32	5.72399998
MtrCurrQaxPrevIntg_Volt_M_f32	14.2131004
MtrCurrQaxRef_Amp_M_f32[0]	31.5869999
MtrCurrQaxRef_Amp_M_f32[1]	-186.395996
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	2.36999989
MtrPosComputationDelay_Rad_M_f32[1]	2.67000008
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.25999999
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.133000001
PICurrCntrl_InverterFailSclFac_Uls_M_f32 PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.222000003 0.763899982
PICurrCntrl MtrCurrQaxSatFluxRatio_Uis_M_132	0.0469999984
PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32	22.239998
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	-10.21
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	44.2909012
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.210999995
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	22.2399998
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-10.21
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	44.2909012
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.210999995
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7174.7002
k_DualEcuSignalSclFacSlew_UlspS_f32	172
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	1940.90002
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.159999996
k MtrCtrlVirualResQax Ohm f32	0.109999999
k MtrCurrQaxRefModifDsb Cnt Igc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	14.0096998
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	19.7115993
k_MtrVoltQaxIntegLoLim_Volt_f32 k_MtrVoltVecuFiltEnable_Cnt_lgc	-25.6000004 1

PICurrCntrl_Per1



	I		
Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	14.1599998		
k_VoltSatQaxPolyCoeff_Uls_f32	-17.3999996		
k_deadtimeVScale_Uls_f32	0.967999995		
t_CommOffsetTblX_Uls_u3p13[0]	1532		
t_CommOffsetTblX_Uls_u3p13[1]	2851		
t_CommOffsetTblY_Cnt_u16[0]	912		
t_CommOffsetTblY_Cnt_u16[1]	1211		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	827		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	827	827	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	25.862999	25.862999 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-2.4196074	-2.4196074 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.19178963	-4.19178963 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	62948	62948 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.154500008	0.154500008 ± 0.0625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	-
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	-

Test Step 2.138 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.33800006
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.481999993
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	406.850006
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	57.3230019
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125

2016-09-15, 18:23:31+0530





Name	Input Value		
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.810000002		
MtrCtrl MtrQaxIntegralGain Ohm M f32[1]	0.185000002		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	222.291		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	1002.88		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003		
MtrCtrl_Vecu_Volt_M_f32[0]	21.3729992		
MtrCtrl_Vecu_Volt_M_f32[1]	23.7329998		
MtrCurrDaxPrevIntg_Volt_M_f32	20.066		
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007		
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001		
MtrCurrQaxCog Amp M f32	59.3040009		
MtrCurrQaxPrevIntg_Volt_M_f32	17.0116005		
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006		
MtrCurrQaxRef Amp M f32[1]	75.7020035		
	0		
MtrCurrQaxRpl_Amp_M_f32 MtrPosComputationDelay Rad M f32[0]	2.68400002		
MtrPosComputationDelay_Rad_M_f32[1]	5.81400013		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.185000002		
PICurrCntrl_DualEcuFailSclFac_UIs_M_f32	0.134000003		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.737999976		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.89200002		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	570.700012		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	79.9187012		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.882499993		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	79.9187012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.882499993		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3789.18994		
k_DualEcuSignalSclFacSlew_UlspS_f32	173.199997		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3865.98999		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.159999996		
k_MtrCtrlVirualResQax_Ohm_f32	0.0689999983		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	6.20769978		
k_MtrVoltDaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	9.04409981		
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	7.02699995		
k_VoltSatQaxPolyCoeff_Uls_f32	-15.4709997		
k_deadtimeVScale_Uls_f32	0.975000024		
t_CommOffsetTblX_Uls_u3p13[0]	0		
t_CommOffsetTblX_Uls_u3p13[1]	0		
t_CommOffsetTblY_Cnt_u16[0]	49		
t_CommOffsetTblY_Cnt_u16[1]	735		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-198.285995		
target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	4450		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	1.62199998		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
	735	735	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)			
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	57194	57194 ± 1	Ž
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-193.251007	-193.251007 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-0.736000001	-0.736000001 ± 4.88E-04	Ž
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	18.6380005 27583	18.6380005 ± 4.88E-04	
MtrCurrDayPrevinto_Volt_M_f32	0	27583 ± 1.52588E-05	

MtrCurrDaxPrevIntg_Volt_M_f32





Name	Actual Value	Expected Value	Result
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.112350002	0.112350002 ± 0.0625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

lame	Input Value	
rastDataAccessBufIndex_Cnt_M_u16	0	
ItrCntrl Read DualEcuMotCtrlMtgnEna Cnt Igc(ptr)	target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr	
trCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr	
trCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val	
trCntrl Read MotCurrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr	
trCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val	
trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
trCtrl MtrCurrDaxMaxVal Amp M f32[0]	67.4899979	
trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001	
trCtrl MtrDampTermDax Ohm M f32[0]	0.010999999	
trCtrl MtrDampTermDax Ohm M f32[1]	0.039000008	
trCtrl MtrDampTermQax Ohm M f32[0]	0.098999995	
trCtrl MtrDampTermQax Ohm M f32[1]	0.0170000009	
trCtrl MtrDaxIntegralGain Ohm M f32[0]	1.10300004	
trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.991999984	
trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	841.302979	
trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-775.062012	
trCtrl MtrImpedDax Ohm M f32[0]	0.0419999994	
trCtrl MtrImpedDax Ohm M f32[1]	0.0280000009	
trCtrl MtrImpedQax Ohm M f32[0]	0.0419999994	
trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009	
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.25199997	
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.245	
trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	582.953003	
trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	578.156982	
trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002	
trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008	
trCtrl MtrVoltQaxFF Volt M f32[0]	-7.66699982	
trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008	
trCtrl_Vecu_Volt_M_f32[0]	5.12099981	
trCtrl_Vecu_Volt_M_f32[1]	7.48099995	
trCurrDaxPrevIntg_Volt_M_f32	-7.71299982	
trCurrDaxRef_Amp_M_f32[0]	31.5869999	
trCurrDaxRef_Amp_M_f32[1]	-186.395996	
trCurrQaxCog_Amp_M_f32	-144.667007	
trCurrQaxPrevIntg_Volt_M_f32	15.2893	
trCurrQaxRef_Amp_M_f32[0]	171.485992	
trCurrQaxRef_Amp_M_f32[1]	163.787003	
trCurrQaxRpl_Amp_M_f32	0	
trPosComputationDelay_Rad_M_f32[0]	2.86899996	
trPosComputationDelay_Rad_M_f32[1]	0.83999974	
CurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.723999977	
ICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.135000005	
CurrCntrl InverterFailSclFac Uls M f32	0.80099999	

PICurrCntrl_Per1

2016-09-15, 18:23:31+0530



Name	Input Value		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.752200007		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-38.7999992		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	13.1514997		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.821500003		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-38.7999992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	13.1514997		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.821500003		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3827.27002		
k_DualEcuSignalSclFacSlew_UlspS_f32	174.399994		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2156.63989		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.0219999999		
k_MtrCtrlVirualResQax_Ohm_f32	0.0960000008		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	19.5093002		
k_MtrVoltDaxIntegLoLim_Volt_f32	-1.39999998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	29.0611992		
k_MtrVoltQaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	20.5559998		
k_VoltSatQaxPolyCoeff_Uls_f32	-5.53700018		
k_deadtimeVScale_Uls_f32	0.957000017		
t_CommOffsetTbIX_Uls_u3p13[0]	8192		
t_CommOffsetTbIX_Uls_u3p13[1]	8192		
t_CommOffsetTblY_Cnt_u16[0]	63		
t_CommOffsetTblY_Cnt_u16[1]	327		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1730		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1730	1730	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-1.77754962	-1.77754986 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.44258213	4.44258308 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	25955	25955 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	•
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.156800002	0.156800002 ± 0.0625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~



Fest Step 2.140 (Repeat Count = 1)	Input Value
astDataAccessBufIndex_Cnt_M_u16	Input value
htrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
/trCntrl Read IvtrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr
// htrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
/trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
htrCtrl_MtrDampTermDax_Ohm_M_f32[0] htrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998 0.0769999996
/trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
/trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.62699997
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.414000005
htrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-64.4329987
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	243.455002
/trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997
ftrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997
/ltrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.94700003
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.40900009
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	648.445007
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-828.104004
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964 -28.2420006
NtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] NtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002
/trCtrl_Vecu_Volt_M_f32[0]	18.9510002
/trCtrl_Vecu_Volt_M_f32[1]	21.3110008
/trCurrDaxPrevIntg_Volt_M_f32	-7.6500001
/trCurrDaxRef_Amp_M_f32[0]	-65.1900024
htrCurrDaxRef_Amp_M_f32[1]	-216.972
/trCurrQaxCog_Amp_M_f32	5.72399998
MtrCurrQaxPrevIntg_Volt_M_f32	14.2027998
MtrCurrQaxRef_Amp_M_f32[0]	31.5869999
/ltrCurrQaxRef_Amp_M_f32[1]	-186.395996
/trCurrQaxRpl_Amp_M_f32	0
/trPosComputationDelay_Rad_M_f32[0]	5.21500015
htrPosComputationDelay_Rad_M_f32[1]	0.550000012
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.848999977
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.136000007
PICurrCntrl_InverterFailSclFac_UIs_M_f32	0.296000004
PICurrCntrl_MtrCurrDaxSatFluxRatio_UIs_M_f32	0.194800004
PICurrCntrl_MtrCurrQaxSatFluxRatio_UIs_M_f32	0.046999984
PlCurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 PlCurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	267.119995 -194.190002
PICurrCntrl MtrVecuFilt M str.TermN Uls f32	60.8638
CurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.472600013
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	267.119995
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	60.8638
CurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.472600013
CLOAFdbackSignalSclFacSlew_UlspS_f32	208.033005
DualEcuSignalSclFacSlew_UlspS_f32	175.600006
_ILOAFdbackSignalSclFacSlew_UlspS_f32	5517.5
_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
_MtrCtrlVirualResDax_Ohm_f32	0.180000007
_MtrCtrlVirualResQax_Ohm_f32	0.0839999989
_MtrCurrQaxRefModifDsb_Cnt_lgc	1
_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
_MtrVoltDaxIntegHiLim_Volt_f32	12.5352001
_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008
_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
_MtrVoltQaxIntegHiLim_Volt_f32	5.54530001





Name	Input Value		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	24.8269997		
k_VoltSatQaxPolyCoeff_Uls_f32	-17.3369999		
k_deadtimeVScale_Uls_f32	0.958000004		
t_CommOffsetTblX_Uls_u3p13[0]	3808		
t_CommOffsetTblX_Uls_u3p13[1]	7298		
t_CommOffsetTblY_Cnt_u16[0]	889		
t_CommOffsetTblY_Cnt_u16[1]	1543		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3235		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1273	1273	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	46868	46868 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-192.119995	-192.119995 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	8.55099964	8.55099964 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	12.6160002	12.6160002 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	11950	11950 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	-30.2000008	-30.2000008	✓
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.114050008	0.114050008 ± 0.0625	✓

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	✓
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	-
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	-
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	_

Test Step 2.141 (Repeat Count = 1)	ullet
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.079999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.028
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.828000009
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	670.815002
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-276.028992
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994

PICurrCntrl_Per1



PICUITCHIII_Peri			TOP CITAL
Name	Input Value		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.136000007		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.861999989		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	360.989014		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	26.3950005		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003		
MtrCtrl_Vecu_Volt_M_f32[0]	18.5559998		
MtrCurrDovProvIntg Volt M #32	20.9160004		
MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrDaxRef_Amp_M_f32[0]	-146.723007		
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001		
MtrCurrQaxCog_Amp_M_f32	59.3040009		
MtrCurrQaxPrevIntg_Volt_M_f32	-20.8540993		
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006		
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	-1.46899998		
MtrPosComputationDelay_Rad_M_f32[1]	-5.42999983		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.231999993		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.136999995		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.448000014		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.0320999995		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	267.119995		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	9.46790028		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.962000012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	267.119995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_UIs_f32	-194.190002		
PICurrCotrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	9.46790028 0.962000012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6145.56982		
k_DualEcuSignalSclFacSlew_UlspS_f32	176.800003		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4019.20996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.131999999		
k_MtrCtrlVirualResQax_Ohm_f32	0.112999998		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	25.1942005		
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	29.8994007		
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	6.06099987		
k_VoltSatQaxPolyCoeff_Uls_f32	-12.2449999		
k_deadtimeVScale_Uls_f32	0.963999987		
t_CommOffsetTbIX_UIs_u3p13[0] t CommOffsetTbIX UIs_u3p13[1]	5022 7003		
t_CommOffsetTblY_Cnt_u16[0]	0		
t CommOffsetTblY Cnt u16[1]	0		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4296		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4296	4296	→
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-193.251007	-193.251007 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	4.07818031	4.07818031 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-2.56921101	-2.56921077 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	6926	6926 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	•
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.159099996	0.159099996 ± 0.0625	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Name	Input Value
FastDataAccessBufIndex Cnt M u16	0
VtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr
/trCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr
/trCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModidxSrlComSvcDft Cnt lgc Val
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val
AtrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
/trCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
/trCntrl Read SysState Cnt Enum(Val)	target MtrCntrl Read SysState Cnt Enum Val
MtrCtrl MtrCurrDaxMaxVal Amp M f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
/trCtrl MtrDampTermDax Ohm M f32[0]	0.010999999
VtrCtrl MtrDampTermDax Ohm M f32[1]	0.0390000008
VtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995
VtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.75899994
VtrCtrl MtrDaxIntegralGain Ohm M f32[1]	1.45799994
MtrCtrl MtrDaxPropotionalGain Ohm M f32[0]	-948.984009
MtrCtrl MtrDaxPropotionalGain Ohm M f32[1]	-975.934021
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.261000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.754000008
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-227.466003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-453.338013
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	18.9990005
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.395996
/trCurrQaxCog_Amp_M_f32	-144.667007
MtrCurrQaxPrevIntg_Volt_M_f32	29.6310005
/trCurrQaxRef_Amp_M_f32[0]	171.485992
/trCurrQaxRef_Amp_M_f32[1]	163.787003
MtrCurrQaxRpl_Amp_M_f32	0
/trPosComputationDelay_Rad_M_f32[0]	-0.344999999
MtrPosComputationDelay_Rad_M_f32[1]	0.467999995
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.777999997
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.137999997

PICurrCntrl_Per1

2016-09-15, 18:23:31+0530



Name	Input Value		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0450000018		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.0951000005		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004		
PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32	404.899994		
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	20.7000008		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	10.3985996		
PICurrCntrl MtrVecuFilt M str.TermD Uls f32	0.630500019		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	404.899994		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	20.7000008		
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	10.3985996		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32	0.630500019		
k CLOAFdbackSignalSclFacSlew UlspS f32	6667.54004		
k DualEcuSignalSclFacSlew UlspS f32	178		
k ILOAFdbackSignalSclFacSlew UlspS f32	7823.27002		
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lqc	0		
k MtrCtrlFeedbackControlDisable Cnt lgc	0		
k MtrCtrlVirualResDax Ohm f32	0.0820000023		
k_MtrCtrlVirualResQax_Ohm_f32	0.0120000001		
k MtrCurrQaxRefModifDsb Cnt lgc	0		
k MtrCurrQaxRefModifRplEn Cnt lqc	0		
k MtrVoltDaxIntegHiLim Volt f32	21.4818001		
k MtrVoltDaxIntegLoLim Volt f32	-22.4099998		
k MtrVoltQaxFiltFFEnable Cnt lgc	1		
k MtrVoltQaxIntegHiLim Volt f32	25.8833008		
k MtrVoltQaxIntegLoLim Volt f32	-22.4099998		
k MtrVoltVecuFiltEnable Cnt lgc	1		
k VoltSatDaxPolyCoeff Uls f32	-7.19700003		
k VoltSatQaxPolyCoeff Uls f32	-19.9090004		
k deadtimeVScale Uls f32	0.963999987		
t_CommOffsetTbIX_Uls_u3p13[0]	4611		
t CommOffsetTbIX Uls u3p13[1]	5579		
t_CommOffsetTblY_Cnt_u16[0]	2000		
t_CommOffsetTbIY_Cnt_u16[1]	2000		
target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target MtrCntrl Read ModIdxSrlComSvcDft Cnt Igc Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003		
target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	674		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Resul
	674	Expected Value	Resul
MtrCntrl_Write_CommOffset_Cnt_u16(val)		674	
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	0	0 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-14.6940002	-14.6940002 ± 4.88E-04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-7.66699982	-7.66699982 ± 4.88E-04	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	40537	40537 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	•
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.11575	0.11575 ± 0.0625	•

Test Step Call Trace				T Y
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	-
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~



est Step 2.143 (Repeat Count = 1)	Input Value
astDataAccessBufIndex_Cnt_M_u16	1
ItrCntrl Read DualEcuMotCtrlMtgnEna Cnt Igc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
trCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr
ItrCntrl Read ModIdxSrlComSvcDft Cnt Igc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
ItrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
ItrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
ltrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
ltrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
trCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
ltrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995
ltrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
ItrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.36399996
trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.949
trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-232.371994
trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-761.935974
trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
ltrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
ltrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.65799999
ltrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.30399999
ltrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-174.839996
ltrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	903.403015
ltrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
ItrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
ItrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
ItrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
ItrCtrl_Vecu_Volt_M_f32[0]	18.9510002
ItrCtrl_Vecu_Volt_M_f32[1]	21.3110008
ItrCurrDaxPrevIntg_Volt_M_f32	-3.78200006
ItrCurrDaxRef_Amp_M_f32[0]	-146.723007
ltrCurrDaxRef_Amp_M_f32[1] ltrCurrQaxCog_Amp_M_f32	-121.943001 -144.667007
	0.371499985
ltrCurrQaxPrevIntg_Volt_M_f32 ltrCurrQaxRef_Amp_M_f32[0]	-133.947006
ItrCurrQaxRef_Amp_M_f32[1]	75.7020035
ItrCurrQaxRpl Amp M f32	0
trPosComputationDelay_Rad_M_f32[0]	-2.48799992
ItrPosComputationDelay Rad M f32[1]	0.00100000005
ICurrCntrl CurrSensFailSclFac Uls M f32	0.851999998
ICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.13899999
CurrCntrl InverterFailScIFac Uls M f32	0.398999989
CurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.208499998
CurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.83300004
CurrCntrl MtrVecuFilt M str.PrevInput UIs f32	865.320007
CurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-340.130005
CurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	69.4054031
CurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0452999994
CurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	865.320007
CurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32	-340.130005
CurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	69.4054031
CurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.0452999994
CLOAFdbackSignalSclFacSlew_UlspS_f32	7980.1499
DualEcuSignalSclFacSlew_UlspS_f32	179.199997
ILOAFdbackSignalScIFacSlew_UlspS_f32	6489.7002
MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
MtrCtrlFeedbackControlDisable Cnt lgc	0
MtrCtrlVirualResDax Ohm f32	0.0099999978
MtrCtrlVirualResQax Ohm f32	0.163000003
MtrCurrQaxRefModifDsb Cnt lgc	1
_MtrCurrQaxRefModifRpIEn_Cnt_lgc	0
MtrVoltDaxIntegHiLim_Volt_f32	16.9069004
MtrVoltDaxIntegLoLim Volt f32	-8.6899958
MtrVoltQaxFiltFFEnable_Cnt_lgc	0
_MtrVoltQaxIntegHiLim_Volt_f32	11.2285004





Name	Input Value		
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-18.3610001		
k_VoltSatQaxPolyCoeff_Uls_f32	22.7819996		
k_deadtimeVScale_Uls_f32	0.963999987		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	49		
t_CommOffsetTblY_Cnt_u16[1]	735		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4608		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	735	735	-
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63176	63176 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	-
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-20.4382973	-20.4382992 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	2.07938766	2.0793879 ± 4.88E-04	-
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	50220	50220 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	14.9324055	14.9324188	•
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.161400005	0.161400005 ± 0.0625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	•
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	-
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	_

Test Step 2.144 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.344999999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.98000002
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-475.019012
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-121.873001
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125

2016-09-15, 18:23:31+0530





Name	Input Value		
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.633000016		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.5		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-892.642029		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-451.738007		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003		
MtrCtrl_Vecu_Volt_M_f32[0]	18.5559998		
MtrCtrl_Vecu_Volt_M_f32[1]	20.9160004		
MtrCurrDaxPrevIntg_Volt_M_f32	-9.05200005		
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007		
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001		
MtrCurrQaxCog_Amp_M_f32	59.3040009		
MtrCurrQaxPrevIntg_Volt_M_f32	14.3669996		
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006		
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	-5.50199986		
MtrPosComputationDelay_Rad_M_f32[1]	4.21099997		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.231999993		
PICurrCntrl DualEcuFailSclFac Uls M f32	0.140000001		
PICurrCntrl InverterFailSclFac Uls M f32	0.448000014		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.254799992		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.725000024		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	267.119995		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	22.2399998		
	87.5784988		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32			
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.966000021		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	267.119995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	22.2399998		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	87.5784988		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.966000021		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6145.56982		
k_DualEcuSignalSclFacSlew_UlspS_f32	180.399994		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4019.20996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0		
k_MtrCtrlVirualResQax_Ohm_f32	0.094999988		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	30.8994007		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k MtrVoltQaxIntegHiLim Volt f32	12.4134998		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017		
k MtrVoltVecuFiltEnable Cnt lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	2.44199991		
k_VoltSatQaxPolyCoeff_Uls_f32	19.2689991		
k deadtimeVScale Uls f32	0.963999987		
t CommOffsetTbIX UIs u3p13[0]	5022		
t_CommOffsetTblX_Uls_u3p13[1]	7003		
	671		
t_CommOffsetTblY_Cnt_u16[0]			
t_CommOffsetTblY_Cnt_u16[1]	876		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2125		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Resul
MtrCntrl_Write_CommOffset_Cnt_u16(val)	2125	2125	•
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-193.251007	-193.251007 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-13.601759	-13.601759 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	26.6091251	26.6091251 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	3219	3219 ± 1.52588E-05	
MtrCurrDaxPrevInto_Volt_M_f32	0	0	

MtrCurrDaxPrevIntg_Volt_M_f32





Name	Actual Value	Expected Value	Result
PICurrCntrl DualEcuFailSclFac Uls M f32	0.117449999	0.117449999 ± 0.0625	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	•	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~	
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~	

Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
VtrCntrl Read DualEcuMotCtrlMtgnEna Cnt Igc(ptr)	target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr	
/trCntrl Read IvtrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr	
/trCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt Igc Val	
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val	
MtrCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	
//trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val	
MtrCntrl Read SysState Cnt Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl MtrCurrDaxMaxVal Amp M f32[0]	67.4899979	
	119.721001	
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	0.0109999999	
/trCtrl_MtrDampTermDax_Ohm_M_f32[0]		
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.039000008	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995 0.0170000009	
/trCtrl_MtrDampTermQax_Ohm_M_f32[1]		
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.40900009	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.16600001	
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	967.463013	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-285.221985	
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994	
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009	
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994	
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.305999994	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.232999995	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-121.924004	
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	483.27301	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002	
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008	
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982	
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008	
/trCtrl_Vecu_Volt_M_f32[0]	5.12099981	
/trCtrl_Vecu_Volt_M_f32[1]	7.48099995	
/trCurrDaxPrevIntg_Volt_M_f32	18.9990005	
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999	
/ltrCurrDaxRef_Amp_M_f32[1]	-186.395996	
/trCurrQaxCog_Amp_M_f32	-144.667007	
/ltrCurrQaxPrevIntg_Volt_M_f32	1.06570005	
/trCurrQaxRef_Amp_M_f32[0]	171.485992	
/ltrCurrQaxRef_Amp_M_f32[1]	163.787003	
/trCurrQaxRpI_Amp_M_f32	0	
MtrPosComputationDelay_Rad_M_f32[0]	-3.16700006	
MtrPosComputationDelay_Rad_M_f32[1]	3.09599996	
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.777999997	

PICurrCntrl_Per1



Name	Input Value		
PICurrCntrl DualEcuFailSclFac Uls M f32	0.141000003		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0450000018		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.837800026		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.833000004		
PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32	-657.099976		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	267.119995		
PICurrCntrl MtrVecuFilt M str.TermN Uls f32	75.4597015		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.830900013		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-657.099976		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32	267.119995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	75.4597015		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.830900013		
k CLOAFdbackSignalSclFacSlew UlspS f32	6667.54004		
k DualEcuSignalSclFacSlew UlspS f32	181.600006		
k ILOAFdbackSignalSclFacSlew UlspS f32	7823.27002		
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt Igc	0		
k MtrCtrlFeedbackControlDisable Cnt lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.200000003		
k MtrCtrlVirualResQax Ohm f32	0.0109999999		
k MtrCurrQaxRefModifDsb Cnt lgc	0		
k MtrCurrQaxRefModifRpIEn Cnt lgc	0		
k MtrVoltDaxIntegHiLim Volt f32	26.6909008		
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004		
k MtrVoltQaxFiltFFEnable Cnt lgc	1		
k MtrVoltQaxIntegHiLim Volt f32	20.4568005		
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k VoltSatDaxPolyCoeff Uls f32	20.2509995		
k_VoltSatQaxPolyCoeff_Uls_f32	18.1280003		
k deadtimeVScale Uls f32	0.963999987		
t_CommOffsetTbIX_UIs_u3p13[0]	4611		
t CommOffsetTblX Uls u3p13[1]	5579		
t_CommOffsetTbIY_Cnt_u16[0]	912		
t CommOffsetTblY Cnt u16[1]	1211		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1468		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl Write CommOffset Cnt u16(val)	1468	1468	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-14.6940002	-14.6940002 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-7.66699982	-7.66699982 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	11103	11103 ± 1.52588E-05	•
MtrCurrDaxPrevIntg Volt M f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.163699999	0.163699999 ± 0.0625	~



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	-
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	1	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	
//dtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
/trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979	
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001	
/trCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999	
/trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008	
/trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.93900001	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.36699998	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	826.950989	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-163.621994	
/trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994	
htrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009	
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994	
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009	
MtrCtrl MtrQaxIntegralGain Ohm M f32[0]	1.26100004	
MtrCtrl MtrQaxIntegralGain Ohm M f32[1]	1.60300004	
MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	-401.145996	
MtrCtrl MtrQaxPropotionalGain Ohm M f32[1]	-278.5	
MtrCtrl MtrVoltDaxFF Volt M f32[0]	-14.6940002	
/trCtrl MtrVoltDaxFF Volt M f32[1]	-25.6930008	
MtrCtrl MtrVoltQaxFF Volt M f32[0]	-7.66699982	
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008	
MtrCtrl Vecu Volt M f32[0]	18.9510002	
/trCtrl_Vecu_Volt_M_f32[1]	21.3110008	
MtrCurrDaxPrevIntg_Volt_M_f32	-3.78200006	
/trCurrDaxRef_Amp_M_f32[0]	-146.723007	
/trCurrDaxRef_Amp_M_f32[1]	-121.943001	
/trCurrQaxCog_Amp_M_f32	-144.667007	
/trCurrQaxPrevIntg Volt M f32	13.5303001	
/trCurrQaxRef_Amp_M_f32[0]	-133.947006	
/trCurrQaxRef_Amp_M_f32[1]	75.7020035	
/trCurrQaxRpl_Amp_M_f32	0	
/trPosComputationDelay_Rad_M_f32[0]	3.42499995	
/trPosComputationDelay_Rad_M_f32[1]	-0.836000025	
PlCurrCntrl CurrSensFailSclFac Uls M f32	0.851999998	
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.142000005	
PICurrCntrl InverterFailSclFac Uls M f32	0.39899989	

PICurrCntrl_Per1

2016-09-15, 18:23:31+0530



Name Input Value		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.633599997		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.833000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 -43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -657.099976		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 17.0797005		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.727199972		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 -43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 -657.099976		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32 17.0797005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32 0.727199972		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32 7980.1499		
k_DualEcuSignalSclFacSlew_UlspS_f32 182.800003		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32 6489.7002		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0		
k_MtrCtrlVirualResDax_Ohm_f32 0.182999998		
k_MtrCtrlVirualResQax_Ohm_f32 0.169		
k_MtrCurrQaxRefModifDsb_Cnt_lgc 1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0		
k_MtrVoltDaxIntegHiLim_Volt_f32 16.2366009		
k_MtrVoltDaxIntegLoLim_Volt_f32 -10.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc 0		
k_MtrVoltQaxIntegHiLim_Volt_f32 22.8831997		
k_MtrVoltQaxIntegLoLim_Volt_f32 -10.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc 0		
k_VoltSatDaxPolyCoeff_Uls_f32 3.96000004		
k_VoltSatQaxPolyCoeff_Uls_f32 7.87699986		
k_deadtimeVScale_Uls_f32 0.963999987		
t_CommOffsetTblX_Uls_u3p13[0] 459		
t_CommOffsetTblX_Uls_u3p13[1] 5775		
t_CommOffsetTblY_Cnt_u16[0] 49		
t_CommOffsetTblY_Cnt_u16[1] 735		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr 1		
target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr 0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val 0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr 1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 2363		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val 2		
Name Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val) 735	735	~
MtrCntrl_Write_Modldx_Uls_u16p16(val) 63176	63176 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 220	220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -20.4382973	-20.4382992 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 2.07938766	2.0793879 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 41490	41490 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32 -10.5	-10.5	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	-
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	-
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	-





Test Step 2.147 (Repeat Count = 1)	· ·
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val) MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	target_MtrCntrl_Read_SysState_Cnt_Enum_Val 67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.010999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.039000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.527999997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.268999994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-311.075012
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-305.570007
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.041999994
MtrCtrl_MtrlmpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0] MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1] MtrCtrl MtrQaxIntegralGain Ohm M f32[0]	0.0280000009 1.12899995
MtrCtrl MtrQaxIntegralGain Ohm M f32[1]	0.556999981
MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	354.154999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	556.525024
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.66699982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	24.8479996
MtrCtrl_Vecu_Volt_M_f32[1]	27.2080002
MtrCurrDayPet Amp. M. (2010)	-7.71299982
MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1]	31.5869999 -186.395996
MtrCurrQaxCog_Amp_M_f32	-144.667007
MtrCurrQaxPrevIntg_Volt_M_f32	30.4999008
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1]	163.787003
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	5.14300013
MtrPosComputationDelay_Rad_M_f32[1]	0.453999996
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.723999977
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.143000007
PICurrCntrl_InverterFailSclFac_Uls_M_f32 PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.111000001 0.233400002
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.83300004
PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32	1118
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	52.7086983
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.584299982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	52.7086983
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32	0.584299982
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3827.27002
k_DualEcuSignalSclFacSlew_UlspS_f32 k_ILOAFdbackSignalSclFacSlew_UlspS_f32	184 2156.63989
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	2130.03969
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.131999999
k_MtrCtrlVirualResQax_Ohm_f32	0
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	15.3010998
k_MtrVoltDaxIntegLoLim_Volt_f32	-11.6000004
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	13.0423002
k_MtrVoltQaxIntegLoLim_Volt_f32 k MtrVoltVecuFiltEnable Cnt lgc	-11.6000004 1
K_IVILI VOILVECUI IIILIIADIE_CIIL_IYC	

PICurrCntrl_Per1



Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	7.04799986		
k_VoltSatQaxPolyCoeff_Uls_f32	6.82399988		
k_deadtimeVScale_Uls_f32	0.968999982		
t_CommOffsetTblX_Uls_u3p13[0]	5022		
t_CommOffsetTbIX_Uls_u3p13[1]	7003		
t_CommOffsetTblY_Cnt_u16[0]	63		
t_CommOffsetTblY_Cnt_u16[1]	327		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-118.848		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	672		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-118.848		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	672	672	~
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-2.19647002	-2.19647002 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.31851149	4.31851149 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	48736	48736 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.166000009	0.166000009 ± 0.0625	~

Test Step Call Trace	est Step Call Trace				
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~	
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~	

Test Step 2.148 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.75999999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.0970000029
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	979.52301
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	987.510986
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997





Name	Input Value		
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997		
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0970000029		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.10699999		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.653999984		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-420.446991		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	525.913025		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302		
MtrCtrl MtrVoltDaxFF Volt M f32[1]	8.55099964		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002		
MtrCtrl_Vecu_Volt_M_f32[0]	29.0240002		
MtrCtrl_Vecu_Volt_M_f32[1]	30.3600006		
MtrCurrDaxPrevIntg_Volt_M_f32	-7.6500001		
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024		
MtrCurrDaxRef_Amp_M_f32[1]	-216.972		
MtrCurrQaxCog_Amp_M_f32	5.72399998		
MtrCurrQaxPrevIntg_Volt_M_f32	-16.7549		
MtrCurrQaxRef_Amp_M_f32[0]	31.5869999		
	-186.395996		
MtrCurrQaxRef_Amp_M_f32[1]			
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	-2.02900004		
MtrPosComputationDelay_Rad_M_f32[1]	-1.17700005		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.848999977		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.143999994		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.816999972		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.9375		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0469999984		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-627.179993		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	87.7649002		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.605599999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-627.179993		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	87.7649002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.605599999		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	208.033005		
k_DualEcuSignalSclFacSlew_UlspS_f32	185.199997		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5517.5		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.0179999992		
k MtrCtrlVirualResQax Ohm f32	0.20000003		
k MtrCurrQaxRefModifDsb Cnt Igc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	17.5195007		
k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008		
	-30.2000006		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc			
k_MtrVoltQaxIntegHiLim_Volt_f32	4.98169994		
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-20.4769993		
k_VoltSatQaxPolyCoeff_Uls_f32	4.35599995		
k_deadtimeVScale_Uls_f32	0.97799985		
t_CommOffsetTblX_Uls_u3p13[0]	4611		
t_CommOffsetTblX_Uls_u3p13[1]	5579		
t_CommOffsetTblY_Cnt_u16[0]	889		
t_CommOffsetTblY_Cnt_u16[1]	1543		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.3040009		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1798		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	50.0610008		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Resu
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1798	1798	Resu
WILLOUID WHILE COMMONSEL CHE UTO(VAI)	11/90	1798 0 ± 1	
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	0		
MtrCntrl_Write_ModIdx_UIs_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	0 25.862999	25.862999 ± 7.81E-03	•
MtrCntrl_Write_ModIdx_Uls_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0 25.862999 -28.2809677	25.862999 ± 7.81E-03 -28.2809677 ± 4.88E-04	•
MtrCntrl_Write_ModIdx_UIs_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	0 25.862999	25.862999 ± 7.81E-03	





Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl DualEcuFailSclFac Uls M f32	0.120849997	0.120849997 ± 0.0625	✓

Fest Step Call Trace			✓	
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

l de la companya de
Input Value
1
target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr
target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr
target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
target MtrCntrl Read MtrCurrDax Amp f32 Val
target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr
target MtrCntrl Read MtrCurrQax Amp f32 Val
target_MtrCntrl_Read_SysState_Cnt_Enum_Val
-212.632996
-205.085007
0.079999982
0.00899999961
0.0099999978
0.0799999982
1.99100006
1.046
-57.7280006
352.100006
0.112999998
0.125
0.0529999994
0.0939999968
0.634000003
1.12699997
-489.911011
-1007.60999
-0.736000001
-13.6160002
18.6380005
-23.1870003
17.7010002
20.0610008
-9.05200005
-146.723007
-121.943001
59.3040009
30.4363995
-133.947006
75.7020035
0
-5.35500002
1.125



i iouiioniii_i ci i	PICurrCntrl_	Per1
---------------------	--------------	------

Name	Input Value		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.231999993		
PICurrCntrl DualEcuFailSclFac Uls M f32	0.144999996		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.657000005		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.83099997		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-627.179993		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	58.6543999		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.82130003		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput Uls f32	-1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-627.179993		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	58.6543999		
PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.82130003		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6145.56982		
k_DualEcuSignalSclFacSlew_UlspS_f32	186.399994		
k ILOAFdbackSignalSclFacSlew UlspS f32	4019.20996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k MtrCtrlVirualResDax Ohm f32	0.032999998		
k_MtrCtrlVirualResQax_Ohm_f32	0.0590000004		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k MtrCurrQaxRefModifRplEn Cnt lgc	0		
, <u> </u>	27.2359009		
k_MtrVoltDaxIntegHiLim_Volt_f32			
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0 27.6382999		
k_MtrVoltQaxIntegHiLim_Volt_f32			
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962 0		
k_MtrVoltVecuFiltEnable_Cnt_lgc			
k_VoltSatDaxPolyCoeff_Uls_f32	2.04900002		
k_VoltSatQaxPolyCoeff_Uls_f32	-6.98999977		
k_deadtimeVScale_UIs_f32	0.996999979		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	771		
t_CommOffsetTblY_Cnt_u16[1]	1636		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	50.0610008		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2210		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1636	1636	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	65339	65339 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	16.3980026	16.3980026 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-10.1278782	-10.1278772 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-17.2469959	-17.246994 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	50040	50040 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	-9.64999962	-9.64999962	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.168300003	0.168300003 ± 0.0625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~



Section	est Step 2.150 (Repeat Count = 1)	Innut Value
Microst Passed Junis Country		
Incord Peach Metabolistics (Control Supply) Surget, Microfil Peach Medical Consideration Control Surget, Microfil Peach Medical Consideration Coulty Surget, Microfil Peach Medical Consideration Collection Surget, Microfil Peach Medical Consideration Collection Surget, Microfil Peach Medical Consideration Collection Surget, Microfil Surget, Microfil Peach Surget, Microfil Surget, Microfil Surget, Microfil Peach Surget, Microfil Surget, Mic		i i
		·
Incomp. Paccal And Control and Ingeline, Pour (1907)		
Incomp. Paris Jahr Compton J		
##CONT_PRESS_MINCTURE_CONTON_CONTENT_CONT_LISE_PTY Interpt. Min'CONT_PRESS_MINCTURE_ARM_CONTENT_CONT		
Michael, Read, SysState, Cott, Fanum, Val target, Michael, Read, SysState, Cott, Fanum, Val Michael, Michael Moder, March, M. (202) 200.588 Michael, Michael, Parisson, Chin, M. (202) 0.04.449907 Michael, Michael, Parisson, Chin, M. (202) 0.01.2999988 Michael, Michael, China, M. (202) 0.01.2999988 Michael, Michael, China, M. (202) 0.0290000000 Michael, Michael, China, M. (202) 1.74600003 Michael, Michael, China, M. (202) 1.73 Michael, Michael, China, M. (202) 1.13 Michael, Michael, Chin, M. (202) 1.13 Michael, Michael, Chin, M. (202) 0.055000009 Michael, Michael, Chin, M. (202) 0.07500000 Michael, Michael, Michael, Chin, M. (202) 0.07500000 Michael, Michael, Michael, M. (202) 0.07500000 Michael, Michael, Michael, M. (202) 0.07500000	,	
### ### ### ### ### ### ### ### ### ##	ItrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
mich Michael	ItrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
Brich J. McChamp Fermodox. Chim. M. 152(9) 0.18999994 Brich J. McChamp Fermodox. Chim. M. 152(9) 0.119999994 Brich J. McChamp Fermodox. Chim. M. 152(9) 0.2019999994 Brich J. McChamp Fermodox. Chim. M. 152(9) 1.784000003 Brich J. McChamp Fermodox. Chim. M. 152(9) 1.784000003 Brich J. McChamp Fermodox. Chim. M. 152(9) 1.784000000 Brich J. McChamp Fermodox. Chim. M. 152(9) 0.8850000009 Brich J. McChamp German J. 152(9) 0.12999999 Brich J. McChamp German J. 152(9) 0.12999999 Brich J. McChamp German J. 152(9) 0.12999999 Brich J. McChamp German J. 152(9) 0.12099999 Brich J. McChamp J. 152(9) 0.12099999 Brich J. McChamp J. 152(9) 0.1209999 Brich J. McChamp J. 152(9) 0.1209000 Brich J. McChamp J. 152(9) 0.1209000 Brich J. 152(9) 0.1209000 Brich J. 152(9) 0.12090000 Brich J. 152(9) 0.12090000	ltrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-200.556
Incide_MicrolampFermiss_Chm_M_1020	ltrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4449997
Incide Michaer Females Ohm M. 52(1)		
##CH_MRDvartegraGan_Chem_M_R2[0]		
Incid Michael Processing Commun. M. (201) 1.794-0003 1.794-000		
Incit MichaelropianGain Ohm M, 192(1) Incit MiringedDax, Ohm M, 192(1) Incit Miringe		
Incid MinisaPropolinaGain, Ohm M, 12(9) 1.13		
Inical Mariand Propotonia Gain., Ohm. M. 1921 Inicial Mariand Pack, Ohm. M. 1920 Inici		
Incl. Mimpechae, Ohm M, 13(1)		
Incid. Mirmeedax, Ohm. M, 13(1)	_ : _ = = = :;	
Lincid Milmigeadac, Ohm, M. (32(1)) Initical Milmigeadac		
Inticit Minispedian, Ohm M,		
Inticit Microlangea Cain Dinn M.		
Incirt Michashrepara Camp, Orm. M, 152 1 1.03400004 1.05440004 1.05440004 1.05440004 1.05440004 1.05440004 1.05440004 1.054400		
Hitch MichaePropotionalGan, Ohm M_52[1] 23,3 619995		1.03400004
Inicit MirviolDaxFF Volt M. 1520 25.377008	ItrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-816.005981
Intert MivolicasFF Voil M. 1221 2.1 8880005 1.1	ltrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-233.619995
Intert. MirviolCaseF. Volt. M. 52(9) 3.559500003 Intert. Vecu. Volt. M. 52(9) 18.9510002 18	ltrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
NiCtrl Micvi Voice Voi	trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
licit.f. Veou_Volt_M_I32(I) 18.9510002 ltCurrDaxPrevintg_Volt_M_I32(I) 21.3110008 ltCurrDaxPrevintg_Volt_M_I32(I) 9.66300011 ltCurrDaxRef_Amp_M_I32(I) 17.7020035 ltCurrDaxRef_Amp_M_I32(I) 17.7020035 ltCurrDaxRef_Amp_M_I32(I) 20.9910999 ltCurrDaxRef_Amp_M_I32(I) 10.072998 ltCurrDaxRef_Amp_M_I32(I) 112.455002 ltCurrDaxRef_Amp_M_I32(I) 0 ltCurrDaxRef_Amp_M_I32(I) 0.12000024 ltPosComputationDelay_Rad_M_I32(I) 0.512000024 ltPosComputationDelay_Rad_M_I32(I) 0.512000024 CurrCntrl_DualEcuFailsGrEac_Uls_M_I32 0.911000013 CurrCntrl_InverterFailsGrEac_Uls_M_I32 0.14599998 CurrCntrl_InverterFailsGrEac_Uls_M_I32 0.14599998 CurrCntrl_MtrCurrDaxSafluxRatio_Uls_M_I32 0.145999998 CurrCntrl_MtrCurrDaxSafluxRatio_Uls_M_I32 0.145999998 CurrCntrl_MtrCurrDaxSafluxRatio_Uls_M_I32 0.749000013 CurrCntrl_MtrVoltQaxSafluxRatio_Uls_M_I32 0.749000013 CurrCntrl_MtrVoltQaxFFEIL_M_Ist_Term_Uls_I32 40.2612 CurrCntrl_MtrVoltQaxFFEIL_M_Ist_Term_Uls_I32 0.925999992 <td>trCtrl_MtrVoltQaxFF_Volt_M_f32[0]</td> <td>-3.59500003</td>	trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003
11 11 12 13 10 10 11 11 11 11 11	trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-28.4209995
InticurbarPrevinty Volt M. 932 9,68300011 133 947006 133 947006 133 947006 133 947006 133 947006 133 947006 133 947006 133 947006 133 947006 133 947006 133 947006 133 947006 133 947006 133 947006 133 947007 133 947006 133 947007 133 94700	ltrCtrl_Vecu_Volt_M_f32[0]	18.9510002
IntCurrDaxRef_Amp_M_[32[0] -133.947006 IntCurrDaxRef_Amp_M_[32[1] 75.7020035 IntCurrDaxApreving_Voll_M_[32] 83.9499975 IntCurrDaxApreving_Voll_M_[32] 20.9910999 IntCurrDaxApreving_Voll_M_[32[0] 106.072998 IntCurrDaxRef_Amp_M_[32[1] -112.455002 IntCurrDaxRef_Amp_M_[32[1] -112.455002 IntCurrDaxRef_Amp_M_[32[0] -6.1909998 IntProSComputationDelay_Rad_M_[32[0] -6.1909998 IntProSComputationDelay_Rad_M_[32[0] -6.1909998 IntProSComputationDelay_Rad_M_[32[1] 0.512000024 CurrCntr_UrrSensFallSefac_Uls_M_[32] 0.911000013 CurrCntr_UrrSensFallSefac_Uls_M_[32] 0.911000013 CurrCntr_UrrDexAssFallSefac_Uls_M_[32] 0.749000013 CurrCntr_UrrDexAssFallSefac_Uls_M_[32] 0.749000013 CurrCntr_UrrDexAssFallSefac_Uls_M_[32] 0.56909995 CurrCntr_UrrDexAssFallSefac_Uls_M_[32] 0.56909995 CurrCntr_UrrDexAssFallSefac_Uls_M_[32] 0.569099995 CurrCntr_UrrDexAssFallSefac_Uls_M_[32] 0.925999992 CurrCntr_UrrDexAssFallSefac_Uls_M_[32] 0.925999992 CurrCntr_UrrDexAssFallSefac_Uls_M_[32] 0.925999992 CurrCntr_UrrDexAssFallSefac_Uls_M_[32] 0.925999992 CurrCntr_UrrDexAssFall_M_[31] 5.72 0.925999992 CurrCntr_UrrDexAssFall_M_[31] 5.72 0.92599992 CurrCntr_UrrDexAssFall_M_[ltrCtrl_Vecu_Volt_M_f32[1]	21.3110008
	ItrCurrDaxPrevIntg_Volt_M_f32	-9.66300011
Introurn Marco M		
Introduction Intr		
thrcurrQaxRef_Amp_M_[32[0] 106.072998 thrcurrQaxRef_Amp_M_[32[1] -112.455002 thrcurrQaxRef_Amp_M_[32[1] -112.455002 thrPosComputationDelay_Rad_M_[32[0] -6.19099998 thrPosComputationDelay_Rad_M_[32[1] 0.512000024 (CurrCntr_CurrSensFailsClac_UIs_M_[32] 0.911000013 (CurrCntr_DursCarsFailsClac_UIs_M_[32] 0.145999998 (CurrCntr_ImcretrailsClac_UIs_M_[32] 0.513000011 (CurrCntr_MrcurrDaxSaftFuxRatio_UIs_M_[32] 0.513000011 (CurrCntr_MrcurrDaxSaftFuxRatio_UIs_M_[32] 0.555999995 (CurrCntr_MrvecuFill_M_str.PrevOupt_UIs_[32] 1118 (CurrCntr_MrvecuFill_M_str.PrevOupt_UIs_[32] -1118 (CurrCntr_MrvecuFill_M_str.PrevOupt_UIs_[32] 0.925599992 (CurrCntr_MrvecuFill_M_str.PrevOupt_UIs_[32] 1118 (CurrCntr_MrvecuFill_M_str.PrevOupt_UIs_[32] 1118 (CurrCntr_MrvecuFill_M_str.PrevOupt_UIs_[32] 1118 (CurrCntr_MrvecuFill_M_str.PrevOupt_UIs_[32] 40.2612 (CurrCntr_MrvecuFill_M_str.PrevOupt_UIs_[32] 40.2612 (CurrCntr_MrvecuFill_M_str.PrevOupt_UIS_[32] 40.2612 (CurrCntr_MrvecuFill_M_str.PrevOupt_UIS_[32] 616.02002	·	
thr/CurrQaxRp_Amp_M_32 0 thr/PosComputationDelay_Rad_M_[32[0] -6.19099998 thr/PosComputationDelay_Rad_M_[32[1] 0.512000024 t/CurrCntrl_CurrSensFallSciFac_Uls_M_52 0.911000013 t/CurrCntrl_DualEcuFailSciFac_Uls_M_52 0.911000013 t/CurrCntrl_InverterFailSciFac_Uls_M_132 0.145999998 t/CurrCntrl_InverterFailSciFac_Uls_M_132 0.513000011 t/CurrCntrl_MitrCurrQaxSatiFluxRatio_Uls_M_132 0.565999985 t/CurrCntrl_MitrVecuFiit_M_str.Prev/output_Uls_132 1118 t/CurrCntrl_MitrVecuFiit_M_str.Prev/output_Uls_132 1118 t/CurrCntrl_MitrVecuFiit_M_str.Prev/output_Uls_132 40.2612 t/CurrCntrl_MitrVecuFiit_M_str.Prev/output_Uls_132 1118 t/CurrCntrl_MitrVolQaxFFFiit_M_str.Prev/output_Uls_132 1118 t/CurrCntrl_MitrVolQaxFFFiit_M_str.Prev/output_Uls_132 1118 t/CurrCntrl_MitrVolQaxFFFiit_M_str.Prev/output_Uls_132 1118 t/CurrCntrl_MitrVolQaxFFFiit_M_str.TermD_Uls_132 0.2612 t/CurrCntrl_MitrVolQaxFFFiit_M_str.TermD_Uls_132 0.2619 t/CurrCntrl_MitrVolQaxFFFiit_M_str.TermD_Uls_132 0.2610 t/CurrCntrl_MitrVolQaxFiit_M_str.TermD_Uls_132 0.777.7096	_ := - ::	
httrPosComputationDelay_Rad_M_f32[0] -6.19099998 httrPosComputationDelay_Rad_M_f32[1] 0.512000024 lCurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.911000013 lCurrCntrl_DualEcuFailSclFac_Uls_M_f32 0.145999998 lCurrCntrl_InverterFailSclFac_Uls_M_f32 0.513000011 lCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.0749000013 lCurrCntrl_MtrVecuFailt_M_str.Previnput_Uls_f32 0.565999985 lCurrCntrl_MtrVecuFilt_M_str.Previnput_Uls_f32 1118 lCurrCntrl_MtrVecuFilt_M_str.Previnput_Uls_f32 40.2612 lCurrCntrl_MtrVecuFilt_M_str.Previnput_Uls_f32 40.2612 lCurrCntrl_MtrVecuFilt_M_str.Previnput_Uls_f32 1118 lCurrCntrl_MtrVolQaxFFFilt_M_str.Previnput_Uls_f32 1118 lCurrCntrl_MtrVolQaxFFFilt_M_str.Previnput_Uls_f32 1118 lCurrCntrl_MtrVolQaxFFFilt_M_str.TermD_Uls_f32 40.2612 lCurrCntrl_MtrVolQaxFFFilt_M_str.TermD_Uls_f32 40.2612 lCurrCntrl_MtrVolQaxFFFilt_M_str.TermD_Uls_f32 40.2612 lCurrCntrl_MtrVolQaxFFilt_M_str.TermD_Uls_f32 40.2612 lCurrCntrl_MtrVolQaxFFilt_M_str.TermD_Uls_f32 5777.70996 lLi_OAFdbackSignalSclFacSlew_UlspS_f32 5777.70996		
CurrCntrl_CurrSensFailSclFac_Uls_M_f32		
CurrCntrl_DualEcuFailSclFac_Uls_M_f32		
CurrCntr InverterFailSclFac_Uls_M_132		
CurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32		
CurrCntr MtrCurrQaxSatFluxRatio Uls M f32 0.56599985 CurrCntr MtrVecuFilt M str. Previnput Uls f32 1118 CurrCntr MtrVecuFilt M str. Previnput Uls f32 1118 CurrCntr MtrVecuFilt M str. Previnput Uls f32 40.2612 CurrCntr MtrVecuFilt M str. TermD Uls f32 0.925599992 CurrCntr MtrVoliQaxFFFilt M str. Previnput Uls f32 1118 CurrCntr MtrVoliQaxFFFilt M str. Previnput Uls f32 1118 CurrCntr MtrVoliQaxFFFilt M str. Previnput Uls f32 40.2612 CurrCntr MtrVoliQaxFFFilt M str. TermD Uls f32 40.2612 CurrCntr MtrVoliQaxFFFilt M str. TermD Uls f32 40.2612 CurrCntr MtrVoliQaxFFFilt M str. TermD Uls f32 6616.0202 CurrCntr MtrVoliQaxFFFilt M str. TermD Uls f32 6777.70996 CurrCntr MtrVoliQaxFFFilt M str. TermD Uls f32 6777.70996 MtrCtr CurrCntr MtrVoliQaxFFFilt M str. TermD Uls f32 6777.70996 MtrCtr CurrCntr MtrVoliDaxControlDisable Cnt gc 1 MtrCtr VirualResDax Ohm f32 0.14000001 MtrCtr VirualResDax Ohm f32 0.056000017 MtrCurrQaxRefModiffpel Cnt gc 1 MtrCurrQaxRefModiffpel Cnt gc 1 MtrVoltDaxIntegHiLim Volt f32 622.409998 MtrVoltDaxIntegLo.Lim Volt f32 622.409998 MtrVoltQaxFiltFFEnable Cnt gc 1 MtrVoltQaxFiltFenable Cn		
CurrCntf MtrVecuFilt M_str.PrevInput_Uls_f32		
CurrCntrl MtrVecuFilt M_str.PrevOutput_Uls f32		
CurrCntrl MtrVecuFilt M_str.TermN_Uls f32		
CurrCntr MtrVecuFilt_M_str.TermD_Uls_f32 0.925599992 CurrCntr MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 1118 CurrCntr MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 -1118 CurrCntr MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 40.2612 CurrCntr MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 40.2612 CurrCntr MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.925599992 CLOAFdbackSignalSclFacSlew_UlspS_f32 6616.02002 DualEcuSignalSclFacSlew_UlspS_f32 187.600006 LOAFdbackSignalSclFacSlew_UlspS_f32 5777.70996 MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 1 MtrCtrlVirualResDax_Ohm_f32 0.14000001 MtrCtrlVirualResDax_Ohm_f32 0.14000001 MtrCtrlVirualResQax_Ohm_f32 0.0560000017 MtrCurrQaxRefModifDsb_Cnt_lgc 1 MtrCurrQaxRefModifDsb_Cnt_lgc 0 MtrCurrQaxRefModifPplEn_Cnt_lgc 0 MtrVoltDaxIntegHiLim_Volt_f32 15.4277 MtrVoltDaxIntegLoLim_Volt_f32 -22.4099998 MtrVoltQaxFiltFFEnable_Cnt_lgc 1		
CurrCntri_MtrVoltQaxFFFit_M_str.PrevInput_UIs_f32		
CurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_132 40.2612 CurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_132 0.925599992 CLOAFdbackSignalSclFacSlew_UlspS_f32 6616.02002 DualEcuSignalSclFacSlew_UlspS_f32 187.600006 LOAFdbackSignalSclFacSlew_UlspS_f32 5777.70996 LOAFdbackSignalSclFacSlew_UlspS_f32 177.70996 MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 1 MtrCtrlVirualResDax_Ohm_f32 0.14000001 MtrCtrlVirualResDax_Ohm_f32 0.056000017 MtrCurrQaxRefModifDsb_Cnt_lgc 1 MtrCurrQaxRefModifRplEn_Cnt_lgc 0 MtrCurrQaxRefModifRplEn_Cnt_lgc 0 MtrVoltDaxIntegHiLim_Volt_f32 15.4277 MtrVoltDaxIntegLoLim_Volt_f32 -22.4099998 MtrVoltQaxFiltFFEnable_Cnt_lgc 1		1118
CurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_132 40.2612 CurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_132 0.925599992 CLOAFdbackSignalSclFacSlew_UlspS_f32 6616.02002 DualEcuSignalSclFacSlew_UlspS_f32 187.600006 LOAFdbackSignalSclFacSlew_UlspS_f32 5777.70996 LOAFdbackSignalSclFacSlew_UlspS_f32 177.70996 MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 1 MtrCtrlVirualResDax_Ohm_f32 0.14000001 MtrCtrlVirualResDax_Ohm_f32 0.056000017 MtrCurrQaxRefModifDsb_Cnt_lgc 1 MtrCurrQaxRefModifRplEn_Cnt_lgc 0 MtrCurrQaxRefModifRplEn_Cnt_lgc 0 MtrVoltDaxIntegHiLim_Volt_f32 15.4277 MtrVoltDaxIntegLoLim_Volt_f32 -22.4099998 MtrVoltQaxFiltFFEnable_Cnt_lgc 1	CurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-1118
CLOAFdbackSignalSclFacSlew_UlspS_f32 6616.02002 DualEcuSignalSclFacSlew_UlspS_f32 187.600006 ILOAFdbackSignalSclFacSlew_UlspS_f32 5777.70996 MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 1 MtrCtrlFeedbackControlDisable_Cnt_lgc 1 MtrCtrlVirualResDax_Ohm_f32 0.140000001 MtrCtrlVirualResQax_Ohm_f32 0.0560000017 MtrCurrQaxRefModifDsb_Cnt_lgc 1 MtrCurrQaxRefModifRplEn_Cnt_lgc 0 MtrVoltDaxIntegHiLim_Volt_f32 15.4277 MtrVoltDaxIntegLoLim_Volt_f32 -22.4099998 MtrVoltQaxFiltFFEnable_Cnt_lgc 1	CurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	40.2612
DualEcuSignalSclFacSlew_UlspS_f32 187.600006 ILOAFdbackSignalSclFacSlew_UlspS_f32 5777.70996 MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 1 MtrCtrlVirualResDax_Ohm_f32 0.140000001 MtrCtrlVirualResQax_Ohm_f32 0.0560000017 MtrCurrQaxRefModifDsb_Cnt_lgc 1 MtrCurrQaxRefModifRplEn_Cnt_lgc 0 MtrVoltDaxIntegHiLim_Volt_f32 15.4277 MtrVoltDaxIntegLoLim_Volt_f32 -22.4099998 MtrVoltQaxFiltFFEnable_Cnt_lgc 1	CurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.925599992
ILOAFdbackSignalSclFacSlew_UlspS_f32	CLOAFdbackSignalSclFacSlew_UlspS_f32	6616.02002
MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 1 MtrCtrlFeedbackControlDisable_Cnt_lgc 1 MtrCtrlVirualResDax_Ohm_f32 0.140000001 MtrCtrlVirualResQax_Ohm_f32 0.0560000017 MtrCurrQaxRefModifDsb_Cnt_lgc 1 MtrCurrQaxRefModifRplEn_Cnt_lgc 0 MtrVoltDaxIntegHiLim_Volt_f32 15.4277 MtrVoltDaxIntegLoLim_Volt_f32 -22.4099998 MtrVoltQaxFiltFFEnable_Cnt_lgc 1		
MtrCtrlFeedbackControlDisable_Cnt_lgc 1 MtrCtrlVirualResDax_Ohm_f32 0.140000001 MtrCtrlVirualResQax_Ohm_f32 0.0560000017 MtrCurrQaxRefModifDsb_Cnt_lgc 1 MtrCurrQaxRefModifRplEn_Cnt_lgc 0 MtrVoltDaxIntegHiLim_Volt_f32 15.4277 MtrVoltDaxIntegLoLim_Volt_f32 -22.4099998 MtrVoltQaxFiltFFEnable_Cnt_lgc 1	_ILOAFdbackSignalSclFacSlew_UlspS_f32	
MtrCtrlVirualResDax_Ohm_f32 0.140000001 MtrCtrlVirualResQax_Ohm_f32 0.0560000017 MtrCurrQaxRefModifDsb_Cnt_lgc 1 MtrCurrQaxRefModifRplEn_Cnt_lgc 0 MtrVoltDaxIntegHiLim_Volt_f32 15.4277 MtrVoltDaxIntegLoLim_Volt_f32 -22.4099998 MtrVoltQaxFiltFFEnable_Cnt_lgc 1		
MtrCtrlVirualResQax_Ohm_f32 0.0560000017 MtrCurrQaxRefModifDsb_Cnt_lgc 1 MtrCurrQaxRefModifRplEn_Cnt_lgc 0 MtrVoltDaxIntegHiLim_Volt_f32 15.4277 MtrVoltDaxIntegLoLim_Volt_f32 -22.4099998 MtrVoltQaxFiltFFEnable_Cnt_lgc 1		
MtrCurrQaxRefModifDsb_Cnt_lgc 1 _MtrCurrQaxRefModifRplEn_Cnt_lgc 0 _MtrVoltDaxIntegHiLim_Volt_f32 15.4277 _MtrVoltDaxIntegLoLim_Volt_f32 -22.4099998 _MtrVoltQaxFiltFFEnable_Cnt_lgc 1		
MtrCurrQaxRefModifRpIEn_Cnt_Igc 0 MtrVoltDaxIntegHiLim_Volt_f32 15.4277 MtrVoltDaxIntegLoLim_Volt_f32 -22.4099998 MtrVoltQaxFiltFFEnable_Cnt_Igc 1		
MtrVoltDaxIntegLoLim_Volt_f32 -22.4099998 MtrVoltQaxFiltFFEnable_Cnt_lgc 1		
MtrVoltQaxFiltFFEnable_Cnt_lgc 1		

PICurrCntrl_Per1



Name	Input Value		
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-25		
k_VoltSatQaxPolyCoeff_Uls_f32	0.398000002		
k_deadtimeVScale_Uls_f32	0.971000016		
t_CommOffsetTblX_Uls_u3p13[0]	5022		
t_CommOffsetTblX_Uls_u3p13[1]	7003		
t_CommOffsetTblY_Cnt_u16[0]	297		
t_CommOffsetTblY_Cnt_u16[1]	1110		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1099		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1099	1099	~
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	22.1240005	22.1240005 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-0.164528802	-0.164528832 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.852211	-4.85221148 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	0	0 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	✓
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.122549996	0.122549996 ± 0.0625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	-

Test Step 2.151 (Repeat Count = 1)	· · · · · · · · · · · · · · · · · · ·
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.684000015
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.978999972
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-984.268005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	119.455002





Name	Input Value		
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994		
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009		
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.566999972		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.83399999		
MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	762.239014		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-528.901978		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964		
MtrCtrl MtrVoltQaxFF Volt M f32[0]	-25.3770008		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	21.3880005		
MtrCtrl Vecu Volt M f32[0]	18.5559998		
	20.9160004		
MtrCtrl_Vecu_Volt_M_f32[1]	-27.3339996		
MtrCurrDavPrevIntg_Volt_M_f32			
MtrCurrDaxRef_Amp_M_f32[0]	209.052002		
MtrCurrDaxRef_Amp_M_f32[1]	-124.994003		
MtrCurrQaxCog_Amp_M_f32	-144.667007		
MtrCurrQaxPrevIntg_Volt_M_f32	2.85220003		
MtrCurrQaxRef_Amp_M_f32[0]	24.6130009		
MtrCurrQaxRef_Amp_M_f32[1]	-20.9400005		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	3.82500005		
MtrPosComputationDelay_Rad_M_f32[1]	2.33800006		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.702000022		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.147		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0040000019		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.716700017		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	570.700012		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	60.2319984		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.522199988		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	60.2319984		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.522199988		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	5911.31982		
k_DualEcuSignalSclFacSlew_UlspS_f32	188.800003		
	2332.93994		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32			
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.0209999997		
k_MtrCtrlVirualResQax_Ohm_f32	0.150999993		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	13.2849998		
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	11.2693005		
k_MtrVoltQaxIntegLoLim_Volt_f32	-8.68999958		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	25		
k_VoltSatQaxPolyCoeff_Uls_f32	20.7369995		
k_deadtimeVScale_Uls_f32	0.963		
t_CommOffsetTbIX_UIs_u3p13[0]	4611		
t_CommOffsetTblX_Uls_u3p13[1]	5579		
t_CommOffsetTblY_Cnt_u16[0]	23		
t CommOffsetTblY Cnt u16[1]	212		
target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	665		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		1_
Name	Actual Value	Expected Value	Result
	Actual Value		
MtrCntrl_Write_CommOffset_Cnt_u16(val)	665	665	~
MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_ModIdx_Uls_u16p16(val)		665 0 ± 1	Ž
MtrCntrl_Write_CommOffset_Cnt_u16(val)	665		
MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_ModIdx_Uls_u16p16(val)	665 0	0 ± 1	



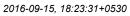


Name	Actual Value	Expected Value	Result
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	13084	13084 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl DualEcuFailSclFac Uls M f32	0.170599997	0.170599997 ± 0.0625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	-
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr
VtrCntrl Read IvtrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr
MtrCntrl Read ModIdxSrlComSvcDft Cnt Igc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt Igc Val
WtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
/trCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr
MtrCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
MtrCntrl Read SysState Cnt Enum(Val)	target MtrCntrl Read SysState Cnt Enum Val
VtrCtrl MtrCurrDaxMaxVal Amp M f32[0]	-147.343002
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	127.972
MtrCtrl MtrDampTermDax Ohm M f32[0]	0.035999985
VtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.075000003
MtrCtrl MtrDampTermQax Ohm M f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl MtrDaxIntegralGain Ohm M f32[0]	0.904999971
MtrCtrl MtrDaxIntegralGain Ohm M f32[1]	0.704999983
MtrCtrl MtrDaxPropotionalGain Ohm M f32[0]	-822.044006
	-449.259003
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0359999985 0.075000003
/trCtrl_MtrImpedDax_Ohm_M_f32[1]	
/trCtrl_MtrImpedQax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.125
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.922999978
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.349999994
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	161.807007
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	765.385986
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-7.6669982
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	2.61400008
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-29.3959999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-1.94000006
/trCtrl_Vecu_Volt_M_f32[0]	5.12099981
/trCtrl_Vecu_Volt_M_f32[1]	7.48099995
/trCurrDaxPrevIntg_Volt_M_f32	20.9669991
MtrCurrDaxRef_Amp_M_f32[0]	-139.906998
/trCurrDaxRef_Amp_M_f32[1]	115.814003
MtrCurrQaxCog_Amp_M_f32	-41.5750008
/trCurrQaxPrevIntg_Volt_M_f32	30.6926994
MtrCurrQaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrQaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	4.6960001

PICurrCntrl_Per1





Name	Input Value		
MtrPosComputationDelay_Rad_M_f32[1]	2.56500006		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.662		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.148000002		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.481000006		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.65079999		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.686999977		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	570.700012		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	76.1873016		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0882999972		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	0		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	76.1873016		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32	0.0882999972		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7083.27002		
k DualEcuSignalSclFacSlew UlspS f32	190		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	947.890015		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.126000002		
k MtrCtrlVirualResQax Ohm f32	0.115999997		
k MtrCurrQaxRefModifDsb Cnt lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	5.48570013		
k MtrVoltDaxIntegLoLim Volt f32	-4.57000017		
k MtrVoltQaxFiltFFEnable Cnt lgc	0		
k MtrVoltQaxIntegHiLim Volt f32	13.9652004		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k VoltSatDaxPolyCoeff Uls f32	0		
k_VoltSatQaxPolyCoeff_Uls_f32	-1.46500003		
k deadtimeVScale Uls f32	0.987999976		
t_CommOffsetTbIX_UIs_u3p13[0]	459		
t_CommOffsetTbIX_UIs_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	1237		
t_CommOffsetTbIY_Cnt_u16[1]	383		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003		
target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	1724		
target MtrCntrl Read MtrCurrQax Amp f32 Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
		From a set of Walton	D
Name Marchal Write Commontone Cost (145(1))	Actual Value	Expected Value	Resul
MtrCntrl_Write_CommOffset_Cnt_u16(val)	739	739	•
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	28516	28516 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-175.397003	-175.397003 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	2.61400008	2.61400008 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-1.9400006	-1.94000006 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	49797	49797 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	5.48570013	5.48570013	•
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.124250002	0.124250002 ± 0.0625	<u> </u>



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	•
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Input Value
0
target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
target_MtrCntrl_Read_ModidxSrlComSvcDft_Cnt_lgc_Val
target_MtrCntrl_Read_MotCurrLoaMtgtnEn Cnt lgc ptr
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
target_MtrCntrl_Read_SysState_Cnt_Enum_Val
6.18900013
83.0540009
0.0099999978
0.079999982
0.098999995
0.0170000009
0.273999989
0.469999999
-538.278992
-363.735992
0.0099999978
0.0799999982
0.0939999968
0.0879999995
1.36099994
0.019999996
-870.234009
991.184998
-3.59500003
-28.4209995
-17.1070004
15.9390001
18.9510002
21.3110008
10.2959995
-82.2979965
46.8180008
48.840002
7.01200008
-146.723007
-121.943001
0
4.78000021
-2.88599992
0.851000011
0.14900004
0.465999991
0.371499985 0.143000007

PICurrCntrl_Per1





Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-627.179993		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	99.3730011		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.1426		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-627.179993		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	0		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	99.3730011		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.1426		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2409.94995		
k_DualEcuSignalSclFacSlew_UlspS_f32	191.199997		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4854.70996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.093999968		
k_MtrCtrlVirualResQax_Ohm_f32	0.0769999996		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	18.2031002		
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	3.43330002		
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-24.0909996		
k_VoltSatQaxPolyCoeff_Uls_f32	20.9540005		
k_deadtimeVScale_Uls_f32	0.954999983		
t_CommOffsetTblX_Uls_u3p13[0]	5022		
t_CommOffsetTblX_Uls_u3p13[1]	7003		
t_CommOffsetTblY_Cnt_u16[0]	63		
t_CommOffsetTblY_Cnt_u16[1]	327		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	568		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Resul
MtrCntrl_Write_CommOffset_Cnt_u16(val)	568	568	
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-195.563004	-195.563004 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	4.68526125	4.68526077 ± 4.88E-04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-0.921387315	-0.921387196 ± 4.88E-04	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	2731	2731 ± 1.52588E-05	
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.172900006	0.172900006 ± 0.0625	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~



Test Step 2.154 (Repeat Count = 1)	· ·
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val) MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	target_MtrCntrl_Read_SysState_Cnt_Enum_Val -147.343002
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	127.972
MtrCtrl MtrDampTermDax Ohm M f32[0]	0.0359999985
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.648
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.782000005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-530.372009
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-420.145996
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0120000001
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.0560000017 1.47300005
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] MtrCtrl MtrQaxIntegralGain Ohm M f32[1]	0.94400006
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	736.344971
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	379.115997
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-7.6669982
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-29.3959999
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-1.9400006
MtrCtrl_Vecu_Volt_M_f32[0]	18.5559998
MtrCtrl_Vecu_Volt_M_f32[1]	20.9160004
MtrCurrDaxPrevIntg_Volt_M_f32	7.36499977
MtrCurrDaxRef_Amp_M_f32[0]	-65.1900024
MtrCurrDaxRef_Amp_M_f32[1]	-216.972
MtrCurrQaxCog_Amp_M_f32	-41.5750008
MtrCurrQaxPrevIntg_Volt_M_f32	11.7653999
MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[1]	31.5869999 -186.395996
MtrCurrQaxRpl Amp M f32	0
MtrPosComputationDelay Rad M f32[0]	-0.195999995
MtrPosComputationDelay_Rad_M_f32[1]	-0.303000003
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.237000003
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.150000006
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.481000006
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.982200027
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.0469999984
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-627.179993
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	64.5255966
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput Uls f32	0.514999986 -43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uis_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982 -627.179993
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	64.5255966
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.514999986
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6110.83008
k_DualEcuSignalSclFacSlew_UlspS_f32	192.399994
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	7208.8501
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.0909999982
k_MtrCtrlVirualResQax_Ohm_f32	0.172999993
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1
k_MtrVoltDaxIntegHiLim_Volt_f32	27.6355991
k_Mtr/oltOayEittEEEpable_Cot_tag	-10.5
k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32	1 26.4790993
k_MtrVoltQaxIntegLoLim_Volt_f32	-10.5
k MtrVoltVecuFiltEnable Cnt lgc	1
	1:

PICurrCntrl_Per1



Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	8.55000019		
k_VoltSatQaxPolyCoeff_Uls_f32	-12.8280001		
k_deadtimeVScale_Uls_f32	0.987999976		
t_CommOffsetTblX_Uls_u3p13[0]	4611		
t_CommOffsetTblX_Uls_u3p13[1]	5579		
t_CommOffsetTblY_Cnt_u16[0]	889		
t_CommOffsetTblY_Cnt_u16[1]	1543		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2927		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	2927	2927	-
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	73.1620026	73.1620026 ± 7.81E-03	-
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-1.24673235	-1.24673235 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.78008938	-4.78008938 ± 4.88E-04	-
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	33385	33385 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	-
PICurrCntrl DualEcuFailSclFac Uls M f32	0.125950009	0.125950009 ± 0.0625	-

Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	-
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	-
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	-
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	-
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	-

Test Step 2.155 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	6.18900013
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	83.0540009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.079999982
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.10399997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.256000012
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	458.355011
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-1015.39001
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrlmpedDax_Ohm_M_f32[1]	0.0799999982

PICurrCntrl_Per1



			, , , , , , ,
Name	Input Value		
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0939999968		
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.087999995		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.77700001		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.27199996		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	309.817993		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-766.486023		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltDaxFE_Volt_M_f32[1]	-3.59500003 -28.4209995		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-17.1070004		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	15.9390001		
MtrCtrl Vecu Volt M f32[0]	17.7010002		
MtrCtrl Vecu Volt M f32[1]	20.0610008		
MtrCurrDaxPrevIntg_Volt_M_f32	0.85199998		
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007		
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001		
MtrCurrQaxCog_Amp_M_f32	48.8400002		
MtrCurrQaxPrevIntg_Volt_M_f32	24.9650002		
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006		
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	-4.36899996		
MtrPosComputationDelay_Rad_M_f32[1]	-4.83900023		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.231999993		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.150999993		
PICurrCotrl_InverterFailSclFac_UIs_M_f32	0.657000005		
PICurrCotrl_MtrCurrOavSatFluxRatio_UIs_M_f32	0.287900001		
PICurrCntrl_Mtr/CourEith_M_ctr_Provingut_Ulg_f32	0.725000024		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118 -43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	70.1921005		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.3741		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	70.1921005		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.3741		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6145.56982		
k_DualEcuSignalSclFacSlew_UlspS_f32	193.600006		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4019.20996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.193000004		
k_MtrCtrlVirualResQax_Ohm_f32	0.114		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRpIEn_Cnt_lgc	14.0504007		
k_MtrVoltDaxIntegHiLim_Volt_f32 k_MtrVoltDaxIntegLoLim_Volt_f32	14.8564997 -11.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k MtrVoltQaxIntegHiLim Volt f32	28.1163998		
k MtrVoltQaxIntegLoLim Volt f32	-11.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k VoltSatDaxPolyCoeff Uls f32	18.2779999		
k VoltSatQaxPolyCoeff Uls f32	-25		
k_deadtimeVScale_Uls_f32	0.996999979		
t_CommOffsetTbIX_UIs_u3p13[0]	459		
t_CommOffsetTbIX_UIs_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	771		
t_CommOffsetTblY_Cnt_u16[1]	1636		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	50.0610008		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1789		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1636	1636	•
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	65339	65339 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	26.8620033	26.8620033 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-17.4447498	-17.4447479 ± 4.88E-04	<u> </u>
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	9.7833252	9.78332424 ± 4.88E-04	
MtrCurrDayProvinte_Volt_M_f32	4010 5.81056300	4010 ± 1.52588E-05	- J
MtrCurrDaxPrevIntg_Volt_M_f32	-5.81056309	-5.81056309	



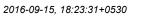


Name	Actual Value	Expected Value	Result
PICurrCntrl DualEcuFailSclFac Uls M f32	0.1752	0.1752 ± 0.0625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

lame	Input Value	
astDataAccessBufIndex_Cnt_M_u16	1	
/trCntrl Read DualEcuMotCtrlMtgnEna Cnt Igc(ptr)	target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr	
trCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr	
htrCntrl Read ModidxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModidxSrlComSvcDft Cnt lgc Val	
ItrCntrl Read MotCurrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc ptr	
/trCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val	
/trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
htrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
ltrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	31.5869999	
trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-186.395996	
ItrCtrl MtrDampTermDax Ohm M f32[0]	0.0340000018	
trCtrl_MtrDampTermDax_Ohm_M_i32[1]	0.104999997	
trCtrl MtrDampTermQax Ohm M f32[0]	0.115999997	
trCtrl_MtrDampTermQax_Onm_M_is2[0]	0.115999997	
ItrCtrl_MtrDaxIntegralGain Ohm M f32[0]	1.4739998	
	1.90199995	
htrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	-146.214005	
ItrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-942.195007	
ItrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]		
trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994	
ItrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009	
ItrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994	
ItrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.123000003	
ItrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.4900001	
ItrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.95999979	
ItrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-53.862999	
ItrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	75.7020035	
ltrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-30.2169991	
ltrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	19.2049999	
ltrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-27.0669994	
ltrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	28.1070004	
ItrCtrl_Vecu_Volt_M_f32[0]	18.9510002	
ltrCtrl_Vecu_Volt_M_f32[1]	21.3110008	
ltrCurrDaxPrevIntg_Volt_M_f32	-18.5370007	
ltrCurrDaxRef_Amp_M_f32[0]	-146.723007	
htrCurrDaxRef_Amp_M_f32[1]	-121.943001	
ItrCurrQaxCog_Amp_M_f32	79.6729965	
ItrCurrQaxPrevIntg_Volt_M_f32	-26.8785992	
ltrCurrQaxRef_Amp_M_f32[0]	-146.173996	
trCurrQaxRef_Amp_M_f32[1]	-213.335007	
trCurrQaxRpl_Amp_M_f32	0	
trPosComputationDelay_Rad_M_f32[0]	1.52600002	
ltrPosComputationDelay_Rad_M_f32[1]	-2.68400002	
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.40000006	
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.151999995	
ICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0590000004	

PICurrCntrl_Per1





Name	Input Value		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.967899978		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.337000012		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	22.2399998		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	16.5851002		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.887899995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	22.2399998		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	16.5851002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.887899995		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2663.65991		
k_DualEcuSignalSclFacSlew_UlspS_f32	194.800003		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5194.8999		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.112000003		
k_MtrCtrlVirualResQax_Ohm_f32	0.021999999		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	6.98460007		
k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	29.5142002		
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-2.4230001		
k_VoltSatQaxPolyCoeff_Uls_f32	25		
k_deadtimeVScale_Uls_f32	0.958999991		
t_CommOffsetTblX_Uls_u3p13[0]	6528		
t_CommOffsetTblX_Uls_u3p13[1]	8192		
t_CommOffsetTblY_Cnt_u16[0]	76		
t_CommOffsetTblY_Cnt_u16[1]	211		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-9.31999969		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	65		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-9.31999969		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Resul
MtrCntrl_Write_CommOffset_Cnt_u16(val)	65	65	
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	
MtrCntrl Write MtrCurrQaxFinalRef Amp f32(val)	-220	-220 ± 7.81E-03	
MtrCntrl Write MtrDaxVoltage Volt f32(val)	2.70515442	2.70515442 ± 4.88E-04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	3.95906138	3.95906138 ± 4.88E-04	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	43793	43793 ± 1.52588E-05	
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	-
PICurrCntrl DualEcuFailSclFac Uls M f32	0.127649993	0.127649993 ± 0.0625	

Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	-
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	-
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	-
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓



Test Step 2.157 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr) MtrCntrl Read ModIdxSrlComSvcDft Cnt Igc(Val)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
MtrCntrl Read SysState Cnt Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.397000015
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.980000019
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	161.654999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-253.688004
MtrCtrl_MtrlmpedDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrlmpedDax_Ohm_M_f32[1]	0.115999997
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.057 0.633000016
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	-170.535004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-25.7549992
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002
MtrCtrl_Vecu_Volt_M_f32[0]	18.5559998
MtrCtrl_Vecu_Volt_M_f32[1]	20.9160004
MtrCurrDaxPrevIntg_Volt_M_f32	-8.56599998
MtrCurrDaxRef_Amp_M_f32[0]	209.052002
MtrCurrDaxRef_Amp_M_f32[1]	-124.994003
MtrCurrQaxCog_Amp_M_f32	5.72399998
MtrCurrQaxPrevIntg_Volt_M_f32	12.8451004
MtrCurrQaxRef_Amp_M_f32[0]	24.6130009
MtrCurrQaxRef_Amp_M_f32[1]	-20.9400005
MtrCurrQaxRpI_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	2.9749999
MtrPosComputationDelay_Rad_M_f32[1]	0.486999989
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.702000022
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.152999997
PICurrCntrl_InverterFailSclFac_Uls_M_f32 PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.00400000019 0.853799999
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32	0.833000004 1118
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-717.299988
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	87.3075027
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.461600006
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput UIs f32	1118
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-717.299988
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	87.3075027
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.461600006
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	5911.31982
k_DualEcuSignalSclFacSlew_UlspS_f32	196
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2332.93994
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.144999996
k_MtrCtrlVirualResQax_Ohm_f32	0.155000001
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1
k MtrVoltDaxIntegHiLim Volt f32	5.5145998
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962 4
	-9.64999962 1 6.05779982





Name	Input Value		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-3.26600003		
k_VoltSatQaxPolyCoeff_Uls_f32	0		
k_deadtimeVScale_Uls_f32	0.963		
t_CommOffsetTblX_Uls_u3p13[0]	4611		
t_CommOffsetTblX_Uls_u3p13[1]	5579		
t_CommOffsetTblY_Cnt_u16[0]	163		
t_CommOffsetTblY_Cnt_u16[1]	1236		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1357		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1357	1357	~
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	18.8889999	18.8889999 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-2.4071095	-2.4071095 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.17013788	-4.17013788 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	3723	3723 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	✓
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.177499995	0.177499995 ± 0.0625	✓

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	-
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	_

Test Step 2.158 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.079999982
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.49000001
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.741999984
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-517.109009
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-593.112976
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998

2016-09-15, 18:23:31+0530





Name	Input Value		
MtrCtrl_MtrlmpedDax_Ohm_M_f32[1]	0.125		
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.15900004		
MtrCtrl MtrQaxIntegralGain Ohm M f32[1]	1.60500002		
MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	323.631989		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-319.569		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001		
	-13.6160002		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]			
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003		
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981		
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995		
MtrCurrDaxPrevIntg_Volt_M_f32	7.60099983		
MtrCurrDaxRef_Amp_M_f32[0]	-139.906998		
MtrCurrDaxRef_Amp_M_f32[1]	115.814003		
MtrCurrQaxCog_Amp_M_f32	59.3040009		
MtrCurrQaxPrevIntg_Volt_M_f32	20.2117004		
MtrCurrQaxRef_Amp_M_f32[0]	-65.1900024		
MtrCurrQaxRef_Amp_M_f32[1]	-216.972		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay Rad M f32[0]	4.70800018		
MtrPosComputationDelay_Rad_M_f32[1]	-2.68499994		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.662		
PICurrCntrl DualEcuFailSclFac Uls M f32	0.153999999		
PICurrCntrl InverterFailSclFac Uls M f32	0.481000006		
	0.189799994		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32			
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.686999977		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-38.7999992		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	97.3968964		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.662699997		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-38.7999992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	97.3968964		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.662699997		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7083.27002		
k_DualEcuSignalSclFacSlew_UlspS_f32	197.199997		
k ILOAFdbackSignalSclFacSlew UlspS f32	947.890015		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.023		
k_MtrCtrlVirualResQax_Ohm_f32	0.0179999992		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0.0173333332		
k_MtrCurrQaxRefModifRpIEn_Cnt_lgc	1 40 000005		
k_MtrVoltDaxIntegHiLim_Volt_f32	19.8029995		
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	29.0914993		
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	10.1960001		
k_VoltSatQaxPolyCoeff_Uls_f32	-19.0979996		
k_deadtimeVScale_Uls_f32	0.987999976		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	1081		
t CommOffsetTblY Cnt u16[1]	1779		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_var	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	903		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1779	1779	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	64749	64749 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-3.74271941	-3.74271894 ± 4.88E-04	*
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-6.37356281	-6.37356234 ± 4.88E-04	~
MtrCatal Write Phase Advence Final Pay (On 16 (val)	10201	10201 + 1 525005 05	

10301

10301 ± 1.52588E-05





Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	-22.4099998	-22.4099998	~
PICurrCntrl DualEcuFailSclFac Uls M f32	0.129350007	0.129350007 ± 0.0625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Name	Input Value
	Input value
FastDataAccessBufIndex_Cnt_M_u16	
/trCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
/trCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl_Read_ModidxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
/trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
ftrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	31.5869999
htrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-186.395996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0340000018
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.104999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.115999997
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.47399998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.90199995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-146.214005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-942.195007
htrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
/trCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.123000003
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.49000001
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.959999979
htrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-53.862999
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	75.7020035
NtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-30.2169991
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	19.2049999
htrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-27.0669994
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	28.1070004
ftrCtrl_Vecu_Volt_M_f32[0]	18.9510002
/trCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	-18.5370007
/trCurrDaxRef_Amp_M_f32[0]	-146.723007
/trCurrDaxRef_Amp_M_f32[1]	-121.943001
1trCurrQaxCog_Amp_M_f32	79.6729965
ItrCurrQaxPrevIntg_Volt_M_f32	11.4363003
ItrCurrQaxRef_Amp_M_f32[0]	-146.173996
ItrCurrQaxRef_Amp_M_f32[1]	-213.335007
ItrCurrQaxRpl Amp M f32	0
/trPosComputationDelay_Rad_M_f32[0]	1.52600002
/trPosComputationDelay Rad M f32[1]	-2.68400002
PlCurrCntrl CurrSensFailSclFac Uls M f32	0.40000006
PICurrCntrl DualEcuFailSclFac Uls M f32	0.155000001

t_CommOffsetTblX_Uls_u3p13[1]

t_CommOffsetTblY_Cnt_u16[0]

t CommOffsetTblY Cnt u16[1]

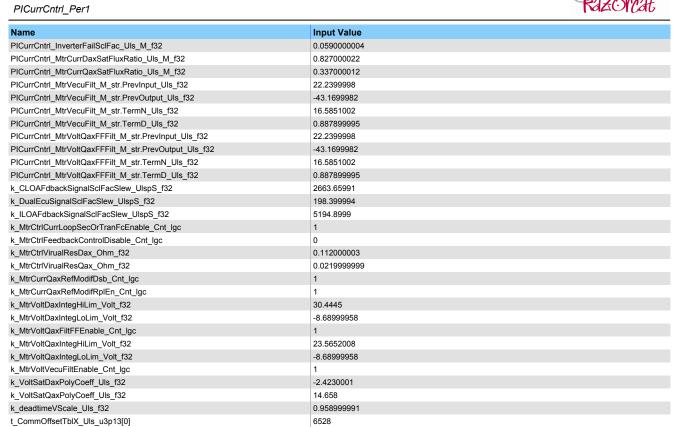
 $target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr$

 $target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val$ $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val

target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr

2016-09-15, 18:23:31+0530





target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	65		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-9.31999969		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	65	65	~
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	2.70515442	2.70515442 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	3.95906138	3.95906138 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	43793	43793 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.179800004	0.179800004 ± 0.0625	✓

-9.31999969

8192

76

211

0

1

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~



Test Step 2.160 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.142000005
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.272000015
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	243.257004
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-253.089996
MtrCtrl_MtrlmpedDax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrlmpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.713999987
MtrCtrl MtrQaxIntegralGain Ohm M f32[1]	0.870999992
MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	49.862999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-366,458008
MtrCtrl MtrVoltDaxFF Volt M f32[0]	-14.6940002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-25.6930008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-7.6669982
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	2.61400008
MtrCtrl_Vecu_Volt_M_f32[0]	17.7010002
MtrCtrl_Vecu_Volt_M_f32[1]	20.0610008
MtrCurrDaxPrevIntg_Volt_M_f32	-1.39499998
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	-144.667007
MtrCurrQaxPrevIntg_Volt_M_f32	30.3402004
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef Amp M f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	-2.15700006
MtrPosComputationDelay_Rad_M_f32[1]	4.67700005
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.231999993
PICurrCntrl DualEcuFailSclFac Uls M f32	0.156000003
PICurrCntrl InverterFailSclFac UIs M f32	0.657000005
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.499500006
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.725000024
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	570.700012
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	-657.099976
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	78.8641968
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.388500005
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput UIs f32	570.700012
PICurrCntrl MtrVoltQaxFFFiit M str.PrevOutput Uls f32	-657.099976
PICurrCntrl_mtrVoltQaxFFFilt_M_str.PrevOutput_Uis_132 PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	78.8641968
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_132 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_132	0.388500005
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6145.56982
k DualEcuSignalSclFacSlew UlspS f32	200
k ILOAFdbackSignalSclFacSlew UlspS f32	4019.20996
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc	1
	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	
k_MtrCtrlVirualResDax_Ohm_f32	0.193000004
k_MtrCtrlVirualResQax_Ohm_f32	0.114
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1
k_MtrVoltDaxIntegHiLim_Volt_f32	3.67009997
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0 15.1749001
k_MtrVoltQaxIntegHiLim_Volt_f32	

PICurrCntrl_DualEcuFailSclFac_Uls_M_f32

PICurrCntrl_Per1

2016-09-15, 18:23:31+0530



0.130999997 ± 0.0625

Name	Input Value		
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	18.2779999		
k_VoltSatQaxPolyCoeff_Uls_f32	20.7369995		
k_deadtimeVScale_Uls_f32	0.996999979		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	771		
t_CommOffsetTblY_Cnt_u16[1]	1636		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	50.0610008		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	0		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1636	1636	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	65339	65339 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-19.8980999	-19.8980999 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	2.02442837	2.02442813 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	33456	33456 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	-4.57000017	-4.57000017	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	~

0.130999997

Test Step 2.161 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-213.026993
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-66.7229996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0769999996
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0359999985
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.075000003
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.51999998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.203999996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-569.184021
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-867
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.115999997
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.115999997

2016-09-15, 18:23:31+0530





		•	10.10
Name	Input Value		
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0970000029		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0270000007		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.116999999		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.852999985		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	442.492004		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-332.345001		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-28.2420006		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	12.6160002		
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981		
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995		
MtrCurrDaxPrevIntg_Volt_M_f32	-27.6930008		
MtrCurrDaxRef_Amp_M_f32[0]	-133.947006		
MtrCurrDaxRef_Amp_M_f32[1]	75.7020035		
MtrCurrQaxCog_Amp_M_f32	5.72399998		
MtrCurrQaxPrevIntg_Volt_M_f32	26.4762993		
MtrCurrQaxRef_Amp_M_f32[0]	106.072998		
MtrCurrQaxRef_Amp_M_f32[1]	-112.455002		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	2.36999989		
MtrPosComputationDelay Rad M f32[1]	2.67000008		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.911000013		
PICurrCntrl DualEcuFailSclFac Uls M f32	0.157000005		
PICurrCntrl InverterFailScIFac UIs M f32	0.513000011		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.1329		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.565999985		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	570.700012		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	43.3250008		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.744499981		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	0		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	570.700012		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	43.3250008		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.744499981		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6616.02002		
k_DualEcuSignalSclFacSlew_UlspS_f32	300		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5777.70996		
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt Igc	1		
	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc k MtrCtrlVirualResDax Ohm f32	0.191		
-	0.0529999994		
k_MtrCtrlVirualResQax_Ohm_f32 k MtrCurrQaxRefModifDsb Cnt lgc	1		
	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc			
k_MtrVoltDaxIntegHiLim_Volt_f32	21.1630993		
k_MtrVoltDaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	26.3924999		
k_MtrVoltQaxIntegLoLim_Volt_f32	-25.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-20.3560009		
k_VoltSatQaxPolyCoeff_Uls_f32	-1.46500003		
k_deadtimeVScale_Uls_f32	0.971000016		
t_CommOffsetTblX_Uls_u3p13[0]	5022		
t_CommOffsetTbIX_UIs_u3p13[1]	7003		
t_CommOffsetTblY_Cnt_u16[0]	365		
t_CommOffsetTblY_Cnt_u16[1]	1530		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	5000		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Resu
MtrCntrl_Write_CommOffset_Cnt_u16(val)	5000	5000	•
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	,
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	100.348999	100.348999 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	19.3640652	19.3640671 ± 4.88E-04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	23.0456734	23.0456753 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	32009	32009 ± 1.52588E-05	
MtrCurrDaxPrevIntg Volt M f32	0	0	

MtrCurrDaxPrevIntg_Volt_M_f32





Name	Actual Value	Expected Value	Result
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.194499999	0.194499999 ± 0.0625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Name	Input Value	
	Input Value	
FastDataAccessBufIndex_Cnt_M_u16		
/trCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	
/trCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	
/trCntrl_Read_ModidxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
/trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996	
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.085007	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.079999982	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00899999961	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0799999982	
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.74399996	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.96099997	
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	756.674988	
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	76.4720001	
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998	
/trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125	
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994	
/trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0939999968	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.591000021	
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.175999999	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	730.219971	
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-1004.94	
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001	
/trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6160002	
/trCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005	
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1870003	
//trCtrl_Vecu_Volt_M_f32[0]	18.9510002	
//trCtrl_Vecu_Volt_M_f32[1]	21.3110008	
/trCurrDaxPrevIntg_Volt_M_f32	20.066	
/trCurrDaxRef_Amp_M_f32[0]	209.052002	
/trCurrDaxRef_Amp_M_f32[1]	-124.994003	
/trCurrQaxCog_Amp_M_f32	59.3040009	
/trCurrQaxPrevIntg_Volt_M_f32	17.6383991	
/trCurrQaxRef_Amp_M_f32[0]	24.6130009	
/trCurrQaxRef_Amp_M_f32[1]	-20.9400005	
/trCurrQaxRpl_Amp_M_f32	0	
MtrPosComputationDelay_Rad_M_f32[0]	2.68400002	
MtrPosComputationDelay_Rad_M_f32[1]	5.81400013	
PICurrCntrl CurrSensFailSclFac Uls M f32	0.702000022	

2016-09-15, 18:23:31+0530



PICurrCntrl Per1 Input Value PICurrCntrl_DualEcuFailSclFac_Uls_M_f32 0.158000007 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.00400000019 PICurrCntrl MtrCurrDaxSatFluxRatio_Uls_M_f32 0.756799996 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.833000004 PICurrCntrl MtrVecuFilt_M_str.PrevInput_Uls_f32 0 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 0 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 44.6861992 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.246199995 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32$ 0 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 44.6861992 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32$ 0.246199995 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 5911.31982 k_DualEcuSignalSclFacSlew_UlspS_f32 400 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 2332.93994 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 $k_MtrCtrlFeedbackControlDisable_Cnt_lgc$ k_MtrCtrlVirualResDax_Ohm_f32 0.144999996 k_MtrCtrlVirualResQax_Ohm_f32 0.155000001 k_MtrCurrQaxRefModifDsb_Cnt_lgc $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ 1.42719996 k_MtrVoltDaxIntegHiLim_Volt_f32 $k_MtrVoltDaxIntegLoLim_Volt_f32$ -10.5 k_MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 4.96659994 k_MtrVoltQaxIntegLoLim_Volt_f32 -10.5 k_MtrVoltVecuFiltEnable_Cnt_lgc -3.26600003 $k_VoltSatDaxPolyCoeff_Uls_f32$ k VoltSatQaxPolyCoeff Uls f32 20.9540005 k_deadtimeVScale_Uls_f32 0.963 t CommOffsetTblX Uls u3p13[0] 4611 t_CommOffsetTblX_Uls_u3p13[1] 5579 t CommOffsetTblY Cnt u16[0] 163 t_CommOffsetTblY_Cnt_u16[1] 1236 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr $target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr$ 0 $target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val$ $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -207.917999 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 841 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.7319984

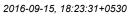
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	841	841	~
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-34.6910019	-34.6910019 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-0.189992487	-0.189992487 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.81125021	4.81125021 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	27583	27583 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.10800001	0.10800001 ± 0.0625	✓



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	•	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	•	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•	
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~	
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•	

Test Step 2.163 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
/trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.721001
htrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0390000008
/trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0170000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.75899994
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.45799994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-948.984009
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-975.934021
ftrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994
ftrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994
ftrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0280000009
htrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.261000007
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.754000008
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-227.466003
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-453.338013
ItrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55099964
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-25.3770008
htrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
/trCtrl_Vecu_Volt_M_f32[1]	21.3110008
1trCurrDaxPrevIntg_Volt_M_f32	-27.3339996
/trCurrDaxRef_Amp_M_f32[0]	31.5869999
/trCurrDaxRef_Amp_M_f32[1]	-186.395996
/trCurrQaxCog_Amp_M_f32	-144.667007
//trCurrQaxPrevIntg_Volt_M_f32	23.1366997
ItrCurrQaxRef_Amp_M_f32[0]	171.485992
ItrCurrQaxRef_Amp_M_f32[1]	163.787003
ItrCurrQaxRpl_Amp_M_f32	0
htrPosComputationDelay_Rad_M_f32[0]	-2.94899988
ItrPosComputationDelay Rad M f32[1]	0.0060000005
PlCurrCntrl CurrSensFailSclFac Uls M f32	0.702000022
PICurrCntrl DualEcuFailSclFac Uls M f32	0.158999994
PICurrCntrl InverterFailSclFac UIs M f32	0.0040000019

PICurrCntrl_Per1





Name	Input Value		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.155699998		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.833000004		
PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	29.8064003		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.197799996		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput UIs f32	-43.1699982		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32	0		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	29.8064003		
PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.197799996		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	5911.31982		
k DualEcuSignalSclFacSlew UlspS f32	500		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2332.93994		
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k MtrCtrlVirualResDax Ohm f32	0.144999996		
k MtrCtrlVirualResQax Ohm f32	0.155000001		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k MtrCurrQaxRefModifRplEn Cnt lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	27.6096992		
k MtrVoltDaxIntegLoLim Volt f32	-11.6000004		
k MtrVoltQaxFiltFFEnable Cnt lgc	0		
	20.0156002		
k_MtrVoltQaxIntegHiLim_Volt_f32			
k_MtrVoltQaxIntegLoLim_Volt_f32	-11.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-3.26600003		
k_VoltSatQaxPolyCoeff_Uls_f32	-12.2449999		
k_deadtimeVScale_UIs_f32	0.963		
t_CommOffsetTblX_Uls_u3p13[0]	4611		
t_CommOffsetTblX_Uls_u3p13[1]	5579		
t_CommOffsetTblY_Cnt_u16[0]	163		
t_CommOffsetTblY_Cnt_u16[1]	1236		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4296		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4296	4296	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-2.60241628	-2.60241628 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.05112982	-4.05112982 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	7965	7965 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	✓
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.221499994	0.221499994 ± 0.0625	✓

Test Step Call Trace ✓					
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~	
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~	



Test Step 2.164 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr) MtrCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_ModidxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(var)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-200.556
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4449997
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.36399996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.949
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-232.371994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-761.935974
MtrCtrl_MtrImpedDax_Ohm_M_f32[0] MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0850000009 0.112999998
MtrCtrl MtrImpedQax Ohm M f32[0]	0.0850000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.65799999
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.30399999
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-174.839996
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	903.403015
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-28.4209995
MtrCtrl_Vecu_Volt_M_f32[0]	24.8479996
MtrCtrl_Vecu_Volt_M_f32[1]	27.2080002
MtrCurrDaxPrevIntg_Volt_M_f32	-9.66300011
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrOavCoa Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32 MtrCurrQaxPrevIntg_Volt_M_f32	-42.6814995 2.99090004
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay Rad M f32[0]	2.33500004
MtrPosComputationDelay_Rad_M_f32[1]	-0.331
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.911000013
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.159999996
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.513000011
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.195500001
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	90.7209015
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.617500007
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Ois_r32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	90.7209015
PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.617500007
k CLOAFdbackSignalSclFacSlew UlspS f32	6616.02002
k_DualEcuSignalSclFacSlew_UlspS_f32	600
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5777.70996
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.191
k_MtrCtrlVirualResQax_Ohm_f32	0.0529999994
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1
k_MtrVoltDaxIntegHiLim_Volt_f32	14.4471998
k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	13.4596996
k_MtrVoltQaxIntegLoLim_Volt_f32	-30.2000008

PICurrCntrl_Per1



Name	Input Value		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-20.3560009		
k_VoltSatQaxPolyCoeff_Uls_f32	-19.9090004		
k_deadtimeVScale_Uls_f32	0.971000016		
t_CommOffsetTblX_Uls_u3p13[0]	5022		
t_CommOffsetTblX_Uls_u3p13[1]	7003		
t_CommOffsetTblY_Cnt_u16[0]	365		
t_CommOffsetTblY_Cnt_u16[1]	1530		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	674		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	674	674	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-91.2655029	-91.2655029 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	4.29176283	4.29176283 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	2.26975727	2.26975703 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	35665	35665 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0849999934	0.0849999934 ± 0.0625	~

Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	-
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	~

Test Step 2.165 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-200.556
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4449997
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.344999999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.98000002
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-475.019012
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-121.873001
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009





Name	Input Value		
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998		
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0850000009		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.112999998		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.633000016		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.5		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-892.642029		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-451.738007		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-28.4209995		
MtrCtrl_Vecu_Volt_M_f32[0]	25.3600006		
	27.7199993		
MtrCtrl_Vecu_Volt_M_f32[1]	-9.66300011		
MtrCurrDavPrevIntg_Volt_M_f32	-146.723007		
MtrCurrDavRef_Amp_M_f32[0]			
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001		
MtrCurrQaxCog_Amp_M_f32	-37.3235016		
MtrCurrQaxPrevIntg_Volt_M_f32	2.34949994		
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006		
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	-0.532000005		
MtrPosComputationDelay_Rad_M_f32[1]	3.19000006		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.911000013		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.160999998		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.513000011		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.211199999		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-717.299988		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	6.4671998		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.0860000029		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-717.299988		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	6.4671998		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0860000029		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6616.02002		
k_DualEcuSignalSclFacSlew_UlspS_f32	700		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5777.70996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.191		
k MtrCtrlVirualResQax Ohm f32	0.0529999994		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc			
k_MtrVoltDaxIntegHiLim_Volt_f32	21.2147999		
k_MtrVoltDaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	7.0927		
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-20.3560009		
k_VoltSatQaxPolyCoeff_Uls_f32	22.7819996		
k_deadtimeVScale_UIs_f32	0.971000016		
t_CommOffsetTblX_Uls_u3p13[0]	5022		
t_CommOffsetTbIX_UIs_u3p13[1]	7003		
t_CommOffsetTblY_Cnt_u16[0]	365		
t_CommOffsetTblY_Cnt_u16[1]	1530		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4608		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Resu
Hallo	Actual value	·	Resu
	4600	4608	
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4608		
MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	
MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_Modldx_Uls_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	0 -96.6235046	0 ± 1 -96.6235046 ± 7.81E-03	•
MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_Modldx_Uls_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0 -96.6235046 16.4143658	0 ± 1 -96.6235046 ± 7.81E-03 16.4143677 ± 4.88E-04	•
MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_Modldx_Uls_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	0 -96.6235046	0 ± 1 -96.6235046 ± 7.81E-03	





Name	Actual Value	Expected Value	Result
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.248500004	0.248500004 ± 0.0625	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•	
MtrCntrl_Write_Modldx_Uls_u16p16	1	MtrCntrl_Write_Modldx_Uls_u16p16	1	~	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~	
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~	

Input Value
1
target MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
target MtrCntrl Read lvtrLoaMtgtnEn Cnt Igc ptr
target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val
target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr
target MtrCntrl Read MtrCurrDax Amp f32 Val
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
target MtrCntrl Read MtrCurrQax Amp f32 Val
target MtrCntrl Read SysState Cnt Enum Val
-212.632996
-205.085007
0.079999982
0.00899999961
0.0099999978
0.079999982
0.409000009
1.16600001
967.463013
-285.221985
0.112999998
0.125
0.0529999994
0.093999968
0.305999994
0.332999995
-121.924004
1 1
483.27301
-0.736000001
-13.6160002
18.6380005
-23.1870003
17.7010002
20.0610008
-9.05200005
31.5869999
-186.395996
-31.9654999
0.29429999
171.485992
163.787003
0
-2.94899988
0.00600000005 0.231999993





Name	Input Value		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.162		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.657000005		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.1822		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.833000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-717.299988		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	57.8652992		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.414700001		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-717.299988		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	57.8652992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.414700001		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6145.56982		
k_DualEcuSignalSclFacSlew_UlspS_f32	800		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4019.20996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.193000004		
k_MtrCtrlVirualResQax_Ohm_f32	0.114		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	20.6170006		
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	29.5634995		
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	18.2779999		
k_VoltSatQaxPolyCoeff_Uls_f32	19.2689991		
k_deadtimeVScale_Uls_f32	0.996999979		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	771		
t CommOffsetTblY Cnt u16[1]	1636		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	0		
target MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc ptr	1		
target MtrCntrl Read MtrCurrDax Amp f32 Val	50.0610008		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2125		
target MtrCntrl Read MtrCurrQax Amp f32 Val	136.341003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1636	1636	
MtrCntrl Write ModIdx Uls u16p16(val)	65339	65339 ± 1	
MtrCntrl Write MtrCurrQaxFinalRef Amp f32(val)	195.752502	195.752502 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-2.52427006	-2.52427006 ± 4.88E-04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.29863739	-4.29863739 ± 4.88E-04	
MtrCntrl Write PhaseAdvanceFinal Rev u0p16(val)	38369	38369 ± 1.52588E-05	
MtrCurrDaxPrevIntg_Volt_M_f32	-22.4099998	-22.4099998	
PICurrCntrl DualEcuFailSclFac Uls M f32	0.061999999	0.061999999 ± 0.0625	

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	-	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~	
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~	



Test Step 2.167 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val) MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
MtrCtrl MtrCurrDaxMaxVal Amp M f32[1]	119.722
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.039999991
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0989999995
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0179999992
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.75899994
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.45899999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-948.984009
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-975.934998
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.041999994
MtrCtrl_MtrlmpedDax_Ohm_M_f32[1]	0.0289999992
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.041999994
MtrCtrl_MtrImpedQax_Ohm_M_f32[1] MtrCtrl MtrQaxIntegralGain Ohm M f32[0]	0.0289999992
MtrCtrl_MtrQaxIntegralGain_Onm_M_132[u] MtrCtrl MtrQaxIntegralGain Ohm M f32[1]	0.261000007 0.754999995
MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	-227.466003
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-453.338989
MtrCtrl MtrVoltDaxFF Volt M f32[0]	-16.302
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.55200005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	21.3889999
MtrCtrl_Vecu_Volt_M_f32[0]	21.3729992
MtrCtrl_Vecu_Volt_M_f32[1]	23.7329998
MtrCurrDaxPrevIntg_Volt_M_f32	-0.216499999
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999
MtrCurrDaxRef_Amp_M_f32[1]	-186.397003
MtrCurrQaxCog_Amp_M_f32	-26.6075001
MtrCurrQaxPrevIntg_Volt_M_f32	24.4969997
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRpl_Amp_M_f32	163.787994 0
MtrPosComputationDelay_Rad_M_f32[0]	-2.94899988
MtrPosComputationDelay_Rad_M_f32[1]	0.00700000022
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.851999998
PICurrCntrl DualEcuFailSclFac Uls M f32	0.163000003
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.513000011
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.953199983
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.778999984
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	386.220001
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	96.5500031
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	69.4054031
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.3134
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	865.320007
PICurrCotrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	22.2399998
PICurrCotrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	76.1873016 0.821500003
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 k_CLOAFdbackSignalSclFacSlew_UlspS_f32	0.821500003 6616.02002
k DualEcuSignalSclFacSlew UlspS f32	900
k ILOAFdbackSignalSclFacSlew_UlspS_f32	3865,98999
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc	1
k MtrCtrlFeedbackControlDisable Cnt Igc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.140000001
k_MtrCtrlVirualResQax_Ohm_f32	0.0769999996
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1
k_MtrVoltDaxIntegHiLim_Volt_f32	20.3593998
k_MtrVoltDaxIntegLoLim_Volt_f32	-8.68999958
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	11.2693005

PICurrCntrl_DualEcuFailSclFac_Uls_M_f32

PICurrCntrl_Per1

2016-09-15, 18:23:31+0530



Input Value k_MtrVoltQaxIntegLoLim_Volt_f32 -31 $k_MtrVoltVecuFiltEnable_Cnt_lgc$ k_VoltSatDaxPolyCoeff_Uls_f32 18.2779999 k_VoltSatQaxPolyCoeff_Uls_f32 -12.2449999 k_deadtimeVScale_Uls_f32 0.996999979 t_CommOffsetTblX_Uls_u3p13[0] 4611 t_CommOffsetTblX_Uls_u3p13[1] 5579 t_CommOffsetTblY_Cnt_u16[0] 23 t_CommOffsetTblY_Cnt_u16[1] 212 $target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr$ n target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 59.3040009 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 665 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val -34.6189995 $target_MtrCntrl_Read_SysState_Cnt_Enum_Val$ 0 Name Actual Value **Expected Value** Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 665 665 MtrCntrl_Write_ModIdx_Uls_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 198.093491 198.093491 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 13.3372936 ± 4.88E-04 13.3372936 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -27.8811626 -27.8811665 ± 4.88E-04 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 62891 62891 ± 1.52588E-05 MtrCurrDaxPrevIntg_Volt_M_f32

Fest Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	-
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	-
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

0.2755

0.2755 ± 0.0625

Test Step 2.168 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-200.556
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4449997
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.36399996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.95000005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-232.371994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-761.937012





Name	Input Value		
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009		
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998		
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0850000009		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.112999998		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.65799999		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.305000007		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-174.839996		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	903.403992		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003		
MtrCtrl MtrVoltQaxFF Volt M f32[1]	-28.4209995		
MtrCtrl Vecu Volt M f32[0]	5.12099981		
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995		
MtrCurrDaxPrevIntg_Volt_M_f32	5.26809978		
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007		
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001		
MtrCurrQaxCog_Amp_M_f32	-21.2495003		
MtrCurrQaxPrevIntg_Volt_M_f32	15.5079002		
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006		
MtrCurrQaxRef Amp M f32[1]	75.7020035		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay Rad M f32[0]	2.33500004		
MtrPosComputationDelay_Rad_M_f32[1]	-0.331999987		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.231999993		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.164000005		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.00400000019		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.486799985		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.76819998		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-784.130005		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	87.5784988		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.889199972		
	267.119995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32			
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	267.119995		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	99.3730011		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.472600013		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	5911.31982		
k_DualEcuSignalSclFacSlew_UlspS_f32	1000		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2156.63989		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k MtrCtrlVirualResDax Ohm f32	0.0209999997		
k MtrCtrlVirualResQax Ohm f32	0.172999993		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k MtrCurrQaxRefModifRplEn Cnt Igc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	0.465799987		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	13.9652004		
k_MtrVoltQaxIntegLoLim_Volt_f32	0		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-2.4230001		
k_VoltSatQaxPolyCoeff_Uls_f32	-19.9090004		
k_deadtimeVScale_Uls_f32	0.958999991		
t_CommOffsetTbIX_UIs_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	1237		
t_CommOffsetTblY_Cnt_u16[1]	383		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	50.0610008		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1724		
	-118.848		
target MtrCntrl Read MtrCurrOay Amp f32 Val			
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val			
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2	Formando d Welling	D
target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name	2 Actual Value	Expected Value	Result
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2	Expected Value 383	Result
target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name	2 Actual Value	· ·	Result
target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name MtrCntrl_Write_CommOffset_Cnt_u16(val)	2 Actual Value 383	383	Result
target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_ModIdx_UIs_u16p16(val)	2 Actual Value 383 62849	383 62849 ± 1	~





Name	Actual Value	Expected Value	Result
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	22576	22576 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	-4.57000017	-4.57000017	~
PICurrCntrl DualEcuFailSclFac Uls M f32	0.0390000045	0.0390000045 ± 0.0625	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.169 (Repeat Count = 1)	Innut Value
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
/trCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
/trCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
htrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	67.4899979
htrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.723
htrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0109999999
htrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0410000011
htrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995
htrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0189999994
MtrCtrl MtrDaxIntegralGain Ohm M f32[0]	1.75899994
htrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.46000004
trCtrl MtrDaxPropotionalGain Ohm M f32[0]	-948.984009
ItrCtrl MtrDaxPropotionalGain Ohm M f32[1]	-975.935974
ItrCtrl MtrImpedDax Ohm M f32[0]	0.0419999994
htrCtrl MtrImpedDax Ohm M f32[1]	0.029999993
htrCtrl MtrImpedQax Ohm M f32[0]	0.0419999994
htrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.029999993
htrCtrl MtrQaxIntegralGain Ohm M f32[0]	0.261000007
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.755999982
trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-227.466003
htrCtrl MtrQaxPropotionalGain Ohm M f32[1]	-453.339996
htrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302
htrCtrl MtrVoltDaxFF Volt M f32[1]	8.55300045
htrCtrl MtrVoltQaxFF_Volt_M_132[0]	-25.3770008
	21.3899994
htrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	5.12099981
htrCtrl_Vecu_Volt_M_f32[0]	7.48099995
htrCtrl_Vecu_Volt_M_f32[1]	
htrCurrDaxPrevIntg_Volt_M_f32	21.7219009
/trCurrDaxRef_Amp_M_f32[0]	31.5869999
ItrCurrDaxRef_Amp_M_f32[1]	-186.397995
htrCurrQaxCog_Amp_M_f32	-5.17549992
MtrCurrQaxPrevIntg_Volt_M_f32	7.74660015
/trCurrQaxRef_Amp_M_f32[0]	171.485992
/ltrCurrQaxRef_Amp_M_f32[1]	163.789001
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	-2.94899988
/trPosComputationDelay_Rad_M_f32[1]	0.00800000038

PICurrCntrl Per1

2016-09-15, 18:23:31+0530



Input Value PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.723999977 PICurrCntrl_DualEcuFailSclFac_Uls_M_f32 0.165000007 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.0450000018 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 0.807699978 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.735800028 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32 -340.130005 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -717.299988 $PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32$ 52.7086983 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.75029999 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32$ 1118 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 1118 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 16 5851002 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.0452999994 6110 83008 k CLOAFdbackSignalSclFacSlew UlspS f32 k_DualEcuSignalSclFacSlew_UlspS_f32 1100 7823.27002 k ILOAFdbackSignalSclFacSlew UlspS f32 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 1 k_MtrCtrlFeedbackControlDisable_Cnt_lgc k_MtrCtrlVirualResDax_Ohm_f32 0.0909999982 k_MtrCtrlVirualResQax_Ohm_f32 0.155000001 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc $k_MtrVoltDaxIntegHiLim_Volt_f32$ 11.4057999 k_MtrVoltDaxIntegLoLim_Volt_f32 -11.6000004 k MtrVoltQaxFiltFFEnable_Cnt_lgc k_MtrVoltQaxIntegHiLim_Volt_f32 28.1163998 k_MtrVoltQaxIntegLoLim_Volt_f32 11.6000004 k_MtrVoltVecuFiltEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 -2.4230001 k_VoltSatQaxPolyCoeff_Uls_f32 18.1280003 k deadtimeVScale Uls f32 0.958999991 t_CommOffsetTblX_Uls_u3p13[0] 459 t CommOffsetTblX Uls u3p13[1] 5775 t_CommOffsetTblY_Cnt_u16[0] 771 t_CommOffsetTblY_Cnt_u16[1] 1636 $target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr$ 0 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 $target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val$ 0 $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ target MtrCntrl Read MtrCurrDax Amp f32 Val 136.341003 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 1789 target MtrCntrl Read MtrCurrQax Amp f32 Val -34.6189995 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 2 Name **Actual Value Expected Value** Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1636 1636 62849 62849 ± 1 MtrCntrl_Write_ModIdx_Uls_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 168.964508 168.964508 ± 7.81E-03 2 6636548 2 6636548 + 4 88F-04 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 6.66147232 6.66147232 ± 4.88E-04 4051 + 1 52588F-05 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 4051 MtrCurrDaxPrevIntg_Volt_M_f32 -11.6000004 -11.6000004

Test Step Call Trace	Fest Step Call Trace				
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	-	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	-	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~	
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	•	

0.30250001

0.30250001 ± 0.0625

PICurrCntrl_DualEcuFailSclFac_Uls_M_f32



Test Step 2.170 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-200.556
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4449997
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.36399996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.95099998
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-232.371994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-761.937988
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.65799999
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.305999994
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-174.839996
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	903.405029
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-28.4209995
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008
MtrCurrDaxPrevIntg_Volt_M_f32	27.2064991
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	0.182500005
MtrCurrQaxPrevIntg_Volt_M_f32	14.6610003
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	2.33500004
MtrPosComputationDelay_Rad_M_f32[1]	-0.333000004
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.848999977
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.165999994
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.398999989
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.203600004
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.725000024
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-784.130005
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1118
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	87.7649002
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.640799999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-627.179993
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-627.179993
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	87.3075027
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.966000021
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6145.56982
k_DualEcuSignalSclFacSlew_UlspS_f32	1200
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	6489.7002
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0.193000004
k_MtrCtrlVirualResQax_Ohm_f32	0.0179999992
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1
k_MtrVoltDaxIntegHiLim_Volt_f32	30.3612995
k_MtrVoltDaxIntegLoLim_Volt_f32	-30.2000008
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	29.5142002

2016-09-15, 18:23:31+0530



PICurrCntrl_Per1

N	Invest Walne		
Name	Input Value		
k_MtrVoltQaxIntegLoLim_Volt_f32	-14.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	18.2779999		
k_VoltSatQaxPolyCoeff_Uls_f32	7.87699986		
k_deadtimeVScale_Uls_f32	0.996999979		
t_CommOffsetTblX_Uls_u3p13[0]	6528		
t_CommOffsetTblX_Uls_u3p13[1]	8192		
t_CommOffsetTblY_Cnt_u16[0]	76		
t_CommOffsetTblY_Cnt_u16[1]	211		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	65		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	65	65	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	75.5195007	75.5195007 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	18.5843525	18.5843525 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-24.6954308	-24.6954308 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	22566	22566 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0159999877	0.0159999877 ± 0.0625	~

Test Step Call Trace	est Step Call Trace			
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	-
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.171 (Repeat Count = 1)	v v
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-200.556
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4449997
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.344999999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.10000002
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-475.019012
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-121.875





Name	Input Value		
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009		
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998		
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0850000009		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.112999998		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.633000016		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.70000005		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-892.642029		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-451.73999		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008		
MtrCtrl MtrVoltDaxFF Volt M f32[1]	21.3880005		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003		
MtrCtrl MtrVoltQaxFF Volt M f32[1]	-28.4209995		
MtrCtrl Vecu Volt M f32[0]	18.5559998		
MtrCtrl_Vecu_Volt_M_f32[1]	20.9160004		
MtrCurrDaxPrevIntg_Volt_M_f32	-18.5370007		
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007		
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001		
MtrCurrQaxCog_Amp_M_f32	5.54050016		
MtrCurrQaxPrevIntg_Volt_M_f32	12.7981997		
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006		
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay Rad M f32[0]	-0.532000005		
MtrPosComputationDelay_Rad_M_f32[1]	3.21000004		
PICurrCntrl CurrSensFailSclFac Uls M f32	0.231999993		
	0.231999993		
PICurrCntrl_DualEcuFailSclFac_UIs_M_f32			
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.111000001		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.398900002		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.71420002		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	386.220001		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-657.130005		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	58.6543999		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.052099999		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	-1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	97.3968964		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32	0.830900013		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2663.65991		
k_DualEcuSignalSclFacSlew_UlspS_f32	1300		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4019.20996		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.112000003		
k_MtrCtrlVirualResQax_Ohm_f32	0.0219999999		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k MtrCurrQaxRefModifRplEn Cnt lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	22.8146992		
k MtrVoltDaxIntegLoLim Volt f32	-9.64999962		
k MtrVoltQaxFiltFFEnable Cnt lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	0		
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-20.3560009		
k_VoltSatQaxPolyCoeff_Uls_f32	6.82399988		
k_deadtimeVScale_Uls_f32	0.971000016		
t_CommOffsetTblX_Uls_u3p13[0]	4611		
t_CommOffsetTblX_Uls_u3p13[1]	5579		
t_CommOffsetTblY_Cnt_u16[0]	163		
t CommOffsetTblY Cnt u16[1]	1236		
target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val			
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1357		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-126.640999		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1357	1357	-
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-139.487503 -17.8397541	-139.487503 ± 7.81E-03 -17.8397541 ± 4.88E-04	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)			
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-2.52724576	-2.52724576 ± 4.88E-04	





Name	Actual Value	Expected Value	Result
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	42135	42135 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl DualEcuFailSclFac Uls M f32	0.32950002	0.32950002 ± 0.0625	✓

est Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 2.172 (Repeat Count = 1)	Innut Value
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
/trCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
/trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.087006
/trCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0109999999
/trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978
/trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.10000001
MtrCtrl MtrDaxIntegralGain Ohm M f32[0]	0.409000009
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.16799998
/trCtrl MtrDaxPropotionalGain Ohm M f32[0]	967.463013
trCtrl MtrDaxPropotionalGain Ohm M f32[1]	-285,223999
htrCtrl MtrImpedDax Ohm M f32[0]	0.112999998
/trCtrl MtrImpedDax Ohm M f32[1]	0.127000004
/trCtrl MtrImpedQax Ohm M f32[0]	0.0529999994
/trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.096000008
/trCtrl MtrQaxIntegralGain Ohm M f32[0]	0.305999994
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.234999999
/trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-121.924004
/trCtrl MtrQaxPropotionalGain_Ohm M f32[1]	483.274994
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
htrCtrl MtrVoltDaxFF Volt M f32[1]	-13.618
	18.6380005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-23.1889992
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	5.12099981
/trCtrl_Vecu_Volt_M_f32[0]	7.48099995
/trCtrl_Vecu_Volt_M_f32[1]	
/trCurrDaxPrevIntg_Volt_M_f32	-1.3949998
/trCurrDaxRef_Amp_M_f32[0]	31.5869999
/trCurrDaxRef_Amp_M_f32[1]	-186.397995
/trCurrQaxCog_Amp_M_f32	10.8985004
MtrCurrQaxPrevIntg_Volt_M_f32	15.8292999
MtrCurrQaxRef_Amp_M_f32[0]	171.485992
/trCurrQaxRef_Amp_M_f32[1]	163.789001
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	-2.94899988
MtrPosComputationDelay_Rad_M_f32[1]	0.00800000038

2016-09-15, 18:23:31+0530



PICurrCntrl Per1 Input Value PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.911000013 PICurrCntrl_DualEcuFailSclFac_Uls_M_f32 0.167999998 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.816999972 $PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32$ 0.489399999 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.703400016 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32 -627.179993 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -194.190002 $PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32$ 40.2612 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.176699996 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32$ 1118 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 1118 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 16 5851002 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.727199972 k CLOAFdbackSignalSclFacSlew UlspS f32 5911 31982 k_DualEcuSignalSclFacSlew_UlspS_f32 1400 k ILOAFdbackSignalSclFacSlew UlspS f32 7823.27002 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.144999996 k_MtrCtrlVirualResQax_Ohm_f32 0.114 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 28.3733006 $k_MtrVoltDaxIntegHiLim_Volt_f32$ k_MtrVoltDaxIntegLoLim_Volt_f32 -22.4099998 $k_MtrVoltQaxFiltFFEnable_Cnt_lgc$ 1 k_MtrVoltQaxIntegHiLim_Volt_f32 31 k_MtrVoltQaxIntegLoLim_Volt_f32 -22.4099998 k_MtrVoltVecuFiltEnable_Cnt_lgc k_VoltSatDaxPolyCoeff_Uls_f32 -3.26600003 k_VoltSatQaxPolyCoeff_Uls_f32 4 35599995 k deadtimeVScale Uls f32 0.963 t_CommOffsetTblX_Uls_u3p13[0] 459 t CommOffsetTblX Uls u3p13[1] 5775 t_CommOffsetTblY_Cnt_u16[0] 1081 t_CommOffsetTblY_Cnt_u16[1] 1779 $target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr$ 1 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 0 $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ 1 target MtrCntrl Read MtrCurrDax Amp f32 Val 136.341003 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 903 target MtrCntrl Read MtrCurrQax Amp f32 Val -34.6189995 target_MtrCntrl_Read_SysState_Cnt_Enum_Val 2 Name **Actual Value Expected Value** Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1779 1779 63111 ± 1 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 63111 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 152.890503 152.890503 ± 7.81E-03 -2 43829679 -2 43829656 + 4 88F-04 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -4.15197992 -4.15197945 ± 4.88E-04

38390

0

-22.4099998

© Report created by TESSY V3.1.13, report template V2.1

MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)

PICurrCntrl DualEcuFailSclFac Uls M f32

MtrCurrDaxPrevIntg_Volt_M_f32

38390 + 1 52588F-05

-22.4099998

0 ± 0.0625



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

ame	Input Value
astDataAccessBufIndex_Cnt_M_u16	1
trCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc(ptr)	target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr
trCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt Igc ptr
trCntrl Read ModidxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt Igc Val
trCntrl Read MotCurrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr
ItrCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val
trCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr
trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
trCntrl Read SysState Cnt Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
trCtrl MtrCurrDaxMaxVal Amp M f32[0]	67.4899979
trCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.723999
trCtrl MtrDampTermDax Ohm M f32[0]	0.010999999
trCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.041999994
trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.098999995
ltrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.019999996
trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.75899994
trCtrl MtrDaxIntegralGain Ohm M f32[1]	1,46099997
trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-948.984009
ltrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-975.937012
trCtrl MtrImpedDax Ohm M f32[0]	0.0419999994
trCtrl MtrImpedDax Ohm M f32[1]	0.030999995
trCtrl MtrImpedQax Ohm M f32[0]	0.0419999994
trCtrl MtrImpedQax Ohm M f32[1]	0.030999995
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.261000007
trCtrl MtrQaxIntegralGain Ohm M f32[1]	0.757000029
ItrCtrl MtrQaxPropotionalGain Ohm M f32[0]	-227.466003
trCtrl MtrQaxPropotionalGain Ohm M f32[1]	-453.341003
trCtrl MtrVoltDaxFF Volt M f32[0]	-16.302
trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	8.5539999
trCtrl MtrVoltQaxFF Volt M f32[0]	-25.3770008
trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	21.3910007
trCtrl_Vecu_Volt_M_f32[0]	18.9510002
trCtrl_Vecu_Volt_M_f32[1]	21.3110008
trCurrDaxPrevIntg_Volt_M_f32	-27.6930008
trCurrDaxRef_Amp_M_f32[0]	31.5869999
trCurrDaxRef_Amp_M_f32[1]	-186.399002
trCurrQaxCog_Amp_M_f32	16.2565002
ItrCurrQaxPrevIntg_Volt_M_f32	15.6167002
trCurrQaxRef_Amp_M_f32[0]	171.485992
trCurrQaxRef_Amp_M_f32[1]	163.789993
trCurrQaxRpl_Amp_M_f32	0
trPosComputationDelay_Rad_M_f32[0]	-2.94899988
trPosComputationDelay_Rad_M_f32[1]	0.00899999961
CurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.702000022
ICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.169
ICurrCntrl_InverterFailSclFac_Uls_M_f32	0.657000005
ICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.446700007

PICurrCntrl Per1

2016-09-15, 18:23:31+0530



Input Value PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.692600012 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32 -10.21 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 386.220001 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 60.2319984 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.306199998 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 570.700012 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 570.700012 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32$ 78.8641968 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.584299982 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 7083 27002 k_DualEcuSignalSclFacSlew_UlspS_f32 1500 k ILOAFdbackSignalSclFacSlew UlspS f32 6489 7002 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc k MtrCtrlFeedbackControlDisable_Cnt_lgc k_MtrCtrlVirualResDax_Ohm_f32 0.023 k_MtrCtrlVirualResQax_Ohm_f32 0.0529999994 $k_MtrCurrQaxRefModifDsb_Cnt_lgc$ k_MtrCurrQaxRefModifRplEn_Cnt_lgc $k_MtrVoltDaxIntegHiLim_Volt_f32$ 5.67889977 k_MtrVoltDaxIntegLoLim_Volt_f32 -8.68999958 $k_MtrVoltQaxFiltFFEnable_Cnt_lgc$ k_MtrVoltQaxIntegHiLim_Volt_f32 14.5 $k_MtrVoltQaxIntegLoLim_Volt_f32$ -8.68999958 k_MtrVoltVecuFiltEnable_Cnt_lgc -3.26600003 k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 -6.98999977 k deadtimeVScale Uls f32 0.963 t_CommOffsetTblX_Uls_u3p13[0] 6528 t_CommOffsetTblX_Uls_u3p13[1] 8192 t_CommOffsetTblY_Cnt_u16[0] 76 t CommOffsetTblY Cnt u16[1] 211 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr 0 target MtrCntrl Read lvtrLoaMtgtnEn Cnt lgc ptr 1 $target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val$ 1 target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 59.7319984 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 59.7319984 target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name **Actual Value Expected Value** Result MtrCntrl Write CommOffset Cnt u16(val) 65 65 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 0 ± 1

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

147.533493

7.62001944

19.0553932

0.35650003

4062

147.533493 ± 7.81E-03

7.62001944 ± 4.88E-04

19.0553932 ± 4.88E-04

4062 ± 1.52588E-05

0.35650003 ± 0.0625

MtrCntrl Write MtrCurrQaxFinalRef Amp f32(val)

MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)

MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)

MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)

PICurrCntrl_DualEcuFailSclFac_Uls_M_f32

MtrCurrDaxPrevIntg_Volt_M_f32





Test Step 2.174 (Repeat Count = 1)	√
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val) MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	target_MtrCntrl_Read_SysState_Cnt_Enum_Val -200.556
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4449997
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.36399996
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.95200002
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-232.371994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-761.939026
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1] MtrCtrl_MtrImpedOax_Ohm_M_f32[0]	0.112999998 0.0850000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[0] MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.112999998
MtrCtrl MtrQaxIntegralGain Ohm M f32[0]	1.65799999
MtrCtrl MtrQaxIntegralGain Ohm M f32[1]	0.307000011
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-174.839996
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	903.406006
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-28.4209995
MtrCtrl_Vecu_Volt_M_f32[0]	24.8479996 27.2080002
MtrCtrl_Vecu_Volt_M_f32[1] MtrCurrDaxPrevIntg_Volt_M_f32	20.066
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	21.6145
MtrCurrQaxPrevIntg_Volt_M_f32	18.8419991
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	2.33500004
MtrPosComputationDelay_Rad_M_f32[1]	-0.333999991 0.662
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.170000002
PICurrCntrl InverterFailSclFac Uls M f32	0.499000013
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.716799974
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.681800008
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	570.700012
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_UIs_f32	-784.130005
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	76.1873016
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.404900014
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	0
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_UIs_f32	0
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_UIs_f32	43.3250008 0.605599999
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2663.65991
k DualEcuSignalSclFacSlew UlspS f32	1600
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2156.63989
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.112000003
k_MtrCtrlVirualResQax_Ohm_f32	0.155000001
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1
k MtrVoltDaxIntegHiLim Volt f32	21.7716999
	4.57000047
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.57000017
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltDaxIntegLoLim_Volt_f32	

PICurrCntrl_Per1



Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	-20.3560009		
k_VoltSatQaxPolyCoeff_Uls_f32	0.398000002		
k_deadtimeVScale_Uls_f32	0.971000016		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	771		
t_CommOffsetTblY_Cnt_u16[1]	1636		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	0		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1636	1636	-
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	63635	63635 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	54.0875015	54.0875015 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	2.91930747	2.91930747 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-3.87926102	-3.87926102 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	22555	22555 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	21.7716999	21.7716999	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0	0 ± 0.0625	✓

Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	•
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	-
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	-
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	-
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	-
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	✓

Test Step 2.175 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-200.556
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4449997
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.344999999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.10099995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-475.019012
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-121.875999
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0850000009

2016-09-15, 18:23:31+0530





Name	Input Value		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.112999998		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.633000016		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.79999995		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-892.642029		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-451.740997		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] MtrCtrl Vecu Volt M f32[0]	-28.4209995 29.0240002		
MtrCtrl_Vecu_Volt_M_f32[1]	30.3600006		
MtrCurrDaxPrevIntg_Volt_M_f32	-27.3339996		
MtrCurrDaxRef Amp M f32[0]	-146.723007		
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001		
MtrCurrQaxCog_Amp_M_f32	26.9724998		
MtrCurrQaxPrevIntg_Volt_M_f32	21.7777996		
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006		
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	-0.532000005		
MtrPosComputationDelay_Rad_M_f32[1]	3.22000003 0.851000011		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.851000011		
PICurrCntrl InverterFailSclFac Uls M f32	0.757000029		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.476399988		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.671000004		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-38.7999992		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	947.73999		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	99.3730011		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.404500008		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	-627.179993		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-627.179993		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	44.6861992 0.82130003		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6145.56982		
k_DualEcuSignalSclFacSlew_UlspS_f32	1700		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5517.5		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k_MtrCtrlVirualResDax_Ohm_f32	0.193000004		
k_MtrCtrlVirualResQax_Ohm_f32	0.155000001		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k_Mtr/oltDaxIntegHiLim_Volt_f32	18.9853992		
k_MtrVoltDaxIntegLoLim_Volt_f32 k_MtrVoltQaxFiltFFEnable_Cnt_lgc	-31 1		
k_MtrVoltQaxInter Erlabic_Ont_ige k_MtrVoltQaxIntegHiLim_Volt_f32	26.3924999		
k MtrVoltQaxIntegLoLim Volt f32	-25.6000004		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-20.3560009		
k_VoltSatQaxPolyCoeff_Uls_f32	20.7369995		
k_deadtimeVScale_Uls_f32	0.971000016		
t_CommOffsetTblX_Uls_u3p13[0]	5022		
t_CommOffsetTblX_Uls_u3p13[1]	7003		
t_CommOffsetTblY_Cnt_u16[0]	365		
t_CommOffsetTblY_Cnt_u16[1]	1530		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	50.0610008		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4000		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-9.31999969		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Resu
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4000	4000	
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-160.91951	-160.91951 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	7.82000065	7.82000065 ± 4.88E-04	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	29.0674706 62728	29.0674706 ± 4.88E-04	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) MtrCurrDaxPrevIntg_Volt_M_f32	0	62728 ± 1.52588E-05	
PIO TOTAL POSTE SESSIONES AND	0 38350001	0 20250004 + 0 0625	

0.38350001

PICurrCntrl_DualEcuFailSclFac_Uls_M_f32

0.38350001 ± 0.0625



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

lame	Input Value
astDataAccessBufIndex Cnt M u16	0
/trCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc(ptr)	target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr
ItrCntrl Read IvtrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr
trCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModidxSrlComSvcDft Cnt lgc Val
trCntrl Read MotCurrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt Igc ptr
trCntrl Read MtrCurrDax Amp f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
trCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr
trCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
trCtrl MtrCurrDaxMaxVal Amp M f32[0]	-212.632996
trCtrl MtrCurrDaxMaxVal Amp M f32[1]	-205.087997
trCtrl MtrDampTermDax Ohm M f32[0]	0.079999982
trCtrl MtrDampTermDax Ohm M f32[1]	0.0120000001
trCtrl MtrDampTermQax Ohm M f32[0]	0.0099999978
trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.109999999
trCtrl MtrDaxIntegralGain Ohm M f32[0]	0.409000009
trCtrl MtrDaxIntegralGain Ohm M f32[1]	1.16900003
trCtrl MtrDaxPropotionalGain Ohm M f32[0]	967.463013
trCtrl MtrDaxPropotionalGain Ohm M f32[1]	-285.225006
trCtrl MtrImpedDax Ohm M f32[0]	0.112999998
trCtrl MtrImpedDax Ohm M f32[1]	0.128000006
trCtrl MtrImpedQax Ohm M f32[0]	0.0529999994
trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0970000029
trCtrl MtrQaxIntegralGain Ohm M f32[0]	0.305999994
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.236000001
trCtrl MtrQaxPropotionalGain Ohm M f32[0]	-121.924004
trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	483.276001
trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6190004
trCtrl MtrVoltQaxFF Volt M f32[0]	18.6380005
trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1900005
trCtrl_Vecu_Volt_M_f32[0]	17.7010002
trCtrl_Vecu_Volt_M_f32[1]	20.0610008
trCurrDaxPrevIntg_Volt_M_f32	-9.66300011
trCurrDaxRef_Amp_M_f32[0]	31.5869999
ItrCurrDaxRef_Amp_M_f32[1]	-186.399002
ItrCurrQaxCog_Amp_M_f32	32.3305016
trCurrQaxPrevIntg_Volt_M_f32	2.29920006
trCurrQaxRef_Amp_M_f32[0]	171.485992
trCurrQaxRef_Amp_M_f32[1]	163.789993
trCurrQaxRpl_Amp_M_f32	0
trPosComputationDelay_Rad_M_f32[0]	-2.94899988
trPosComputationDelay_Rad_M_f32[1]	0.00899999961
ICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.237000003
'ICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.172000006
ICurrCntrl InverterFailSclFac Uls M f32	0.150000006

2016-09-15, 18:23:31+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.844699979		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.6602		
PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32	-194.190002		
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	269.399994		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	64.5255966		
PICurrCntrl MtrVecuFilt M str.TermD Uls f32	0.099799981		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput UIs f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32	29.8064003		
PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.925599992		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6616.02002		
k_DualEcuSignalSclFacSlew_UlspS_f32	1800		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4019.20996		
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt Igc	1		
k MtrCtrlFeedbackControlDisable Cnt Igc	0		
k MtrCtrlVirualResDax Ohm f32	0.191		
k MtrCtrlVirualResQax Ohm f32	0.0529999994		
k MtrCurrQaxRefModifDsb Cnt lgc	1		
k MtrCurrQaxRefModifRplEn Cnt lgc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	15.4042997		
k MtrVoltDaxIntegLoLim Volt f32	0		
k MtrVoltQaxFiltFFEnable Cnt lgc	1		
k MtrVoltQaxIntegHiLim Volt f32	4.96659994		
k MtrVoltQaxIntegLoLim Volt f32	-10.5		
k MtrVoltVecuFiltEnable Cnt lgc	0		
k VoltSatDaxPolyCoeff Uls f32	18.2779999		
k VoltSatQaxPolyCoeff Uls f32	-1.46500003		
k_deadtimeVScale_Uls_f32	0.996999979		
t CommOffsetTbIX Uls u3p13[0]	4611		
	5579		
t_CommOffsetTblX_Uls_u3p13[1]	163		
t_CommOffsetTblY_Cnt_u16[0]	1236		
t_CommOffsetTblY_Cnt_u16[1]	1		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	· ·		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999 656		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr			
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	-	I=	
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	656	656	•
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	139.155487	139.155487 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-0.696358919	-0.6963588 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	17.6341534	17.6341515 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	34365	34365 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0	0 ± 0.0625	→

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	-
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	✓
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	-
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	✓





Test Step 2.177 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-200.556 -98.4449997
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4449997
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0] MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112999998
MtrCtrl MtrDampTermQax Ohm M f32[0]	0.041999994
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0280000009
MtrCtrl MtrDaxIntegralGain Ohm M f32[0]	0.34499999
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.102
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-475.019012
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-121.876999
MtrCtrl MtrImpedDax Ohm M f32[0]	0.0850000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0850000009
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.112999998
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.633000016
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.8999998
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-892.642029
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-451.742004
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3880005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-28.4209995
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981
MtrCtrl_Vecu_Volt_M_f32[1]	7.48099995
MtrCurrDaxPrevIntg_Volt_M_f32	-0.216499999
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007
MtrCurrDaxRef_Amp_M_f32[1]	-121.943001
MtrCurrQaxCog_Amp_M_f32	48.4044991
MtrCurrQaxPrevIntg_Volt_M_f32	14.2393999
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006
MtrCurrQaxRef_Amp_M_f32[1]	75.7020035
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	-0.532000005
MtrPosComputationDelay_Rad_M_f32[1]	3.23000002
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.702000022
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.172999993
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.481000006
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.749800026
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.627799988
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-340.130005
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	386.220001
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	87.3075027
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput Uls f32	0.175400004
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_132 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_UIs_f32	1118 1118
PICUITCHTH MtrVoltQaxFFFIIt_M_str.PrevOutput_Uis_132 PICUITCHTH MtrVoltQaxFFFIIt M str.TermN Uls f32	57.8652992
PICUITCHIII_MITVOIQAXFFFIIL_M_SIT.TerMIN_OIS_132 PICUITCHIII_MITVOIQAXFFFIIL_M_SIT.TerMIN_OIS_132	0.1426
k CLOAFdbackSignalSclFacSlew UlspS f32	6616.02002
k_CLOArubacksignalsciracsiew_Oisps_isz k DualEcuSignalSciFacSiew UlspS f32	1900
k ILOAFdbackSignalScIFacSlew UlspS f32	947.890015
k_HtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.191
k_MtrCtrlVirualResQax_Ohm_f32	0.114
k MtrCurrQaxRefModifDsb Cnt Igc	1
k MtrCurrQaxRefModifRplEn Cnt lgc	1
k_MtrVoltDaxIntegHiLim_Volt_f32	27.0930996
k MtrVoltDaxIntegLoLim Volt f32	-9.64999962
k MtrVoltQaxFiltFFEnable Cnt lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	7.0927
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.64999962
k MtrVoltVecuFiltEnable Cnt lgc	1

PICurrCntrl_Per1



N	Innered Walters		
Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	-3.26600003		
k_VoltSatQaxPolyCoeff_Uls_f32	-1.46500003		
k_deadtimeVScale_Uls_f32	0.987999976		
t_CommOffsetTbIX_UIs_u3p13[0]	5022		
t_CommOffsetTbIX_UIs_u3p13[1]	7003		
t_CommOffsetTblY_Cnt_u16[0]	365		
t_CommOffsetTblY_Cnt_u16[1]	1530		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	59.7319984		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4608		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	4608	4608	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-182.351501	-182.351501 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	3.83600593	3.83600545 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	30.3868313	30.3868275 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	61297	61297 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.41049999	0.41049999 ± 0.0625	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	~

Test Step 2.178 (Repeat Count = 1)		~
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	1	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.089005	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.079999982	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0130000003	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.00999999978	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.119999997	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.409000009	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.16999996	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	967.463013	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-285.226013	
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998	
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.128999993	

PICurrCntrl_Per1



Picuricilli_Peri			i Citat
Name	Input Value		
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0979999974		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.305999994		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.237000003		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-121.924004		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	483.277008		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6199999		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.191		
MtrCtrl_Vecu_Volt_M_f32[0]	18.9510002		
MtrCtrl_Vecu_Volt_M_f32[1]	21.3110008		
MtrCurrDaxPrevIntg_Volt_M_f32	-9.05200005		
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999		
MtrCurrDaxRef_Amp_M_f32[1]	-186.399994		
MtrCurrQaxCog_Amp_M_f32	53.7625008		
MtrCurrQaxPrevIntg_Volt_M_f32	11.8942003		
	171.485992		
MtrCurrQaxRef_Amp_M_f32[0]			
MtrCurrQaxRef_Amp_M_f32[1]	163.791		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	-2.94899988		
MtrPosComputationDelay_Rad_M_f32[1]	0.0099999978		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.662		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.173999995		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.465999991		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.498699993		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.616999984		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	22.2399998		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-627.179993		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	97.3968964		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.763000011		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	-38.7999992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	44.6861992		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.514999986		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6616.02002		
k_DualEcuSignalSclFacSlew_UlspS_f32	2000		
k ILOAFdbackSignalSclFacSlew UlspS f32	4854.70996		
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k MtrCtrlVirualResDax Ohm f32	0.144999996		
k_MtrCtrlVirualResQax_Ohm_f32	0.114		
k MtrCurrQaxRefModifDsb Cnt Igc	0		
k MtrCurrQaxRefModifRplEn Cnt Igc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	21.7719002		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.5		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	29.5634995		
k_MtrVoltQaxIntegLoLim_Volt_f32	-22.4099998		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	10.1960001		
k_VoltSatQaxPolyCoeff_Uls_f32	20.9540005		
k_deadtimeVScale_Uls_f32	0.958999991		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTblX_Uls_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	771		
t_CommOffsetTblY_Cnt_u16[1]	1636		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-207.917999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	2125		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-34.6189995		
	-34.0169995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val			
Name	Actual Value	Expected Value	Resu
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1636	1636	
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	62849	62849 ± 1	,
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	110.028503	110.028503 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-10.3498077	-10.3498068 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-17.6227894	-17.6227875 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	38411	38411 ± 1.52588E-05	
MtrCurrDaxPrevIntg_Volt_M_f32	21.7719002	21.7719002	



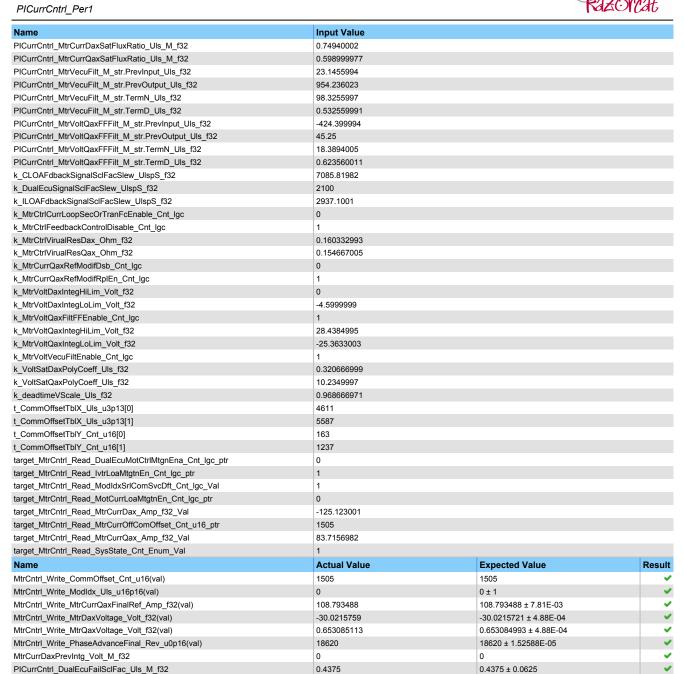


Name	Actual Value	Expected Value	Result
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0	0 ± 0.0625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

lame	Input Value	
astDataAccessBufIndex_Cnt_M_u16	0	
trCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc(ptr)	target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr	
trCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr	
ItrCntrl Read ModidxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val	
ItrCntrl Read MotCurrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr	
ItrCntrl Read MtrCurrDax Amp f32(Val)	target MtrCntrl Read MtrCurrDax Amp f32 Val	
ItrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
ItrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
trCtrl MtrCurrDaxMaxVal Amp M f32[0]	67.4899979	
ltrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	119.725998	
trCtrl MtrDampTermDax Ohm M f32[0]	0.010999999	
trCtrl MtrDampTermDax Ohm M f32[1]	0.043999998	
trCtrl MtrDampTermQax Ohm M f32[0]	0.098999995	
trCtrl MtrDampTermQax Ohm M f32[1]	0.0219999999	
ItrCtrl MtrDaxIntegralGain Ohm M f32[0]	1.75899994	
trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.46300006	
ltrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-948.984009	
trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-975.939026	
trCtrl MtrImpedDax Ohm M f32[0]	0.041999994	
trCtrl MtrImpedDax Ohm M f32[1]	0.032999998	
ItrCtrl MtrImpedQax Ohm M f32[0]	0.041999994	
trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.032999998	
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.261000007	
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.759000003	
trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-227.466003	
trCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-453.342987	
trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-16.302	
trCtrl MtrVoltDaxFF Volt M f32[1]	8.55599976	
trCtrl MtrVoltQaxFF Volt M f32[0]	-25.3770008	
trCtrl MtrVoltQaxFF Volt M f32[1]	21.3929996	
trCtrl_Vecu_Volt_M_f32[0]	18.9510002	
trCtrl Vecu Volt M f32[1]	21.3120003	
ItrCurrDaxPrevIntg_Volt_M_f32	-5.69950008	
trCurrDaxRef_Amp_M_f32[0]	31.5869999	
ItrCurrDaxRef_Amp_M_f32[1]	-186.401001	
ItrCurrQaxCog Amp M f32	62.6925011	
ItrCurrQaxPrevIntg Volt M f32	17.4958	
trCurrQaxRef Amp M f32[0]	171.485992	
trCurrQaxRef_Amp_M_f32[1]	163.792007	
trCurrQaxRpl_Amp_M_f32	0	
trPosComputationDelay_Rad_M_f32[0]	-2.94899988	
trPosComputationDelay_Rad_M_f32[1]	0.0109999999	
ICurrCntrl CurrSensFailSclFac Uls M f32	0.962000012	
CurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.174999997	
ICurrCntrl InverterFailSclFac Uls M f32	0.43966986	





Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	•
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~



Test Step 2.180 (Repeat Count = 1)		~
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	1	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrOffComOffcot_Cnt_u16(ntr)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr) MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
MtrCntrl Read SysState Cnt Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl MtrCurrDaxMaxVal Amp M f32[0]	-200.556	
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-98.4459991	
MtrCtrl MtrDampTermDax Ohm M f32[0]	0.0850000009	
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.114	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0419999994	
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0289999992	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.344999999	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.10300004	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-475.019012	
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-121.877998	
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0850000009	
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.114	
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0850000009	
MtrCtrl_MtrlmpedQax_Ohm_M_f32[1]	0.114	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.633000016	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] MtrCtrl MtrQaxPropotionalGain Ohm M f32[0]	1.10000002 -892.642029	
	-451.743011	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-25.3770008	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	21.3889999	
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-3.59500003	
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-28.4220009	
MtrCtrl_Vecu_Volt_M_f32[0]	5.12099981	
MtrCtrl_Vecu_Volt_M_f32[1]	7.48199987	
MtrCurrDaxPrevIntg_Volt_M_f32	-5.39400005	
MtrCurrDaxRef_Amp_M_f32[0]	-146.723007	
MtrCurrDaxRef_Amp_M_f32[1]	-121.944	
MtrCurrQaxCog_Amp_M_f32	70.7294998	
MtrCurrQaxPrevIntg_Volt_M_f32	3.1875	
MtrCurrQaxRef_Amp_M_f32[0]	-133.947006	
MtrCurrQaxRef_Amp_M_f32[1]	75.7030029	
MtrCurrQaxRpl_Amp_M_f32	0	
MtrPosComputationDelay_Rad_M_f32[0]	-0.532000005	
MtrPosComputationDelay_Rad_M_f32[1]	3.2400001	
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.0769999996	
PICurrCotrl_DualEcuFailSclFac_Uls_M_f32	0.175999999	
PICurrCntrl_InverterFailSclFac_Uls_M_f32 PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.416166991 0.968200028	
PICurrCntrl MtrCurrQaxSatFluxRatio_Ois_M_i32	0.582799971	
PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32	22.5478001	
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	256.125	
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	45.6320992	
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.235679999	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-1002.79999	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	110.253998	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	11.2356005	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.51244998	
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7438.18018	
k_DualEcuSignalSclFacSlew_UlspS_f32	2200	
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2475.6001	
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1	
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0	
k_MtrCtrlVirualResDax_Ohm_f32	0.160332993	
k_MtrCtrlVirualResQax_Ohm_f32	0.185167	
k_MtrCurrQaxRefModifDsb_Cnt_lgc k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1	
k_MtrVoltDaxIntegHiLim Volt f32	31	
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.6999981	
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1	
k MtrVoltQaxIntegHiLim Volt f32	30.2145004	

2016-09-15, 18:23:31+0530



PICurrCntrl_Per1

Name	Input Value		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
k_VoltSatDaxPolyCoeff_Uls_f32	-3.72033		
k_VoltSatQaxPolyCoeff_Uls_f32	11.6547003		
k_deadtimeVScale_Uls_f32	0.968666971		
t_CommOffsetTblX_Uls_u3p13[0]	5022		
t_CommOffsetTblX_Uls_u3p13[1]	7003		
t_CommOffsetTblY_Cnt_u16[0]	365		
t_CommOffsetTblY_Cnt_u16[1]	1531		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-129.938995		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	419		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	129.727005		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1531	1531	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63482	63482 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	4.97350311	4.97350311 ± 7.81E-03	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	4.35798788	4.35798788 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-5.79095459	-5.79095459 ± 4.88E-04	✓
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	59833	59833 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	31	31	✓
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0	0 ± 0.0625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	-
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	_

Test Step 2.181 (Repeat Count = 1)	🗸
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-212.632996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-205.089996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0799999982
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0140000004
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0099999978
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.129999995
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.409000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.171
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	967.463013
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-285.22699
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.112999998
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.129999995
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0529999994

2016-09-15, 18:23:31+0530





Name	Input Value		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0989999995		
MtrCtrl MtrQaxIntegralGain Ohm M f32[0]	0.305999994		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.238000005		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-121.924004		
MtrCtrl MtrQaxPropotionalGain Ohm M f32[1]	483.278015		
MtrCtrl MtrVoltDaxFF Volt M f32[0]	-0.736000001		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.6210003		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	18.6380005		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1919994		
MtrCtrl Vecu Volt M f32[0]	18.9510002		
MtrCtrl Vecu Volt M f32[1]	21.3120003		
MtrCurrDaxPrevIntg Volt M f32	-5.08850002		
MtrCurrDaxRef_Amp_M_f32[0]	31.5869999		
	-186.401001		
MtrCurrOpyCog Amp M 633	78.7665024		
MtrCurrQaxCog_Amp_M_f32			
MtrCurrQaxPrevIntg_Volt_M_f32	12.9105997		
MtrCurrQaxRef_Amp_M_f32[0]	171.485992		
MtrCurrQaxRef_Amp_M_f32[1]	163.792007		
MtrCurrQaxRpl_Amp_M_f32	0		
MtrPosComputationDelay_Rad_M_f32[0]	-2.94899988		
MtrPosComputationDelay_Rad_M_f32[1]	0.0109999999		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.0120000001		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.177000001		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.392666996		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.642499983		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.566600025		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	65.3214035		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	144.326004		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	44.2155991		
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.13256		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1250.13		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	125.012001		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	10.2364998		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.142560005		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7790.52979		
k_DualEcuSignalSclFacSlew_UlspS_f32	2300		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2014.09998		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1		
k MtrCtrlVirualResDax Ohm f32	0.160332993		
k_MtrCtrlVirualResQax_Ohm_f32	0.156669945		
k MtrCurrQaxRefModifDsb Cnt lgc	1		
k MtrCurrQaxRefModifRplEn Cnt Igc	1		
k_MtrVoltDaxIntegHiLim_Volt_f32	11.7658005		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.80000019		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0		
k_MtrVoltQaxIntegHiLim_Volt_f32	30.2355995		
k_MtrVoltQaxIntegLoLim_Volt_f32	-20.5599995		
	1		
k_MtrVoltVecuFiltEnable_Cnt_lgc			
k_VoltSatDaxPolyCoeff_Uls_f32	-7.76133013		
k_VoltSatQaxPolyCoeff_Uls_f32	21.3213997		
k_deadtimeVScale_Uls_f32	0.968666971		
t_CommOffsetTblX_Uls_u3p13[0]	459		
t_CommOffsetTbIX_UIs_u3p13[1]	5775		
t_CommOffsetTblY_Cnt_u16[0]	771		
t_CommOffsetTblY_Cnt_u16[1]	1637		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-134.755005		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	0		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	175.738007		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	3		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	0	0	~
MtrCntrl_Write_ModIdx_UIs_u16p16(val)	0	0 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	92.7194901	92.7194901 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	29.8242722	29.8242722 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	3.49775219	3.49775219 ± 4.88E-04	-
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	49943	49943 ± 1.52588E-05	~
ovviito_i nacortavancei mai_rev_uop ro(vai)		0	
MtrCurrDaxPrevIntg_Volt_M_f32	10		
MtrCurrDaxPrevIntg_Volt_M_f32 PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0 0.46450001	0.46450001 ± 0.0625	·



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	-	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~	
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~	
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~	

lame	Input Value	
astDataAccessBufIndex_Cnt_M_u16	1	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	
/trCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr	
/trCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val	
//dtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	
/trCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
MtrCtrl MtrCurrDaxMaxVal Amp M f32[0]	67.4899979	
MtrCtrl MtrCurrDaxMaxVal Amp M f32[1]	119.726997	
htrCtrl MtrDampTermDax Ohm M f32[0]	0.0109999999	
MtrCtrl MtrDampTermDax Ohm M f32[1]	0.0450000018	
MtrCtrl MtrDampTermQax Ohm M f32[0]	0.098999995	
/trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.023	
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.75899994	
htrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.46399999	
/trCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-948.984009	
htrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-975.940002	
trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0419999994	
trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0340000018	
/trCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0419999994	
htrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0340000018	
trCtrl MtrQaxIntegralGain Ohm M f32[0]	0.261000007	
trCtrl MtrQaxIntegralGain Ohm M f32[1]	0.75999999	
/trCtrl MtrQaxPropotionalGain Ohm M f32[0]	-227.466003	
/trCtrl MtrQaxPropotionalGain Ohm M f32[1]	-453.343994	
htrCtrl MtrVoltDaxFF Volt M f32[0]	-16.302	
htrCtrl MtrVoltDaxFF Volt M f32[1]	8.55700016	
htrCtrl MtrVoltQaxFF Volt M f32[0]	-25.3770008	
ItrCtrl MtrVoltQaxFF Volt M f32[1]	21.3939991	
ItrCtrl Vecu Volt M f32[0]	18.9510002	
trCtrl_vecu_volt_M_f32[1]	21.3129997	
ItrCurrDaxPrevIntg Volt M f32	-4.78299999	
9 <u>-</u>	31.5869999	
ItrCurrDaxRef_Amp_M_f32[0] ItrCurrDaxRef Amp M f32[1]	-186.401993	
	86.8034973	
ItrCurrQaxCog_Amp_M_f32	3.9934001	
ItrCurrQaxPrevIntg_Volt_M_f32	171.485992	
ItrCurrQaxRef_Amp_M_f32[0]		
ItrCurrQaxRef_Amp_M_f32[1]	163.792999 0	
ItrCurrQaxRpl_Amp_M_f32	-2.94899988	
htrPosComputationDelay_Rad_M_f32[0]		
htrPosComputationDelay_Rad_M_f32[1]	0.0120000001	
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.059999987	
PlCurrCntrl_DualEcuFailSclFac_Uls_M_f32 PlCurrCntrl InverterFailSclFac Uls M f32	0.178000003 0.369167	

PICurrCntrl Per1

2016-09-15, 18:23:31+0530



Input Value PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32 0.936200023 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.550400019 PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32 14.2356005 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 112.364998 PICurrCntrl MtrVecuFilt M str.TermN Uls f32 25.2145004 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.432559997 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 1125.02002 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32$ 113.021004 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 0.532540023 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32$ 0.235599995 k_CLOAFdbackSignalSclFacSlew_UlspS_f32 122.320999 k_DualEcuSignalSclFacSlew_UlspS_f32 2400 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 1552.59998 $k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc$ 1 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0.160332993 k_MtrCtrlVirualResDax_Ohm_f32 k_MtrCtrlVirualResQax_Ohm_f32 0.123400003 k_MtrCurrQaxRefModifDsb_Cnt_lgc 0 $k_MtrCurrQaxRefModifRplEn_Cnt_lgc$ k_MtrVoltDaxIntegHiLim_Volt_f32 9 k_MtrVoltDaxIntegLoLim_Volt_f32 -4.9000001 k_MtrVoltQaxFiltFFEnable_Cnt_lgc 10 2356005 $k_MtrVoltQaxIntegHiLim_Volt_f32$ k_MtrVoltQaxIntegLoLim_Volt_f32 -10.2360001 k MtrVoltVecuFiltEnable_Cnt_lgc 0 k_VoltSatDaxPolyCoeff_Uls_f32 -11.8023005 k VoltSatQaxPolyCoeff Uls f32 24.5214005 k_deadtimeVScale_Uls_f32 0.968666971 t CommOffsetTblX Uls u3p13[0] 4611 $t_CommOffsetTblX_Uls_u3p13[1]$ 5594 t CommOffsetTblY Cnt u16[0] 163 t_CommOffsetTblY_Cnt_u16[1] 1238 target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr 1 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 1 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val 0 $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val -139.570999 $target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr$ target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 201.748993 $target_MtrCntrl_Read_SysState_Cnt_Enum_Val$ **Actual Value Expected Value** Name Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 1238 1238 63482 63482 ± 1 MtrCntrl_Write_ModIdx_Uls_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 76.989502 76.989502 ± 7.81E-03 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 7.66697168 7.66697121 ± 4.88E-04 19.1687717 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 19 1687717 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 4094 4094 ± 1.52588E-05

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	-
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

n

0

0

0 ± 0.0625

MtrCurrDaxPrevIntg_Volt_M_f32

PICurrCntrl_DualEcuFailSclFac_Uls_M_f32



Test Step 2.183 (Repeat Count = 1)	🗸
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val) MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-110.000999 100.021004
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0225000009
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0425000004
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0421000011
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0282000005
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.214499995
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.321449995
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	102.325996
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	102.214996
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0320999995
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0313999988
MtrCtrl_MtrlmpedQax_Ohm_M_f32[0]	0.0124000004
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0214000009 0.321399987
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] MtrCtrl MtrQaxIntegralGain Ohm M f32[1]	0.321399987
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	102.021004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	102.320999
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-1.02139997
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	2.32139993
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-22.0214005
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	11.0214005
MtrCtrl_Vecu_Volt_M_f32[0]	11.0214005
MtrCtrl_Vecu_Volt_M_f32[1]	20.1452007
MtrCurrDaxPrevIntg_Volt_M_f32	-3.21420002
MtrCurrDaxRef_Amp_M_f32[0]	10.2356005
MtrCurrDaxRef_Amp_M_f32[1]	25.3255997
MtrCurrQaxCog_Amp_M_f32	22.3255997 2.36540008
MtrCurrQaxPrevIntg_Volt_M_f32 MtrCurrQaxRef Amp M f32[0]	2.30540006
MtrCurrQaxRef_Amp_M_f32[1]	25.2145004
MtrCurrQaxRpl Amp M f32	0
MtrPosComputationDelay Rad M f32[0]	-2.0144999
MtrPosComputationDelay_Rad_M_f32[1]	5.02139997
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	9.9999975e-005
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.179000005
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.0122999996
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.214499995
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.654100001
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	10.2356005 110.236
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	20.2145004
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32 PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	0.214499995
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput UIs f32	110.325996
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	112.320999
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	10.3213997
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.0122999996
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	10.0214005
k_DualEcuSignalSclFacSlew_UlspS_f32	2500
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	100.213997
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.00120000006
k_MtrCtrlVirualResQax_Ohm_f32 k_MtrCurrQaxRefModifDsb_Cnt_lgc	0.132499993
k_MtrCurrQaxRefModifRpIEn Cnt_lgc	0
k MtrVoltDaxIntegHiLim Volt f32	10.2356005
k MtrVoltDaxIntegLoLim Volt f32	-2.36540008
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	11.3255997
k_MtrVoltQaxIntegLoLim_Volt_f32	-9.32559967

PICurrCntrl_Per1



Name	Input Value		
k_VoltSatDaxPolyCoeff_Uls_f32	-10.2356005		
k_VoltSatQaxPolyCoeff_Uls_f32	20.3213997		
k_deadtimeVScale_Uls_f32	0.987999976		
t_CommOffsetTblX_Uls_u3p13[0]	10		
t_CommOffsetTblX_Uls_u3p13[1]	1757		
t_CommOffsetTblY_Cnt_u16[0]	160		
t_CommOffsetTblY_Cnt_u16[1]	260		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	23.0214005		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	10		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	100.214996		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	10	10	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-10	-10 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-1.06032157	-1.06032157 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-22.2143288	-22.2143288 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	12253	12253 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	-2.36540008	-2.36540008	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.49150002	0.49150002 ± 0.0625	✓

Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	-
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	-
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	-
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	-
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	-
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	-

Test Step 2.184 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	110.200996
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	99.0123978
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0120999999
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.112300001
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0214000009
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0125000002
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.02139997
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.23559999
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	10.2356005
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	21.2145004
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0214000009
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0122999996

2016-09-15, 18:23:31+0530

PICurrCntrl_Per1



Input Value		
0.00124999997		
9.9999975e-005		
0		
-1.0244		
9.99999975e-005		
0.20000003		
0.231399998		
111.325996		
11.3255997		
0.145600006		
145.320999		
123.250999		
12.3255997		
0.123600014		
122.320999		
5000		
200.214005		
1		
0		
0.123599999		
0.112300001		
0		
1		
11.3255997		
-1.02559996		
1		
12.3255997		
-8.25689983		
0		
-5.02139997		
10.2356005		
0.971000016		
8192		
2633		
110		
365		
1		
1		
0		
22.3213997		
12		
1		
	Expected Value	Resu
Actual Value	·	
110	110	•
110 18429	110 18429 ± 1	
110 18429 20.9598999	110 18429 ± 1 20.9598999 ± 7.81E-03	•
110 18429 20.9598999 3.21449995	110 18429 ± 1 20.9598999 ± 7.81E-03 3.21449995 ± 4.88E-04	•
110 18429 20.9598999	110 18429 ± 1 20.9598999 ± 7.81E-03	•
	0.0012499997 9.9999975e-005 0.214499995 0.421499997 90.2141037 254.320999 -2.02139997 3.21449995 -10.0214005 5.02139997 6.32140017 21.2014008 -2.36540008 32.1245003 21.0214005 11.2545996 10.3249998 10.2356005 32.2145004 0 -1.0244 3.02139997 9.9999975e-005 0.20000003 0.23139998 0.145300001 0.7411997 11.325996 11.3255997 0.14560006 145.320999 12.3255997 0.145600014 122.320999 5000 200.214005 1 0 0.123599999 0.112300001 0 1 11.3255997 -1.02559996 1 12.3255997 -1.02559999 0.112300001 0 1 12.3255997 -8.25689983 0 -5.02139997 10.2356005 0.971000016 8192 2633 110 365 1 1 0 1 22.3213997 12 90.2145004	0.0012499997 9.9999975e-005 0.21449997 90.2141037 2254.32099 -2.02139997 3.21449995 -1.0214005 5.02139997 6.32140017 21.2014008 -2.36540008 32.1245003 21.0214005 11.2545996 10.3249998 10.2356005 32.2145004 0 -1.0244 3.02139997 9.99999975e-005 0.20000003 0.231399998 0.145300001 0.74119997 11.3255997 111.3255997 0.145600006 145.320999 123.255997 0.123600014 122.320999 5000 200.214005 1 0 0 11.3255997 -1.02559996 1 1 1.3255997 -1.02559996 1 1 1.3255997 -1.02559996 1 1 1.3255997 -1.02559996 1 1 1.3255997 -1.02559996 1 1 1.3255997 -1.02559996 1 1 1.3255997 -1.02559996 1 1 1.3255997 -1.02559996 1 1 1.3255997 -1.02559996 1 1 1 2.3255997 -1.02559996 1 1 1 2.3255997 -1.02559996 1 1 1 2.3255997 -1.02559996 1 1 1 2.3255997 -1.02559996 1 1 1 2.3255997 -1.02559996 1 1 1 2.3255997 -1.02559996 1 1 1 2.3255997 -1.02559996 1 1 1 2.325997 -1.02559996 1 1 1 2.3255997 -1.02559996 1 1 1 3.355997 -1.02559996 1 1 1 3.355997 -1.02559996 1 1 1 3.355997 -1.02559996 1 1 1 3.355997 -1.02559996 1 1 1 3.355997 -1.02559996 1 1 1 3.355997 -1.02559996 1 1 1 3.355997 -1.02559996 1 1 2 3.313997 1 2 3.313997 1 2 3.313997 1 2 3.313997 1 2 3.313997 1 2 3.0145004

2016-09-15, 18:23:31+0530



PICurrCntrl_Per1

News	A stud Value	Fyma ato d Volus	Deculé
Name	Actual Value	Expected Value	Result
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0	0 ± 0.0625	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	✓
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	✓
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Case 3: Path Test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cycles:

TS 3.1 7117 Cycles

TS 3.1 7117 Cycles TS 3.2 7040 Cycles TS 3.3 7134 Cycles TS 3.5 7100 Cycles TS 3.6 7134 Cycles TS 3.6 7134 Cycles TS 3.7 293 Cycles TS 3.8 7145 Cycles TS 3.9 7236 Cycles

Description

Vector Description:

TS 3.1(k_MtrCurrQaxRefModifRplEn_Cnt_lgc == TRUE)=False&&(MtrCurrQaxRefModif_Amp_T_f32>=220)=True&&(MtrCurrQaxFinalRef_Amp_T_f32>=220)=True&&(MtrCurrDaxRefModif_Amp_k_MtrVoltQaxIntegLoLim_Volt_f32)=False&&(MtrCurrQaxIntg_Volt_T_f32<=k_MtrVoltQaxIntegHiLim_Volt_f32)=True&&(MtrCurrQaxIntg_Volt_T_f12)=K_MtrVoltQaxIntegHiLim_Volt_f32)=True&&(MtrCurrDaxIntg_Volt_T_f12)=K_MtrVoltDaxIntegHiLim_Volt_f32)=True&&(MtrCurrDaxIntg_Volt_T_f12)=True&&(MtrCurr

k_MtrVoltDaxIntegLoLim_Volt_f32)=True&&(k_MtrCtrlFeedbackControlDisable_Cnt_lgc ==
TRUE)=False&&(k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc ==
FALSE)=False&&(k_MtrVoltQaxFiltFFEnable_Cnt_lgc==TRUE)=False&&(VoltSatnRatio_Uls_T_f32 >
D_ONE_ULS_F32)=True&&(ModldxSrlComSvcDft_Cnt_T_lgc==TRUE)=False&&(k_MtrCurrQaxRefModifDsb_Cnt_lgc == FALSE)=True
TS 3.2(k_MtrCurrQaxRefModifEn_Cnt_lgc ==
TRUE)=True&&(MtrCurrQaxRefModif_Amp_T_f32<=220)=False&&(MtrCurrQaxRefModif_Amp_T_f32<=-220)=True&&(MtrCurrQaxFinalRef_Amp_
== TRUE)=True&&(k_MtrVoltQaxFiltFFEnable_Cnt_lgc==TRUE)=True &&
(lvtrLoaMtgtnEn_Cnt_T_lgc==FALSE)=False&&(ModldxSrlComSvcDft_Cnt_T_lgc==TRUE)=True&&(k_MtrCurrQaxRefModifDsb_Cnt_lgc ==

FALSE)=False TS 3.3&&(MtrCurrQaxRefModif_Amp_T_f32<=-220)=False&&(MtrCurrQaxFinalRef_Amp_T_f32<=-220)=False&&(MtrCurrQaxIntg_Volt_T_f32>= k_MtrVoltQaxIntegLoLim_Volt_f32)=True&&&&(MtrCurrQaxIntg_Volt_T_f32>= k_MtrVoltQaxIntegHiLim_Volt_f32)=True&&(MtrCurrDaxIntg_Volt_T _ 132>= k_MtrVoltQaxIntegLoLim_Volt_f32)=True&&(MtrCurrQaxIntg_Volt_T _ 132>= k_MtrVoltQaxIntegLoLim_Volt_f32)=True&&(k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc == FALSE)=True&&(VoltSatnRatio_Uls_T_f32>= k_MtrVoltQaxIntegLoLim_Volt_f32)=True&&(VoltSatnRatio_Uls_T_f32>= k_MtrVoltQaxIntegLoLim_Volt_f32= k D_ONE_ULS_F32)=False

TS 3.4 TS 3.4

(MtrCurrDaxRefModif_Amp_T_f32<=0)=False&&&&(MtrCurrDaxIntg_Volt_T_f32>=k_MtrVoltDaxIntegHiLim_Volt_f32)=True&&(MtrCurrDaxIntg_Volt_T_f32>=k_MtrVoltDaxIntegHiLim_Volt_f32)=True&&(MtrCurrDaxIntg_Volt_T_f32>=k_MtrVoltDaxIntegHiLim_Volt_f32)=True&&(MtrCurrDaxIntg_Volt_T_f32>=k_MtrVoltDaxIntegHiLim_Volt_f32)=True&&(MtrCurrDaxIntg_Volt_T_lgc==FALSE)=True&&(MtrVoltQaxFiltFFEnable_Cnt_lgc=TRUE)=True&&(IvtrLoaMtgtnEn_Cnt_T_lgc==FALSE)=True&&(MotCurrLoaMtgtnEn_Cnt_T_lgc==FALSE)=True&&(MotCurrLoaMtgtnEn_Cnt_T_lgc==FALSE)=True&&(IvtrLoaMtgtnEn_Cnt_T_lgc==FALSE)=Tr

k_MtrVoltQaxIntegLoLim_Volt_f32)=False

TS 3.9

(MtrCurrDaxIntg_Volt_T_f32<=k_MtrVoltDaxIntegLoLim_Volt_f32)=False&&(MtrCurrDaxIntg_Volt_T_f32<=k_MtrVoltDaxIntegLoLim_Volt_f32)=False&&(MtrCurrDaxIntg_Volt_T_f32<=k_MtrVoltDaxIntegLoLim_Volt_f32)=False&&(MtrCurrDaxIntg_Volt_T_f32<=k_MtrVoltDaxIntegLoLim_Volt_f32)=False&&(MtrCurrDaxIntg_Volt_T_f32<=k_MtrVoltDaxIntegLoLim_Volt_f32)=False&&(MtrCurrDaxIntg_Volt_T_f32<=k_MtrVoltDaxIntegLoLim_Volt_f32)=False&&(MtrCurrDaxIntg_Volt_T_f32<=k_MtrVoltDaxIntegLoLim_Volt_f32)=False&&(MtrCurrDaxIntg_Volt_T_f32<=k_MtrVoltDaxIntegLoLim_Volt_f32)=False&&(MtrCurrDaxIntg_Volt_T_f32<=k_MtrVoltDaxIntegLoLim_Volt_f32)=False&&(MtrCurrDaxIntg_Volt_T_f32<=k_MtrVoltDaxIntegLoLim_Volt_f32)=False&&(MtrCurrDaxIntg_Volt_T_f32<=k_MtrVoltDaxIntegLoLim_Volt_f32)=False&&(MtrCurrDaxIntg_Volt_T_f32<=k_MtrVoltDaxIntegLoLim_Volt_f32)=False&&(MtrCurrDaxIntg_Volt_T_f32<=k_MtrVoltDaxIntegLoLim_Volt_f32)=False&&(MtrCurrDaxIntg_Volt_T_f32<=k_MtrVolt_DaxIntegLoLim_Volt_f32)=False&&(MtrCurrDaxIntg_Volt_T_f32<=k_MtrVolt_DaxIntegLoLim_Volt_f32)=False&&(MtrCurrDaxIntg_Volt_T_f32<=k_MtrVolt_DaxIntegLoLim_Volt_f32)=False&&(MtrCurrDaxIntegLoLim_Volt_T_f32)=False&&(MtrCurrDaxIntegLoLim_Volt_T_f32<=k_MtrVolt_DaxIntegLoLim_Volt_T_f32<=k_MtrVolt_DaxIntegLoLim_Volt_T_f32<=k_MtrVolt_DaxIntegLoLim_Volt_T_f32<=k_MtrVolt_DaxIntegLoLim_Volt_T_f32<=k_MtrVolt_DaxIntegLoLim_Volt_T_f32<=k_MtrVolt_DaxIntegLoLim_Volt_T_f32<=k_MtrVolt_DaxIntegLoLim_Volt_DaxInteg k_MtrVoltDaxIntegLoLim_Volt_f32)=False

Test Step 3.1 (Repeat Count = 1)		✓
Name	Input Value	
FastDataAccessBufIndex_Cnt_M_u16	0	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	





Name	Input Value
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-220
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-220
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0049999989
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0049999989
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0049999989
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0049999989
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-1024
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-1024
MtrCtrl_MtrlmpedDax_Ohm_M_f32[0]	0.00499999989
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0049999989
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0049999989
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0049999989
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0
	0
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	-1024
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-1024
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-31
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-31
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-31
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-31
MtrCtrl_Vecu_Volt_M_f32[0]	5
MtrCtrl_Vecu_Volt_M_f32[1]	5
MtrCurrDaxPrevIntg_Volt_M_f32	-31
MtrCurrDaxRef_Amp_M_f32[0]	-220
MtrCurrDaxRef_Amp_M_f32[1]	-220
MtrCurrQaxCog_Amp_M_f32	-220
MtrCurrQaxPrevIntg_Volt_M_f32	-31
MtrCurrQaxRef_Amp_M_f32[0]	-220
MtrCurrQaxRef_Amp_M_f32[1]	-220
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	-2.89063835
MtrPosComputationDelay_Rad_M_f32[1]	-2.98318529
PICurrCntrl_CurrSensFailSclFac_UIs_M_f32	0
PICurrCntrl DualEcuFailSclFac Uls M f32	0.100000001
PICurrCntrl InverterFailSclFac UIs M f32	0
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-1350
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	-1350
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	-0.996816993
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.96346009e-005
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	-1350
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-1350
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	-0.996816993
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	1.96346009e-005
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	10
k_DualEcuSignalSclFacSlew_UlspS_f32	10
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	10
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0
k_MtrCtrlVirualResDax_Ohm_f32	0
k_MtrCtrlVirualResQax_Ohm_f32	0
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	0
k_MtrVoltDaxIntegLoLim_Volt_f32	-31
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	0
k_MtrVoltQaxIntegHiLim_Volt_f32	0
k_MtrVoltQaxIntegLoLim_Volt_f32	-31
k_MtrVoltVecuFiltEnable_Cnt_lgc	0
k_VoltSatDaxPolyCoeff_Uls_f32	-25
k_VoltSatQaxPolyCoeff_Uls_f32	-25
k_deadtimeVScale_UIs_f32	0.949999988
	0
t_CommOffsetTblX_Uls_u3p13[0]	
t_CommOffsetTblX_Uls_u3p13[0] t_CommOffsetTblX_Uls_u3p13[1]	0
t_CommOffsetTbIX_UIs_u3p13[0] t_CommOffsetTbIX_UIs_u3p13[1] t_CommOffsetTbIY_Cnt_u16[0]	0 0
t_CommOffsetTblX_Uls_u3p13[0] t_CommOffsetTblX_Uls_u3p13[1]	0

PICurrCntrl_Per1



Name	Input Value		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-220		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	0		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-220		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	0		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	0	0	~
MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0 62259	0 62259 ± 1	V
	-	•	· ·
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	62259	62259 ± 1	•
MtrCntrl_Write_ModIdx_Uls_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	62259 0	62259 ± 1 0 ± 7.81E-03	· · · · · · · · · · · · · · · · · · ·
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	62259 0 -3.3568294	62259 ± 1 0 ± 7.81E-03 -3.3568294 ± 4.88E-04	· · · · · · · · · · · · · · · · · · ·
MtrCntrl_Write_ModIdx_Uls_u16p16(val) MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	62259 0 -3.3568294 -3.36068416	62259 ± 1 0 ± 7.81E-03 -3.3568294 ± 4.88E-04 -3.36068416 ± 4.88E-04	***************************************

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	✓
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 3.2 (Repeat Count = 1)	√
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	220
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	220
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	2
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	2
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	1024
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	1024
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.125650004
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	2
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	2
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	1024
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	1024
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	31
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	31
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	31
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	31

PICurrCntrl Per1

2016-09-15, 18:23:31+0530



Input Value MtrCtrl_Vecu_Volt_M_f32[0] 31 MtrCtrl_Vecu_Volt_M_f32[1] MtrCurrDaxPrevIntg_Volt_M_f32 31 MtrCurrDaxRef_Amp_M_f32[0] 220 MtrCurrDaxRef_Amp_M_f32[1] 220 MtrCurrQaxCog_Amp_M_f32 220 MtrCurrQaxPrevIntg_Volt_M_f32 31 MtrCurrQaxRef_Amp_M_f32[0] 220 MtrCurrQaxRef_Amp_M_f32[1] 220 MtrCurrQaxRpl Amp M f32 0 MtrPosComputationDelay_Rad_M_f32[0] 3.1400001 MtrPosComputationDelay_Rad_M_f32[1] 3 1400001 PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 PICurrCntrl DualEcuFailSclFac Uls M f32 0.0199999996 PICurrCntrl_InverterFailSclFac_Uls_M_f32 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 1 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32 1350 $PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32$ 1350 PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 50928.6016 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.996827006 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32 1350 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32$ 1350 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 50928.6016 0.996827006 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 $k_CLOAFdbackSignalSclFacSlew_UlspS_f32$ 8000 k_DualEcuSignalSclFacSlew_UlspS_f32 8000 k_ILOAFdbackSignalSclFacSlew_UlspS_f32 8000 k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc 1 k_MtrCtrlFeedbackControlDisable_Cnt_lgc k MtrCtrlVirualResDax Ohm f32 0.20000003 k MtrCtrlVirualResQax Ohm f32 0.200000003 k MtrCurrQaxRefModifDsb Cnt lgc k_MtrCurrQaxRefModifRplEn_Cnt_lgc k_MtrVoltDaxIntegHiLim_Volt_f32 31 k MtrVoltDaxIntegLoLim Volt f32 0 k_MtrVoltQaxFiltFFEnable_Cnt_lgc $k_MtrVoltQaxIntegHiLim_Volt_f32$ 31 k_MtrVoltQaxIntegLoLim_Volt_f32 0 k MtrVoltVecuFiltEnable_Cnt_lgc 1 k_VoltSatDaxPolyCoeff_Uls_f32 25 k VoltSatQaxPolyCoeff Uls f32 25 k_deadtimeVScale_Uls_f32 1 t_CommOffsetTblX_Uls_u3p13[0] 8192 t_CommOffsetTblX_Uls_u3p13[1] 8192 t_CommOffsetTblY_Cnt_u16[0] 2000 t_CommOffsetTblY_Cnt_u16[1] 2000 target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr $target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr$ 1 target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ 1 target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val 220 $target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr$ 5000 target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val 220 target_MtrCntrl_Read_SysState_Cnt_Enum_Val **Actual Value** Name **Expected Value** Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 5000 5000 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 0 0 ± 1 0 + 7 81F-03 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 21.9203072 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) 21.9203072 ± 4.88E-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) 21.9203072 21.9203072 ± 4.88E-04 40943 40943 ± 1.52588E-05 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) MtrCurrDaxPrevIntg_Volt_M_f32 0 0 PICurrCntrl_DualEcuFailSclFac_Uls_M_f32 0 0 ± 0.0625



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	-
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

lame	Input Value	
astDataAccessBufIndex_Cnt_M_u16	1	
/trCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	
ItrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	
ItrCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val	
ItrCntrl Read MotCurrLoaMtgtnEn Cnt Igc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr	
ItrCntrl Read MtrCurrDax Amp f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	
/trCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	
ItrCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val	
ItrCntrl Read SysState Cnt Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val	
ItrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	205.820999	
htrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-206.792007	
ltrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.115000002	
ItrCtrl MtrDampTermDax Ohm M f32[1]	0.0579999983	
ItrCtrl MtrDampTermQax Ohm M f32[0]	0.0320000015	
ItrCtrl MtrDampTermQax Ohm M f32[1]	0.0869999975	
htrCtrl MtrDaxIntegralGain Ohm M f32[0]	0.0649999976	
htrCtrl MtrDaxIntegralGain Ohm M f32[1]	1.227	
htrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	537.232971	
htrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	67.9840012	
trCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0549999997	
ItrCtrl MtrImpedDax Ohm M f32[1]	0.109999999	
trCtrl MtrImpedQax Ohm M f32[0]	0.0270000007	
trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0920000002	
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	0.703000009	
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.75199997	
htrCtrl MtrQaxPropotionalGain Ohm M f32[0]	462.437012	
htrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-685.195984	
htrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	30.6930008	
htrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	0.219999999	
ItrCtrl MtrVoltQaxFF Volt M f32[0]	3.45499992	
ItrCtrl MtrVoltQaxFF Volt M f32[1]	-19.1830006	
ItrCtrl Vecu Volt M f32[0]	22.3540001	
trCtrl Vecu Volt M f32[1]	24.7140007	
ItrCurrDaxPrevIntg Volt M f32	-23.0620003	
/trCurrDaxRef_Amp_M_f32[0]	37.4550018	
ttrCurrDaxRef_Amp_M_f32[1]	-2.84500003	
ttrCurrQaxCog_Amp_M_f32	-55.5390015	
ItrCurrQaxPrevIntg_Volt_M_f32	8.08899975	
ItrCurrQaxRef Amp M f32[0]	220	
ItrCurrQaxRef Amp M f32[1]	220	
ItrCurrQaxRpl Amp M f32	0	
ItrPosComputationDelay_Rad_M_f32[0]	-3.08800006	
htrPosComputationDelay_Rad_M_f32[1]	-3.26300001	
III POSCOTTPUTATION DETAY_RAU_M_132[1] IICurrCntrl_CurrSensFailSclFac_Uls_M_f32	-5.20300001	
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.029999993	
PICurrCntrl InverterFailSclFac Uls M f32	0.638000011	

2016-09-15, 18:23:31+0530



PICurrCntrl_Per1

Name	Input Value		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.880900025		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.978999972		
PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32	-657.099976		
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	47050.1992		
PICurrCntrl MtrVecuFilt M str.TermD Uls f32	0.0229000002		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput UIs f32	-657.099976		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	47050.1992		
PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.0229000002		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	10		
k_DualEcuSignalSclFacSlew_UlspS_f32	11.1999998		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	7088.3501		
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt lgc	1		
k MtrCtrlFeedbackControlDisable Cnt Igc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.19499993		
k MtrCtrlVirualResQax Ohm f32	0.142000005		
k MtrCurrQaxRefModifDsb Cnt Igc	0		
k MtrCurrQaxRefModifRplEn Cnt lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	17.9123993		
k MtrVoltDaxIntegLoLim Volt f32	-0.69999988		
k MtrVoltQaxFiltFFEnable Cnt lgc	0		
k MtrVoltQaxInterHiLim Volt f32	19.4449997		
k MtrVoltQaxIntegLoLim Volt_132	-0.69999988		
k MtrVoltVecuFiltEnable Cnt lgc	0		
k VoltSatDaxPolyCoeff Uls f32	-19.4559994		
k VoltSatQaxPolyCoeff Uls f32	-18.6200008		
k_deadtimeVScale_Uls_f32	0.95599997		
t CommOffsetTbIX UIs u3p13[0]	4170		
t_CommOffsetTbIX_UIs_u3p13[1]	6749		
t_CommOffsetTblY_Cnt_u16[0]	177		
t CommOffsetTblY Cnt u16[1]	340		
target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr	0		
target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr	1		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target MtrCntrl Read MtrCurrDax Amp f32 Val	83.9489975		
target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr	335		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-145.169006		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2		
	Actual Value	Expected Value	Daguit
Name			Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	315	315	
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	50872	50872 ± 1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	220	220 ± 7.81E-03	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0.219999999	0.219999999 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-19.1830006	-19.1830006 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	64150	64150 ± 1.52588E-05	•
MtrCurrDaxPrevIntg_Volt_M_f32	-0.69999988	-0.69999988	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0313999988	0.0313999988 ± 0.0625	•

Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~		
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~		
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	-		
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~		
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	-		
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	~		
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-		
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~		
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~		
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~		
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~		
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~		
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~		
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~		
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~		
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~		
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~		



Test Step 3.4 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val 171.485992
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0] MtrCtrl MtrCurrDaxMaxVal Amp M f32[1]	163.787003
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.114
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.039000008
MtrCtrl MtrDampTermQax Ohm M f32[0]	0.0179999992
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.046000001
MtrCtrl MtrDaxIntegralGain Ohm M f32[0]	0.167999998
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.62100005
MtrCtrl MtrDaxPropotionalGain Ohm M f32[0]	720.525024
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-487.845001
MtrCtrl_MtrlmpedDax_Ohm_M_f32[0]	0.0960000008
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.103
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.075000003
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0649999976
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.60500002
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	1.33500004
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-418.748993
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	590.754028
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-11.3319998
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-5.40700006
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-22.3460007
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-17.8169994
MtrCtrl_Vecu_Volt_M_f32[0]	16.8080006
MtrCtrl_Vecu_Volt_M_f32[1]	19.1679993
MtrCurrDaxPrevIntg_Volt_M_f32	14.7060003
MtrCurrDaxRef_Amp_M_f32[0]	220
MtrCurrDaxRef_Amp_M_f32[1]	220
MtrCurrQaxCog_Amp_M_f32	177.763
MtrCurrQaxPrevIntg_Volt_M_f32	12.4979
MtrCurrQaxRef_Amp_M_f32[0]	160.044006
MtrCurrQaxRef_Amp_M_f32[1]	165.242004
MtrCurrQaxRpl_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	0.277999997
MtrPosComputationDelay_Rad_M_f32[1]	-3.2579999
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.426999986 0.039999991
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32 PICurrCntrl InverterFailSclFac Uls M f32	0.469999999
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.194700003
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.860000014
PICurrCntrl MtrVecuFilt M str.PrevInput UIs f32	1118
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	-340.130005
PICurrCntrl MtrVecuFilt M str.TermN Uls f32	31081.1992
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.797699988
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32	1118
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-340.130005
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	31081.1992
PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.797699988
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	5764.10986
k_DualEcuSignalSclFacSlew_UlspS_f32	1200
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3350.96997
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	1
k_MtrCtrlVirualResDax_Ohm_f32	0.175999999
k_MtrCtrlVirualResQax_Ohm_f32	0.061999999
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0
k_MtrVoltDaxIntegHiLim_Volt_f32	12.2978001
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.5
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1
k_MtrVoltQaxIntegHiLim_Volt_f32	12.2735996
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.5
k MtrVoltVecuFiltEnable Cnt lgc	1

2016-09-15, 18:23:31+0530



PICurrCntrl_Per1

Name	Input Value		
k VoltSatDaxPolyCoeff Uls f32	21.7950001		
k VoltSatQaxPolyCoeff Uls f32	21.1380005		
k deadtimeVScale UIs f32	0.958999991		
t CommOffsetTbIX UIs u3p13[0]	4432		
t CommOffsetTbIX Uls u3p13[1]	5751		
t CommOffsetTblY Cnt u16[0]	132		
t CommOffsetTblY Cnt u16[1]	216		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	75.0830002		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3800		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	54.1119995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	4		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	3800	3800	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-17.7189941	-17.7189941 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	-2.16869807	-2.16869807 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.2765379	-4.2765379 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	40563	40563 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0	0 ± 0.0625	~

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	-
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl Write CommOffset Cnt u16	1	MtrCntrl Write CommOffset Cnt u16	1	_

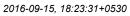
Test Step 3.5 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-216.921997
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	-184.923996
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0370000005
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0379999988
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0549999997
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0850000009
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	0.5
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.824000001
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-1024
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-1024
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0700000003
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0130000003





MtrCtrl_MtrImpedQax_Ohm_M_f32[0] MtrCtrl_MtrImpedQax_Ohm_M_f32[1] MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] MtrCtrl_Vecu_Volt_M_f32[0] MtrCtrl_Vecu_Volt_M_f32[1] MtrCtrl_Vecu_Volt_M_f32[1] MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1] MtrCurrQaxPerevIntg_Volt_M_f32 MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[1]	Input Value 0.0970000029 0.0160000008 0.286000013 1.41499996 -730.362 -412.898987 14.4589996 -5.13000011 22.5750008 22.8969994 18.7189999 21.0790005 15.9169998 -69.0940018 161.973007 -152.050995 20.0867996 -200.556 -98.4449997		
MtrCtrl_MtrImpedQax_Ohm_M_f32[1] MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] MtrCtrl_Volt_M_f32[0] MtrCtrl_Volt_M_f32[1] MtrCtrl_Volt_N_f32[1] MtrCtrl_Volt_N_f32[1] MtrCurrDaxPerevIntg_Volt_M_f32 MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1] MtrCurrQaxCog_Amp_M_f32 MtrCurrQaxPrevIntg_Volt_M_f32 MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32[0]	0.0160000008 0.286000013 1.41499996 -730.362 -412.898987 14.4589996 -5.13000011 22.5750008 22.8969994 18.7189999 21.0790005 15.9169998 -69.0940018 161.973007 -152.050995 20.0867996 -200.556		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0] MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] MtrCtrl_Vecu_Volt_M_f32[0] MtrCtrl_Vecu_Volt_M_f32[1] MtrCtrl_Vecu_Volt_M_f32[1] MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrDaxRef_Amp_M_f32[0] MtrCurrQaxCog_Amp_M_f32 MtrCurrQaxPrevIntg_Volt_M_f32 MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32 MtrCurrQaxRef_Amp_M_f32[1]	0.286000013 1.41499996 -730.362 -412.898987 14.4589996 -5.13000011 22.5750008 22.8969994 18.7189999 21.0790005 15.9169998 -69.0940018 161.973007 -152.050995 20.0867996 -200.556		
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] MtrCtrl_Vccu_Volt_M_f32[0] MtrCtrl_Vccu_Volt_M_f32[1] MtrCtrl_Vccu_Volt_M_f32[1] MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1] MtrCurrQaxCog_Amp_M_f32 MtrCurrQaxPrevIntg_Volt_M_f32 MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32 MtrCurrQaxRef_Amp_M_f32 MtrCurrQaxRef_Amp_M_f32 MtrCurrQaxRef_Amp_M_f32[1]	1.4149996 -730.362 -412.898987 14.458996 -5.13000011 22.5750008 22.8969994 18.7189999 21.0790005 15.9169998 -69.0940018 161.973007 -152.050995 20.0867996 -200.556		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] MtrCtrl_Vecu_Volt_M_f32[0] MtrCtrl_Vecu_Volt_M_f32[1] MtrCtrl_Vecu_Volt_M_f32[1] MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1] MtrCurrQaxCog_Amp_M_f32 MtrCurrQaxCog_Amp_M_f32 MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32 MtrCurrQaxRef_Amp_M_f32 MtrCurrQaxRef_Amp_M_f32 MtrCurrQaxRef_Amp_M_f32 MtrCurrQaxRef_Amp_M_f32 MtrCurrQaxRef_Amp_M_f32 MtrCurrQaxRef_Amp_M_f32 MtrPosComputationDelay_Rad_M_f32[0]	-730.362 -412.898987 14.4589996 -5.13000011 22.5750008 22.8969994 18.7189999 21.0790005 15.9169998 -69.0940018 161.973007 -152.050995 20.0867996 -200.556		
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] MtrCtrl_Vecu_Volt_M_f32[0] MtrCtrl_Vecu_Volt_M_f32[1] MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1] MtrCurrQaxCog_Amp_M_f32 MtrCurrQaxCog_Amp_M_f32 MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32 MtrCurrQaxRef_Amp_M_f32 MtrCurrQaxRef_Amp_M_f32 MtrCurrQaxRef_Amp_M_f32 MtrCurrQaxRef_Amp_M_f32 MtrCurrQaxRef_Amp_M_f32 MtrPosComputationDelay_Rad_M_f32[0]	-412.898987 14.4589996 -5.13000011 22.5750008 22.8969994 18.7189999 21.0790005 15.9169998 -69.0940018 161.973007 -152.050995 20.0867996 -200.556		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] MtrCtrl_Vecu_Volt_M_f32[0] MtrCtrl_Vecu_Volt_M_f32[1] MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1] MtrCurrQaxCog_Amp_M_f32 MtrCurrQaxCrq_Amp_M_f32 MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32 MtrPosComputationDelay_Rad_M_f32[0]	14.4589996 -5.13000011 22.5750008 22.8969994 18.7189999 21.0790005 15.9169998 -69.0940018 161.973007 -152.050995 20.0867996 -200.556		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] MtrCtrl_Vecu_Volt_M_f32[0] MtrCtrl_Vecu_Volt_M_f32[1] MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1] MtrCurrQaxCog_Amp_M_f32 MtrCurrQaxPrevIntg_Volt_M_f32 MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32 MtrPosComputationDelay_Rad_M_f32[0]	-5.13000011 22.5750008 22.8969994 18.7189999 21.0790005 15.9169998 -69.0940018 161.973007 -152.050995 20.0867996 -200.556		
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] MtrCtrl_Vecu_Volt_M_f32[0] MtrCtrl_Vecu_Volt_M_f32[1] MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1] MtrCurrQaxCog_Amp_M_f32 MtrCurrQaxPrevIntg_Volt_M_f32 MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32 MtrPosComputationDelay_Rad_M_f32[0]	22.5750008 22.8969994 18.7189999 21.0790005 15.9169998 -69.0940018 161.973007 -152.050995 20.0867996 -200.556		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] MtrCtrl_Vecu_Volt_M_f32[0] MtrCtrl_Vecu_Volt_M_f32[1] MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1] MtrCurrQaxCog_Amp_M_f32 MtrCurrQaxPrevIntg_Volt_M_f32 MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32 MtrPosComputationDelay_Rad_M_f32[0]	22.8969994 18.7189999 21.0790005 15.9169998 -69.0940018 161.973007 -152.050995 20.0867996 -200.556		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] MtrCtrl_Vecu_Volt_M_f32[0] MtrCtrl_Vecu_Volt_M_f32[1] MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1] MtrCurrQaxCog_Amp_M_f32 MtrCurrQaxPrevIntg_Volt_M_f32 MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRpl_Amp_M_f32 MtrPosComputationDelay_Rad_M_f32[0]	18.7189999 21.0790005 15.9169998 -69.0940018 161.973007 -152.050995 20.0867996 -200.556		
MtrCtrl_Vecu_Volt_M_f32[0] MtrCtrl_Vecu_Volt_M_f32[1] MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1] MtrCurrQaxCog_Amp_M_f32 MtrCurrQaxPrevIntg_Volt_M_f32 MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32 MtrPosComputationDelay_Rad_M_f32[0]	18.7189999 21.0790005 15.9169998 -69.0940018 161.973007 -152.050995 20.0867996 -200.556		
MtrCtrl_Vecu_Volt_M_f32[1] MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1] MtrCurrQaxCog_Amp_M_f32 MtrCurrQaxPrevIntg_Volt_M_f32 MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRef_Amp_M_f32 MtrCurrQaxRef_Amp_M_f32 MtrPosComputationDelay_Rad_M_f32[0]	21.0790005 15.9169998 -69.0940018 161.973007 -152.050995 20.0867996 -200.556		
MtrCurrDaxPrevIntg_Volt_M_f32 MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1] MtrCurrQaxCog_Amp_M_f32 MtrCurrQaxPrevIntg_Volt_M_f32 MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRpl_Amp_M_f32 MtrPosComputationDelay_Rad_M_f32[0]	15.9169998 -69.0940018 161.973007 -152.050995 20.0867996 -200.556		
MtrCurrDaxRef_Amp_M_f32[0] MtrCurrDaxRef_Amp_M_f32[1] MtrCurrQaxCog_Amp_M_f32 MtrCurrQaxPrevIntg_Volt_M_f32 MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRpl_Amp_M_f32 MtrPosComputationDelay_Rad_M_f32[0]	-69.0940018 161.973007 -152.050995 20.0867996 -200.556		
MtrCurrDaxRef_Amp_M_f32[1] MtrCurrQaxCog_Amp_M_f32 MtrCurrQaxPrevIntg_Volt_M_f32 MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRpl_Amp_M_f32 MtrPosComputationDelay_Rad_M_f32[0]	161.973007 -152.050995 20.0867996 -200.556		
MtrCurrQaxCog_Amp_M_f32 MtrCurrQaxPrevIntg_Volt_M_f32 MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRpl_Amp_M_f32 MtrPosComputationDelay_Rad_M_f32[0]	-152.050995 20.0867996 -200.556		
MtrCurrQaxPrevIntg_Volt_M_f32 MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRpl_Amp_M_f32 MtrPosComputationDelay_Rad_M_f32[0]	20.0867996 -200.556		
MtrCurrQaxRef_Amp_M_f32[0] MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRpl_Amp_M_f32 MtrPosComputationDelay_Rad_M_f32[0]	-200.556		
MtrCurrQaxRef_Amp_M_f32[1] MtrCurrQaxRpl_Amp_M_f32 MtrPosComputationDelay_Rad_M_f32[0]			
MtrCurrQaxRpl_Amp_M_f32 MtrPosComputationDelay_Rad_M_f32[0]	-98.4449997		
MtrPosComputationDelay_Rad_M_f32[0]			
	0		
MtrPosComputationDelay Rad M f32[1]	-0.430000007		
	-2.92700005		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.418000013		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0500000007		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.300000012		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.902100027		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.675000012		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	1118		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	8419.69043		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.634800017		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-194.190002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	8419.69043		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.634800017		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6857.12012		
k_DualEcuSignalSclFacSlew_UlspS_f32	13.6000004		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2799.87988		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k MtrCtrlVirualResDax Ohm f32	0.0289999992		
k_MtrCtrlVirualResQax_Ohm_f32	0.188999996		
k MtrCurrQaxRefModifDsb Cnt Igc	0		
k MtrCurrQaxRefModifRplEn Cnt Igc	0		
_ : _ = =:	18.2152004		
k_MtrVoltDaxIntegHiLim_Volt_f32	3.5		
k_MtrVoltDaxIntegLoLim_Volt_f32			
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	18.2434006		
k_MtrVoltQaxIntegLoLim_Volt_f32	3.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	12.026		
k_VoltSatQaxPolyCoeff_Uls_f32	-23.2660007		
k_deadtimeVScale_Uls_f32	0.999000013		
t_CommOffsetTblX_Uls_u3p13[0]	4342		
t_CommOffsetTblX_Uls_u3p13[1]	7724		
t_CommOffsetTblY_Cnt_u16[0]	1124		
t_CommOffsetTblY_Cnt_u16[1]	1178		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	3317		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	3.89299989		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Resu
	3317	3317	Resu
MtrCntrl_Write_CommOffset_Cnt_u16(val)			
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-48.5050049	-48.5050049 ± 7.81E-03	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	0.0226566643	0.0226566698 ± 4.88E-04	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-4.99494839	-4.99494886 ± 4.88E-04	•
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	28236	28236 ± 1.52588E-05	
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	

PICurrCntrl_Per1





Name	Actual Value	Expected Value	Result
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0516999997	0.0516999997 ± 0.0625	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Test Step 3.6 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-82.2979965
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	46.8180008
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.114
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.00600000005
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0710000023
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.057
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.15900004
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.762000024
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	1024
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	1024
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0970000029
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0270000007
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.120999999
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0909999982
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.87699997
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.648999989
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	-603.161987
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	-712.994019
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-7.3130002
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	3.05299997
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	-11.3319998
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-5.40700006
MtrCtrl_Vecu_Volt_M_f32[0]	16.4099998
MtrCtrl_Vecu_Volt_M_f32[1]	18.7700005
MtrCurrDaxPrevIntg_Volt_M_f32	-24.1620007
MtrCurrDaxRef_Amp_M_f32[0]	-132.813004
MtrCurrDaxRef_Amp_M_f32[1]	-9.14299965
MtrCurrQaxCog_Amp_M_f32	-51.1100006
MtrCurrQaxPrevIntg_Volt_M_f32	13.3757
MtrCurrQaxRef_Amp_M_f32[0]	67.4899979
MtrCurrQaxRef_Amp_M_f32[1]	119.721001
MtrCurrQaxRpI_Amp_M_f32	0
MtrPosComputationDelay_Rad_M_f32[0]	-3.12700009
MtrPosComputationDelay_Rad_M_f32[1]	-3.13499999

PICurrCntrl Per1

2016-09-15, 18:23:31+0530



Input Value PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.423999995 PICurrCntrl_DualEcuFailSclFac_Uls_M_f32 0.0599999987 PICurrCntrl_InverterFailSclFac_Uls_M_f32 0.395000011 $PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32$ 0.712199986 PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.651000023 PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32 -38.7999992 PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -194.190002 $PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32$ 12079.9004 PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.298200011 $PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32$ -38 7999992 PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 -194.190002 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32 12079 9004 PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32 0.298200011 3678 44995 k CLOAFdbackSignalSclFacSlew UlspS f32 k_DualEcuSignalSclFacSlew_UlspS_f32 14.8000002 k ILOAFdbackSignalSclFacSlew UlspS f32 7603.6001 k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0 k_MtrCtrlFeedbackControlDisable_Cnt_lgc 0 k_MtrCtrlVirualResDax_Ohm_f32 0.0439999998 k_MtrCtrlVirualResQax_Ohm_f32 0.166999996 k_MtrCurrQaxRefModifDsb_Cnt_lgc 1 k_MtrCurrQaxRefModifRplEn_Cnt_lgc 30.1203003 $k_MtrVoltDaxIntegHiLim_Volt_f32$ k_MtrVoltDaxIntegLoLim_Volt_f32 -4.5 $k_MtrVoltQaxFiltFFEnable_Cnt_lgc$ k_MtrVoltQaxIntegHiLim_Volt_f32 8.95559978 k_MtrVoltQaxIntegLoLim_Volt_f32 -4.5 k_MtrVoltVecuFiltEnable_Cnt_lgc 24.5209999 k_VoltSatDaxPolyCoeff_Uls_f32 k_VoltSatQaxPolyCoeff_Uls_f32 -20.1860008 k deadtimeVScale Uls f32 0.99000001 t_CommOffsetTblX_Uls_u3p13[0] 1516 t CommOffsetTblX Uls u3p13[1] 5882 t_CommOffsetTblY_Cnt_u16[0] 1813 t_CommOffsetTblY_Cnt_u16[1] 183 $target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr$ 1 target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr 0 $target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val$ $target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr$ target MtrCntrl Read MtrCurrDax Amp f32 Val 59.7319984 target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr 3803 target MtrCntrl Read MtrCurrQax Amp f32 Val 45.3779984 target_MtrCntrl_Read_SysState_Cnt_Enum_Val Name Actual Value **Expected Value** Result MtrCntrl_Write_CommOffset_Cnt_u16(val) 3803 3803 MtrCntrl_Write_ModIdx_Uls_u16p16(val) 0 0 ± 1 MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val) 118.599998 118.599998 ± 7.81E-03 -2 68405437 MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val) -2 68405461 + 4 88F-04 MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val) -4.15912819 -4.15912867 ± 4.88E-04 6130 + 1 52588F-05 MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val) 6130 MtrCurrDaxPrevIntg_Volt_M_f32 0 PICurrCntrl DualEcuFailSclFac Uls M f32 0.0581499971 0.0581499971 ± 0.0625



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	-
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	-
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

Name	Input Value
FastDataAccessBufIndex Cnt M u16	1
/trCntrl Read DualEcuMotCtrlMtgnEna Cnt Igc(ptr)	target MtrCntrl Read DualEcuMotCtrlMtgnEna Cnt lgc ptr
/trCntrl Read lvtrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read lvtrLoaMtgtnEn Cnt Igc ptr
/trCntrl Read ModldxSrlComSvcDft Cnt lgc(Val)	target MtrCntrl Read ModldxSrlComSvcDft Cnt lgc Val
/trCntrl Read MotCurrLoaMtgtnEn Cnt lgc(ptr)	target MtrCntrl Read MotCurrLoaMtgtnEn Cnt lgc ptr
/trCntrl Read MtrCurrDax Amp f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
/trCntrl Read MtrCurrOffComOffset Cnt u16(ptr)	target MtrCntrl Read MtrCurrOffComOffset Cnt u16 ptr
/trCntrl Read MtrCurrQax Amp f32(Val)	target MtrCntrl Read MtrCurrQax Amp f32 Val
ItrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
ItrCtrl MtrCurrDaxMaxVal Amp M f32[0]	-212.632996
htrCtrl MtrCurrDaxMaxVal Amp M f32[1]	-205.087006
trCtrl MtrDampTermDax Ohm M f32[0]	0.079999982
ItrCtrl MtrDampTermDax Ohm M f32[1]	0.0109999999
htrCtrl MtrDampTermQax Ohm M f32[0]	0.0099999978
htrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.100000001
ItrCtrl MtrDaxIntegralGain Ohm M f32[0]	0.409000009
ItrCtrl MtrDaxIntegralGain Ohm M f32[1]	1.16799998
ItrCtrl MtrDaxPropotionalGain Ohm M f32[0]	967.463013
ItrCtrl MtrDaxPropotionalGain Ohm M f32[1]	-285.223999
ItrCtrl MtrImpedDax Ohm M f32[0]	0.112999998
ItrCtrl MtrImpedDax Ohm M f32[1]	0.127000004
ItrCtrl MtrImpedQax Ohm M f32[0]	0.0529999994
trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0960000008
ItrCtrl MtrQaxIntegralGain Ohm M f32[0]	0.305999994
trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.234999999
ItrCtrl MtrQaxPropotionalGain Ohm M f32[0]	-121.924004
ltrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	483.274994
trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-0.736000001
trCtrl_MtrVoltDaxFF_Volt_M_f32[1]	-13.618
ItrCtrl MtrVoltQaxFF Volt M f32[0]	18.6380005
trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-23.1889992
trCtrl_Vecu_Volt_M_f32[0]	5.12099981
ItrCtrl_Vecu_Volt_M_f32[1]	7.48099995
ItrCurrDaxPrevIntg_Volt_M_f32	-1.39499998
ItrCurrDaxRef_Amp_M_f32[0]	31.5869999
/trCurrDaxRef_Amp_M_f32[1]	-186.397995
/trCurrQaxCog_Amp_M_f32	10.8985004
ItrCurrQaxPrevIntg_Volt_M_f32	15.8292999
ItrCurrQaxRef Amp M f32[0]	171.485992
ItrCurrQaxRef Amp M f32[1]	163.789001
ItrCurrQaxRpl_Amp_M_f32	0
ItrPosComputationDelay_Rad_M_f32[0]	-2.94899988
htrPosComputationDelay_Rad_M_f32[1]	0.00800000038
CurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.911000013
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.070000003
PICurrCntrl InverterFailSclFac Uls M f32	0.816999972

PICurrCntrl_Per1

2016-09-15, 18:23:31+0530



Name	Input Value		
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32	0.489399999		
PICurrCntrl MtrCurrQaxSatFluxRatio Uls M f32	0.703400016		
PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32	-627.179993		
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	-194.190002		
PICurrCntrl MtrVecuFilt M str.TermN Uls f32	40.2612		
PICurrCntrl MtrVecuFilt M str.TermD Uls f32	0.176699996		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevInput Uls f32	1118		
PICurrCntrl MtrVoltQaxFFFilt M str.PrevOutput Uls f32	1118		
PICurrCntrl MtrVoltQaxFFFilt M str.TermN Uls f32	16.5851002		
PICurrCntrl MtrVoltQaxFFFilt M str.TermD Uls f32	0.727199972		
k CLOAFdbackSignalSclFacSlew UlspS f32	5911.31982		
k DualEcuSignalSclFacSlew UlspS f32	6000		
k ILOAFdbackSignalScIFacSlew UlspS f32	7823.27002		
k MtrCtrlCurrLoopSecOrTranFcEnable Cnt Igc	0		
k MtrCtrlFeedbackControlDisable Cnt lgc	0		
k MtrCtrlVirualResDax Ohm f32	0.144999996		
k_MtrCtrlVirualResQax_Ohm_f32	0.114		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	1		
k MtrVoltDaxIntegHiLim Volt f32	28.3733006		
k_MtrVoltDaxIntegLoLim_Volt_f32	-22.4099998		
k MtrVoltQaxFiltFFEnable Cnt lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	31		
k MtrVoltQaxIntegLoLim Volt f32	-22.4099998		
k MtrVoltVecuFiltEnable Cnt lgc	1		
k VoltSatDaxPolyCoeff Uls f32	-3.26600003		
k VoltSatQaxPolyCoeff Uls f32	4.35599995		
k deadtimeVScale Uls f32	0.963		
t CommOffsetTblX Uls u3p13[0]	459		
t CommOffsetTblX Uls u3p13[1]	5775		
t CommOffsetTblY Cnt u16[0]	1081		
t CommOffsetTblY Cnt u16[1]	1779		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0		
target MtrCntrl Read IvtrLoaMtgtnEn Cnt Igc ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	136.341003		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	903		
target MtrCntrl Read MtrCurrQax Amp f32 Val	-34.6189995		
target MtrCntrl Read SysState Cnt Enum Val	2		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1779	1779	~
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	63111	63111 ± 1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	152.890503	152.890503 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	2.36908245	2.36908221 ± 4.88E-04	✓
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	4.19185829	4.19185781 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	5449	5449 ± 1.52588E-05	✓
MtrCurrDaxPrevIntg_Volt_M_f32	-22.4099998	-22.4099998	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	✓
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	~
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	~
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	-
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	-
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	✓
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

0.820000052

0.820000052 ± 0.0625

PICurrCntrl_DualEcuFailSclFac_Uls_M_f32



MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr) target_MtrCnt MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_Igc(Val) target_MtrCnt MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_Igc(ptr) target_MtrCnt MtrCntrl_Read_MtrCurrDax_Amp_f32(Val) target_MtrCnt MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr) target_MtrCnt MtrCntrl_Read_MtrCurrQax_Amp_f32(Val) target_MtrCnt	3
FastDataAccessBufIndex_Cnt_M_u16 0 MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr) target_MtrCnt MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr) target_MtrCnt MtrCntrl_Read_MotlCurrLoaMtgtnEn_Cnt_lgc(ptr) target_MtrCnt MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr) target_MtrCnt MtrCntrl_Read_MtrCurrDax_Amp_f32(Val) target_MtrCnt MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr) target_MtrCnt MtrCntrl_Read_MtrCurrQax_Amp_f32(Val) target_MtrCnt MtrCntrl_Read_SysState_Cnt_Enum(Val) target_MtrCnt MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0] -212.632996 MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1] -205.085007 MtrCtrl_MtrDampTermDax_Ohm_M_f32[1] 0.114 MtrCtrl_MtrDampTermDax_Ohm_M_f32[1] 0.057999998 MtrCtrl_MtrDampTermQax_Ohm_M_f32[1] 0.057999998 MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1] 0.70599997 MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1] 65.1259995 MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1] 0.064000003 MtrCtrl_MtrImpedDax_Ohm_M_f32[1] 0.020999999 MtrCtrl_MtrImpedDax_Ohm_M_f32[1] 0.020999999 MtrCtrl_MtrImpedDax_Ohm_M_f32[1] 0.0209999999 MtrCtrl_MtrImpedDax_Ohm_M_f32[1] 0.0209999999 MtrCtrl_MtrImpedDax_Ohm_M_f32[1] 0.0209999999999999999999999999999999999	rI_Read_IvtrLoaMtgtnEn_Cnt_Igc_ptr rI_Read_ModIdxSrIComSvcDft_Cnt_Igc_Val rI_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr rI_Read_MtrCurrDax_Amp_f32_Val rI_Read_MtrCurrOffComOffset_Cnt_u16_ptr rI_Read_MtrCurrQax_Amp_f32_Val rI_Read_SysState_Cnt_Enum_Val
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr) target_MtrCnt MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr) target_MtrCnt MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr) target_MtrCnt MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr) target_MtrCnt MtrCntrl_Read_MtrCurrDax_Amp_f32(Val) target_MtrCnt MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr) target_MtrCnt MtrCntrl_Read_MtrCurrQax_Amp_f32(Val) target_MtrCnt MtrCntrl_Read_SysState_Cnt_Enum(Val) target_MtrCnt MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0] -212.632996 MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1] -205.085007 MtrCtrl_MtrDampTermDax_Ohm_M_f32[0] 0.0430000018 MtrCtrl_MtrDampTermDax_Ohm_M_f32[1] 0.057999998 MtrCtrl_MtrDampTermQax_Ohm_M_f32[1] 0.057999998 MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0] 1.43400002 MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0] 362.112 MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0] 0.064000003 MtrCtrl_MtrImpedDax_Ohm_M_f32[0] 0.064000003 MtrCtrl_MtrImpedDax_Ohm_M_f32[1] 0.020999999	rI_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr rI_Read_ModldxSrlComSvcDft_Cnt_lgc_Val rI_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr rI_Read_MtrCurrDax_Amp_f32_Val rI_Read_MtrCurrOffComOffset_Cnt_u16_ptr rI_Read_MtrCurrOax_Amp_f32_Val rI_Read_MtrCurrOax_Amp_f32_Val rI_Read_SysState_Cnt_Enum_Val
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val) target_MtrCnt MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr) target_MtrCnt MtrCntrl_Read_MtrCurrDax_Amp_f32(Val) target_MtrCnt MtrCntrl_Read_MtrCurrOamComffset_Cnt_u16(ptr) target_MtrCnt MtrCntrl_Read_MtrCurrQax_Amp_f32(Val) target_MtrCnt MtrCntrl_Read_SysState_Cnt_Enum(Val) target_MtrCnt MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0] -212.632996 MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1] -205.085007 MtrCtrl_MtrDampTermDax_Ohm_M_f32[0] 0.0430000018 MtrCtrl_MtrDampTermDax_Ohm_M_f32[1] 0.114 MtrCtrl_MtrDampTermQax_Ohm_M_f32[0] 0.0610000007 MtrCtrl_MtrDampTermQax_Ohm_M_f32[1] 0.0579999983 MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0] 1.43400002 MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1] 0.7059997 MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0] 362.112 MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1] 65.125995 MtrCtrl_MtrImpedDax_Ohm_M_f32[0] 0.064000003 MtrCtrl_MtrImpedDax_Ohm_M_f32[1] 0.0209999993	rl_Read_ModIdxSriComSvcDft_Cnt_Igc_Val rl_Read_MotCurrLoaMtgtnEn_Cnt_Igc_ptr rl_Read_MtrCurrDax_Amp_f32_Val rl_Read_MtrCurrOffComOffset_Cnt_u16_ptr rl_Read_MtrCurrQax_Amp_f32_Val rl_Read_SysState_Cnt_Enum_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr) target_MtrCnt MtrCntrl_Read_MtrCurrDax_Amp_f32(Val) target_MtrCnt MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr) target_MtrCnt MtrCntrl_Read_MtrCurrQax_Amp_f32(Val) target_MtrCnt MtrCntrl_Read_SysState_Cnt_Enum(Val) target_MtrCnt MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0] -212.632996 MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1] -205.085007 MtrCtrl_MtrDampTermDax_Ohm_M_f32[0] 0.0430000018 MtrCtrl_MtrDampTermDax_Ohm_M_f32[1] 0.114 MtrCtrl_MtrDampTermQax_Ohm_M_f32[0] 0.0610000007 MtrCtrl_MtrDampTermQax_Ohm_M_f32[1] 0.0579999983 MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0] 1.43400002 MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1] 0.7059997 MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0] 362.112 MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1] 65.125995 MtrCtrl_MtrImpedDax_Ohm_M_f32[0] 0.064000003 MtrCtrl_MtrImpedDax_Ohm_M_f32[1] 0.0209999993	rl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr rl_Read_MtrCurrDax_Amp_f32_Val rl_Read_MtrCurrOffComOffset_Cnt_u16_ptr rl_Read_MtrCurrQax_Amp_f32_Val rl_Read_SysState_Cnt_Enum_Val
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val) target_MtrCnt MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr) target_MtrCnt MtrCntrl_Read_MtrCurrQax_Amp_f32(Val) target_MtrCnt MtrCntrl_Read_SysState_Cnt_Enum(Val) target_MtrCnt MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0] -212.632996 MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1] -205.085007 MtrCtrl_MtrDampTermDax_Ohm_M_f32[0] 0.0430000018 MtrCtrl_MtrDampTermDax_Ohm_M_f32[1] 0.114 MtrCtrl_MtrDampTermQax_Ohm_M_f32[0] 0.0610000007 MtrCtrl_MtrDampTermQax_Ohm_M_f32[1] 0.0579999983 MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0] 1.43400002 MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0] 362.112 MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0] 365.1259995 MtrCtrl_MtrImpedDax_Ohm_M_f32[0] 0.064000003 MtrCtrl_MtrImpedDax_Ohm_M_f32[1] 0.0209999993	rl_Read_MtrCurrDax_Amp_f32_Val rl_Read_MtrCurrOffComOffset_Cnt_u16_ptr rl_Read_MtrCurrQax_Amp_f32_Val rl_Read_SysState_Cnt_Enum_Val
MtrCntrl Read_MtrCurrOffComOffset_Cnt_u16(ptr) target_MtrCnt MtrCntrl_Read_MtrCurrQax_Amp_f32(Val) target_MtrCnt MtrCntrl_Read_SysState_Cnt_Enum(Val) target_MtrCnt MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0] -212.632996 MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1] -205.085007 MtrCtrl_MtrDampTermDax_Ohm_M_f32[0] 0.0430000018 MtrCtrl_MtrDampTermDax_Ohm_M_f32[1] 0.114 MtrCtrl_MtrDampTermQax_Ohm_M_f32[0] 0.0610000007 MtrCtrl_MtrDampTermQax_Ohm_M_f32[1] 0.0579999983 MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0] 1.43400002 MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1] 0.7059997 MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0] 362.112 MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1] 65.1259995 MtrCtrl_MtrImpedDax_Ohm_M_f32[0] 0.064000003 MtrCtrl_MtrImpedDax_Ohm_M_f32[1] 0.0209999993	rl_Read_MtrCurrOffComOffset_Cnt_u16_ptr rl_Read_MtrCurrQax_Amp_f32_Val rl_Read_SysState_Cnt_Enum_Val
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val) target_MtrCnt MtrCntrl_Read_SysState_Cnt_Enum(Val) target_MtrCnt MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0] -212.632996 MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1] -205.085007 MtrCtrl_MtrDampTermDax_Ohm_M_f32[0] 0.0430000018 MtrCtrl_MtrDampTermDax_Ohm_M_f32[1] 0.114 MtrCtrl_MtrDampTermQax_Ohm_M_f32[0] 0.0610000007 MtrCtrl_MtrDampTermQax_Ohm_M_f32[1] 0.0579999983 MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0] 1.43400002 MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1] 0.7059997 MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0] 362.112 MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1] 65.1259995 MtrCtrl_MtrImpedDax_Ohm_M_f32[0] 0.064000003 MtrCtrl_MtrImpedDax_Ohm_M_f32[1] 0.0209999993	rl_Read_MtrCurrQax_Amp_f32_Val rl_Read_SysState_Cnt_Enum_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val) target_MtrCnt MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0] -212.632996 MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1] -205.085007 MtrCtrl_MtrDampTermDax_Ohm_M_f32[0] 0.0430000018 MtrCtrl_MtrDampTermDax_Ohm_M_f32[1] 0.114 MtrCtrl_MtrDampTermQax_Ohm_M_f32[0] 0.0610000007 MtrCtrl_MtrDampTermQax_Ohm_M_f32[1] 0.057999998 MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0] 1.43400002 MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1] 0.7059997 MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0] 362.112 MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1] 65.1259995 MtrCtrl_MtrImpedDax_Ohm_M_f32[0] 0.064000003 MtrCtrl_MtrImpedDax_Ohm_M_f32[1] 0.020999999	rl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0] -212.632996 MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1] -205.085007 MtrCtrl_MtrDampTermDax_Ohm_M_f32[0] 0.0430000018 MtrCtrl_MtrDampTermDax_Ohm_M_f32[1] 0.114 MtrCtrl_MtrDampTermQax_Ohm_M_f32[0] 0.0610000007 MtrCtrl_MtrDampTermQax_Ohm_M_f32[1] 0.0579999983 MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0] 1.43400002 MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1] 0.7059997 MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0] 362.112 MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1] 65.1259995 MtrCtrl_MtrImpedDax_Ohm_M_f32[0] 0.064000003 MtrCtrl_MtrImpedDax_Ohm_M_f32[1] 0.0209999993	5
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1] -205.085007 MtrCtrl_MtrDampTermDax_Ohm_M_f32[0] 0.0430000018 MtrCtrl_MtrDampTermDax_Ohm_M_f32[1] 0.114 MtrCtrl_MtrDampTermQax_Ohm_M_f32[0] 0.0610000007 MtrCtrl_MtrDampTermQax_Ohm_M_f32[1] 0.0579999983 MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0] 1.43400002 MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1] 0.7059997 MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0] 362.112 MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1] 65.1259995 MtrCtrl_MtrImpedDax_Ohm_M_f32[0] 0.064000003 MtrCtrl_MtrImpedDax_Ohm_M_f32[1] 0.0209999993	3
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0] 0.0430000018 MtrCtrl_MtrDampTermDax_Ohm_M_f32[1] 0.114 MtrCtrl_MtrDampTermQax_Ohm_M_f32[0] 0.0610000007 MtrCtrl_MtrDampTermQax_Ohm_M_f32[1] 0.0579999983 MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0] 1.43400002 MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1] 0.70599997 MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0] 362.112 MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1] 65.1259995 MtrCtrl_MtrImpedDax_Ohm_M_f32[0] 0.064000003 MtrCtrl_MtrImpedDax_Ohm_M_f32[1] 0.0209999993	3
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1] 0.114 MtrCtrl_MtrDampTermQax_Ohm_M_f32[0] 0.061000000 MtrCtrl_MtrDampTermQax_Ohm_M_f32[0] 0.057999998 MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0] 1.43400002 MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1] 0.70599997 MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0] 362.112 MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1] 65.1259995 MtrCtrl_MtrImpedDax_Ohm_M_f32[0] 0.064000003 MtrCtrl_MtrImpedDax_Ohm_M_f32[1] 0.020999999	3
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0] 0.061000000 MtrCtrl_MtrDampTermQax_Ohm_M_f32[1] 0.057999998 MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0] 1.43400002 MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1] 0.70599997 MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0] 362.112 MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1] 65.1259995 MtrCtrl_MtrImpedDax_Ohm_M_f32[0] 0.064000003 MtrCtrl_MtrImpedDax_Ohm_M_f32[1] 0.020999999	3
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0] 1.43400002 MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1] 0.70599997 MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0] 362.112 MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1] 65.1259995 MtrCtrl_MtrImpedDax_Ohm_M_f32[0] 0.064000003 MtrCtrl_MtrImpedDax_Ohm_M_f32[1] 0.0209999993	
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1] 0.70599997 MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0] 362.112 MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1] 65.1259995 MtrCtrl_MtrImpedDax_Ohm_M_f32[0] 0.064000003 MtrCtrl_MtrImpedDax_Ohm_M_f32[1] 0.020999999	,
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0] 362.112 MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1] 65.1259995 MtrCtrl_MtrImpedDax_Ohm_M_f32[0] 0.064000003 MtrCtrl_MtrImpedDax_Ohm_M_f32[1] 0.0209999999	,
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1] 65.1259995 MtrCtrl_MtrImpedDax_Ohm_M_f32[0] 0.064000003 MtrCtrl_MtrImpedDax_Ohm_M_f32[1] 0.0209999999	
MtrCtrl_MtrImpedDax_Ohm_M_f32[0] 0.064000003 MtrCtrl_MtrImpedDax_Ohm_M_f32[1] 0.0209999999	
MtrCtrl_MtrImpedDax_Ohm_M_f32[1] 0.0209999997	
TANDON AND THE PROPERTY OF THE	
MtrCtrl_MtrImpedQax_Ohm_M_f32[1] 0.063000001	
MtrCtrl MtrQaxIntegralGain Ohm M f32[0] 0.356999993	
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1] 0.65200001	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0] -894.130005	
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] -888.995972	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0] -29.9890003	
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1] 29.243	
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0] -29.7110004	
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1] 3.61899996	
MtrCtrl_Vecu_Volt_M_f32[0] 14.2779999	
MtrCtrl_Vecu_Volt_M_f32[1] 16.6380005 MtrCurrDaxPrevIntg_Volt_M_f32 19.7509995	
MtrCurrDaxRef_Amp_M_f32[0] 67.4899979	
MtrCurrDaxRef_Amp_M_f32[1] 119.721001	
MtrCurrQaxCog_Amp_M_f32 -181.929001	
MtrCurrQaxPrevIntg_Volt_M_f32 7.82140017	
MtrCurrQaxRef_Amp_M_f32[0] -220	
MtrCurrQaxRef_Amp_M_f32[1] -220	
MtrCurrQaxRpl_Amp_M_f32 0	
MtrPosComputationDelay_Rad_M_f32[0] -0.541999996	
MtrPosComputationDelay_Rad_M_f32[1] 3.08400011 PICurrCntrl CurrSensFailSclFac Uls M f32 0.416999996	
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32 0.416999996 PICurrCntrl_DualEcuFailSclFac_Uls_M_f32 0.0799999982	
PICurrCntrl InverterFailSclFac Uls M f32 0.787	
PICurrCntrl MtrCurrDaxSatFluxRatio Uls M f32 0.190799996	
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 0.708000004	
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32 267.119995	
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 -657.130005	
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 48410.1016	
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 0.0835999995	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_UIs_f32 267.119995	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32 -657.130005	
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_UIs_f32 48410.1016 PICurrCntrl MtrVoltQaxFFFilt M str.TermD UIs f32 0.0835999999	
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	,
k DualEcuSignalSclFacSlew UlspS f32 17.2000008	
k_ILOAFdbackSignalSclFacSlew_UlspS_f32 4233.2002	
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc 0	
k_MtrCtrlFeedbackControlDisable_Cnt_lgc 1	
k_MtrCtrlVirualResDax_Ohm_f32 0.08799999995	
k_MtrCtrlVirualResQax_Ohm_f32 0.00999999997	78
k_MtrCurrQaxRefModifDsb_Cnt_lgc 0	
k_MtrCurrQaxRefModifRplEn_Cnt_lgc 0	
k_MtrVoltDaxIntegHiLim_Volt_f32 12.9371996	
k_MtrVoltDaxIntegLoLim_Volt_f32 -0.5	
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	
k_MtrVoltQaxIntegInLim_Volt_f32	
k MtrVoltVecuFiltEnable Cnt lgc 0	

PICurrCntrl_Per1



Name	Input Value			
k_VoltSatDaxPolyCoeff_Uls_f32	-1.59399998			
k_VoltSatQaxPolyCoeff_Uls_f32	8.35700035			
k_deadtimeVScale_Uls_f32	0.950999975			
t_CommOffsetTblX_Uls_u3p13[0]	4914			
t_CommOffsetTblX_Uls_u3p13[1]	7782			
t_CommOffsetTblY_Cnt_u16[0]	1099			
t_CommOffsetTblY_Cnt_u16[1]	1672			
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	0			
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0			
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	0			
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	1			
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	-72.4260025	-72.4260025		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	4932	4932		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	77.189003			
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	2			
Name	Actual Value	Expected Value	Result	
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1672	1672	~	
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	62324	62324 ± 1	~	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-38.0709991	-38.0709991 ± 7.81E-03	~	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	6.6950984	6.69509745 ± 4.88E-04	~	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	11.8130436	11.8130436 ± 4.88E-04	~	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	65261	65261 ± 1.52588E-05	~	
MtrCurrDaxPrevIntg_Volt_M_f32	12.9371996	12.9371996	~	
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0821499974	0.0821499974 ± 0.0625	~	

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~	
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~	
MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	~	
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•	
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•	
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	~	
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~	
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•	
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	~	
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~	
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~	
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•	
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	-	
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•	
MtrCntrl_Write_ModIdx_UIs_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~	
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~	
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~	

Test Step 3.9 (Repeat Count = 1)	✓
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	1
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_Igc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc_Val
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
MtrCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
MtrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	-133.947006
MtrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	75.7020035
MtrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.0320000015
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.0869999975
MtrCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0579999983
MtrCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0500000007
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.34800005
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	1.11099994
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-942.771973
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	-380.85199
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0939999968
MtrCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0879999995
MtrCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0960000008
MtrCtrl_MtrImpedQax_Ohm_M_f32[1]	0.103

PICurrCntrl_Per1





Name	Input Value			
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.35000002			
MtrCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.749000013			
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	1024			
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1] MtrCtrl_MtrVoltDaxFF_Volt_M_f32[0]	1024 -29.3530006			
MtrCtrl MtrVoltDaxFF Volt M f32[1]		27.3040009		
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	26.4720001			
MtrCtrl_MtrVoltQaxFF_Volt_M_f32[1]		6.099999		
MtrCtrl_Vecu_Volt_M_f32[0]	5.56799984			
MtrCtrl_Vecu_Volt_M_f32[1]	7.92799997			
MtrCurrDaxPrevIntg_Volt_M_f32	15.0869999			
MtrCurrDaxRef_Amp_M_f32[0]	-166.035004			
MtrCurrDaxRef_Amp_M_f32[1]	183.065002			
MtrCurrQaxCog_Amp_M_f32	114.531998			
MtrCurrQaxPrevIntg_Volt_M_f32	5.42920017			
MtrCurrQaxRef_Amp_M_f32[0]	191.369003			
MtrCurrQaxRef_Amp_M_f32[1]	107.137001			
MtrCurrQaxRpl_Amp_M_f32	0			
MtrPosComputationDelay_Rad_M_f32[0]	2.61800003			
MtrPosComputationDelay_Rad_M_f32[1]	-1.04299998			
PICurrCotrl_CurrSensFailSclFac_UIs_M_f32	0.600000024			
PICurrCotrl_DualEcuFailSclFac_Uls_M_f32	0.090000036 0.89999976			
PICurrCntrl_InverterFailSclFac_Uls_M_f32 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.899999976			
PICurrCntrl MtrCurrQaxSatFluxRatio_Ois_M_132	0.179199994			
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1118			
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	404.899994			
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	46120.5			
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.578299999			
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	1118			
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	404.899994			
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	46120.5			
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.578299999			
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2048.80005			
k_DualEcuSignalSclFacSlew_UlspS_f32	18.3999996			
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3548.88989			
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	0			
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0			
k_MtrCtrlVirualResDax_Ohm_f32	0.079000036			
k_MtrCtrlVirualResQax_Ohm_f32	0.177000001			
k_MtrCurrQaxRefModifDsb_Cnt_lgc	0			
k_MtrCurrQaxRefModifRpIEn_Cnt_lgc k MtrVoltDaxIntegHiLim Volt f32	2.39529991			
k MtrVoltDaxIntegLoLim Volt f32	-13.1999998			
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1			
k_MtrVoltQaxIntegHiLim_Volt_f32	13.2297001			
k_MtrVoltQaxIntegLoLim_Volt_f32	-4.5			
k_MtrVoltVecuFiltEnable_Cnt_lgc	1			
k_VoltSatDaxPolyCoeff_Uls_f32	21.0030003			
k_VoltSatQaxPolyCoeff_Uls_f32	-9.26399994			
k_deadtimeVScale_Uls_f32	0.950999975			
t_CommOffsetTblX_Uls_u3p13[0]	1810			
t_CommOffsetTblX_Uls_u3p13[1]	2335			
t_CommOffsetTblY_Cnt_u16[0]	157			
t_CommOffsetTblY_Cnt_u16[1]	712			
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1			
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	1			
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1			
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0			
target_MtrCntrl_Read_MtrCurrOffcomOffcot_Cnt_u16_ntr	107.702003			
target_MtrCntrl_Read_MtrCurrOgy_Amp_f32_Val	4540 -34.6189995			
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val target_MtrCntrl_Read_SysState_Cnt_Enum_Val	-34.6189995			
		Expected Value	D. a. i	
Name MtrCntrl Write CommOffset Cnt u16(val)	Actual Value	Expected Value	Resul	
MtrCntrl_Write_CommOffset_Cnt_u16(val) MtrCntrl Write ModIdx Uls u16p16(val)	4540 0	4540 0 ± 1		
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-7.39499664	-7.39499664 ± 7.81E-03		
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	3.10204124	3.10204101 ± 4.88E-04		
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-29.3173428	-29.3173428 ± 4.88E-04		
MtrCntrl Write PhaseAdvanceFinal Rev u0p16(val)	20790	20790 ± 1.52588E-05		
MtrCurrDaxPrevIntg Volt M f32	0.910027504	0.910027981		
Miliculf DaxPrevinig Voil M 132			1	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	•
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	~
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	•
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	•
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	•
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	•
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	•
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	•
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	•
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	•
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	•
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_UIs_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	✓
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	•

Test Step 3.10 (Repeat Count = 1)	
Name	Input Value
FastDataAccessBufIndex_Cnt_M_u16	0
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc(ptr)	target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr
/trCntrl_Read_ModldxSrlComSvcDft_Cnt_lgc(Val)	target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val
/trCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc(ptr)	target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr
MtrCntrl_Read_MtrCurrDax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16(ptr)	target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr
/trCntrl_Read_MtrCurrQax_Amp_f32(Val)	target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val
ItrCntrl_Read_SysState_Cnt_Enum(Val)	target_MtrCntrl_Read_SysState_Cnt_Enum_Val
htrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[0]	171.485992
htrCtrl_MtrCurrDaxMaxVal_Amp_M_f32[1]	163.787003
htrCtrl_MtrDampTermDax_Ohm_M_f32[0]	0.125650004
MtrCtrl_MtrDampTermDax_Ohm_M_f32[1]	0.125650004
/trCtrl_MtrDampTermQax_Ohm_M_f32[0]	0.0370000005
/trCtrl_MtrDampTermQax_Ohm_M_f32[1]	0.0379999988
MtrCtrl_MtrDaxIntegralGain_Ohm_M_f32[0]	1.80200005
/trCtrl_MtrDaxIntegralGain_Ohm_M_f32[1]	0.740999997
MtrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[0]	-489.436005
htrCtrl_MtrDaxPropotionalGain_Ohm_M_f32[1]	938.341003
MtrCtrl_MtrImpedDax_Ohm_M_f32[0]	0.0199999996
/trCtrl_MtrImpedDax_Ohm_M_f32[1]	0.0879999995
/trCtrl_MtrImpedQax_Ohm_M_f32[0]	0.0120000001
/trCtrl_MtrImpedQax_Ohm_M_f32[1]	0.0560000017
/ltrCtrl_MtrQaxIntegralGain_Ohm_M_f32[0]	1.10899997
/trCtrl_MtrQaxIntegralGain_Ohm_M_f32[1]	0.479999989
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[0]	916.997009
MtrCtrl_MtrQaxPropotionalGain_Ohm_M_f32[1]	1002.97998
/trCtrl_MtrVoltDaxFF_Volt_M_f32[0]	-26.5079994
MtrCtrl_MtrVoltDaxFF_Volt_M_f32[1]	4.36100006
htrCtrl_MtrVoltQaxFF_Volt_M_f32[0]	15.1960001
/trCtrl_MtrVoltQaxFF_Volt_M_f32[1]	-2.83699989
/trCtrl_Vecu_Volt_M_f32[0]	5.33099985
/trCtrl_Vecu_Volt_M_f32[1]	7.69099998
/trCurrDaxPrevIntg_Volt_M_f32	6.17600012
/trCurrDaxRef_Amp_M_f32[0]	-146.173996
MtrCurrDaxRef_Amp_M_f32[1]	-213.335007
/trCurrQaxCog_Amp_M_f32	152.016006
/trCurrQaxPrevIntg_Volt_M_f32	1.08770001
/trCurrQaxRef Amp M f32[0]	-216.921997
ItrCurrQaxRef Amp M f32[1]	-184.923996
ItrCurrQaxRpl Amp M f32	0
ItrPosComputationDelay Rad M f32[0]	-3.13800001
/trPosComputationDelay Rad M f32[1]	2.11599994
PlCurrCntrl CurrSensFailSclFac Uls M f32	0.432999998
PlCurrCntrl DualEcuFailSclFac Uls M f32	0.5
PICurrCntrl InverterFailSclFac Uls M f32	0.0109999999



PICurrCntrl	_Per1

·			
Name	Input Value		
PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32	0.335599989		
PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32	0.851999998		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-10.21		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	12079.9004		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0.620700002		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevInput_Uls_f32	-43.1699982		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.PrevOutput_Uls_f32	-10.21		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermN_Uls_f32	12079.9004		
PICurrCntrl_MtrVoltQaxFFFilt_M_str.TermD_Uls_f32	0.620700002		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2475.81006		
k_DualEcuSignalSclFacSlew_UlspS_f32	19.6000004		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2645.06006		
k_MtrCtrlCurrLoopSecOrTranFcEnable_Cnt_lgc	1		
k_MtrCtrlFeedbackControlDisable_Cnt_lgc	0		
k_MtrCtrlVirualResDax_Ohm_f32	0.179000005		
k_MtrCtrlVirualResQax_Ohm_f32	0.0120000001		
k_MtrCurrQaxRefModifDsb_Cnt_lgc	1		
k_MtrCurrQaxRefModifRplEn_Cnt_lgc	0		
k_MtrVoltDaxIntegHiLim_Volt_f32	7.70650005		
k_MtrVoltDaxIntegLoLim_Volt_f32	-4.0999999		
k_MtrVoltQaxFiltFFEnable_Cnt_lgc	1		
k_MtrVoltQaxIntegHiLim_Volt_f32	0.614899993		
k_MtrVoltQaxIntegLoLim_Volt_f32	-6.5		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
k_VoltSatDaxPolyCoeff_Uls_f32	-1.26999998		
k_VoltSatQaxPolyCoeff_Uls_f32	16.9449997		
k_deadtimeVScale_Uls_f32	0.962000012		
t_CommOffsetTblX_Uls_u3p13[0]	4809		
t_CommOffsetTblX_Uls_u3p13[1]	5553		
t_CommOffsetTblY_Cnt_u16[0]	663		
t_CommOffsetTblY_Cnt_u16[1]	905		
target_MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc_ptr	1		
target_MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc_Val	1		
target_MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc_ptr	0		
target_MtrCntrl_Read_MtrCurrDax_Amp_f32_Val	114.946999		
target_MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16_ptr	1956		
target_MtrCntrl_Read_MtrCurrQax_Amp_f32_Val	-198.285995		
target_MtrCntrl_Read_SysState_Cnt_Enum_Val	1		
Name	Actual Value	Expected Value	Result
MtrCntrl_Write_CommOffset_Cnt_u16(val)	1956	1956	-
MtrCntrl_Write_ModIdx_Uls_u16p16(val)	0	0 ± 1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32(val)	-220	-220 ± 7.81E-03	~
MtrCntrl_Write_MtrDaxVoltage_Volt_f32(val)	3.72783184	3.7278316 ± 4.88E-04	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32(val)	-3.03963304	-3.03963351 ± 4.88E-04	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16(val)	56324	56324 ± 1.52588E-05	~
MtrCurrDaxPrevIntg_Volt_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.497550011	0.497550011 ± 0.0625	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
MtrCntrl_Read_MtrCurrQax_Amp_f32	1	MtrCntrl_Read_MtrCurrQax_Amp_f32	1	~
MtrCntrl_Read_MtrCurrDax_Amp_f32	1	MtrCntrl_Read_MtrCurrDax_Amp_f32	1	~
MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	MtrCntrl_Read_ModIdxSrlComSvcDft_Cnt_lgc	1	•
MtrCntrl_Read_SysState_Cnt_Enum	1	MtrCntrl_Read_SysState_Cnt_Enum	1	•
MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_MotCurrLoaMtgtnEn_Cnt_lgc	1	•
MtrCntrl_Read_IvtrLoaMtgtnEn_Cnt_lgc	1	MtrCntrl_Read_lvtrLoaMtgtnEn_Cnt_lgc	1	✓
MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	MtrCntrl_Read_DualEcuMotCtrlMtgnEna_Cnt_lgc	1	~
CalLowPassFiltVecuOut	1	CalLowPassFiltVecuOut	1	~
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	-
IntegralStateVarNonOperState	1	IntegralStateVarNonOperState	1	✓
LoaMtgtnSclFac	1	LoaMtgtnSclFac	1	~
IntplVarXY_u16_u16Xu16Y_Cnt	1	IntplVarXY_u16_u16Xu16Y_Cnt	1	~
MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	MtrCntrl_Read_MtrCurrOffComOffset_Cnt_u16	1	~
MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	MtrCntrl_Write_MtrCurrQaxFinalRef_Amp_f32	1	✓
MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrDaxVoltage_Volt_f32	1	~
MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	MtrCntrl_Write_MtrQaxVoltage_Volt_f32	1	~
MtrCntrl_Write_ModIdx_Uls_u16p16	1	MtrCntrl_Write_ModIdx_Uls_u16p16	1	~
MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	MtrCntrl_Write_PhaseAdvanceFinal_Rev_u0p16	1	~
MtrCntrl_Write_CommOffset_Cnt_u16	1	MtrCntrl_Write_CommOffset_Cnt_u16	1	~

2016-09-15, 18:23:31+0530

PICurrCntrl_Per1



2016-09-15, 18:05:30+0530



LoaMtgtnSclFac

Project MtrCtrl_CM_SF99B

 Module
 PICurrCntrl

 Test Object
 LoaMtgtnSclFac

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\MtrCtrl_CM_SF99B
Configuration File	D:\Synergy_Work_Area\MtrCtrl_CM_SF99B\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\MtrCtrl_CM\src\Ap_PICurrCntrl.c
Compiler Options	-D_DATA_ACCESS= -Dsqrtf=sqrtf -Dconst= -I\$(PROJECTROOT)\MtrCtrl_CM\utp\contract\Ap_PlCurrCntrl -I\$(PROJECTROOT) \MtrCtrl_CM\utp\contract -I\$(PROJECTROOT)\MtrCtrl_CM\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef \include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dsqrtf=sqrtf -Dconst= -I\$(PROJECTROOT)\MtrCtrl_CM\utp\contract\Ap_PlCurrCntrl -I\$(PROJECTROOT) \MtrCtrl_CM\utp\contract -1\$(PROJECTROOT)\MtrCtrl_CM\utp\contract -1\$(PROJECTROOT)\MtrCtrl_CM\utp\contract -1\$(PROJECTROOT)\StdDef \\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\

Comments/Description/Specification			
Name	Text		



Module 'PICurrCntri'

Name of Tester:Konal Sharma
Code File(s) Under Test:Ap_PICurrCntrl.c
Code File(s) Version:16
Module Design Document:PICurrentContrl.doc
Module Design Document Version:12
Data Dictionary Version:15
Unit Test Plan Version:4
Optimization Level:Level 2
Compiler (CodeGen) Version:TMS570_4.9.5
Model Type:Excel Macro
Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32
Total FLASH Used (Bytes):2865
Special Test Requirements:NA
Test Date:9/15/2016
Comments:"Note 1: INLINE functions defined in globalmacro.h are not unit tested.

Note 3: Out of range value is given in function ""LoaMtgtnSclFacSlew_UlspS_f32, PlCurrCntrl_DualEcuFailSclFac_Uls_M_f32 and PlCurrCntrl_InverterFailSclFac_Uls_M_f32" to achieve 100% path coverage in Path sheet.

Note 4: In function PICurrCntrl_Per1 PICurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_f32 and PICurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_f32 variables are going out of range.

Note 5: In function PICurrCntrl_Per1, the range of MtrPosComputationDelay_Rad_M_f32[2] is considered as -3.14 to 3.14"

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj</pre>
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
Timer Unit	Cycles
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\MtrCtrl_CM_SF99B\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



Test Case 1: Metric Test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles:

TS 1.1 170.00 Cycles TS 1.2 225.00 Cycles

Description Vector Description:

TS 1.1Shortest Path==>(MotCurrLoaMtgtnEn_Cnt_T_lgc == TRUE)=False&&(1>=((lD_MTRCTRLISRRATE_MS_F32 * - k_CLOAFdbackSignalSclFacSlew_UlspS_f32)+ PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32))=True&&(1>=(lD_MTRCTRLISRRATE_MS_F32 * k_ILOAFdbackSignalSclFacSlew_UlspS_f32)+PlCurrCntrl_inverterFailSclFac_Uls_M_f32))=True
TS 1.2Longest Path==>(MotCurrLoaMtgtnEn_Cnt_T_lgc == TRUE)=True&&(0>=((lD_MTRCTRLISRRATE_MS_F32 * - k_CLOAFdbackSignalSclFacSlew_UlspS_f32)+ PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32))=False&&(0<=((lD_MTRCTRLISRRATE_MS_F32 * - k_CLOAFdbackSignalSclFacSlew_UlspS_f32)+PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32))=False&&(0<=(lD_MTRCTRLISRRATE_MS_F32 * k_ILOAFdbackSignalSclFacSlew_UlspS_f32)+PlCurrCntrl_inverterFailSclFac_Uls_M_f32))=false&&(0<=(lD_MTRCTRLISRRATE_MS_F32 * k_ILOAFdbackSignalSclFacSlew_UlspS_f32)+PlCurrCntrl_inverterFailSclFac_Uls_M_f32))=false

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	0		
IvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	10		
k_DualEcuSignalSclFacSlew_UlspS_f32	10		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	10		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.00125000009	0.00125000009	✓
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.00125000009	0.00125000009	✓
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.00125000009	0.00125000009	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	1		
IvtrLoaMtgtnEn_Cnt_T_lgc	1		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	1		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	8000		
k_DualEcuSignalSclFacSlew_UlspS_f32	1000		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	8000		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0	0	✓
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.875	0.875	✓
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0	0	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~



Test Case 2: Boundary Test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles:

TS 2.1 170.00 Cycles
TS 2.2 225.00 Cycles
TS 2.2 225.00 Cycles
TS 2.3 187.00 Cycles
TS 2.4 181.00 Cycles
TS 2.5 170.00 Cycles
TS 2.6 196.00 Cycles
TS 2.7 170.00 Cycles
TS 2.8 201.00 Cycles
TS 2.9 201.00 Cycles
TS 2.10 201.00 Cycles
TS 2.11 214.00 Cycles
TS 2.12 201.00 Cycles
TS 2.12 201.00 Cycles
TS 2.13 188.00 Cycles
TS 2.14 164.00 Cycles
TS 2.15 200.00 Cycles
TS 2.16 236.00 Cycles
TS 2.17 190.00 Cycles
TS 2.18 201.00 Cycles
TS 2.19 200.00 Cycles
TS 2.19 200.00 Cycles
TS 2.19 200.00 Cycles

Description Vector Description:

TS 2.1All_Min

TS 2.1AII_Min
TS 2.2AII_Max
TS 2.2AII_Max
TS 2.3MotCurrLoaMtgtnEn_Cnt_T_lgc==>Min
TS 2.4MotCurrLoaMtgtnEn_Cnt_T_lgc==>Max
TS 2.5IvtrLoaMtgtnEn_Cnt_T_lgc==>Max
TS 2.5IvtrLoaMtgtnEn_Cnt_T_lgc==>Min
TS 2.6IvtrLoaMtgtnEn_Cnt_T_lgc==>Max
TS 2.7PICurrCntrl_CurrSensFailSclFac_Uls_M_f32==>Min
TS 2.8PICurrCntrl_CurrSensFailSclFac_Uls_M_f32==>Mid
TS 2.9PICurrCntrl_CurrSensFailSclFac_Uls_M_f32==>Mid
TS 2.10k_CLOAFdbackSignalSclFacSlew_UlspS_f32==>Min
TS 2.11k_CLOAFdbackSignalSclFacSlew_UlspS_f32==>Max
TS 2.12k_CLOAFdbackSignalSclFacSlew_UlspS_f32==>Mid
TS 2.13k_CLOAFdbackSignalSclFacSlew_UlspS_f32==>Mid
TS 2.15k_ILOAFdbackSignalSclFacSlew_UlspS_f32==>Min
TS 2.15k_ILOAFdbackSignalSclFacSlew_UlspS_f32==>Mix
TS 2.15k_ILOAFdbackSignalSclFacSlew_UlspS_f32==>Mid
TS 2.17k_ILOAFdbackSignalSclFacSlew_UlspS_f32==>Mid
TS 2.18PICurrCntrl_InverterFailSclFac_Uls_M_f32==>Mix
TS 2.19PICurrCntrl_InverterFailSclFac_Uls_M_f32==>Mid

Test Step 2.1 (Repeat Count = 1)			✓
Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	0		
IvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	10		
k_DualEcuSignalSclFacSlew_UlspS_f32	10		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	10		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.00125000009	0.00125000009	·
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.00125000009	0.00125000009	•
PICurrCntrl InverterEailSclEac Llls M f32	0.00125000009	0.00125000009	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.2 (Repeat Count = 1)			✓
Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	1		
lvtrLoaMtgtnEn_Cnt_T_lgc	1		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	1		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	8000		
k_DualEcuSignalSclFacSlew_UlspS_f32	8000		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	8000		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0	0	-

2016-09-15, 18:05:30+0530



Name	Actual Value	Expected Value	Result
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0	0	~
PICurrCntrl InverterFailSclFac Uls M f32	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.3 (Repeat Count = 1)			✓
Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	0		
IvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.235599995		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.100000001		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.235599995		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7702.76172		
k_DualEcuSignalSclFacSlew_UlspS_f32	11		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	6599.25586		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1	1	✓
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.101374999	0.101374999	~
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.4 (Repeat Count = 1)			✓
Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	1		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.325599998		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.20000003		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.324999988		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7466.021		
k_DualEcuSignalSclFacSlew_UlspS_f32	21.2000008		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5753.875		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.197349995	0.197349995	~
PICurrCntrl InverterFailSclFac Uls M f32	1	1	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	

Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	0		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.142499998		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.30000012		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.124499999		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	6719.13281		
k_DualEcuSignalSclFacSlew_UlspS_f32	310		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	1788.25342		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.982391596	0.982391596	•
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.338750005	0.338750005	•
PICurrCntrl InverterFailSclFac Uls M f32	0.3480317	0.3480317	•



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.6 (Repeat Count = 1)			✓
Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	0		
lvtrLoaMtgtnEn_Cnt_T_lgc	1		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.325599998		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.40000006		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.325599998		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3280.9021		
k_DualEcuSignalSclFacSlew_UlspS_f32	41		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2855.32861		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.735712767	0.735712767	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.405124992	0.405124992	•
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0	0	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.7 (Repeat Count = 1)			✓
Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	0		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.5		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.225600004		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7916.521		
k_DualEcuSignalSclFacSlew_UlspS_f32	15.5		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	2851.41992		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.989565194	0.989565194	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.501937509	0.501937509	✓
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.582027555	0.582027555	~

Test Step Call Trace					V
Actual Function	Co	ount	Expected Function	Count	Result
none	0		*** No Call Expected ***	0	~

Name	Input Value		
DualEcuMotCtrlMtgnEna Cnt T lgc	1		
IvtrLoaMtgtnEn Cnt T Igc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.60000024		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.325599998		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2075.21021		
k_DualEcuSignalSclFacSlew_UlspS_f32	61		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	1642.60645		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.740598679	0.740598679	•
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.59237504	0.59237504	•
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.53092581	0.53092581	•



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.9 (Repeat Count = 1)			✓
Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	1		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.5		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.69999988		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.324400008		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	1404.30225		
k_DualEcuSignalSclFacSlew_UlspS_f32	71		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4636.45898		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.324462205	0.324462205	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.691124976	0.691124976	~
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.903957427	0.903957427	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.10 (Repeat Count = 1)			✓
Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	0		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.532599986		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.80000012		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.125599995		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	10		
k_DualEcuSignalSclFacSlew_UlspS_f32	810		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5044.229		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.531349957	0.531349957	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.901250005	0.901250005	~
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.756128609	0.756128609	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	1		
lvtrLoaMtgtnEn_Cnt_T_lgc	1		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.321399987		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.89999976		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.123559996		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	8000		
k_DualEcuSignalSclFacSlew_UlspS_f32	91.1999969		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	1124.24878		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1	1	•
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.888599992	0.888599992	•
PICurrCntrl InverterFailSclFac Uls M f32	0	0	•



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	

Test Step 2.12 (Repeat Count = 1)			✓
Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	1		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.235599995		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0099999978		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.325599998		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	100		
k_DualEcuSignalSclFacSlew_UlspS_f32	101		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4636.45898		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.223099992	0.223099992	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0	0	•
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.905157447	0.905157447	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.13 (Repeat Count = 1)			✓
Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	1		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.833199978		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.019999996		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.142499998		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	4217.00098		
k_DualEcuSignalSclFacSlew_UlspS_f32	111.300003		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	497.261292		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1	1	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.00608749874	0.00608749874	✓
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.204657659	0.204657659	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	0		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.246099994		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.029999993		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.788800001		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	5544.1499		
k_DualEcuSignalSclFacSlew_UlspS_f32	121		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	10		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0	0	•
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0451250002	0.0451250002	•
PICurrCntrl InverterFailSclFac Uls M f32	0.79005003	0.79005003	•



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	

Test Step 2.15 (Repeat Count = 1)			✓
Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	1		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.039999991		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.874260008		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	892.101318		
k_DualEcuSignalSclFacSlew_UlspS_f32	133		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	8000		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.888487339	0.888487339	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0233749989	0.0233749989	~
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1	1	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.16 (Repeat Count = 1)			✓
Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	0		
lvtrLoaMtgtnEn_Cnt_T_lgc	1		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.214499995		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.050000007		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.612500012		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	892.101318		
k_DualEcuSignalSclFacSlew_UlspS_f32	141		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	100		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.326012671	0.326012671	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0676250011	0.0676250011	~
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.60000024	0.600000024	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	1		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.321399987		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.059999987		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.536199987		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	886.920471		
k_DualEcuSignalSclFacSlew_UlspS_f32	151		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	1071.5		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.432265043	0.432265043	•
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0411249995	0.0411249995	•
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.670137525	0.670137525	•



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	

Test Step 2.18 (Repeat Count = 1)			✓
Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	0		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.369800001		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.070000003		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2332.00488		
k_DualEcuSignalSclFacSlew_UlspS_f32	464		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3405.60864		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.0782993734	0.0782993734	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.128000006	0.128000006	✓
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.425701112	0.425701112	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.19 (Repeat Count = 1)			✓
Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	1		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.803200006		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.079999982		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3197.43726		
k_DualEcuSignalSclFacSlew_UlspS_f32	571.22998		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	1505.17786		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.403520316	0.403520316	✓
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.00859624892	0.00859624892	✓
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1	1	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Name	Input Value		
DualEcuMotCtrlMtgnEna Cnt T Igc	0		
IvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.0324999988		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.090000036		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.653999984		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	4477.14648		
k_DualEcuSignalSclFacSlew_UlspS_f32	678		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3969.39355		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.592143297	0.592143297	•
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.17475	0.17475	•
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1	1	•





Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.21 (Repeat Count = 1)			✓
Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	0		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.803200006		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.10000001		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	3197.43726		
k_DualEcuSignalSclFacSlew_UlspS_f32	785.200012		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	1505.17786		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.403520316	0.403520316	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.198150009	0.198150009	•
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1	1	✓

Test Step 2.22 (Repeat Count = 1)			✓
Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	1		
lvtrLoaMtgtnEn_Cnt_T_lgc	1		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.0324999988		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.109999999		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.653999984		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	4477.14648		
k_DualEcuSignalSclFacSlew_UlspS_f32	892		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3969.39355		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.592143297	0.592143297	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0	0	~
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.157825768	0.157825768	✓

Test Step 2.23 (Repeat Count = 1)			~
Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	0		
IvtrLoaMtgtnEn_Cnt_T_lgc	1		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.214499995		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.119999997		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.612500012		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	892.101318		
k_DualEcuSignalSclFacSlew_UlspS_f32	10		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	100		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.326012671	0.326012671	-
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.121249996	0.121249996	✓
PICurrCntrl InverterFailSclFac Uls M f32	0.60000024	0.600000024	✓

Test Step 2.24 (Repeat Count = 1)		✓
Name	Input Value	
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	1	
lvtrLoaMtgtnEn_Cnt_T_lgc	0	
MotCurrLoaMtgtnEn_Cnt_T_lgc	0	
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.321399987	
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.129999995	
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.536199987	
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	886.920471	
k_DualEcuSignalSclFacSlew_UlspS_f32	8000	

2016-09-15, 18:05:30+0530



Name	Input Value		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	1071.5		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.432265043	0.432265043	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0	0	✓
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.670137525	0.670137525	✓

Test Step 2.25 (Repeat Count = 1)			✓
Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	0		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.369800001		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.14000001		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	2332.00488		
k_DualEcuSignalSclFacSlew_UlspS_f32	100		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	3405.60864		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.0782993734	0.0782993734	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.152500004	0.152500004	✓
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.425701112	0.425701112	~

Test Step 2.26 (Repeat Count = 1)			
Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	1		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.5		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.324400008		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	1404.30225		
k_DualEcuSignalSclFacSlew_UlspS_f32	1320		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	4636.45898		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.324462205	0.324462205	•
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0	0	•
PICurrCntrl InverterFailSclFac Uls M f32	0.903957427	0.903957427	•

Test Step 2.27 (Repeat Count = 1)			J
Name	Input Value		Ť
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	0		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.532599986		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	1		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.125599995		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	10		
k_DualEcuSignalSclFacSlew_UlspS_f32	251.100006		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5044.229		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.531349957	0.531349957	*
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	1	1	~
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.756128609	0.756128609	~

Test Step 2.28 (Repeat Count = 1)	✓
Name	Input Value
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	1
IvtrLoaMtgtnEn_Cnt_T_lgc	0
MotCurrLoaMtgtnEn_Cnt_T_lgc	1
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.325599998
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.170000002
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.324999988
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7466.021



Name	Input Value		
k_DualEcuSignalSclFacSlew_UlspS_f32	261		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	5753.875		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.137374997	0.137374997	✓
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1	1	~

Test Case 3: Path test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cycles:

TS 3.1 170.00 Cycles
TS 3.2 225.00 Cycles
TS 3.3 187.00 Cycles
TS 3.4 223.00 Cycles
TS 3.5 190.00 Cycles
TS 3.6 243.00 Cycles
TS 3.7 206.00 Cycles
TS 3.8 206.00 Cycles
TS 3.9 208.00 Cycles
TS 3.10 206.00 Cycles
TS 3.10 206.00 Cycles

TS 3.10 206.00 Cycles TS 3.11 206.00 Cycles

Description

Vector Description:

TS 3.1(MotCurLoaMtgtnEn_Cnt_T_igc == TRUE)=False&&(1>=((ID_MTRCTRLISRRATE_MS_F32 * - k_CLOAFdbackSignalSclFacSlew_UlspS_f32)+ PlCurrCntrl_currSensFailSclFac_Uls_M_f32))=True&&(1>=(D_MTRCTRLISRRATE_MS_F32 * k_LOAFdbackSignalSclFacSlew_UlspS_f32)+ PlCurrCntrl_inverterFailSclFac_Uls_M_f32))=True
TS 3.2(MotCurLoaMtgtnEn_Cnt_T_igc == TRUE)=True&&(0>=((ID_MTRCTRLISRRATE_MS_F32 * - k_CLOAFdbackSignalSclFacSlew_UlspS_f32)+ PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32))=False&&(0<=((ID_MTRCTRLISRRATE_MS_F32 * - k_CLOAFdbackSignalSclFacSlew_UlspS_f32)+ PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32))=False&&(0<=((ID_MTRCTRLISRRATE_MS_F32 * - k_CLOAFdbackSignalSclFacSlew_UlspS_f32)+ PlCurrCntrl_inverterFailSclFac_Uls_M_f32))=false&&(0<=(ID_MTRCTRLISRRATE_MS_F32 * k_LOAFdbackSignalSclFacSlew_UlspS_f32)+ PlCurrCntrl_inverterFailSclFac_Uls_M_f32))=false
TS 3.3(1>=((ID_MTRCTRLISRRATE_MS_F32 * - k_CLOAFdbackSignalSclFacSlew_UlspS_f32)+ PlCurrCntrl_inverterFailSclFac_Uls_M_f32))=false
TS 3.3(1>=(ID_MTRCTRLISRRATE_MS_F32 * - k_CLOAFdbackSignalSclFacSlew_UlspS_f32)+ PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32))=False&&(1>=(D_MTRCTRLISRRATE_MS_F32 * k_CLOAFdbackSignalSclFacSlew_UlspS_f32)+ PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32))=False&&(1>=(D_MTRCTRLISRRATE_MS_F32 * k_LOAFdbackSignalSclFacSlew_UlspS_f32)+ PlCurrCntrl_InverterFailSclFac_Uls_M_f32))=False&&(1>=(D_MTRCTRLISRRATE_MS_F32 * k_LOAFdbackSignalSclFacSlew_UlspS_f32)+ PlCurrCntrl

k_CLOAFdbackSignalSclFacSlew_UlspS_f32)+ PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32))=False&&(1>=(D_MTRCTRLISRRATE_MS_F3 k_ILOAFdbackSignalSclFacSlew_UlspS_f32)+PlCurrCntrl_InverterFailSclFac_Uls_M_f32))=False &&(1<=(D_MTRCTRLISRRATE_MS_F32 k_ILOAFdbackSignalSclFacSlew_UlspS_f32)+PlCurrCntrl_InverterFailSclFac_Uls_M_f32))=False TS 3.4(1<=((D_MTRCTRLISRRATE_MS_F32 k_CLOAFdbackSignalSclFacSlew_UlspS_f32)+PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32))=true TS 3.5(0>=((D_MTRCTRLISRRATE_MS_F32 k_CLOAFdbackSignalSclFacSlew_UlspS_f32)+PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32))=False TS 3.6&&(0<=((D_MTRCTRLISRRATE_MS_F32 k_CLOAFdbackSignalSclFacSlew_UlspS_f32)+PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32))=False TS 3.6&&(0<=((D_MTRCTRLISRRATE_MS_F32 k_CLOAFdbackSignalSclFacSlew_UlspS_f32)+PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32))=Talse TS 3.6&&(0<=((D_MTRCTRLISRRATE_MS_F32 k_CLOAFdbackSignalSclFacSlew_UlspS_f32)+PlCurrCntrl_CurrSensFailSclFac_Uls_M_f32)=Talse TS 3.6&&(0<=((D_MTRCTRLISRRATE_MS_f32 k_CLOAFdbackSignalSclFacSlew_Uls_MS_f32 k_CLOAFdbackSignalSclFacSlew_Uls_MS_f32 k_CLOAFdbackSignalSclFacSlew_Uls_MS_f32 k_CLOAFdbackSignalSclFacSlew_Uls_MS_f32 k_CLOAFdb

TS 3.6&&(0<=((D_MTRCTRLISRRATE_MS_F32 * -k_CLOAFdbackSignalSclFacSiew_uisps_f32)+ PlCurrCntrl_currSensFailSclFac_Uls_M_f32))=True TS 3.7(1<=(D_MTRCTRLISRRATE_MS_F32 * k_ILOAFdbackSignalSclFacSiew_Ulsps_f32)+PlCurrCntrl_InverterFailSclFac_Uls_M_f32))=True TS 3.8(0>=(D_MTRCTRLISRRATE_MS_F32 * k_ILOAFdbackSignalSclFacSiew_Ulsps_f32)+PlCurrCntrl_inverterFailSclFac_Uls_M_f32))=True TS 3.9(0<=(D_MTRCTRLISRRATE_MS_F32 * k_ILOAFdbackSignalSclFacSiew_Ulsps_f32)+PlCurrCntrl_inverterFailSclFac_Uls_M_f32))=True TS 3.10(1<=(D_MTRCTRLISRRATE_MS_F32 * -k_DualEcuSignalSclFacSiew_Ulsps_f32)+PlCurrCntrl_DualEcuFailSclFac_Uls_M_f32))=True TS 3.11(0>=(D_MTRCTRLISRRATE_MS_F32 * -k_DualEcuSignalSclFacSiew_Ulsps_f32)+ PlCurrCntrl_DualEcuFailSclFac_Uls_M_f32))=True

Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	0		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	10		
k_DualEcuSignalSclFacSlew_UlspS_f32	10		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	10		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.00125000009	0.00125000009	•
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.00125000009	0.00125000009	•
PICurrCntrl InverterFailSclFac Uls M f32	0.00125000009	0.00125000009	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	

Test Step 3.2 (Repeat Count = 1)		✓
Name	Input Value	
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	1	
lvtrLoaMtgtnEn_Cnt_T_lgc	1	
MotCurrLoaMtgtnEn_Cnt_T_lgc	1	

2016-09-15, 18:05:30+0530



Name	Input Value		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.100000001		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	8000		
k_DualEcuSignalSclFacSlew_UlspS_f32	20		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	8000		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0975000039	0.0975000039	•
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0	0	~

Test Step Call Trace					V	
	Actual Function	Count	Expected Function	Count	Resu	ılt
	none	0	*** No Call Expected ***	0		~

Test Step 3.3 (Repeat Count = 1)			✓
Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	0		
IvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.235599995		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.20000003		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0.235599995		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7702.75977		
k_DualEcuSignalSclFacSlew_UlspS_f32	30		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	6599.25977		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1	1	✓
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.203749999	0.203749999	✓
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 3.4 (Repeat Count = 1)			✓
Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	1		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	2		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.30000012		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	0		
k_DualEcuSignalSclFacSlew_UlspS_f32	40		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	6599.25977		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	2	2	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.295000017	0.295000017	✓
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 3.5 (Repeat Count = 1)	✓
Name	Input Value
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	0
lvtrLoaMtgtnEn_Cnt_T_lgc	1
MotCurrLoaMtgtnEn_Cnt_T_lgc	1
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	-1
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.40000006
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1

2016-09-15, 18:05:30+0530



Name	Input Value		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	0		
k_DualEcuSignalSclFacSlew_UlspS_f32	50		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	8000		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	-1	-1	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.40625	0.40625	✓
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 3.6 (Repeat Count = 1)			✓
Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	1		
lvtrLoaMtgtnEn_Cnt_T_lgc	1		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.5		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	0		
k_DualEcuSignalSclFacSlew_UlspS_f32	60		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	8000		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1	1	✓
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.492500007	0.492500007	✓
PICurrCntrl_InverterFailSclFac_Uls_M_f32	0	0	✓

Test Step Call Trace					✓
Ac	tual Function	Count	Expected Function	Count	Result
no	ne	0	*** No Call Expected ***	0	~

Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	0		
IvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.235599995		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.60000024		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	2		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7702.75977		
k_DualEcuSignalSclFacSlew_UlspS_f32	70		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	0		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1	1	-
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.608750045	0.608750045	•
PICurrCntrl InverterFailSclFac Uls M f32	2	2	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~





Test Step 3.8 (Repeat Count = 1)			✓
Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	1		
IvtrLoaMtgtnEn_Cnt_T_lgc	1		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.69999988		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	-1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	8000		
k_DualEcuSignalSclFacSlew_UlspS_f32	80		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	0		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.68999998	0.68999998	~
PICurrCntrl_InverterFailSclFac_Uls_M_f32	-1	-1	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 3.9 (Repeat Count = 1)			✓
Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	1		
IvtrLoaMtgtnEn_Cnt_T_lgc	1		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.019999996		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	2		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	8000		
k_DualEcuSignalSclFacSlew_UlspS_f32	100		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	8000		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0	0	✓
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	0.0074999937	0.0074999937	✓
PICurrCntrl InverterFailSclFac Uls M f32	1	1	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 3.10 (Repeat Count = 1)			✓
Name	Input Value		
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	0		
lvtrLoaMtgtnEn_Cnt_T_lgc	0		
MotCurrLoaMtgtnEn_Cnt_T_lgc	0		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0.235599995		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	2		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	-1		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	7702.75977		
k_DualEcuSignalSclFacSlew_UlspS_f32	0		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	0		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1	1	✓
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	2	2	✓
PICurrCntrl_InverterFailSclFac_Uls_M_f32	-1	-1	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 3.11 (Repeat Count = 1)	→
Name	Input Value
DualEcuMotCtrlMtgnEna_Cnt_T_lgc	1

2016-09-15, 18:05:30+0530



Name	Input Value		
IvtrLoaMtgtnEn_Cnt_T_lgc	1		
MotCurrLoaMtgtnEn_Cnt_T_lgc	1		
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	1		
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	-1		
PICurrCntrl_InverterFailSclFac_Uls_M_f32	2		
k_CLOAFdbackSignalSclFacSlew_UlspS_f32	8000		
k_DualEcuSignalSclFacSlew_UlspS_f32	0		
k_ILOAFdbackSignalSclFacSlew_UlspS_f32	0		
Name	Actual Value	Expected Value	Result
PICurrCntrl_CurrSensFailSclFac_Uls_M_f32	0	0	~
PICurrCntrl_DualEcuFailSclFac_Uls_M_f32	-1	-1	✓
PICurrCntrl_InverterFailSclFac_Uls_M_f32	2	2	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	•

2016-09-15, 18:00:45+0530



CalLowPassFiltVecuOut

Project MtrCtrl_CM_SF99B

Module PICurrCntrl

Test Object CalLowPassFiltVecuOut

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\MtrCtrl_CM_SF99B
Configuration File	D:\Synergy_Work_Area\MtrCtrl_CM_SF99B\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\MtrCtrl_CM\src\Ap_PlCurrCntrl.c
Compiler Options	-D_DATA_ACCESS= -Dsqrtf=sqrtf -Dconst= -I\$(PROJECTROOT)\MtrCtrl_CM\utp\contract\Ap_PlCurrCntrl -I\$(PROJECTROOT) \MtrCtrl_CM\utp\contract -I\$(PROJECTROOT)\\MtrCtrl_CM\utp\contract -I\$(PROJECTROOT)\\StdDef \include -I\$(Compiler Install Path)\include
File	\$(PROJECTROOT)\NxtrLib\src\interpolation.c
Compiler Options	-D_DATA_ACCESS= -Dsqrtf=sqrtf -Dconst= -I\$(PROJECTROOT)\MtrCtrl_CM\utp\contract\Ap_PlCurrCntrl -I\$(PROJECTROOT) \MtrCtrl_CM\utp\contract -1\$(PROJECTROOT)\MtrCtrl_CM\utp\contract -1\$(PROJECTROOT)\MtrCtrl_CM\utp\contract -1\$(PROJECTROOT)\StdDef \\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\

Comments/Description/Spe	ecification
Name	Text





Module 'PlCurrCntrl'

Name of Tester:Komal Sharma
Code File(s) Under Test:Ap_PlCurrCntrl.c
Code File(s) Version:16
Module Design Document:PlCurrentContrl.doc
Module Design Document Version:12
Data Dictionary Version:15
Unit Test Plan Version:35
Unit Test Plan Version:4
Optimization Level:Level 2
Compiler (CodeGen) Version:TMS570_4.9.5
Model Type:Excel Macro
Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32
Total FLASH Used (Bytes):2834
Total RAM Used (Bytes):164
Total CAL S Used (Bytes):164
Total CAL S Used (Bytes):2865
Special Test Requirements:NA
Test Date:9/15/2016
Comments:"Note 1: INLINE functions defined in globalmacro.h are not unit tested.

Note 2: ""CBD_Sandbox_dbg.map""map file is embedded for reference.

Note 3: Out of range value is given in function "LoaMfgtnSclFac" for variables
""k_CLOAFdbackSignalSclFacSlew_UlspS_132.k_ILOAFdbackSignalSclFacSlew_UlspS_532,k_DualEcuSignalSclFacSlew_UlspS_132, PlCurrCntrl_DualEcuFailSclFac_Uls_M_132 and PlCurrCntrl_InverterFailSclFac_Uls_M_132 variables are going out of range.

Note 4: In function PlCurrCntrl_Per1 PlCurrCntrl_MtrCurrDaxSatFluxRatio_Uls_M_132 and PlCurrCntrl_MtrCurrQaxSatFluxRatio_Uls_M_132 variables are going out of range.

Note 5: In function PlCurrCntrl_Per1, the range of MtrPosComputationDelay_Rad_M_132[3] is considered as -3.14 to 3.14"

Attuibutas	
Attributes	
Name	Value
CTE File	<pre>\$(PROJECTROOT)\tessy\persist\tessy\project\00000412\0000099f\.database\.tdb \000009B3\CalLowPassFiltVecuOut.cte</pre>
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
Timer Unit	Cycles
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy Work Area\MtrCtrl CM SF99B\UnitTestEnv\config\UDE TMS570 DEBUG.WSP



Test Case 1: Metric Test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles:

TS 1.1 112.00 Cycles TS 1.2 141.00 Cycles

Description Vector Description:

 $TS 1.1Shortest Path==>(k_MtrVoltVecuFiltEnable_Cnt_lgc == TRUE) = True\&\&(*Vecu_Volt_T_f32>=D_VECUMIN_VOLTS_F32) = True \\ TS 1.2Longest Path==>(*Vecu_Volt_T_f32>=D_VECUMIN_VOLTS_F32) = False\&\&\&(*Vecu_Volt_T_f32<=D_VECUMIN_VOLTS_F32) = True \\ TS 1.2Longest Path==>(k_MtrVoltVecuFiltEnable_Cnt_lgc == TRUE) = True\&(*Vecu_Volt_T_f32>=D_VECUMIN_VOLTS_F32) = True \\ TS 1.2Longest Path==>(k_MtrVoltVecuFiltEnable_Cnt_lgc == TRUE) = True\&(*Vecu_Volt_T_f32>=D_VECUMIN_VOLTS_F32) = True \\ TS 1.2Longest Path==>(k_MtrVoltVecuFiltEnable_Cnt_lgc == TRUE) = True\&(*Vecu_Volt_T_f32>=D_VECUMIN_VOLTS_F32) = True \\ TS 1.2Longest Path==>(k_MtrVoltVecuFiltEnable_Cnt_lgc == TRUE) = True\&(*Vecu_Volt_T_f32>=D_VECUMIN_VOLTS_F32) = True \\ TS 1.2Longest Path==>(k_MtrVoltT_f32>=D_VECUMIN_VOLTS_F32) = True \\ TS 1.2Longest Path==(k_MtrVoltT_f32>=D_VECUMIN_VOLTS_F32) = True \\ TS 1.2Longest Path=(k_MtrVoltT_f32>=D_VECUMIN_VOLTS_F32) = True \\ TS 1.2Longest Path=(k_MtrVoltT_f32>=D_VECUMIN_VO$

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	2.14748365e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	31		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	31	31	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	9.90352031e+027	9.90352031e+027	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.14748365e+009	2.14748365e+009	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	2.14748365e+009	2.14748365e+009	✓
target_Vecu_Volt_T_f32	31	31	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~

Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_UIs_f32	360171232		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-954881472		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.88593933e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	5.23559999		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	5.23559999	5.23559999	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_UIs_f32	-6.48613864e+026	-6.48614011e+026	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-954881472	-954881024	~
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.88593933e+009	1.88593997e+009	✓
target_Vecu_Volt_T_f32	5	5	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~



Test Case 2: Boundary Test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles:

TS 2.1 80.00 Cycles
TS 2.2 112.00 Cycles
TS 2.3 80.00 Cycles
TS 2.4 112.00 Cycles
TS 2.5 80.00 Cycles
TS 2.6 112.00 Cycles
TS 2.6 112.00 Cycles
TS 2.7 80.00 Cycles
TS 2.7 80.00 Cycles
TS 2.10 Cycles
TS 2.10 80.00 Cycles
TS 2.10 80.00 Cycles
TS 2.11 128.00 Cycles
TS 2.12 80.00 Cycles
TS 2.13 112.00 Cycles
TS 2.14 112.00 Cycles
TS 2.14 112.00 Cycles
TS 2.15 80.00 Cycles TS 2.14 112.00 Cycles TS 2.15 80.00 Cycles TS 2.16 80.00 Cycles TS 2.17 141.00 Cycles TS 2.18 128.00 Cycles TS 2.19 80.00 Cycles TS 2.19 80.00 Cycles
TS 2.20 141.00 Cycles
TS 2.21 80.00 Cycles
TS 2.22 80.00 Cycles
TS 2.23 80.00 Cycles
TS 2.24 121.00 Cycles
TS 2.25 80.00 Cycles
TS 2.26 121.00 Cycles
TS 2.27 80.00 Cycles

Description

Vector Description:

TS 2.1All Min IS 2.1All Min

TS 2.2All Max

TS 2.3k _MtrVoltVecuFiltEnable_Cnt_lgc => min

TS 2.4k _MtrVoltVecuFiltEnable_Cnt_lgc => max

TS 2.5Vecu_Volt1_T_f32 => min

TS 2.6Vecu_Volt1_T_f32 => max

TS 2.7Vecu_Volt1_T_f32 => mid

TS 2.8PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 => min

TS 2.9PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 => max

TS 2.10PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32 => max TS 2.10PICurrCntrl_MtrVecuFilt_M_str.Previnput_Uls_f32 => zero TS 2.11PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32 => pos
TS 2.12PICurrCntrl MtrVecuFilt M str.PrevInput Uls f32 => neg
TS 2.13PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32 => nin TS 2.14PlCurrCntrl MtrVecuFilt M str.PrevOutput Uls f32 =>max
TS 2.15PlCurrCntrl MtrVecuFilt M str.PrevOutput Uls f32 =>zero
TS 2.16PlCurrCntrl MtrVecuFilt M str.PrevOutput Uls f32 =>pos TS 2.16PlCurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 =>pos
TS 2.17PlCurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32 =>neg
TS 2.18PlCurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 =>min
TS 2.19PlCurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 =>max
TS 2.20PlCurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 =>zero
TS 2.21PlCurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 =>pos
TS 2.22PlCurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32 =>neg
TS 2.33PlCurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 =>min
TS 2.24PlCurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 =>max
TS 2.25PlCurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 =>zero
TS 2.26PlCurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 =>pos
TS 2.27PlCurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 =>pos
TS 2.27PlCurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32 =>pos

TS 2.27PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32 =>neg

Test Step 2.1 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-2.14748006e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-2.14748006e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-2.14748006e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-2.14748006e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	5		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-2.14748006e+009	-2.14748006e+009	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-2.14748006e+009	-2.14748006e+009	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-2.14748006e+009	-2.14748006e+009	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-2.14748006e+009	-2.14748006e+009	~
target_Vecu_Volt_T_f32	5	5	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	

Test Step 2.2 (Repeat Count = 1)	✓
Name	Input Value
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	2.14748365e+009

2016-09-15, 18:00:45+0530



CalLowPassFiltVecuOut

Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	2.14748365e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	31		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	31	31	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	9.90352031e+027	9.90352031e+027	~
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	2.14748365e+009	2.14748365e+009	~
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	2.14748365e+009	2.14748365e+009	~
target_Vecu_Volt_T_f32	31	31	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~	

Test Step 2.3 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1.25422003e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1.78840998e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.03557005e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-453076992		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	6.12529993		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1.25422003e+009	1.25422003e+009	✓
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1.78840998e+009	-1.78840998e+009	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.03557005e+009	2.03557005e+009	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-453076992	-453076992	✓
target_Vecu_Volt_T_f32	6.12529993	6.12529993	✓

Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
none	0	*** No Call Expected ***	0	•		

Test Step 2.4 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	1.59821005e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1.50935002e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-334438016		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.17052006e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	7.23559999		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	7.23559999	7.23559999	✓
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	5.90859838e+026	5.90859986e+026	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-334438016	-334438016	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.17052006e+009	1.17052006e+009	✓
target_Vecu_Volt_T_f32	31	31	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~	

Test Step 2.5 (Repeat Count = 1)		
Name	Input Value	
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	1.30338995e+009	
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-526129984	
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	1.46070003e+009	
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.44466995e+009	

CalLowPassFiltVecuOut

2016-09-15, 18:00:45+0530



Name	Input Value		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	5		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	1.30338995e+009	1.30338995e+009	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-526129984	-526129984	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	1.46070003e+009	1.46070003e+009	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.44466995e+009	1.44466995e+009	✓
target_Vecu_Volt_T_f32	5	5	

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none					

Test Step 2.6 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-1.93612864e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1.5751543e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	255735808		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.20936077e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	31		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	31	31	✓
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	4.87158773e+026	4.87158994e+026 ± 9E+20	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	255735808	255736000	•
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.20936077e+009	1.20936e+009	✓
target_Vecu_Volt_T_f32	31	31	~

Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~		

Test Step 2.7 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1.44801997e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-2.04112998e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-608366976		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-1.91424998e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	23.0214005		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1.44801997e+009	1.44801997e+009	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-2.04112998e+009	-2.04112998e+009	~
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-608366976	-608366976	~
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-1.91424998e+009	-1.91424998e+009	✓
target_Vecu_Volt_T_f32	23.0214005	23.0214005	✓

Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
none	0	*** No Call Expected ***	0	~		

 $PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32$

target_Vecu_Volt_T_f32

CalLowPassFiltVecuOut



1.88593997e+009

Test Step 2.8 (Repeat Count = 1)			
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	360171232		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-954881472		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	1.88593933e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	5.23559999		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	5.23559999	5.23559999	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-6.48613864e+026	-6.48614011e+026	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-954881472	-954881024	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	_	

1.88593933e+009

Test Step 2.9 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	2.14748006e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1.26342003e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	552748032		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-256136992		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	12.3255997		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	2.14748006e+009	2.14748006e+009	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1.26342003e+009	-1.26342003e+009	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	552748032	552748032	~
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	-256136992	-256136992	✓
target Vecu Volt T f32	12.3255997	12.3255997	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	•

Test Step 2.10 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	6524180		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	20154800		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	231546		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	7.25360012		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	0	0	✓
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	6524180	6524180	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	20154800	20154800	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	231546	231546	✓
target Vecu Volt T f32	7.25360012	7.25360012	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	

Test Step 2.11 (Repeat Count = 1)	✓
Name	Input Value
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	32145700

2016-09-15, 18:00:45+0530



CalLowPassFiltVecuOut

Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	12365500		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	23651500		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	12546.2998		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	8.21450043		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	8.21450043	8.21450043	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	3.66932401e+018	3.66931989e+018 ± 9000000000000	~
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	23651500	23651500	~
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	12546.2998	12546.2998	~
target_Vecu_Volt_T_f32	31	31	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~

Test Step 2.12 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-96547904		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	63254200		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	11254700		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	96321		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	9.32559967		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-96547904	-96547904	✓
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	63254200	63254200	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	11254700	11254700	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	96321	96321	✓
target_Vecu_Volt_T_f32	9.32559967	9.32559967	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	•	

Test Step 2.13 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-1.75868147e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_UIs_f32	-2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-1.92536422e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	795439744		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	5.21449995		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	5.21449995	5.21449995	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	3.28889538e+027	3.28890011e+027 ± 9E+21	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-1.92536422e+009	-1.92536422e+009	~
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	795439744	795440000	~
target_Vecu_Volt_T_f32	31	31	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~	

Test Step 2.14 (Repeat Count = 1)		
Name	Input Value	
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	91816200	
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	2.14748006e+009	
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-2.02622003e+009	
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-237474000	

CalLowPassFiltVecuOut

2016-09-15, 18:00:45+0530



Name	Input Value		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	13.3255997		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	13.3255997	13.3255997	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1.03331282e+027	1.03331001e+027 ± 9E+21	~
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-2.02622003e+009	-2.02622003e+009	~
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-237474000	-237474000	~
target_Vecu_Volt_T_f32	31	31	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~	

Test Step 2.15 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-465380000		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1.63317005e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-1.11672e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-1.30057997e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	8.12450027		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-465380000	-465380000	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1.63317005e+009	1.63317005e+009	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-1.11672e+009	-1.11672e+009	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-1.30057997e+009	-1.30057997e+009	✓
target_Vecu_Volt_T_f32	8.12450027	8.12450027	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.16 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	2543700		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	63527100		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	21456.3008		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	152639		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	5.32560015		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	2543700	2543700	✓
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	63527100	63527100	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	21456.3008	21456.3008	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	152639	152639	✓
target_Vecu_Volt_T_f32	5.32560015	5.32560015	✓

Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
none	0	*** No Call Expected ***	0	~		



CalLowPassFiltVecuOut

Test Step 2.17 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	96412600		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-65981500		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	25634		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	3654.02002		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	6.21460009		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	6.21460009	6.21460009	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-6.1799463e+015	-6.17995006e+015	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	25634	25634	~
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	3654.02002	3654.02002	~
target_Vecu_Volt_T_f32	5	5	•

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~	

Test Step 2.18 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-1.084e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1.28743002e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-2.14748006e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-302148992		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	6.32560015		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	6.32560015	6.32560015	✓
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	8.35360436e+026	8.35359994e+026	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-2.14748006e+009	-2.14748006e+009	~
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-302148992	-302148992	✓
target Vecu Volt T f32	31	31	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	•	

Test Step 2.19 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-1.41749005e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-721043968		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	2.14748006e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	2.06957005e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	22.1424999		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-1.41749005e+009	-1.41749005e+009	✓
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-721043968	-721043968	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.14748006e+009	2.14748006e+009	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	2.06957005e+009	2.06957005e+009	✓
target_Vecu_Volt_T_f32	22.1424999	22.1424999	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.20 (Repeat Count = 1)	✓
Name	Input Value
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-45511800

CalLowPassFiltVecuOut

2016-09-15, 18:00:45+0530



Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	758132992		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	1.75474995e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-1.26762995e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	6.32560015		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	6.32560015	6.32560015	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1.68637109e+027	-1.68637006e+027	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	1.75474995e+009	1.75474995e+009	~
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-1.26762995e+009	-1.26762995e+009	~
target_Vecu_Volt_T_f32	5	5	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~

Test Step 2.21 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	65478900		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1235700		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	0		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	124563		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	7.32560015		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	65478900	65478900	✓
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1235700	1235700	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	0	0	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	124563	124563	~
target Vecu Volt T f32	7.32560015	7.32560015	✓

Test Step Call Trace			✓	
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	•

Test Step 2.22 (Repeat Count = 1)				
Name	Input Value			
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	23564200			
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	65987400			
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-12456.2998			
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	132645			
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32			
k_MtrVoltVecuFiltEnable_Cnt_lgc	0			
target_Vecu_Volt_T_f32	6.21449995			
Name	Actual Value	Expected Value	Result	
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	23564200	23564200	✓	
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	65987400	65987400	✓	
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-12456.2998	-12456.2998	✓	
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	132645	132645	✓	
target_Vecu_Volt_T_f32	6.21449995	6.21449995	✓	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.23 (Repeat Count = 1)		
Name	Input Value	
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-980435008	
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	991660032	
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	-416540992	
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-2.14748006e+009	

CalLowPassFiltVecuOut

2016-09-15, 18:00:45+0530



Name	Input Value		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	11.2563		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-980435008	-980435008	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	991660032	991660032	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-416540992	-416540992	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_UIs_f32	-2.14748006e+009	-2.14748006e+009	✓
target_Vecu_Volt_T_f32	11.2563	11.2563	✓

Test Step Call Trace					✓
	Actual Function	Count	Expected Function	Count	Result
	none	0	*** No Call Expected ***	0	•

Test Step 2.24 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1.12730099e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-2.10160973e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	1.93757453e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	2.14748365e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	30.2355995		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	30.2355995	30.2355995	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-8.7446079e+027	-8.74461026e+027	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	1.93757453e+009	1.93757453e+009	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	2.14748365e+009	2.14748365e+009	✓
target_Vecu_Volt_T_f32	5	5	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~

Test Step 2.25 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-2.06284006e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1.40768998e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	1.76896e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	12455600		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	5.21449995		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-2.06284006e+009	-2.06284006e+009	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1.40768998e+009	1.40768998e+009	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	1.76896e+009	1.76896e+009	~
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	12455600	12455600	✓
target_Vecu_Volt_T_f32	5.21449995	5.21449995	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	



CalLowPassFiltVecuOut

Test Step 2.26 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	112563		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	1223650		
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	124556		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	6.32567978		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	6.32567978	6.32567978	✓
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	0	0	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_UIs_f32	124556	124556	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	0	0	✓
target_Vecu_Volt_T_f32	5	5	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~

Test Step 2.27 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	325648		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	302145		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	21456.3008		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	124540000		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	11.2546997		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	325648	325648	~
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	302145	302145	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	21456.3008	21456.3008	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	124540000	124540000	✓
target Vecu Volt T f32	11.2546997	11.2546997	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Case 3: Path Test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles:

TS 3.1 80.00 Cycles TS 3.2 112.00 Cycles TS 3.3 80.00 Cycles

Description

Vector Description:

 $TS \ 3.1 (k_MtrVoltVecuFiltEnable_Cnt_lgc == TRUE) = False \& (*Vecu_Volt_T_f32 >= D_VECUMIN_VOLTS_F32) = False \& (*Vecu_Volt_T_f32 <= D_VECUMIN_T_f32 <= D_V$ TS 3.1(k_MtrVoltVecurinicriable_Crit_igc == TNOE)=True&&(*Vecu_Volt_T_f32>= D_VECUMIN_VOLTS_F32)=True
TS 3.2(k_MtrVoltVecuriitEnable_Cnt_igc == TRUE)=True&&(*Vecu_Volt_T_f32>= D_VECUMIN_VOLTS_F32)=True
TS 3.3(*Vecu_Volt_T_f32<= D_VECUMIN_VOLTS_F32)=False

Test Step 3.1 (Repeat Count = 1)			
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	-2.14748006e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-2.14748006e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-2.14748006e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-2.14748006e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	0		
target_Vecu_Volt_T_f32	5		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	-2.14748006e+009	-2.14748006e+009	-
PICurrCntrl MtrVecuFilt M str.PrevOutput Uls f32	-2.14748006e+009	-2.14748006e+009	✓

CalLowPassFiltVecuOut

2016-09-15, 18:00:45+0530



Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	-2.14748006e+009	-2.14748006e+009	~
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-2.14748006e+009	-2.14748006e+009	~
target_Vecu_Volt_T_f32	5	5	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 3.2 (Repeat Count = 1)			✓
Name	Input Value		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_UIs_f32	2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.14748365e+009		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	2.14748365e+009		
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32		
k_MtrVoltVecuFiltEnable_Cnt_lgc	1		
target_Vecu_Volt_T_f32	31		
Name	Actual Value	Expected Value	Result
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	31	31	✓
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	9.90352031e+027	9.90352031e+027	✓
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.14748365e+009	2.14748365e+009	✓
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	2.14748365e+009	2.14748365e+009	✓
target_Vecu_Volt_T_f32	31	31	✓

Test Step Call Trace							
Actual Function	Count	Expected Function	Count	Result			
CalLowPassFiltBilinearOut	1	CalLowPassFiltBilinearOut	1	~			

Test Step 3.3 (Repeat Count = 1)					
Name	Input Value				
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1.25422003e+009				
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1.78840998e+009				
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.03557005e+009				
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-453076992				
Vecu_Volt_T_f32	target_Vecu_Volt_T_f32				
k_MtrVoltVecuFiltEnable_Cnt_lgc	0				
target_Vecu_Volt_T_f32	6.12529993				
Name	Actual Value	Expected Value	Result		
PICurrCntrl_MtrVecuFilt_M_str.PrevInput_Uls_f32	1.25422003e+009	1.25422003e+009	✓		
PICurrCntrl_MtrVecuFilt_M_str.PrevOutput_Uls_f32	-1.78840998e+009	-1.78840998e+009	✓		
PICurrCntrl_MtrVecuFilt_M_str.TermN_Uls_f32	2.03557005e+009	2.03557005e+009	✓		
PICurrCntrl_MtrVecuFilt_M_str.TermD_Uls_f32	-453076992	-453076992	✓		
target_Vecu_Volt_T_f32	6.12529993	6.12529993	✓		

Test Step Call Trace							
Actual Function	Count	Expected Function	Count	Result			
none	0	*** No Call Expected ***	0	•			