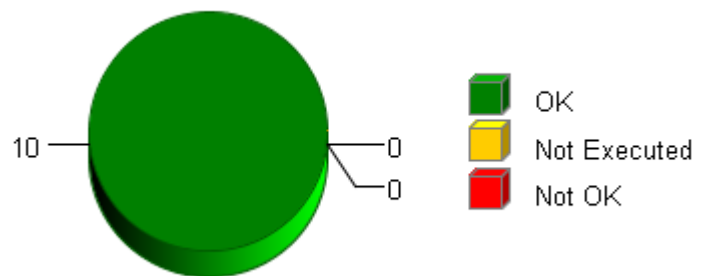


Summary

Total Test Objects: 10
Successful: 10
Failed: 0
Not Executed: 0
Date: 2017-02-06
Time: 14:12:31+0530

Overall Test Object Results (including Coverage)



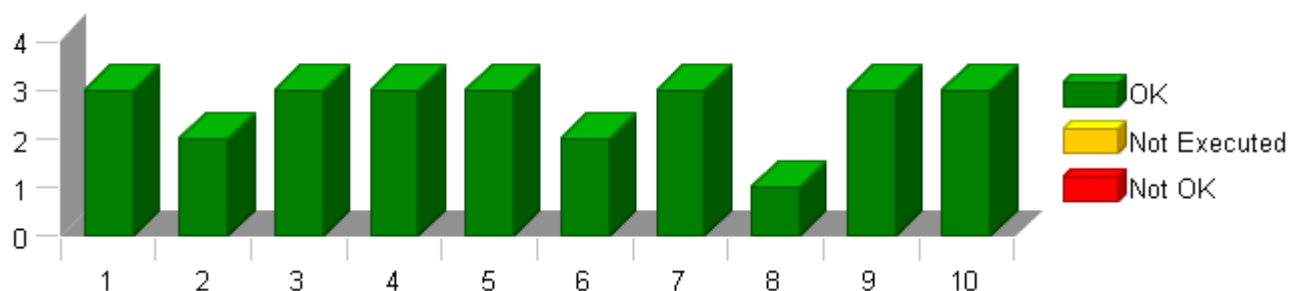
Selected Project Items

Test Object "CBD_UnitTest/AbsHwPos_Tcl2IVd/AbsHwPos_Init1"
Test Object "CBD_UnitTest/AbsHwPos_Tcl2IVd/AbsHwPos_Per1"
Test Object "CBD_UnitTest/AbsHwPos_Tcl2IVd/AbsHwPos_Per2"
Test Object "CBD_UnitTest/AbsHwPos_Tcl2IVd/AbsHwPos_Per3"
Test Object "CBD_UnitTest/AbsHwPos_Tcl2IVd/AbsHwPos_Per4"
Test Object "CBD_UnitTest/AbsHwPos_Tcl2IVd/AbsHwPos_SCom_CustClrTrim"
Test Object "CBD_UnitTest/AbsHwPos_Tcl2IVd/AbsHwPos_SCom_CustSetTrim"
Test Object "CBD_UnitTest/AbsHwPos_Tcl2IVd/AbsHwPos_SCom_NxtClearTrim"
Test Object "CBD_UnitTest/AbsHwPos_Tcl2IVd/AbsHwPos_SCom_NxtSetTrim"
Test Object "CBD_UnitTest/AbsHwPos_Tcl2IVd/TrimNotPerfDiag"

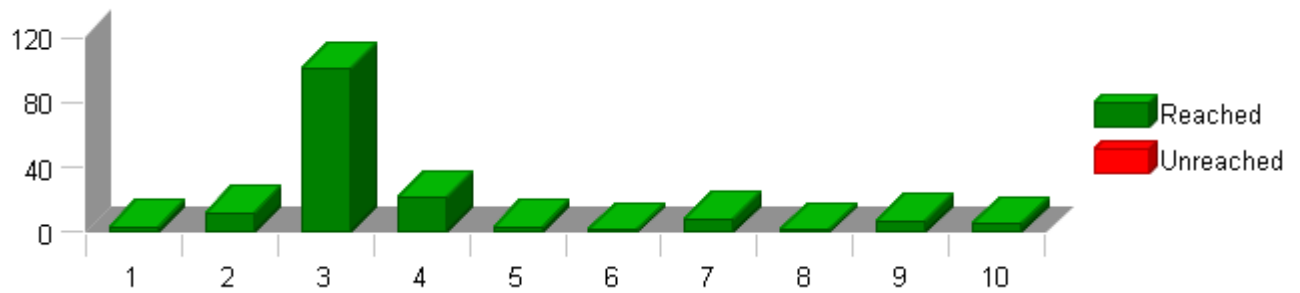
Used Test Environments

TI TMS 570 PLS UDE (Default)

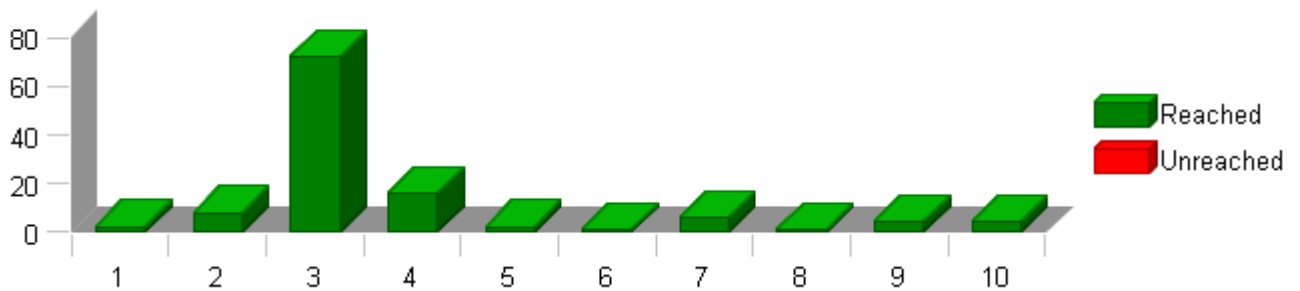
Test Case Results for Each Test Object (without Coverage)



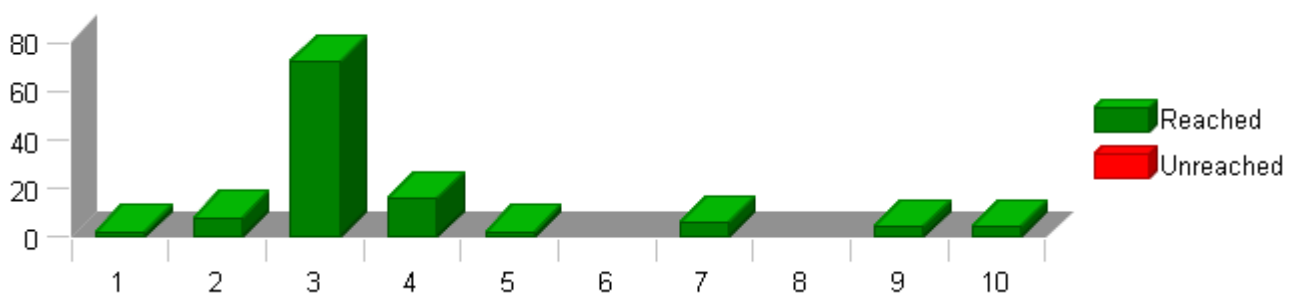
The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

Statement (C0) Coverage: Total Statements for Each Test Object

The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

Branch (C1) Coverage: Total Branches for Each Test Object

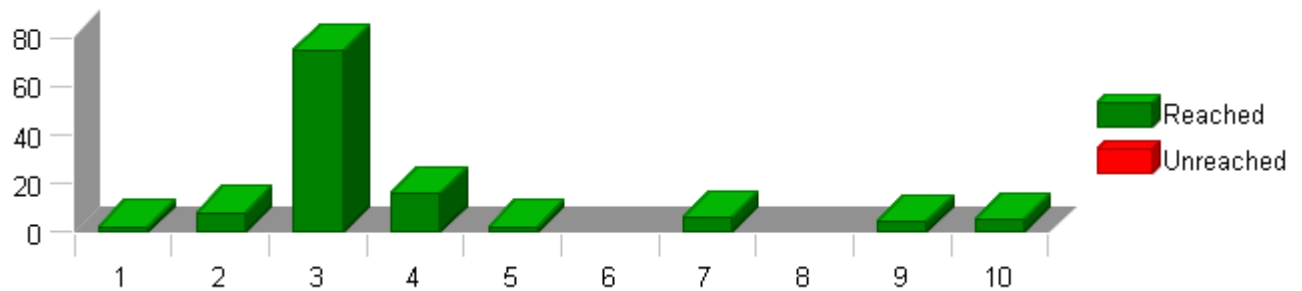
The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

Decision Coverage: Total Decision Outcomes for Each Test Object

The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.

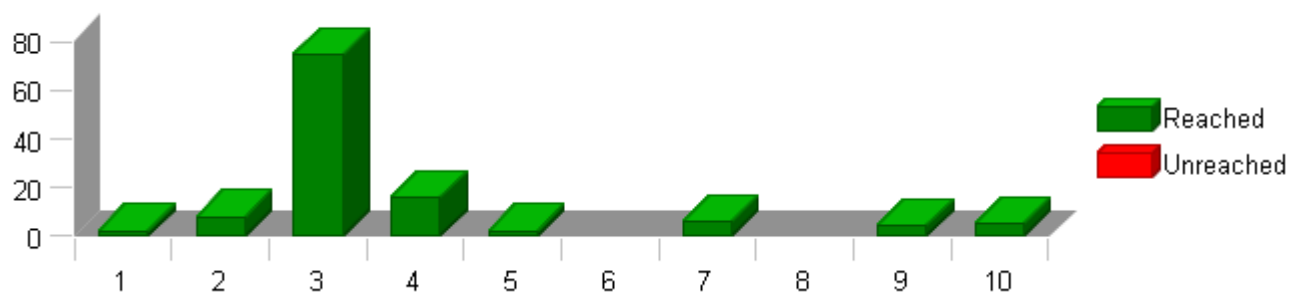
MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

TEST OVERVIEW REPORT

2017-02-06, 14:12:31+0530

Project AbsHwPos



Test Object List

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

| No. | Name | C0 | C1 | DC | MC/DC | MCC | Test Cases | Result |
|-----|--|-------|-------|-------|-------|-------|-----------------|--------|
| | AbsHwPos | 100 % | 100 % | 100 % | 100 % | 100 % | 26 of 26 passed | ✓ |
| | CBD_UnitTest | 100 % | 100 % | 100 % | 100 % | 100 % | 26 of 26 passed | ✓ |
| | AbsHwPos_Tcl2IVd | 100 % | 100 % | 100 % | 100 % | 100 % | 26 of 26 passed | ✓ |
| 1 | AbsHwPos_Init1 | 100 % | 100 % | 100 % | 100 % | 100 % | 3 of 3 passed | ✓ |
| 2 | AbsHwPos_Per1 | 100 % | 100 % | 100 % | 100 % | 100 % | 2 of 2 passed | ✓ |
| 3 | AbsHwPos_Per2 | 100 % | 100 % | 100 % | 100 % | 100 % | 3 of 3 passed | ✓ |
| 4 | AbsHwPos_Per3 | 100 % | 100 % | 100 % | 100 % | 100 % | 3 of 3 passed | ✓ |
| 5 | AbsHwPos_Per4 | 100 % | 100 % | 100 % | 100 % | 100 % | 3 of 3 passed | ✓ |
| 6 | AbsHwPos_SCom_CustClrTrim | 100 % | 100 % | - | - | - | 2 of 2 passed | ✓ |
| 7 | AbsHwPos_SCom_CustSetTrim | 100 % | 100 % | 100 % | 100 % | 100 % | 3 of 3 passed | ✓ |
| 8 | AbsHwPos_SCom_NxtClearTrim | 100 % | 100 % | - | - | - | 1 of 1 passed | ✓ |
| 9 | AbsHwPos_SCom_NxtSetTrim | 100 % | 100 % | 100 % | 100 % | 100 % | 3 of 3 passed | ✓ |
| 10 | TrimNotPerfDiag | 100 % | 100 % | 100 % | 100 % | 100 % | 3 of 3 passed | ✓ |

TEST DETAILS REPORT

2017-02-06, 14:02:08+0530

AbsHwPos_SCom_NxtSetTrim



| | |
|-------------|--------------------------|
| Project | AbsHwPos |
| Module | AbsHwPos_Tcl2IVd |
| Test Object | AbsHwPos_SCom_NxtSetTrim |

Instrumentation: Test Object Only

| | |
|-------------------------|-------|
| Statement (C0) Coverage | 100 % |
| Decision Coverage | 100 % |
| Branch (C1) Coverage | 100 % |
| MCC Coverage | 100 % |
| MC/DC Coverage | 100 % |

Statistics

| | |
|-----------------|-----|
| Total Testcases | 3 |
| Successful | 3 ✓ |
| Failed | 0 |
| Not Executed | 0 |

Module Properties

| | |
|------------------------|--|
| Project Root Directory | D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G |
| Configuration File | D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml |
| Target Environment | TI TMS 570 PLS UDE (Default) |
| Kind of Test | Unit Test |
| Linker Options | |
| Source File(s) | |
| File | \$(SOURCEROOT)\Ap_AbsHwPos.c |
| Compiler Options | -D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap_AbsHwPos -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include |

Comments/Description/Specification

| Name | Text |
|---------------------------|--|
| Module 'AbsHwPos_Tcl2IVd' | *****Unit Test Description***** Name of Tester: Medha Sabale Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version: 10 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:11 Data Dictionary Version: 10 Unit Test Plan Version:3 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):3004 Total RAM Used (Bytes):64 Total CALS Used (Bytes):56 Special Test Requirements:NA Test Date:2/6/2017 Comments: NOTE 1:Inline functions defined in "Globalmacro.h" are not unit tested. NOTE 2: "CBD_Sandbox_dbg.map" file is embedded for reference." ***** |

Attributes

| Name | Value |
|-----------------------|---|
| Compiler Install Path | \$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5 |
| Float Precision | 9 |
| InitObjDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\obj |
| InitSrcDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\src |
| Linker File | \$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd |
| Makefile Template | \$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl |
| Target Install Path | \$(ProgramFiles)\pls\UDE 4.4 |
| Time Unit | Cycles |

TEST DETAILS REPORT

2017-02-06, 14:02:08+0530

AbsHwPos_SCom_NxtSetTrim



| Attributes | |
|------------------|--|
| Name | Value |
| Timer Enabled | False |
| Timer Prescale | 0 |
| Timer Resolution | 1 |
| UDE Config File | \$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_I2PIN_JTAG.cfg |
| Workspace File | D:\Synergy_Work_Area\AbsHwPos_TcI2cVd_FDD05G\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP |

TEST DETAILS REPORT

2017-02-06, 14:02:08+0530

AbsHwPos_SCom_NxtSetTrim



Test Case 1: Metrics Test

| | |
|----------------------|---|
| Specification | "Performance Metrics (With "None" Instrumentation and WithPS Environment)" CPU Cycles: TS1.1 937.00 Cycles TS1.2 1724.00 Cycles |
| Description | Vector Description: TS1.1 Shortest Execution Path: if (TurnsCntValidity_Cnt_T_u08 == D_TCVVALID_CNT_U08)=>False" TS1.2 Longest Execution Path: if (TurnsCntValidity_Cnt_T_u08 == D_TCVVALID_CNT_U08)=>True if (Abs_f32_m(TmpOffset_HwDeg_T_f32) <= k_VehCntOffValidLimit_HwDeg_f32)=>True" |

Test Step 1.1 (Repeat Count = 1)

| Name | Input Value | | | |
|--|--|----------------|--------|--|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -10125 | | | |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | -1600 | | | |
| AbsHwPos_VehCntValid_Cnt_M_lgc | 0 | | | |
| Offset_HwDeg_T_f32 | -4.2949673e+009 | | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntValidity_Cnt_u08_data | | | |
| k_VehCntOffValidLimit_HwDeg_f32 | 0 | | | |
| tgt_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | -1600 | | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntOffset | tgt_Pim_EOLVehCntOffset | | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntValidity_Cnt_u08_data | 0 | | | |
| Name | Actual Value | Expected Value | Result | |
| AbsHwPos_SCom_NxtSetTrim() | 34 | 34 | ✓ | |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | -1600 | -1600 ± 0.009 | ✓ | |
| AbsHwPos_VehCntValid_Cnt_M_lgc | 0 | 0 | ✓ | |
| tgt_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | -1600 | -1600 ± 0.009 | ✓ | |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Read_Ap_AbsHwPos_TurnsCntValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntValidity_Cnt_u08 | 1 | ✓ |

Test Step 1.2 (Repeat Count = 1)

| Name | Input Value | | | |
|--|--|----------------|--------|--|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 500 | | | |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | 1268.56006 | | | |
| AbsHwPos_VehCntValid_Cnt_M_lgc | 0 | | | |
| Offset_HwDeg_T_f32 | 0 | | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntValidity_Cnt_u08_data | | | |
| k_VehCntOffValidLimit_HwDeg_f32 | 1589.10815 | | | |
| tgt_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | -700 | | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntOffset | tgt_Pim_EOLVehCntOffset | | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntValidity_Cnt_u08_data | 100 | | | |
| Name | Actual Value | Expected Value | Result | |
| AbsHwPos_SCom_NxtSetTrim() | 0 | 0 | ✓ | |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | 500 | 500 ± 0.0009 | ✓ | |
| AbsHwPos_VehCntValid_Cnt_M_lgc | 1 | 1 | ✓ | |
| tgt_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 500 | 500 ± 0.0009 | ✓ | |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Read_Ap_AbsHwPos_TurnsCntValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_WriteBlock | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 14:02:08+0530

AbsHwPos_SCom_NxtSetTrim



Test Case 2: Range Test

Specification "Performance Metrics
(With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS`2.1 937.00 Cycles
TS2.2 936.00 Cycles
TS2.3 1511.00 Cycles
TS2.4 1512.00 Cycles
TS2.5 1725.00 Cycles
TS2.6 1511.00 Cycles
TS2.7 1511.00 Cycles
TS2.8 936.00 Cycles
TS2.9 935.00 Cycles
TS2.10 934.00 Cycles
TS2.11 1511.00 Cycles
TS2.12 1512.00 Cycles
TS2.13 1512.00 Cycles
TS2.14 1512.00 Cycles
TS2.15 1512.00 Cycles
TS2.16 1512.00 Cycles
TS2.17 1725.00 Cycles
TS2.18 1511.00 Cycles

Description Vector Description:

TS2.1 All Min
TS2.2 All Max
TS2.3 Offset_HwDeg_T_f32=>Min
TS2.4 Offset_HwDeg_T_f32=>Max
TS2.5 Offset_HwDeg_T_f32=>Zero
TS2.6 Offset_HwDeg_T_f32=>Pos
TS2.7 Offset_HwDeg_T_f32=>Neg
TS2.8 TurnsCounterValidity_Cnt_u08=>Min
TS2.9 TurnsCounterValidity_Cnt_u08=>Max
TS2.10 TurnsCounterValidity_Cnt_u08=>Zero
TS2.11 AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32=>Min
TS2.12 AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32=>Max
TS2.13 AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32=>Zero
TS2.14 AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32=>Pos
TS2.15 AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32=>Neg
TS2.16 k_VehCntrOffValidLimit_HwDeg_f32=>Min
TS2.17 k_VehCntrOffValidLimit_HwDeg_f32=>Max/Default
TS2.18 k_VehCntrOffValidLimit_HwDeg_f32=>Pos

Test Step 2.1 (Repeat Count = 1)

| Name | Input Value | | |
|---|---|----------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -10125 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1600 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Offset_HwDeg_T_f32 | -4.2949673e+009 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 0 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -1600 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 0 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_NxtSetTrim() | 34 | 34 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1600 | -1600 ± 0.009 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -1600 | -1600 ± 0.009 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |

Test Step 2.2 (Repeat Count = 1)

| Name | Input Value | | |
|---|---|----------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 10125 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Offset_HwDeg_T_f32 | 4.2949673e+009 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1600 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 255 | | |
| Name | Actual Value | Expected Value | Result |
| | | | ✓ |

TEST DETAILS REPORT

2017-02-06, 14:02:08+0530



AbsHwPos_SCom_NxtSetTrim

| Name | Actual Value | Expected Value | Result |
|---|--------------|----------------|--------|
| AbsHwPos_SCom_NxtSetTrim() | 34 | 34 | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | 1 | ✓ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |

Test Step 2.3 (Repeat Count = 1)

| Name | Input Value | | |
|---|---|--------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -2171.08569 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 1039.48999 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Offset_HwDeg_T_f32 | -4.2949673e+009 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1541.24792 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 1039.22998 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 100 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_NxtSetTrim() | 49 | 49 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 1039.48999 | 1039.48999 ± 0.009 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 1039.22998 | 1039.22998 ± 0.009 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |

Test Step 2.4 (Repeat Count = 1)

| Name | Input Value | | |
|---|---|---------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 2617.10376 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 700.849976 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Offset_HwDeg_T_f32 | 4.2949673e+009 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1493.81677 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 0 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 100 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_NxtSetTrim() | 49 | 49 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 700.849976 | 700.849976 ± 0.0009 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | 1 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 0 | 0 ± 0.000009 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |

Test Step 2.5 (Repeat Count = 1)

| Name | Input Value |
|--|---|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 500 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 1268.56006 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Offset_HwDeg_T_f32 | 0 |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1589.10815 |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -700 |

TEST DETAILS REPORT

2017-02-06, 14:02:08+0530



AbsHwPos_SCom_NxtSetTrim

| Name | Input Value | | |
|---|--------------------------|----------------|--------|
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 100 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_NxtSetTrim() | 0 | 0 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 500 | 500 ± 0.0009 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | 1 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 500 | 500 ± 0.0009 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |

Test Step 2.6 (Repeat Count = 1)

| Name | Input Value | | |
|---|---|----------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 600 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -887 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Offset_HwDeg_T_f32 | 277082.563 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1344.17737 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 100 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 100 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_NxtSetTrim() | 49 | 49 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -887 | -887 ± 0.0009 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | 1 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 100 | 100 ± 0.0009 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |

Test Step 2.7 (Repeat Count = 1)

| Name | Input Value | | |
|---|---|----------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 1639.56409 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -460.230011 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Offset_HwDeg_T_f32 | -123.209999 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 655.326538 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 200 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 100 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_NxtSetTrim() | 49 | 49 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -460.230011 | -460.230011 ± 0.0009 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 200 | 200 ± 0.0009 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |

Test Step 2.8 (Repeat Count = 1)

| Name | Input Value |
|--------------------------------------|-------------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -648.309998 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 171.100006 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Offset_HwDeg_T_f32 | 1296856.88 |

TEST DETAILS REPORT

2017-02-06, 14:02:08+0530



AbsHwPos_SCom_NxtSetTrim

| Name | Input Value | | |
|---|---|---------------------|--------|
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1289.24597 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 300.01001 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 0 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_NxtSetTrim() | 34 | 34 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 171.100006 | 171.100006 ± 0.0009 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | 1 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 300.01001 | 300.01001 ± 0.0009 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |

| Test Step 2.9 (Repeat Count = 1) | | | | |
|---|--|---|----------------------|--------|
| Name | | Input Value | | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | 988.5 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | -461.790009 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 0 | | |
| Offset_HwDeg_T_f32 | | 332748.344 | | |
| Rte_Inst_Ap_AbsHwPos | | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | | 1584.07458 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | | 400 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | 255 | | |
| Name | | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_NxtSetTrim() | | 34 | 34 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | -461.790009 | -461.790009 ± 0.0009 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 0 | 0 | ✓ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | | 400 | 400 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |

| Test Step 2.10 (Repeat Count = 1) | | | | |
|---|--|---|----------------|--------|
| Name | | Input Value | | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | 1024 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | -946 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 1 | | |
| Offset_HwDeg_T_f32 | | 60205312 | | |
| Rte_Inst_Ap_AbsHwPos | | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | | 413.765717 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | | 500 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | 56 | | |
| Name | | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_NxtSetTrim() | | 34 | 34 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | -946 | -946 ± 0.0009 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 1 | 1 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | | 500 | 500 ± 0.0009 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |

| Test Step 2.11 (Repeat Count = 1) | |
|--------------------------------------|-------------|
| Name | Input Value |
| AbsHwPos AlignedRelHwPos HwDeg M f32 | -10125 |

TEST DETAILS REPORT

2017-02-06, 14:02:08+0530



AbsHwPos_SCom_NxtSetTrim

| Name | Input Value | | |
|---|---|--------------------|--------|
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 1039.48999 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Offset_HwDeg_T_f32 | 3267.28003 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1541.24792 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 1039 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 100 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_NxtSetTrim() | 49 | 49 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 1039.48999 | 1039.48999 ± 0.009 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 1039 | 1039 ± 0.009 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |

| Test Step 2.12 (Repeat Count = 1) | | | | |
|---|--|---|--------------------|--------|
| Name | | Input Value | | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | 10125 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | 416.5 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 0 | | |
| Offset_HwDeg_T_f32 | | 547 | | |
| Rte_Inst_Ap_AbsHwPos | | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | | 1112.06665 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | | 700.02002 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | 100 | | |
| Name | | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_NxtSetTrim() | | 49 | 49 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | 416.5 | 416.5 ± 0.0009 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 0 | 0 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | | 700.02002 | 700.02002 ± 0.0009 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |

| Test Step 2.13 (Repeat Count = 1) | | | | |
|---|--|---|---------------------|--------|
| Name | | Input Value | | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | 0 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | -1153.65002 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 1 | | |
| Offset_HwDeg_T_f32 | | 305.294006 | | |
| Rte_Inst_Ap_AbsHwPos | | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | | 27.1989002 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | | 800 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | 100 | | |
| Name | | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_NxtSetTrim() | | 49 | 49 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | -1153.65002 | -1153.65002 ± 0.009 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 1 | 1 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | | 800 | 800 ± 0.0009 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 14:02:08+0530

AbsHwPos_SCom_NxtSetTrim



Test Step 2.14 (Repeat Count = 1)

| Name | Input Value | | |
|---|---|---------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 1245.01001 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1404.81006 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Offset_HwDeg_T_f32 | 194.787994 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 698 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -100 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 100 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_NxtSetTrim() | 49 | 49 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1404.81006 | -1404.81006 ± 0.009 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -100 | -100 ± 0.0009 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |

Test Step 2.15 (Repeat Count = 1)

| Name | Input Value | | |
|---|---|---------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -3264.02002 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 430.045013 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Offset_HwDeg_T_f32 | 177.820007 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1347.19995 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -200 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 100 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_NxtSetTrim() | 49 | 49 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 430.045013 | 430.045013 ± 0.0009 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -200 | -200 ± 0.0009 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |

Test Step 2.16 (Repeat Count = 1)

| Name | Input Value | | |
|---|---|----------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -2590.63379 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 1084.21997 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Offset_HwDeg_T_f32 | 542.099976 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 0 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -300.019989 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 100 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_NxtSetTrim() | 49 | 49 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 1084.21997 | 1084.21997 ± 0.009 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | 1 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -300.019989 | -300.019989 ± 0.0009 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 14:02:08+0530

AbsHwPos_SCom_NxtSetTrim



Test Step 2.17 (Repeat Count = 1)

| Name | Input Value | | |
|---|---|----------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 104.297646 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -564 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Offset_HwDeg_T_f32 | 964.200012 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1600 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 364 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 100 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_NxtSetTrim() | 0 | 0 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -859.902344 | -859.902344 ± 0.0009 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | 1 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -859.902344 | -859.902344 ± 0.0009 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |

Test Step 2.18 (Repeat Count = 1)

| Name | Input Value | | |
|---|---|----------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 1481.36023 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 874 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Offset_HwDeg_T_f32 | 356 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1000.12 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 736 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 100 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_NxtSetTrim() | 49 | 49 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 874 | 874 ± 0.0009 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | 1 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 736 | 736 ± 0.0009 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |

Test Case 3: Path Test

| | |
|----------------------|---|
| Specification | "Performance Metrics (With "None" Instrumentation and WithPS Environment)" CPU Cycles: TS3.1 937.00 Cycles TS3.2 1724.00 Cycles TS3.3 1513.00 Cycles |
| Description | Vector Description: TS3.1 if (TurnsCntrValidity_Cnt_T_u08 == D_TCVVALID_CNT_U08)=>False TS3.2 "if (TurnsCntrValidity_Cnt_T_u08 == D_TCVVALID_CNT_U08)=>True if (Abs_f32_m(TmpOffset_HwDeg_T_f32) <= k_VehCntrOffValidLimit_HwDeg_f32)=>True" TS3.3 if (Abs_f32_m(TmpOffset_HwDeg_T_f32) <= k_VehCntrOffValidLimit_HwDeg_f32)=>False |

Test Step 3.1 (Repeat Count = 1)

| Name | Input Value |
|--------------------------------------|-------------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -10125 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1600 |

TEST DETAILS REPORT

2017-02-06, 14:02:08+0530



AbsHwPos_SCom_NxtSetTrim

| Name | Input Value | | |
|---|---|----------------|--------|
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Offset_HwDeg_T_f32 | -4.2949673e+009 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 0 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -1600 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 0 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_NxtSetTrim() | 34 | 34 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1600 | -1600 ± 0.009 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -1600 | -1600 ± 0.009 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |

| Test Step 3.2 (Repeat Count = 1) | | | | ✔ |
|---|--|---|----------------|--------|
| Name | | Input Value | | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | 500 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | 1268.56006 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 0 | | |
| Offset_HwDeg_T_f32 | | 0 | | |
| Rte_Inst_Ap_AbsHwPos | | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | | 1589.10815 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | | -700 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | 100 | | |
| Name | | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_NxtSetTrim() | | 0 | 0 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | 500 | 500 ± 0.0009 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 1 | 1 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | | 500 | 500 ± 0.0009 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |

| Test Step 3.3 (Repeat Count = 1) | | | | |
|---|--|---|--------------------|--------|
| Name | | Input Value | | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | -2171.08569 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | 1039.48999 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 0 | | |
| Offset_HwDeg_T_f32 | | -4.2949673e+009 | | |
| Rte_Inst_Ap_AbsHwPos | | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | | 1541.24792 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | | 1039.22998 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | 100 | | |
| Name | | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_NxtSetTrim() | | 49 | 49 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | 1039.48999 | 1039.48999 ± 0.009 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 0 | 0 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | | 1039.22998 | 1039.22998 ± 0.009 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:46:36+0530

AbsHwPos_Per1



| | |
|-------------|------------------|
| Project | AbsHwPos |
| Module | AbsHwPos_Tcl2IVd |
| Test Object | AbsHwPos_Per1 |

Instrumentation: Test Object Only

| | |
|-------------------------|-------|
| Statement (C0) Coverage | 100 % |
| Decision Coverage | 100 % |
| Branch (C1) Coverage | 100 % |
| MCC Coverage | 100 % |
| MC/DC Coverage | 100 % |

Statistics

| | |
|-----------------|-----|
| Total Testcases | 2 |
| Successful | 2 ✓ |
| Failed | 0 |
| Not Executed | 0 |

Module Properties

| | |
|------------------------|--|
| Project Root Directory | D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G |
| Configuration File | D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml |
| Target Environment | TI TMS 570 PLS UDE (Default) |
| Kind of Test | Unit Test |
| Linker Options | |
| Source File(s) | |
| File | \$(SOURCEROOT)\Ap_AbsHwPos.c |
| Compiler Options | -D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap_AbsHwPos -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include |

Comments/Description/Specification

| Name | Text |
|---------------------------|--|
| Module 'AbsHwPos_Tcl2IVd' | *****Unit Test Description***** Name of Tester: Medha Sabale Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version: 10 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:11 Data Dictionary Version: 10 Unit Test Plan Version:3 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):3004 Total RAM Used (Bytes):64 Total CALS Used (Bytes):56 Special Test Requirements:NA Test Date:2/6/2017 Comments: NOTE 1:Inline functions defined in "Globalmacro.h" are not unit tested. NOTE 2: "CBD_Sandbox_dbg.map" file is embedded for reference." ***** |

Attributes

| Name | Value |
|-----------------------|---|
| Compiler Install Path | \$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5 |
| Float Precision | 9 |
| InitObjDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\obj |
| InitSrcDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\src |
| Linker File | \$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd |
| Makefile Template | \$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl |
| Target Install Path | \$(ProgramFiles)\pls\UDE 4.4 |
| Time Unit | Cycles |

TEST DETAILS REPORT

2017-02-06, 13:46:36+0530

AbsHwPos_Per1



| Attributes | |
|------------------|--|
| Name | Value |
| Timer Enabled | False |
| Timer Prescale | 0 |
| Timer Resolution | 1 |
| UDE Config File | \$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_I2PIN_JTAG.cfg |
| Workspace File | D:\Synergy_Work_Area\AbsHwPos_TcI2cVd_FDD05G\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP |

TEST DETAILS REPORT

AbsHwPos_Per1

2017-02-06, 13:46:36+0530



Test Case 1: Metrics Test

| | |
|---------------|--|
| Specification | "Performance Metrics (With "None" Instrumentation and WithPS Environment" |
| | CPU Cycles: TS1.1 1158.00 Cycles TS1.2 1714.00 Cycles |
| Description | Vector Description: TS1.1 Shortest Execution Path: (AbsHwPos_RelHwPos_HwDeg_M_f32>=D_RELHWPOSMAX_HWDEG_F32)=>True" |
| | TS1.2 Longest Execution Path: (AbsHwPos_RelHwPos_HwDeg_M_f32<=D_RELHWPOSMAX_HWDEG_F32)=>True" |

Test Step 1.1 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|----------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 10125 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_GearRatio_Uls_f32 | 1 | | |
| tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value | 10000 | | |
| tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value | 125 | | |
| tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value | 10000 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32 | tgt_AbsHwPos_Per1_ComplError_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32 | tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 9875 | 9875 | ✔ |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 9875 | 9875 ± 0.009 | ✔ |
| target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value | 3200 | 3200 | ✔ |
| tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value | 3200 | 3200 ± 0.1 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | ✓ |

Test Step 1.2 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|----------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -10125 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_GearRatio_Uls_f32 | 0.00999999978 | | |
| tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value | -10000 | | |
| tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value | -125 | | |
| tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value | -10000 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32 | tgt_AbsHwPos_Per1_ComplError_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32 | tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 25 | 25 | ✔ |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 25 | 25 ± 0.00009 | ✔ |
| target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value | 25 | 25 | ✔ |
| tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value | 25 | 25 ± 0.1 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:46:36+0530

AbsHwPos_Per1



Test Case 2: Range Test

Specification

"Performance Metrics
(With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS2.1 1718.00 Cycles
TS2.2 1158.00 Cycles
TS2.3 1715.00 Cycles
TS2.4 1714.00 Cycles
TS2.5 1715.00 Cycles
TS2.6 1715.00 Cycles
TS2.7 1721.00 Cycles
TS2.8 1714.00 Cycles
TS2.9 1715.00 Cycles
TS2.10 1715.00 Cycles
TS2.11 1715.00 Cycles
TS2.12 1715.00 Cycles
TS2.13 1715.00 Cycles
TS2.14 1715.00 Cycles
TS2.15 1715.00 Cycles
TS2.16 1715.00 Cycles
TS2.17 1715.00 Cycles
TS2.18 1715.00 Cycles
TS2.19 1715.00 Cycles
TS2.20 1715.00 Cycles
TS2.21 1715.00 Cycles
TS2.22 1715.00 Cycles
TS2.23 1715.00 Cycles
TS2.24 1715.00 Cycles
TS2.25 1715.00 Cycles

Description

Vector Description:

TS 2.1All Min
TS 2.2All Max
TS 2.3AlignedCumMechMtrPosCRF_Deg_f32==>Min
TS 2.4AlignedCumMechMtrPosCRF_Deg_f32==>Max
TS 2.5AlignedCumMechMtrPosCRF_Deg_f32==>Pos
TS 2.6AlignedCumMechMtrPosCRF_Deg_f32==>Zero
TS 2.7AlignedCumMechMtrPosCRF_Deg_f32==>Neg
TS 2.8ComplError_HwDeg_f32==>Min
TS 2.9ComplError_HwDeg_f32==>Max
TS 2.10ComplError_HwDeg_f32==>Pos
TS 2.11ComplError_HwDeg_f32==>Zero
TS 2.12ComplError_HwDeg_f32==>Neg
TS 2.13CumMechMtrPosCRF_Deg_f32==>Min
TS 2.14CumMechMtrPosCRF_Deg_f32==>Max
TS 2.15CumMechMtrPosCRF_Deg_f32==>Pos
TS 2.16CumMechMtrPosCRF_Deg_f32==>Zero
TS 2.17CumMechMtrPosCRF_Deg_f32==>Neg
TS 2.18k_GearRatio_Uls_f32==>Min
TS 2.19k_GearRatio_Uls_f32==>Max
TS 2.20k_GearRatio_Uls_f32==>Pos/Default
TS 2.21AbsHwPos_AlignedRelHwPos_HwDeg_M_f32==>Min
TS 2.22AbsHwPos_AlignedRelHwPos_HwDeg_M_f32==>Max
TS 2.23AbsHwPos_AlignedRelHwPos_HwDeg_M_f32==>Zero
TS 2.24AbsHwPos_AlignedRelHwPos_HwDeg_M_f32==>Pos
TS 2.25AbsHwPos_AlignedRelHwPos_HwDeg_M_f32==>Neg

Test Step 2.1 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|----------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -10125 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_GearRatio_Uls_f32 | 0.00999999978 | | |
| tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value | -10000 | | |
| tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value | -125 | | |
| tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value | -10000 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32 | tgt_AbsHwPos_Per1_ComplError_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32 | tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 25 | 25.0000019 | ✓ |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 25 | 25 ± 0.00009 | ✓ |
| target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value | 25 | 25.0000019 | ✓ |
| tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value | 25 | 25 ± 0.1 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:46:36+0530



AbsHwPos_Per1

Test Step 2.2 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|----------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 10125 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_GearRatio_Uls_f32 | 1 | | |
| tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value | 10000 | | |
| tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value | 125 | | |
| tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value | 10000 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32 | tgt_AbsHwPos_Per1_ComplError_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32 | tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 9875 | 9875 | ✔ |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 9875 | 9875 ± 0.009 | ✔ |
| target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value | 3200 | 3200 | ✔ |
| tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value | 3200 | 3200 ± 0.1 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.3 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|---------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -2171.08569 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_GearRatio_Uls_f32 | 1 | | |
| tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value | -10000 | | |
| tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value | 125 | | |
| tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value | 647.200012 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32 | tgt_AbsHwPos_Per1_ComplError_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32 | tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -10125 | -10125 | ✓ |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 522.200012 | 522.200012 ± 0.0009 | ✓ |
| target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value | -3200 | -3200 | ✓ |
| tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value | 522.200012 | 522.200012 ± 0.0009 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.4 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|----------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 2617.10376 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_GearRatio_Uls_f32 | 0.0299999993 | | |
| tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value | 10000 | | |
| tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value | 101.199997 | | |
| tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value | -361 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32 | tgt_AbsHwPos_Per1_ComplError_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32 | tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 198.800003 | 198.800003 | ✓ |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -112.029999 | -112.029999 ± 0.0009 | ✓ |
| target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value | 198.800003 | 198.800003 | ✓ |
| tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value | -112.029999 | -112.029999 ± 0.1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:46:36+0530



AbsHwPos_Per1

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.5 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|----------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 500 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_GearRatio_Uls_f32 | 0.0399999991 | | |
| tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value | 543.869995 | | |
| tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value | -54.7999992 | | |
| tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value | 912 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32 | tgt_AbsHwPos_Per1_ComplError_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32 | tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 76.5547943 | 76.5548019 | ✔ |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 91.2799988 | 91.2799988 ± 0.00009 | ✔ |
| target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value | 76.5547943 | 76.5548019 | ✔ |
| tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value | 91.2799988 | 91.2799988 ± 0.1 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.6 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|---------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 600 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_GearRatio_Uls_f32 | 0.0500000007 | | |
| tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value | 0 | | |
| tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value | 31.7000008 | | |
| tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value | 6123.6001 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32 | tgt_AbsHwPos_Per1_ComplError_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32 | tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -31.7000008 | -31.7000008 | ✓ |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 274.480011 | 274.480011 ± 0.0009 | ✓ |
| target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value | -31.7000008 | -31.7000008 | ✓ |
| tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value | 274.480011 | 274.480011 ± 0.1 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.7 (Repeat Count = 1)

| Name | Input Value |
|--|---|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 1639.56409 |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos |
| k_GearRatio_Uls_f32 | 1 |
| tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value | -879.599976 |
| tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value | 89 |
| tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value | -4456 |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 |

TEST DETAILS REPORT

2017-02-06, 13:46:36+0530



AbsHwPos_Per1

| Name | Input Value | | |
|---|--|----------------|--------|
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32 | tgt_AbsHwPos_Per1_ComplError_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32 | tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -968.599976 | -968.599976 | ✔ |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -4545 | -4545 ± 00.009 | ✔ |
| target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value | -968.599976 | -968.599976 | ✔ |
| tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value | -3200 | -3200 ± 0.1 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.8 (Repeat Count = 1) | | | | |
|--|--|---|---------------------|--------|
| Name | | Input Value | | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | -648.309998 | | |
| Rte_Inst_Ap_AbsHwPos | | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_GearRatio_Uls_f32 | | 0.0700000003 | | |
| tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value | | 7645.12012 | | |
| tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value | | -125 | | |
| tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value | | 1023.53998 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | | tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | | target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32 | | tgt_AbsHwPos_Per1_ComplError_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | |
| Name | | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | 660.158386 | 660.158386 | ✔ |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | 196.647797 | 196.647797 ± 0.0009 | ✔ |
| target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value | | 660.158386 | 660.158386 | ✔ |
| tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value | | 196.647797 | 196.647797 ± 0.1 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.9 (Repeat Count = 1) | | | | |
|--|--|---|---------------------|--------|
| Name | | Input Value | | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | 988.5 | | |
| Rte_Inst_Ap_AbsHwPos | | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_GearRatio_Uls_f32 | | 0.0799999982 | | |
| tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value | | 8123.3999 | | |
| tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value | | 125 | | |
| tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value | | 4789.6001 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | | tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | | target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32 | | tgt_AbsHwPos_Per1_ComplError_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | |
| Name | | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | 524.871948 | 524.871948 | ✓ |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | 258.167999 | 258.167999 ± 0.0009 | ✓ |
| target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value | | 524.871948 | 524.871948 | ✓ |
| tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value | | 258.167999 | 258.167999 ± 0.1 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:46:36+0530



AbsHwPos_Per1

Test Step 2.10 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|---------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 1024 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_GearRatio_Uls_f32 | 0.0900000036 | | |
| tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value | -8601.67969 | | |
| tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value | 66.5 | | |
| tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value | 8555.66016 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32 | tgt_AbsHwPos_Per1_ComplError_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32 | tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -840.651184 | -840.651184 | ✔ |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 703.50946 | 703.509399 ± 0.0009 | ✔ |
| target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value | -840.651184 | -840.651184 | ✔ |
| tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value | 703.50946 | 703.509399 ± 0.1 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.11 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|----------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 125.146599 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_GearRatio_Uls_f32 | 0.100000001 | | |
| tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value | 9079.95996 | | |
| tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value | 0 | | |
| tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value | 648.599976 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32 | tgt_AbsHwPos_Per1_ComplError_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32 | tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 907.996033 | 907.996033 | ✔ |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 64.8600006 | 64.8600006 ± 0.00009 | ✔ |
| target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value | 907.996033 | 907.996033 | ✔ |
| tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value | 64.8600006 | 64.8600006 ± 0.1 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.12 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|---------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 102.424004 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_GearRatio_Uls_f32 | 0.109999999 | | |
| tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value | 647.200012 | | |
| tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value | -75.5 | | |
| tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value | 987.099976 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32 | tgt_AbsHwPos_Per1_ComplError_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32 | tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 146.692001 | 146.692001 | ✔ |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 184.080994 | 184.080994 ± 0.0009 | ✔ |
| target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value | 146.692001 | 146.692001 | ✔ |
| tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value | 184.080994 | 184.080994 ± 0.1 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:46:36+0530

AbsHwPos_Per1



Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.13 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|---------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 135.449997 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_GearRatio_Uls_f32 | 0.119999997 | | |
| tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value | -361 | | |
| tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value | 41.9000015 | | |
| tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value | -10000 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32 | tgt_AbsHwPos_Per1_ComplError_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32 | tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -85.2200012 | -85.2200012 | ✔ |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -1241.90002 | -1241.90002 ± 0.009 | ✔ |
| target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value | -85.2200012 | -85.2200012 | ✔ |
| tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value | -1241.90002 | -1241.90002 ± 0.1 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.14 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|--------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 1245.01001 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_GearRatio_Uls_f32 | 0.129999995 | | |
| tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value | 912 | | |
| tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value | 69.4000015 | | |
| tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value | 10000 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32 | tgt_AbsHwPos_Per1_ComplError_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32 | tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 49.159996 | 49.159996 | ✔ |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 1230.59998 | 1230.59998 ± 0.009 | ✔ |
| target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value | 49.159996 | 49.159996 | ✔ |
| tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value | 1230.59998 | 1230.59998 ± 0.1 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.15 (Repeat Count = 1)

| Name | Input Value |
|--|---|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -3264.02002 |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos |
| k_GearRatio_Uls_f32 | 0.140000001 |
| tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value | 545.299988 |
| tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value | -50.7999992 |
| tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value | 743.869995 |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 |

TEST DETAILS REPORT






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AbsHwPos_Per1

| Name | Input Value | | |
|---|--|---------------------|--------|
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32 | tgt_AbsHwPos_Per1_ComplError_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32 | tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 127.141998 | 127.141998 | ✔ |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 154.941803 | 154.941803 ± 0.0009 | ✔ |
| target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value | 127.141998 | 127.141998 | ✔ |
| tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value | 154.941803 | 154.941803 ± 0.1 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.16 (Repeat Count = 1) | | | |  |
|--|--|---|----------------|---|
| Name | | Input Value | | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | -2590.63379 | | |
| Rte_Inst_Ap_AbsHwPos | | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_GearRatio_Uls_f32 | | 0.15000006 | | |
| tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value | | -3479 | | |
| tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value | | 99 | | |
| tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value | | 0 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | | tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | | target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32 | | tgt_AbsHwPos_Per1_ComplError_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | |
| Name | | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | -620.850037 | -620.850037 |  |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | -99 | -99 ± 0.00009 |  |
| target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value | | -620.850037 | -620.850037 |  |
| tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value | | -99 | -99 ± 0.1 |  |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.17 (Repeat Count = 1) | | | | |
|--|--|---|-----------------------|--------|
| Name | | Input Value | | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | 104.297646 | | |
| Rte_Inst_Ap_AbsHwPos | | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_GearRatio_Uls_f32 | | 0.159999996 | | |
| tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value | | 7258 | | |
| tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value | | -105.599998 | | |
| tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value | | -849.599976 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | | tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | | target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32 | | tgt_AbsHwPos_Per1_ComplError_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | |
| Name | | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | 1266.88 | 1266.88 | ✓ |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | -30.3359909 | -30.3360004 ± 0.00009 | ✓ |
| target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value | | 1266.88 | 1266.88 | ✓ |
| tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value | | -30.3359909 | -30.3360004 ± 0.1 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:46:36+0530



AbsHwPos_Per1

Test Step 2.18 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|-----------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 1481.36023 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_GearRatio_Uls_f32 | 0.00999999978 | | |
| tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value | -6123.6001 | | |
| tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value | 120.5 | | |
| tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value | 4426 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32 | tgt_AbsHwPos_Per1_ComplError_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32 | tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -181.735992 | -181.735992 | ✔ |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -76.2400055 | -76.2399979 ± 0.00009 | ✔ |
| target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value | -181.735992 | -181.735992 | ✔ |
| tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value | -76.2400055 | -76.2399979 ± 0.1 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.19 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|---------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -655.123474 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_GearRatio_Uls_f32 | 1 | | |
| tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value | -2456 | | |
| tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value | 96.5999985 | | |
| tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value | 589 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32 | tgt_AbsHwPos_Per1_ComplError_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32 | tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -2552.6001 | -2552.6001 | ✓ |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 492.399994 | 492.399994 ± 0.0009 | ✓ |
| target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value | -2552.6001 | -2552.6001 | ✓ |
| tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value | 492.399994 | 492.399994 ± 0.1 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.20 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|---------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 1324.41345 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_GearRatio_Uls_f32 | 0.0454545468 | | |
| tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value | 1023.53998 | | |
| tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value | -44.7999992 | | |
| tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value | -456.5 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32 | tgt_AbsHwPos_Per1_ComplError_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32 | tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 91.3245468 | 91.3245468 | ✔ |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 24.0499992 | 24.0499992 ± 0.0009 | ✔ |
| target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value | 91.3245468 | 91.3245468 | ✔ |
| tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value | 24.0499992 | 24.0499992 ± 0.0009 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:46:36+0530



AbsHwPos_Per1

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.21 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|----------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -10125 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_GearRatio_Uls_f32 | 0.100000001 | | |
| tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value | 8123.3999 | | |
| tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value | -50.7999992 | | |
| tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value | -4456 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32 | tgt_AbsHwPos_Per1_ComplError_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32 | tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 863.140015 | 863.140015 | ✔ |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -394.800018 | -394.799988 ± 0.0009 | ✔ |
| target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value | 863.140015 | 863.140015 | ✔ |
| tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value | -394.800018 | -394.799988 ± 0.0009 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.22 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|---------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 10125 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_GearRatio_Uls_f32 | 0.109999999 | | |
| tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value | -8601.67969 | | |
| tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value | 99 | | |
| tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value | 1023.53998 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32 | tgt_AbsHwPos_Per1_ComplError_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32 | tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -1045.18481 | -1045.18481 | ✔ |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 13.5893936 | 13.5894003 ± 0.0009 | ✔ |
| target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value | -1045.18481 | -1045.18481 | ✔ |
| tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value | 13.5893936 | 13.5894003 ± 0.0009 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.23 (Repeat Count = 1)

| Name | Input Value |
|--|---|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 0 |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos |
| k_GearRatio_Uls_f32 | 0.119999997 |
| tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value | 9079.95996 |
| tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value | -105.599998 |
| tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value | 4789.6001 |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 |

TEST DETAILS REPORT

2017-02-06, 13:46:36+0530



AbsHwPos_Per1

| Name | Input Value | | |
|---|--|--------------------|--------|
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32 | tgt_AbsHwPos_Per1_ComplError_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32 | tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 1195.19519 | 1195.19519 | ✔ |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 680.35199 | 680.35199 ± 0.0009 | ✔ |
| target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value | 1195.19519 | 1195.19519 | ✔ |
| tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value | 680.35199 | 680.35199 ± 0.0009 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.24 (Repeat Count = 1) | | | | |
|--|--|---|---------------------|--------|
| Name | | Input Value | | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | 1245.01001 | | |
| Rte_Inst_Ap_AbsHwPos | | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_GearRatio_Uls_f32 | | 0.129999995 | | |
| tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value | | 647.200012 | | |
| tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value | | 120.5 | | |
| tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value | | 8555.66016 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | | tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | | target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32 | | tgt_AbsHwPos_Per1_ComplError_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | |
| Name | | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | -36.3639984 | -36.3640022 | ✓ |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | 991.73584 | 991.735779 ± 0.0009 | ✓ |
| target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value | | -36.3639984 | -36.3640022 | ✓ |
| tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value | | 991.73584 | 991.735779 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.25 (Repeat Count = 1) | | | | |
|--|--|---|---------------------|--------|
| Name | | Input Value | | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | -3264.02002 | | |
| Rte_Inst_Ap_AbsHwPos | | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_GearRatio_Uls_f32 | | 0.239999995 | | |
| tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32.value | | 12.4499998 | | |
| tgt_AbsHwPos_Per1_ComplError_HwDeg_f32.value | | 12.4499998 | | |
| tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32.value | | 648.599976 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | | tgt_AbsHwPos_Per1_AlignedCumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | | target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_ComplError_HwDeg_f32 | | tgt_AbsHwPos_Per1_ComplError_HwDeg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | tgt_AbsHwPos_Per1_CumMechMtrPosCRF_Deg_f32 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32 | | |
| Name | | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | -9.46199989 | -9.46199989 | ✓ |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | 143.213989 | 143.214005 ± 0.0009 | ✓ |
| target_AbsHwPos_Per1_AlignedRelHwPos_HwDeg_f32.value | | -9.46199989 | -9.46199989 | ✓ |
| tgt_AbsHwPos_Per1_RelHwPos_HwDeg_f32.value | | 143.213989 | 143.214005 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per1_CP1_CheckpointReached | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:56:12+0530

AbsHwPos_SCom_CustSetTrim



| | |
|-------------|---------------------------|
| Project | AbsHwPos |
| Module | AbsHwPos_Tcl2IVd |
| Test Object | AbsHwPos_SCom_CustSetTrim |

Instrumentation: Test Object Only

| | |
|-------------------------|-------|
| Statement (C0) Coverage | 100 % |
| Decision Coverage | 100 % |
| Branch (C1) Coverage | 100 % |
| MCC Coverage | 100 % |
| MC/DC Coverage | 100 % |

Statistics

| | |
|-----------------|-----|
| Total Testcases | 3 |
| Successful | 3 ✓ |
| Failed | 0 |
| Not Executed | 0 |

Module Properties

| | |
|------------------------|--|
| Project Root Directory | D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G |
| Configuration File | D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml |
| Target Environment | TI TMS 570 PLS UDE (Default) |
| Kind of Test | Unit Test |
| Linker Options | |
| Source File(s) | |
| File | \$(SOURCEROOT)\Ap_AbsHwPos.c |
| Compiler Options | -D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap_AbsHwPos -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include |

Comments/Description/Specification

| Name | Text |
|---------------------------|--|
| Module 'AbsHwPos_Tcl2IVd' | *****Unit Test Description***** Name of Tester: Medha Sabale Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version: 10 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:11 Data Dictionary Version: 10 Unit Test Plan Version:3 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):3004 Total RAM Used (Bytes):64 Total CALS Used (Bytes):56 Special Test Requirements:NA Test Date:2/6/2017 Comments: NOTE 1:Inline functions defined in "Globalmacro.h" are not unit tested. NOTE 2: "CBD_Sandbox_dbg.map" file is embedded for reference." ***** |

Attributes

| Name | Value |
|-----------------------|---|
| Compiler Install Path | \$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5 |
| Float Precision | 9 |
| InitObjDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\obj |
| InitSrcDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\src |
| Linker File | \$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd |
| Makefile Template | \$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl |
| Target Install Path | \$(ProgramFiles)\pls\UDE 4.4 |
| Time Unit | Cycles |

TEST DETAILS REPORT

2017-02-06, 13:56:12+0530

AbsHwPos_SCom_CustSetTrim



| Attributes | |
|------------------|--|
| Name | Value |
| Timer Enabled | False |
| Timer Prescale | 0 |
| Timer Resolution | 1 |
| UDE Config File | \$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_I2PIN_JTAG.cfg |
| Workspace File | D:\Synergy_Work_Area\AbsHwPos_TcI2cVd_FDD05G\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP |

TEST DETAILS REPORT

2017-02-06, 13:56:12+0530

AbsHwPos_SCom_CustSetTrim



Test Case 1: Metrics Test

Specification

"Performance Metrics
(With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS1.1 2163.00 Cycles
TS1.2 2974.00 Cycles

Description

Vector Description:

TS1.1 Shortest Execution Path:

if (TurnsCntValidity_Cnt_T_u08 == D_TCVVALID_CNT_U08)=>False
if (AbsHwPos_VehCntValid_Cnt_M_lgc == FALSE)=>True"

TS1.2 Longest Execution Path:

if (Abs_f32_m(AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32) <= k_VehCntOffValidLimit_HwDeg_f32)=>True
if (AbsHwPos_VehCntValid_Cnt_M_lgc == FALSE)=>False"

Test Step 1.1 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|----------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -10125 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1600 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 0 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 0 | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 0 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_CustSetTrim() | 34 | 34 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 0 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | 0 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Read_Ap_AbsHwPos_TurnsCntValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |

Test Step 1.2 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|---------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 468.011292 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -820.067871 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1074.49158 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 1933 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 1 | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 100 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_CustSetTrim() | 0 | 0 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 468.011292 | 468.011292 ± 0.0009 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:56:12+0530



AbsHwPos_SCom_CustSetTrim

| Name | Actual Value | Expected Value | Result |
|--|--------------|---------------------|--------|
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 468.011292 | 468.011292 ± 0.0009 | ✓ |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 43690 | 43690 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

Test Case 2: Range Test

Specification "Performance Metrics
(With "None" Instrumentation and WithPS Environment")

CPU Cycles:

TS2.1 2163.00 Cycles
TS2.2 2387.00 Cycles
TS2.3 2753.00 Cycles
TS2.4 2387.00 Cycles
TS2.5 2166.00 Cycles
TS2.6 2385.00 Cycles
TS2.7 2166.00 Cycles
TS2.8 2972.00 Cycles
TS2.9 2166.00 Cycles
TS2.10 2385.00 Cycles
TS2.11 2758.00 Cycles
TS2.12 2387.00 Cycles
TS2.13 2166.00 Cycles
TS2.14 2385.00 Cycles
TS2.15 2758.00 Cycles
TS2.16 2380.00 Cycles
TS2.17 2165.00 Cycles
TS2.18 2965.00 Cycles
TS2.19 2167.00 Cycles
TS2.20 2385.00 Cycles
TS2.21 2758.00 Cycles
TS2.22 2380.00 Cycles
TS2.23 2165.00 Cycles

Description Vector Description:

TS2.1 All Min
TS2.2 All Max
TS2.3 ManufMode_Cnt_enum==>Min
TS2.4 ManufMode_Cnt_enum==>Max
TS2.5 ManufMode_Cnt_enum==>Pos
TS2.6 TurnsCntrValidity_Cnt_enum==>Min
TS2.7 TurnsCntrValidity_Cnt_enum==>Max
TS2.8 TurnsCntrValidity_Cnt_enum==>Pos
TS2.9 AlignedCumMtrPos_HwDeg_M_f32==>Min
TS2.10 AlignedCumMtrPos_HwDeg_M_f32==>Max
TS2.11 AlignedCumMtrPos_HwDeg_M_f32==>Pos
TS2.12 AlignedCumMtrPos_HwDeg_M_f32==>Zero
TS2.13 AlignedCumMtrPos_HwDeg_M_f32==>Neg
TS2.14 k_VehCntrOffValidLimit_HwDeg_f32==>Min
TS2.15 k_VehCntrOffValidLimit_HwDeg_f32==>Max/Default
TS2.16 k_VehCntrOffValidLimit_HwDeg_f32==>Pos
TS2.17 VehCntrOffset_HwDeg_M_f32==>Min
TS2.18 VehCntrOffset_HwDeg_M_f32==>Max
TS2.19 VehCntrOffset_HwDeg_M_f32==>Pos
TS2.20 VehCntrOffset_HwDeg_M_f32==>Zero
TS2.21 VehCntrOffset_HwDeg_M_f32==>Neg
TS2.22 VehCntrValid_Cnt_M_lgc==>Min
TS2.23 VehCntrValid_Cnt_M_lgc==>Max

Test Step 2.1 (Repeat Count = 1)

| Name | Input Value | | |
|---|---|----------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -10125 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1600 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 0 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 0 | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 0 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_CustSetTrim() | 34 | 34 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:56:12+0530



AbsHwPos_SCom_CustSetTrim

| Name | Actual Value | Expected Value | Result |
|--|--------------|----------------|--------|
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 0 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✓ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | 0 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |

Test Step 2.2 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|----------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 10125 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1600 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 43690 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 2 | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 255 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_CustSetTrim() | 34 | 34 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | 0 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

Test Step 2.3 (Repeat Count = 1)

| Name | Input Value | | |
|---|---|----------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -2783.01367 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1077.47595 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 987.40387 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 124 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 0 | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 100 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_CustSetTrim() | 0 | 0 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:56:12+0530



AbsHwPos_SCom_CustSetTrim

| Name | Actual Value | Expected Value | Result |
|--|--------------|----------------|--------|
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | 0 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |

Test Step 2.4 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|----------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -1298.70605 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1377.42493 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1209.60217 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 589 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 2 | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 55 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_CustSetTrim() | 34 | 34 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | 0 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

Test Step 2.5 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|----------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 468.011292 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -820.067871 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1074.49158 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 1054 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 1 | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 16 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_CustSetTrim() | 34 | 34 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:56:12+0530



AbsHwPos_SCom_CustSetTrim

| Name | Actual Value | Expected Value | Result |
|--|--------------|----------------|--------|
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | 0 | ✓ |

Test Step Call Trace ✓

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |

Test Step 2.6 (Repeat Count = 1) ✓

| Name | Input Value | | |
|--|---|----------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 2075.32788 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -64.231987 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1386.30811 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 358 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 2 | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 0 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_CustSetTrim() | 34 | 34 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | 0 | ✔ |

Test Step Call Trace ✓

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

Test Step 2.7 (Repeat Count = 1) ✓

| Name | Input Value | | |
|--|---|----------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -2223.71802 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1012.77295 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1031.59229 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 673 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 0 | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 255 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_CustSetTrim() | 34 | 34 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | 0 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:56:12+0530

AbsHwPos_SCom_CustSetTrim



Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Read_Ap_AbsHwPos_TurnsCtrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCtrValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCtrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCtrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |

Test Step 2.8 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|----------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -2778.2627 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 876.887268 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1053.97559 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 988 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 0 | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 100 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_CustSetTrim() | 0 | 0 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | 0 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Read_Ap_AbsHwPos_TurnsCtrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCtrValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCtrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCtrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

Test Step 2.9 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|----------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -10125 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1324.01794 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 409.805206 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 1303 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 1 | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 123 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_CustSetTrim() | 34 | 34 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | 0 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:56:12+0530

AbsHwPos_SCom_CustSetTrim



Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Read_Ap_AbsHwPos_TurnsCtrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCtrValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCtrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCtrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |

Test Step 2.10 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|----------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 10125 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -524.091675 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 771.315552 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 1618 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 2 | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 255 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_CustSetTrim() | 34 | 34 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | 0 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Read_Ap_AbsHwPos_TurnsCtrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCtrValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCtrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCtrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

Test Step 2.11 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|---------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 468.011292 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -820.067871 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1074.49158 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 1933 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 1 | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 100 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_CustSetTrim() | 0 | 0 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 468.011292 | 468.011292 ± 0.0009 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 468.011292 | 468.011292 ± 0.0009 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 43690 | 43690 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:56:12+0530

AbsHwPos_SCom_CustSetTrim



Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Read_Ap_AbsHwPos_TurnsCtrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCtrValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCtrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCtrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |

Test Step 2.12 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|----------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 0 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1140.87488 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 988.865723 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 2248 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 2 | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 64 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_CustSetTrim() | 34 | 34 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | 0 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Read_Ap_AbsHwPos_TurnsCtrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCtrValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCtrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCtrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

Test Step 2.13 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|----------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -619.01001 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -452.819061 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 205.275681 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 2563 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 1 | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 33 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_CustSetTrim() | 34 | 34 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | 0 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:56:12+0530

AbsHwPos_SCom_CustSetTrim



Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Read_Ap_AbsHwPos_TurnsCtrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCtrValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCtrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCtrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |

Test Step 2.14 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|----------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 166.198761 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 404.824768 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 0 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 2878 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 0 | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 185 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_CustSetTrim() | 34 | 34 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | 0 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Read_Ap_AbsHwPos_TurnsCtrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCtrValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCtrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCtrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

Test Step 2.15 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|----------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -612.046448 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 870.147095 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1600 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 3193 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 1 | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 100 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_CustSetTrim() | 0 | 0 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -612.046448 | -612.046448 ± 0.0009 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -612.046448 | -612.046448 ± 0.0009 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 43690 | 43690 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:56:12+0530

AbsHwPos_SCom_CustSetTrim



Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Read_Ap_AbsHwPos_TurnsCtrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCtrValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCtrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCtrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |

Test Step 2.16 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|----------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 468.011292 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -820.067871 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1074.49158 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 3508 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 1 | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 78 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_CustSetTrim() | 34 | 34 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | 0 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Read_Ap_AbsHwPos_TurnsCtrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCtrValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCtrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCtrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

Test Step 2.17 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|----------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 907.880615 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1600 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 575.52533 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 3823 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 0 | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 50 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_CustSetTrim() | 34 | 34 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | 0 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:56:12+0530

AbsHwPos_SCom_CustSetTrim



Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Read_Ap_AbsHwPos_TurnsCtrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCtrValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCtrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCtrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |

Test Step 2.18 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|----------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -1563.76599 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 178.001389 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 4138 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 1 | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 100 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_CustSetTrim() | 0 | 0 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | 0 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Read_Ap_AbsHwPos_TurnsCtrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCtrValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCtrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCtrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

Test Step 2.19 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|----------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 2688.78174 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 1172.12 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1164.8783 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 4453 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 2 | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 0 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_CustSetTrim() | 34 | 34 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | 0 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:56:12+0530

AbsHwPos_SCom_CustSetTrim



Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Read_Ap_AbsHwPos_TurnsCtrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCtrValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCtrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCtrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |

Test Step 2.20 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|----------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -941.775391 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 0 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1122.76965 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 4768 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 2 | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 255 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_CustSetTrim() | 34 | 34 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | 0 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Read_Ap_AbsHwPos_TurnsCtrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCtrValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCtrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCtrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

Test Step 2.21 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|---------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 933.228271 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -578.130005 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1599.21655 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 5083 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 0 | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 100 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_CustSetTrim() | 0 | 0 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 933.228271 | 933.228271 ± 0.0009 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 933.228271 | 933.228271 ± 0.0009 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 43690 | 43690 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:56:12+0530

AbsHwPos_SCom_CustSetTrim



Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Read_Ap_AbsHwPos_TurnsCtrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCtrValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCtrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCtrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |

Test Step 2.22 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|----------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 468.011292 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -820.067871 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1074.49158 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 5398 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 1 | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 14 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_CustSetTrim() | 34 | 34 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | 0 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Read_Ap_AbsHwPos_TurnsCtrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCtrValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCtrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCtrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

Test Step 2.23 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|----------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 804.195251 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 1562.1189 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 904.213257 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 5713 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 1 | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 225 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_CustSetTrim() | 34 | 34 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | 0 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:56:12+0530

AbsHwPos_SCom_CustSetTrim



Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |

Test Case 3: Path Test

| | |
|----------------------|---|
| Specification | "Performance Metrics (With "None" Instrumentation and WithPS Environment" CPU Cycles: TS3.1 2163.00 Cycles TS3.2 2974.00 Cycles TS3.3 2973.00 Cycles TS3.4 2980.00 Cycles |
| Description | Vector Description: TS3.1 if (TurnsCntrValidity_Cnt_T_u08 == D_TCVVALID_CNT_U08)=>False TS3.2 "if (TurnsCntrValidity_Cnt_T_u08 == D_TCVVALID_CNT_U08)=>True if (Abs_f32_m(AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32) <= k_VehCntrOffValidLimit_HwDeg_f32)=>False if (AbsHwPos_VehCntrValid_Cnt_M_lgc == FALSE)=>True" TS3.3 "if (Abs_f32_m(AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32) <= k_VehCntrOffValidLimit_HwDeg_f32)=>True if (AbsHwPos_VehCntrValid_Cnt_M_lgc == FALSE)=>False" TS3.4 "((ManufMode_Cnt_T_enum != ManufacturingMode) =True&& (Rte_Pim_EOLVehCntrOffset()->EOLHwPosTrimPerformed_Cnt_u16 != D_TRIMPERFORMED_CNT_U16))=False" |

Test Step 3.1 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|----------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -10125 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1600 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 0 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 0 | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 0 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_CustSetTrim() | 34 | 34 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 0 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | 0 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |

Test Step 3.2 (Repeat Count = 1)

| Name | Input Value |
|--|---|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -2783.01367 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1077.47595 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 987.40387 |

TEST DETAILS REPORT

2017-02-06, 13:56:12+0530



AbsHwPos_SCom_CustSetTrim

| Name | Input Value | | |
|--|--------------------------|----------------|--------|
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 124 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 0 | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 100 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_CustSetTrim() | 0 | 0 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | 0 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

| Test Step 3.3 (Repeat Count = 1) | | | | |
|--|--|---|---------------------|--------|
| Name | | Input Value | | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | 468.011292 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | -820.067871 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 0 | | |
| Rte_Inst_Ap_AbsHwPos | | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | |
| k_UseTurnsCntr_Cnt_lgc | | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | | 1074.49158 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | | 1933 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | 1 | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | | 100 | | |
| Name | | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_CustSetTrim() | | 0 | 0 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | 468.011292 | 468.011292 ± 0.0009 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | | 0 | 0 | ✓ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | | 468.011292 | 468.011292 ± 0.0009 | ✓ |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | | 43690 | 43690 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

| Test Step 3.4 (Repeat Count = 1) | | |
|--|---|--|
| Name | Input Value | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 153.139999 | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1077.47595 | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08(data) | tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | |
| k_UseTurnsCntr_Cnt_lgc | 1 | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 425.119995 | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 43690 | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | |

TEST DETAILS REPORT

2017-02-06, 13:56:12+0530



AbsHwPos_SCom_CustSetTrim

| Name | Input Value | | |
|--|--------------|---------------------|--------|
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 0 | | |
| tgt_Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08_data | 100 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_SCom_CustSetTrim() | 0 | 0 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 153.139999 | 153.139999 ± 0.0009 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✓ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 153.139999 | 153.139999 ± 0.0009 | ✓ |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 43690 | 43690 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | Rte_Read_Ap_AbsHwPos_TurnsCntrValidity_Cnt_u08 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 14:04:27+0530

TrimNotPerfDiag



| | |
|-------------|------------------|
| Project | AbsHwPos |
| Module | AbsHwPos_Tcl2IVd |
| Test Object | TrimNotPerfDiag |

Instrumentation: Test Object Only

| | |
|-------------------------|-------|
| Statement (C0) Coverage | 100 % |
| Decision Coverage | 100 % |
| Branch (C1) Coverage | 100 % |
| MCC Coverage | 100 % |
| MC/DC Coverage | 100 % |

Statistics

| | |
|-----------------|-----|
| Total Testcases | 3 |
| Successful | 3 ✓ |
| Failed | 0 |
| Not Executed | 0 |

Module Properties

| | |
|------------------------|--|
| Project Root Directory | D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G |
| Configuration File | D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml |
| Target Environment | TI TMS 570 PLS UDE (Default) |
| Kind of Test | Unit Test |
| Linker Options | |
| Source File(s) | |
| File | \$(SOURCEROOT)\Ap_AbsHwPos.c |
| Compiler Options | -D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap_AbsHwPos -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include |

Comments/Description/Specification

| Name | Text |
|---------------------------|--|
| Module 'AbsHwPos_Tcl2IVd' | *****Unit Test Description***** Name of Tester: Medha Sabale Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version: 10 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:11 Data Dictionary Version: 10 Unit Test Plan Version:3 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):3004 Total RAM Used (Bytes):64 Total CALS Used (Bytes):56 Special Test Requirements:NA Test Date:2/6/2017 Comments: NOTE 1:Inline functions defined in "Globalmacro.h" are not unit tested. NOTE 2: "CBD_Sandbox_dbg.map" file is embedded for reference." ***** |

Attributes

| Name | Value |
|-----------------------|---|
| Compiler Install Path | \$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5 |
| Float Precision | 9 |
| InitObjDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\obj |
| InitSrcDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\src |
| Linker File | \$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd |
| Makefile Template | \$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl |
| Target Install Path | \$(ProgramFiles)\pls\UDE 4.4 |
| Time Unit | Cycles |

TEST DETAILS REPORT

2017-02-06, 14:04:27+0530

TrimNotPerfDiag



| Attributes | |
|------------------|--|
| Name | Value |
| Timer Enabled | False |
| Timer Prescale | 0 |
| Timer Resolution | 1 |
| UDE Config File | \$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_I2PIN_JTAG.cfg |
| Workspace File | D:\Synergy_Work_Area\AbsHwPos_TcI2cVd_FDD05G\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP |

Test Case 1: Metrics Test

| | |
|---------------|--|
| Specification | "Performance Metrics (With "None" Instrumentation and WithPS Environment)" |
| | CPU Cycles: TS1.1 740.00 Cycles TS1.2 1728.00 Cycles |
| Description | Vector Description: TS 1.1 Shortest Execution Path: (k_UseTurnsCntr_Cnt_Igc == TRUE)=False, ((ManufMode_Cnt_T_enum <> ManufacturingMode) And (Rte_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 <> D_TRIMPERFORMED_CNT_U16))=>True |
| | TS 1.2 Longest Execution Path: (k_UseTurnsCntr_Cnt_Igc == TRUE)=True, ((ManufMode_Cnt_T_enum <> ManufacturingMode) And (Rte_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 <> D_TRIMPERFORMED_CNT_U16))=>False |

Test Step 1.1 (Repeat Count = 1)

| Name | Input Value | | |
|--|--------------------------|----------------|--------|
| ManufMode_Cnt_T_enum | 0 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_UseTurnsCntr_Cnt_Igc | 0 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 0 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|-----------------|-------|--------------------------|-------|--------|
| *none* | 0 | *** No Call Expected *** | 0 | ✓ |

Test Step 1.2 (Repeat Count = 1)

| Name | Input Value | | |
|--|--------------------------|----------------|--------|
| ManufMode_Cnt_T_enum | 2 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_UseTurnsCntr_Cnt_Igc | 1 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 43690 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

Test Case 2: Range Test

Specification "Performance Metrics
(With "None" Instrumentation and WithPS Environment")

CPU Cycles:

TS2.1 740.00 Cycles
TS2.2 1728.00 Cycles
TS2.3 739.00 Cycles
TS2.4 1731.00 Cycles
TS2.5 739.00 Cycles
TS2.6 1521.00 Cycles
TS2.7 739.00 Cycles
TS2.8 1731.00 Cycles
TS2.9 739.00 Cycles
TS2.10 1520.00 Cycles

Description Vector Description:

TS 2.1 All Min
TS 2.2 All Max
TS 2.3 ManufMode_Cnt_T_enum=>Min
TS 2.4 ManufMode_Cnt_T_enum=>Max
TS 2.5 ManufMode_Cnt_T_enum=>Pos
TS 2.6 Rte_Pim_EOLVehCntOffset.EOLHwPosTrimPerformed_Cnt_u16=>Min
TS 2.7 Rte_Pim_EOLVehCntOffset.EOLHwPosTrimPerformed_Cnt_u16=>Max
TS 2.8 Rte_Pim_EOLVehCntOffset.EOLHwPosTrimPerformed_Cnt_u16=>Pos
TS 2.9 k_UseTurnsCnt_Cnt_lgc=>Min
TS 2.10 k_UseTurnsCnt_Cnt_lgc=>Max

Test Step 2.1 (Repeat Count = 1)

| Name | Input Value | | |
|--|--------------------------|----------------|--------|
| ManufMode_Cnt_T_enum | 0 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_UseTurnsCnt_Cnt_lgc | 0 | | |
| tgt_Pim_EOLVehCntOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntOffset | tgt_Pim_EOLVehCntOffset | | |
| Name | Actual Value | Expected Value | Result |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 0 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|-----------------|-------|--------------------------|-------|--------|
| *none* | 0 | *** No Call Expected *** | 0 | ✓ |

Test Step 2.2 (Repeat Count = 1)

| Name | Input Value | | |
|--|--------------------------|----------------|--------|
| ManufMode_Cnt_T_enum | 2 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_UseTurnsCnt_Cnt_lgc | 1 | | |
| tgt_Pim_EOLVehCntOffset.EOLHwPosTrimPerformed_Cnt_u16 | 43690 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntOffset | tgt_Pim_EOLVehCntOffset | | |
| Name | Actual Value | Expected Value | Result |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

Test Step 2.3 (Repeat Count = 1)

| Name | Input Value | | |
|--|--------------------------|----------------|--------|
| ManufMode_Cnt_T_enum | 0 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_UseTurnsCnt_Cnt_lgc | 0 | | |
| tgt_Pim_EOLVehCntOffset.EOLHwPosTrimPerformed_Cnt_u16 | 124 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntOffset | tgt_Pim_EOLVehCntOffset | | |
| Name | Actual Value | Expected Value | Result |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✓ |

TEST DETAILS REPORT

2017-02-06, 14:04:27+0530

TrimNotPerfDiag



Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|-----------------|-------|--------------------------|-------|--------|
| *none* | 0 | *** No Call Expected *** | 0 | ✓ |

Test Step 2.4 (Repeat Count = 1)

| Name | Input Value | | |
|--|--------------------------|----------------|--------|
| ManufMode_Cnt_T_enum | 2 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_UseTurnsCntr_Cnt_Igc | 1 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 589 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

Test Step 2.5 (Repeat Count = 1)

| Name | Input Value | | |
|--|--------------------------|----------------|--------|
| ManufMode_Cnt_T_enum | 1 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 1054 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|-----------------|-------|--------------------------|-------|--------|
| *none* | 0 | *** No Call Expected *** | 0 | ✓ |

Test Step 2.6 (Repeat Count = 1)

| Name | Input Value | | |
|--|--------------------------|----------------|--------|
| ManufMode_Cnt_T_enum | 1 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 14:04:27+0530

TrimNotPerfDiag



Test Step 2.7 (Repeat Count = 1)

| Name | Input Value | | |
|--|--------------------------|----------------|--------|
| ManufMode_Cnt_T_enum | 0 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_UseTurnsCntr_Cnt_Igc | 0 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 43690 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|-----------------|-------|--------------------------|-------|--------|
| *none* | 0 | *** No Call Expected *** | 0 | ✓ |

Test Step 2.8 (Repeat Count = 1)

| Name | Input Value | | |
|--|--------------------------|----------------|--------|
| ManufMode_Cnt_T_enum | 2 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_UseTurnsCntr_Cnt_Igc | 1 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 2449 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

Test Step 2.9 (Repeat Count = 1)

| Name | Input Value | | |
|--|--------------------------|----------------|--------|
| ManufMode_Cnt_T_enum | 0 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_UseTurnsCntr_Cnt_Igc | 0 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 1284 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|-----------------|-------|--------------------------|-------|--------|
| *none* | 0 | *** No Call Expected *** | 0 | ✓ |

Test Step 2.10 (Repeat Count = 1)

| Name | Input Value | | |
|--|--------------------------|----------------|--------|
| ManufMode_Cnt_T_enum | 1 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_UseTurnsCntr_Cnt_Igc | 1 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 6258 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✓ |

TEST DETAILS REPORT

2017-02-06, 14:04:27+0530

TrimNotPerfDiag



Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

Test Case 3: Path Test

Specification "Performance Metrics
(With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS3.1 741.00 Cycles
TS3.2 1727.00 Cycles
TS3.3 1731.00 Cycles

Description Vector Description:

TS 3.1 "(k_UseTurnsCntr_Cnt_Igc == TRUE)=False, ((ManufMode_Cnt_T_enum <> ManufacturingMode) And (Rte_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 <> D_TRIMPERFORMED_CNT_U16))=>True"
TS 3.2 "(k_UseTurnsCntr_Cnt_Igc == TRUE)=True, ((ManufMode_Cnt_T_enum <> ManufacturingMode) And (Rte_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 <> D_TRIMPERFORMED_CNT_U16))=>False"
TS 3.3 "(k_UseTurnsCntr_Cnt_Igc == TRUE)=True, ((ManufMode_Cnt_T_enum != ManufacturingMode) && (Rte_Pim_EOLVehCntrOffset()->EOLHwPosTrimPerformed_Cnt_u16 != D_TRIMPERFORMED_CNT_U16))=False"

Test Step 3.1 (Repeat Count = 1)

| Name | Input Value | | |
|--|--------------------------|----------------|--------|
| ManufMode_Cnt_T_enum | 0 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_UseTurnsCntr_Cnt_Igc | 0 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 0 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|-----------------|-------|--------------------------|-------|--------|
| *none* | 0 | *** No Call Expected *** | 0 | ✓ |

Test Step 3.2 (Repeat Count = 1)

| Name | Input Value | | |
|--|--------------------------|----------------|--------|
| ManufMode_Cnt_T_enum | 2 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_UseTurnsCntr_Cnt_Igc | 1 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 43690 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

Test Step 3.3 (Repeat Count = 1)

| Name | Input Value | | |
|--|--------------------------|----------------|--------|
| ManufMode_Cnt_T_enum | 2 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_UseTurnsCntr_Cnt_Igc | 1 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 589 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |

TEST DETAILS REPORT

2017-02-06, 14:04:27+0530

TrimNotPerfDiag



Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:45:26+0530

AbsHwPos_Init1



| | |
|-------------|------------------|
| Project | AbsHwPos |
| Module | AbsHwPos_Tcl2IVd |
| Test Object | AbsHwPos_Init1 |

Instrumentation: Test Object Only

| | |
|-------------------------|-------|
| Statement (C0) Coverage | 100 % |
| Decision Coverage | 100 % |
| Branch (C1) Coverage | 100 % |
| MCC Coverage | 100 % |
| MC/DC Coverage | 100 % |

Statistics

| | |
|-----------------|-----|
| Total Testcases | 3 |
| Successful | 3 ✓ |
| Failed | 0 |
| Not Executed | 0 |

Module Properties

| | |
|------------------------|--|
| Project Root Directory | D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G |
| Configuration File | D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml |
| Target Environment | TI TMS 570 PLS UDE (Default) |
| Kind of Test | Unit Test |
| Linker Options | |
| Source File(s) | |
| File | \$(SOURCEROOT)\Ap_AbsHwPos.c |
| Compiler Options | -D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap_AbsHwPos -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include |

Comments/Description/Specification

| Name | Text |
|---------------------------|--|
| Module 'AbsHwPos_Tcl2IVd' | *****Unit Test Description***** Name of Tester: Medha Sabale Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version: 10 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:11 Data Dictionary Version: 10 Unit Test Plan Version:3 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):3004 Total RAM Used (Bytes):64 Total CALS Used (Bytes):56 Special Test Requirements:NA Test Date:2/6/2017 Comments: NOTE 1:Inline functions defined in "Globalmacro.h" are not unit tested. NOTE 2: "CBD_Sandbox_dbg.map" file is embedded for reference." ***** |

Attributes

| Name | Value |
|-----------------------|---|
| Compiler Install Path | \$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5 |
| Float Precision | 9 |
| InitObjDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\obj |
| InitSrcDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\src |
| Linker File | \$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd |
| Makefile Template | \$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl |
| Target Install Path | \$(ProgramFiles)\pls\UDE 4.4 |
| Time Unit | Cycles |

TEST DETAILS REPORT

2017-02-06, 13:45:26+0530

AbsHwPos_Init1



| Attributes | |
|------------------|--|
| Name | Value |
| Timer Enabled | False |
| Timer Prescale | 0 |
| Timer Resolution | 1 |
| UDE Config File | \$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_I2PIN_JTAG.cfg |
| Workspace File | D:\Synergy_Work_Area\AbsHwPos_TcI2cVd_FDD05G\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP |

TEST DETAILS REPORT

AbsHwPos_Init1

2017-02-06, 13:45:26+0530



Test Case 1: Metrics Test

| | |
|----------------------|---|
| Specification | "Performance Metrics (With "None" Instrumentation and WithPS Environment" CPU Cycles: TS1.1 1113.00 Cycles TS1.2 1347.00 Cycles |
| Description | Vector Description: TS1.1 Shortest Execution Path: If (Abs_m(Rte_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32) <= k_VehCntrOffValidLimit_HwDeg_f32)=>False TS1.2 Longest Execution Path: If (Abs_m(Rte_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32) <= k_VehCntrOffValidLimit_HwDeg_f32)=>True |

Test Step 1.1 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|------------------|--------|
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosOutputLPFCoeffFc_Hz_f32 | 393.0287978 | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 710.623859 | | |
| tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value | 1 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -1600 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 124 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16 | tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum | tgt_AbsHwPos_Init1_ManufMode_Cnt_enum | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrComHwPosStatus_Cnt_u16 | tgt_AbsHwPos_Init1_SrComHwPosStatus_Cnt_u16 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT | HWPOS_STATE_INIT | ✔ |
| AbsHwPos_SrComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 0 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✔ |
| tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| tgt_AbsHwPos_Init1_SrComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|-----------------|-------|-------------------|-------|--------|
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |

Test Step 1.2 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|------------------|--------|
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosOutputLPFCoeffFc_Hz_f32 | 259.5259765 | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1396.947509 | | |
| tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value | 2 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 1039 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 1054 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16 | tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum | tgt_AbsHwPos_Init1_ManufMode_Cnt_enum | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrComHwPosStatus_Cnt_u16 | tgt_AbsHwPos_Init1_SrComHwPosStatus_Cnt_u16 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT | HWPOS_STATE_INIT | ✔ |
| AbsHwPos_SrComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 1039 | 1039 ± 0.009 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |
| tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| tgt_AbsHwPos_Init1_SrComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:45:26+0530

AbsHwPos_Init1



Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

Test Case 2: Range Test

Specification "Performance Metrics
(With "None" Instrumentation and WithPS Environment")

CPU Cycles:

TS2.1 1009.00 Cycles
TS2.2 1332.00 Cycles
TS2.3 1110.00 Cycles
TS2.4 1329.00 Cycles
TS2.5 1123.00 Cycles
TS2.6 1337.00 Cycles
TS2.7 1114.00 Cycles
TS2.8 1335.00 Cycles
TS2.9 1122.00 Cycles
TS2.10 1341.00 Cycles
TS2.11 1009.00 Cycles
TS2.12 1329.00 Cycles
TS2.13 1113.00 Cycles
TS2.14 1342.00 Cycles
TS2.15 1119.00 Cycles
TS2.16 1330.00 Cycles

Description Vector Description:

TS2.1 All Min
TS2.2 All Max
TS2.3 Rte_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32==>Min
TS2.4 Rte_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32==>Max
TS2.5 Rte_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32==>Pos
TS2.6 Rte_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32==>Zero
TS2.7 Rte_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32==>Neg
TS2.8 k_VehCntrOffValidLimit_HwDeg_f32==>Min
TS2.9 k_VehCntrOffValidLimit_HwDeg_f32==>Max/Default
TS2.10 k_VehCntrOffValidLimit_HwDeg_f32==>Pos
TS2.11 k_HwPosOutputLPFCoeffFc_Hz_f32==>Min
TS2.12 k_HwPosOutputLPFCoeffFc_Hz_f32==>Max
TS2.13 k_HwPosOutputLPFCoeffFc_Hz_f32==>Pos/Default
TS2.14 ManufMode_Cnt_enum==>Min
TS2.15 ManufMode_Cnt_enum==>Max
TS2.16 ManufMode_Cnt_enum==>Pos

Test Step 2.1 (Repeat Count = 1)

| Name | Input Value | | |
|---|--|------------------|--------|
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosOutputLPFCoeffFc_Hz_f32 | 0 | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 0 | | |
| tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value | 0 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -1600 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16 | tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum | tgt_AbsHwPos_Init1_ManufMode_Cnt_enum | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16 | tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT | HWPOS_STATE_INIT | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 0 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✔ |
| tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|-----------------|-------|-------------------|-------|--------|
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:45:26+0530

AbsHwPos_Init1



Test Step 2.2 (Repeat Count = 1)

| Name | Input Value | | |
|---|--|------------------|--------|
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosOutputLPFCoeffFc_Hz_f32 | 500 | | |
| k_UseTurnsCntr_Cnt_Igc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1600 | | |
| tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value | 2 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 43690 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16 | tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum | tgt_AbsHwPos_Init1_ManufMode_Cnt_enum | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16 | tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT | HWPOS_STATE_INIT | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_Igc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✔ |
| tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

Test Step 2.3 (Repeat Count = 1)

| Name | Input Value | | |
|---|--|------------------|--------|
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosOutputLPFCoeffFc_Hz_f32 | 393.0287978 | | |
| k_UseTurnsCntr_Cnt_Igc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 710.623859 | | |
| tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value | 1 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -1600 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 124 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16 | tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum | tgt_AbsHwPos_Init1_ManufMode_Cnt_enum | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16 | tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT | HWPOS_STATE_INIT | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_Igc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✔ |
| tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|-----------------|-------|-------------------|-------|--------|
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |

Test Step 2.4 (Repeat Count = 1)

| Name | Input Value |
|---|--------------------------|
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos |
| k_HwPosOutputLPFCoeffFc_Hz_f32 | 25.18166721 |
| k_UseTurnsCntr_Cnt_Igc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 436.9878821 |
| tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value | 0 |

TEST DETAILS REPORT

2017-02-06, 13:45:26+0530



AbsHwPos_Init1

| Name | Input Value |
|--|---|
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 589 |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16 | tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16 |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum | tgt_AbsHwPos_Init1_ManufMode_Cnt_enum |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrComHwPosStatus_Cnt_u16 | tgt_AbsHwPos_Init1_SrComHwPosStatus_Cnt_u16 |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|------------------|------------------|--------|
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT | HWPOS_STATE_INIT | ✓ |
| AbsHwPos_SrComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| tgt_AbsHwPos_Init1_SrComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✓ |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

| Test Step 2.5 (Repeat Count = 1) | | | | |
|---|--|--|--------------------|--------|
| Name | | Input Value | | |
| Rte_Inst_Ap_AbsHwPos | | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosOutputLPFCoeffFc_Hz_f32 | | 259.5259765 | | |
| k_UseTurnsCntr_Cnt_lgc | | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | | 1396.947509 | | |
| tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value | | 2 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | | 1039.01001 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | | 1054 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16 | | tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum | | tgt_AbsHwPos_Init1_ManufMode_Cnt_enum | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16 | | tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | | tgt_Pim_EOLVehCntrOffset | | |
| Name | | Actual Value | Expected Value | Result |
| AbsHwPos_HwPosSource_Cnt_M_u16 | | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | | HWPOS_STATE_INIT | HWPOS_STATE_INIT | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | | 65534 | 65534 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | 1039.01001 | 1039.01001 ± 0.009 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | | 1 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | | 1 | *none* | ✔ |
| tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value | | 65534 | 65534 | ✔ |
| tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value | | 65534 | 65534 | ✔ |

| Test Step Call Trace | | | | |
|----------------------|-------|-------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:45:26+0530

AbsHwPos_Init1



Test Step 2.6 (Repeat Count = 1)

| Name | Input Value | | |
|---|--|------------------|--------|
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosOutputLPFCoeffFc_Hz_f32 | 379.2717962 | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1201.851037 | | |
| tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value | 1 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 0 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 1519 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16 | tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum | tgt_AbsHwPos_Init1_ManufMode_Cnt_enum | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16 | tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT | HWPOS_STATE_INIT | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 0 | 0 ± 0.000009 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✔ |
| tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

Test Step 2.7 (Repeat Count = 1)

| Name | Input Value | | |
|---|--|------------------|--------|
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosOutputLPFCoeffFc_Hz_f32 | 401.1355943 | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 436.9807251 | | |
| tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value | 0 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -700.02 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 1984 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16 | tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum | tgt_AbsHwPos_Init1_ManufMode_Cnt_enum | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16 | tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT | HWPOS_STATE_INIT | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✔ |
| tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|-----------------|-------|-------------------|-------|--------|
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |

Test Step 2.8 (Repeat Count = 1)

| Name | Input Value |
|---|--------------------------|
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos |
| k_HwPosOutputLPFCoeffFc_Hz_f32 | 162.9279281 |
| k_UseTurnsCntr_Cnt_Igc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 0 |
| tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value | 2 |

TEST DETAILS REPORT

2017-02-06, 13:45:26+0530



AbsHwPos_Init1

| Name | Input Value |
|--|---|
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 879.9430598 |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 2449 |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16 | tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16 |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum | tgt_AbsHwPos_Init1_ManufMode_Cnt_enum |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrComHwPosStatus_Cnt_u16 | tgt_AbsHwPos_Init1_SrComHwPosStatus_Cnt_u16 |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|------------------|------------------|--------|
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT | HWPOS_STATE_INIT | ✓ |
| AbsHwPos_SrComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| tgt_AbsHwPos_Init1_SrComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

Test Step 2.9 (Repeat Count = 1)

| Name | Input Value |
|--|---|
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos |
| k_HwPosOutputLPFCoeffFc_Hz_f32 | 487.3372427 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1600 |
| tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value | 1 |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -1555.129529 |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 2914 |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16 | tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16 |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum | tgt_AbsHwPos_Init1_ManufMode_Cnt_enum |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrComHwPosStatus_Cnt_u16 | tgt_AbsHwPos_Init1_SrComHwPosStatus_Cnt_u16 |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|------------------|----------------------|--------|
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT | HWPOS_STATE_INIT | ✓ |
| AbsHwPos_SrComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1555.12952 | -1555.129529 ± 0.009 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |
| tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| tgt_AbsHwPos_Init1_SrComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|-----------------|-------|-------------------|-------|--------|
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:45:26+0530

AbsHwPos_Init1



Test Step 2.10 (Repeat Count = 1)

| Name | Input Value | | |
|---|--|----------------------|--------|
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosOutputLPFCoeffFc_Hz_f32 | 402.9378512 | | |
| k_UseTurnsCntr_Cnt_Igc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1434.02 | | |
| tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value | 0 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 835.0762143 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 3379 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16 | tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum | tgt_AbsHwPos_Init1_ManufMode_Cnt_enum | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16 | tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT | HWPOS_STATE_INIT | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 835.076233 | 835.0762143 ± 0.0009 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_Igc | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |
| tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

Test Step 2.11 (Repeat Count = 1)

| Name | Input Value | | |
|---|--|------------------|--------|
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosOutputLPFCoeffFc_Hz_f32 | 0 | | |
| k_UseTurnsCntr_Cnt_Igc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 49.55192 | | |
| tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value | 2 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 1007.182555 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 3844 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16 | tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum | tgt_AbsHwPos_Init1_ManufMode_Cnt_enum | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16 | tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT | HWPOS_STATE_INIT | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_Igc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |
| tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|-----------------|-------|-------------------|-------|--------|
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |

Test Step 2.12 (Repeat Count = 1)

| Name | Input Value |
|---|--------------------------|
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos |
| k_HwPosOutputLPFCoeffFc_Hz_f32 | 500 |
| k_UseTurnsCntr_Cnt_Igc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 516.6709843 |
| tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value | 1 |

TEST DETAILS REPORT

2017-02-06, 13:45:26+0530



AbsHwPos_Init1

| Name | Input Value |
|--|---|
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 669.6303178 |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 4309 |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16 | tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16 |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum | tgt_AbsHwPos_Init1_ManufMode_Cnt_enum |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrComHwPosStatus_Cnt_u16 | tgt_AbsHwPos_Init1_SrComHwPosStatus_Cnt_u16 |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|------------------|------------------|--------|
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT | HWPOS_STATE_INIT | ✓ |
| AbsHwPos_SrComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✓ |
| tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| tgt_AbsHwPos_Init1_SrComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✓ |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

| Test Step 2.13 (Repeat Count = 1) | | | | |
|---|--|--|------------------|--------|
| Name | | Input Value | | |
| Rte_Inst_Ap_AbsHwPos | | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosOutputLPFCoeffFc_Hz_f32 | | 0.212779999 | | |
| k_UseTurnsCntr_Cnt_lgc | | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | | 1264.996348 | | |
| tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value | | 2 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | | -1454.825825 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | | 4774 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16 | | tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum | | tgt_AbsHwPos_Init1_ManufMode_Cnt_enum | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16 | | tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | | tgt_Pim_EOLVehCntrOffset | | |
| Name | | Actual Value | Expected Value | Result |
| AbsHwPos_HwPosSource_Cnt_M_u16 | | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | | HWPOS_STATE_INIT | HWPOS_STATE_INIT | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | | 65534 | 65534 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | 65535 | 65535 ± 0.09 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | | 0 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | | 0 | *none* | ✓ |
| tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value | | 65534 | 65534 | ✓ |
| tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value | | 65534 | 65534 | ✓ |

| Test Step Call Trace | | | | |
|----------------------|-------|-------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:45:26+0530

AbsHwPos_Init1



Test Step 2.14 (Repeat Count = 1)

| Name | Input Value | | |
|---|--|------------------|--------|
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosOutputLPFCoeffFc_Hz_f32 | 433.7 | | |
| k_UseTurnsCntr_Cnt_Igc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 678 | | |
| tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value | 0 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 564.5 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 5239 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16 | tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum | tgt_AbsHwPos_Init1_ManufMode_Cnt_enum | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16 | tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT | HWPOS_STATE_INIT | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 564.5 | 564.5 ± 0.0009 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_Igc | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |
| tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

Test Step 2.15 (Repeat Count = 1)

| Name | Input Value | | |
|---|--|------------------|--------|
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosOutputLPFCoeffFc_Hz_f32 | 78 | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 984 | | |
| tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value | 2 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -255 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 5704 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16 | tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum | tgt_AbsHwPos_Init1_ManufMode_Cnt_enum | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16 | tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT | HWPOS_STATE_INIT | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -255 | -255 ± 0.0009 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✔ |
| tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|-----------------|-------|-------------------|-------|--------|
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |

Test Step 2.16 (Repeat Count = 1)

| Name | Input Value |
|---|--------------------------|
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos |
| k_HwPosOutputLPFCoeffFc_Hz_f32 | 64 |
| k_UseTurnsCntr_Cnt_Igc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 127.6 |
| tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value | 1 |

TEST DETAILS REPORT

2017-02-06, 13:45:26+0530



AbsHwPos_Init1

| Name | Input Value | | |
|---|--|------------------|--------|
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 987 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 6169 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16 | tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum | tgt_AbsHwPos_Init1_ManufMode_Cnt_enum | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16 | tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT | HWPOS_STATE_INIT | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✔ |
| tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✔ |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

Test Case 3: Path Test

| | |
|----------------------|--|
| Specification | "Performance Metrics (With "None" Instrumentation and WithPS Environment" CPU Cycles: TS3.1 1113.00 Cycles TS3.2 1347.00 Cycles |
| Description | Vector Description: TS3.1 (Abs_m(Rte_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32) <= k_VehCntrOffValidLimit_HwDeg_f32)=False TS3.2 (Abs_m(Rte_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32) <= k_VehCntrOffValidLimit_HwDeg_f32)=True |

Test Step 3.1 (Repeat Count = 1)

| Name | Input Value | | |
|---|--|------------------|--------|
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosOutputLPFCoeffFc_Hz_f32 | 393.0287978 | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 710.623859 | | |
| tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value | 1 | | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -1600 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 124 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16 | tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum | tgt_AbsHwPos_Init1_ManufMode_Cnt_enum | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16 | tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT | HWPOS_STATE_INIT | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 0 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✔ |
| tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✔ |

| Test Step Call Trace | | | | |
|----------------------|-------|-------------------|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:45:26+0530

AbsHwPos_Init1



Test Step 3.2 (Repeat Count = 1)

| Name | Input Value |
|---|--|
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos |
| k_HwPosOutputLPFCoeffFc_Hz_f32 | 259.5259765 |
| k_UseTurnsCnt_Cnt_Igc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1396.947509 |
| tgt_AbsHwPos_Init1_ManufMode_Cnt_enum.value | 2 |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 1039 |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 1054 |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_HwPosSource_Cnt_u16 | tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16 |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_ManufMode_Cnt_enum | tgt_AbsHwPos_Init1_ManufMode_Cnt_enum |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16 | tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16 |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|------------------|------------------|--------|
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT | HWPOS_STATE_INIT | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 1039 | 1039 ± 0.009 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_Igc | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| tgt_AbsHwPos_Init1_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| tgt_AbsHwPos_Init1_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 14:07:39+0530

AbsHwPos_SCom_CustClrTrim



| | |
|-------------|---------------------------|
| Project | AbsHwPos |
| Module | AbsHwPos_Tcl2IVd |
| Test Object | AbsHwPos_SCom_CustClrTrim |

Instrumentation: Test Object Only

| | |
|-------------------------|-------|
| Statement (C0) Coverage | 100 % |
| Branch (C1) Coverage | 100 % |

Statistics

| | |
|-----------------|-----|
| Total Testcases | 2 |
| Successful | 2 ✓ |
| Failed | 0 |
| Not Executed | 0 |

Module Properties

| | |
|------------------------|--|
| Project Root Directory | D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G |
| Configuration File | D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml |
| Target Environment | TI TMS 570 PLS UDE (Default) |
| Kind of Test | Unit Test |
| Linker Options | |
| Source File(s) | |
| File | \$(SOURCEROOT)\Ap_AbsHwPos.c |
| Compiler Options | -D_DATA_ACCESS=-Dconst=-I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap_AbsHwPos -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include |

Comments/Description/Specification

| Name | Text |
|---------------------------|--|
| Module 'AbsHwPos_Tcl2IVd' | *****Unit Test Description***** Name of Tester: Medha Sabale Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version: 10 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:11 Data Dictionary Version: 10 Unit Test Plan Version:3 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):3004 Total RAM Used (Bytes):64 Total CALS Used (Bytes):56 Special Test Requirements:NA Test Date:2/6/2017 Comments: NOTE 1:Inline functions defined in "Globalmacro.h" are not unit tested. NOTE 2: "CBD_Sandbox_dbg.map" file is embedded for reference." ***** |

Attributes

| Name | Value |
|-----------------------|---|
| Compiler Install Path | \$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5 |
| Float Precision | 9 |
| InitObjDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\obj |
| InitSrcDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\src |
| Linker File | \$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd |
| Makefile Template | \$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl |
| Target Install Path | \$(ProgramFiles)\pls\UDE 4.4 |
| Time Unit | Cycles |
| Timer Enabled | False |
| Timer Prescale | 0 |
| Timer Resolution | 1 |
| UDE Config File | \$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg |

TEST DETAILS REPORT

2017-02-06, 14:07:39+0530

AbsHwPos_SCom_CustClrTrim



| Attributes | |
|----------------|--|
| Name | Value |
| Workspace File | D:\Synergy_Work_Area\AbsHwPos_TcI2cVd_FDD05G\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP |

TEST DETAILS REPORT

AbsHwPos_SCom_CustClrTrim

2017-02-06, 14:07:39+0530



Test Case 1: Metrics Test

| | | | |
|----------------------|--|--|--|
| Specification | "Performance Metrics (With "None" Instrumentation and WithPS Environment)" | | |
| | CPU Cycles: | | |
| | TS1.1 792.00 Cycles | | |
| | TS1.2 1012.00 Cycles | | |
| Description | Vector Description: | | |
| | TS1.1 Shortest Execution Path: | | |
| | If ((ManufMode_Cnt_T_enum <> ManufacturingMode) And (Rte_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 <> D_TRIMPERFORMED_CNT_U16))=>False" | | |
| | TS1.2 Longest Execution Path: | | |
| | If ((ManufMode_Cnt_T_enum <> ManufacturingMode) And (Rte_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 <> D_TRIMPERFORMED_CNT_U16))=>True" | | |

Test Step 1.1 (Repeat Count = 1)

| Name | Input Value | | |
|--|--|----------------|--------|
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | |
| k_UseTurnsCntr_Cnt_Igc | 0 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 47 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 1 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_Igc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 0 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✓ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | 0 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |

Test Step 1.2 (Repeat Count = 1)

| Name | Input Value | | |
|--|--|----------------|--------|
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | |
| k_UseTurnsCntr_Cnt_Igc | 1 | | |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 364 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 2 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_Igc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |
| tgt_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | 0 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 14:07:39+0530

AbsHwPos_SCom_CustClrTrim



Test Case 2: Range Test

Specification "Performance Metrics
(With "None" Instrumentation and WithPS Environment")

CPU Cycles:

TS2.1 792.00 Cycles
TS2.2 1012.00 Cycles
TS2.3 792.00 Cycles
TS2.4 1012.00 Cycles
TS2.5 792.00 Cycles

Description Vector Description:

TS2.1 All Min
TS2.2 All Max
TS2.3 ManufMode_Cnt_enum==>Min
TS2.4 ManufMode_Cnt_enum==>Max
TS2.5 ManufMode_Cnt_enum==>Pos

Test Step 2.1 (Repeat Count = 1)

| Name | Input Value | | |
|--|--|----------------|--------|
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | |
| k_UseTurnsCnt_Cnt_Igc | 0 | | |
| tgt_Pim_EOLVehCntOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntOffset | tgt_Pim_EOLVehCntOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 0 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntValid_Cnt_M_Igc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 0 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✔ |
| tgt_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |
| tgt_Pim_EOLVehCntOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | 0 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |

Test Step 2.2 (Repeat Count = 1)

| Name | Input Value | | |
|--|--|----------------|--------|
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | |
| k_UseTurnsCnt_Cnt_Igc | 1 | | |
| tgt_Pim_EOLVehCntOffset.EOLHwPosTrimPerformed_Cnt_u16 | 43690 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntOffset | tgt_Pim_EOLVehCntOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 2 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntValid_Cnt_M_Igc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |
| tgt_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |
| tgt_Pim_EOLVehCntOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | 0 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

Test Step 2.3 (Repeat Count = 1)

| Name | Input Value |
|---|--|
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data |

TEST DETAILS REPORT

2017-02-06, 14:07:39+0530



AbsHwPos_SCom_CustClrTrim

| Name | Input Value | | |
|--|-------------------------|----------------|--------|
| k_UseTurnsCnt_Cnt_Igc | 0 | | |
| tgt_Pim_EOLVehCntOffset.EOLHwPosTrimPerformed_Cnt_u16 | 1542 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntOffset | tgt_Pim_EOLVehCntOffset | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 0 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✓ |
| AbsHwPos_VehCntValid_Cnt_M_Igc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |
| tgt_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |
| tgt_Pim_EOLVehCntOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | 0 | ✓ |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |

| Test Step 2.4 (Repeat Count = 1) | | | | |
|--|--|----------------|--------|--|
| Name | Input Value | | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | | |
| k_UseTurnsCnt_Cnt_Igc | 1 | | | |
| tgt_Pim_EOLVehCntOffset.EOLHwPosTrimPerformed_Cnt_u16 | 364 | | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntOffset | tgt_Pim_EOLVehCntOffset | | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 2 | | | |
| Name | Actual Value | Expected Value | Result | |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✓ | |
| AbsHwPos_VehCntValid_Cnt_M_Igc | 0 | 0 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ | |
| tgt_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ | |
| tgt_Pim_EOLVehCntOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | 0 | ✓ | |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |

| Test Step 2.5 (Repeat Count = 1) | | | | |
|--|--|----------------|--------|--|
| Name | Input Value | | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | | |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum(data) | tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | | | |
| k_UseTurnsCnt_Cnt_Igc | 0 | | | |
| tgt_Pim_EOLVehCntOffset.EOLHwPosTrimPerformed_Cnt_u16 | 47 | | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntOffset | tgt_Pim_EOLVehCntOffset | | | |
| tgt_Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum_data | 1 | | | |
| Name | Actual Value | Expected Value | Result | |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✓ | |
| AbsHwPos_VehCntValid_Cnt_M_Igc | 0 | 0 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ | |
| tgt_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ | |
| tgt_Pim_EOLVehCntOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | 0 | ✓ | |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | Rte_Read_Ap_AbsHwPos_ManufMode_Cnt_enum | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |

TEST DETAILS REPORT

AbsHwPos_SCom_CustClrTrim

2017-02-06, 14:07:39+0530



TEST DETAILS REPORT

2017-02-06, 13:54:22+0530

AbsHwPos_Per4



| | |
|-------------|------------------|
| Project | AbsHwPos |
| Module | AbsHwPos_Tcl2IVd |
| Test Object | AbsHwPos_Per4 |

Instrumentation: Test Object Only

| | |
|-------------------------|-------|
| Statement (C0) Coverage | 100 % |
| Decision Coverage | 100 % |
| Branch (C1) Coverage | 100 % |
| MCC Coverage | 100 % |
| MC/DC Coverage | 100 % |

Statistics

| | |
|-----------------|-----|
| Total Testcases | 3 |
| Successful | 3 ✓ |
| Failed | 0 |
| Not Executed | 0 |

Module Properties

| | |
|------------------------|--|
| Project Root Directory | D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G |
| Configuration File | D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml |
| Target Environment | TI TMS 570 PLS UDE (Default) |
| Kind of Test | Unit Test |
| Linker Options | |
| Source File(s) | |
| File | \$(SOURCEROOT)\Ap_AbsHwPos.c |
| Compiler Options | -D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap_AbsHwPos -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include |

Comments/Description/Specification

| Name | Text |
|---------------------------|--|
| Module 'AbsHwPos_Tcl2IVd' | *****Unit Test Description***** Name of Tester: Medha Sabale Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version: 10 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:11 Data Dictionary Version: 10 Unit Test Plan Version:3 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):3004 Total RAM Used (Bytes):64 Total CALS Used (Bytes):56 Special Test Requirements:NA Test Date:2/6/2017 Comments: NOTE 1:Inline functions defined in "Globalmacro.h" are not unit tested. NOTE 2: "CBD_Sandbox_dbg.map" file is embedded for reference." ***** |

Attributes

| Name | Value |
|-----------------------|---|
| Compiler Install Path | \$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5 |
| Float Precision | 9 |
| InitObjDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\obj |
| InitSrcDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\src |
| Linker File | \$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd |
| Makefile Template | \$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl |
| Target Install Path | \$(ProgramFiles)\pls\UDE 4.4 |
| Time Unit | Cycles |

TEST DETAILS REPORT

2017-02-06, 13:54:22+0530

AbsHwPos_Per4



| Attributes | |
|------------------|--|
| Name | Value |
| Timer Enabled | False |
| Timer Prescale | 0 |
| Timer Resolution | 1 |
| UDE Config File | \$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_I2PIN_JTAG.cfg |
| Workspace File | D:\Synergy_Work_Area\AbsHwPos_TcI2cVd_FDD05G\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP |

Test Case 1: Metrics Test

| | |
|---------------|---|
| Specification | "Performance Metrics (With "None" Instrumentation and WithPS Environment)" |
| | CPU Cycles: |
| | TS1.1 1354.00 Cycles |
| | TS1.2 1339.00 Cycles |
| Description | Vector Description: |
| | TS1.1 Shortest Execution Path: |
| | If (Abs_m(AbsHwPos_AlignedHwPos_HwDeg_M_f32) > k_KinmIntDiagMaxRackLimit_HwDeg_f32)=>True" |
| | TS1.2 Longest Execution Path: |
| | If (Abs_m(AbsHwPos_AlignedHwPos_HwDeg_M_f32) > k_KinmIntDiagMaxRackLimit_HwDeg_f32)=>False" |

Test Step 1.1 (Repeat Count = 1)

| Name | Input Value | | |
|--|--------------|----------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -10125 | | |
| k_KinmIntDiagMaxRackLimit_HwDeg_f32 | 0 | | |
| Name | Actual Value | Expected Value | Result |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 144 | 144 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached | 1 | ✓ |

Test Step 1.2 (Repeat Count = 1)

| Name | Input Value | | |
|--|--------------|----------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 0 | | |
| k_KinmIntDiagMaxRackLimit_HwDeg_f32 | 647 | | |
| Name | Actual Value | Expected Value | Result |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 144 | 144 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:54:22+0530

AbsHwPos_Per4



Test Case 2: Range Test

Specification "Performance Metrics
(With "None" Instrumentation and WithPS Environment")

CPU Cycles:

TS2.1 1354.00 Cycles
TS2.2 1346.00 Cycles
TS2.3 1344.00 Cycles
TS2.4 1344.00 Cycles
TS2.5 1339.00 Cycles
TS2.6 1344.00 Cycles
TS2.7 1338.00 Cycles
TS2.8 1344.00 Cycles
TS2.9 1344.00 Cycles
TS2.10 1336.00 Cycles

Description Vector Description:

TS 2.1All Min
TS 2.2All Max
TS 2.3AbsHwPos_AlignedHwPos_HwDeg_M_f32=>Min
TS 2.4AbsHwPos_AlignedHwPos_HwDeg_M_f32=>Max
TS 2.5AbsHwPos_AlignedHwPos_HwDeg_M_f32=>Mid
TS 2.6AbsHwPos_AlignedHwPos_HwDeg_M_f32=>Pos
TS 2.7AbsHwPos_AlignedHwPos_HwDeg_M_f32=>Neg
TS 2.8k_KinmIntDiagMaxRackLimit_HwDeg_f32=>Min
TS 2.9k_KinmIntDiagMaxRackLimit_HwDeg_f32=>Max
TS 2.10k_KinmIntDiagMaxRackLimit_HwDeg_f32=>Pos/Default

Test Step 2.1 (Repeat Count = 1)

| Name | Input Value | | | |
|--|--------------|----------------|--------|--|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -10125 | | | |
| k_KinmIntDiagMaxRackLimit_HwDeg_f32 | 0 | | | |
| Name | Actual Value | Expected Value | Result | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 144 | 144 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ | |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.2 (Repeat Count = 1)

| Name | Input Value | | | |
|--|--------------|----------------|--------|--|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 10125 | | | |
| k_KinmIntDiagMaxRackLimit_HwDeg_f32 | 5000 | | | |
| Name | Actual Value | Expected Value | Result | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 144 | 144 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ | |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.3 (Repeat Count = 1)

| Name | Input Value | | | |
|--|--------------|----------------|--------|--|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -10125 | | | |
| k_KinmIntDiagMaxRackLimit_HwDeg_f32 | 1478 | | | |
| Name | Actual Value | Expected Value | Result | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 144 | 144 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ | |

TEST DETAILS REPORT

2017-02-06, 13:54:22+0530

AbsHwPos_Per4



Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.4 (Repeat Count = 1)

| Name | Input Value | | |
|--|--------------|----------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 10125 | | |
| k_KinmIntDiagMaxRackLimit_HwDeg_f32 | 99.0100021 | | |
| Name | Actual Value | Expected Value | Result |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 144 | 144 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.5 (Repeat Count = 1)

| Name | Input Value | | |
|--|--------------|----------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 0 | | |
| k_KinmIntDiagMaxRackLimit_HwDeg_f32 | 647 | | |
| Name | Actual Value | Expected Value | Result |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 144 | 144 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.6 (Repeat Count = 1)

| Name | Input Value | | |
|--|--------------|----------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 365.02 | | |
| k_KinmIntDiagMaxRackLimit_HwDeg_f32 | 358.01001 | | |
| Name | Actual Value | Expected Value | Result |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 144 | 144 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.7 (Repeat Count = 1)

| Name | Input Value | | |
|--|--------------|----------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -254.12 | | |
| k_KinmIntDiagMaxRackLimit_HwDeg_f32 | 2769 | | |
| Name | Actual Value | Expected Value | Result |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 144 | 144 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:54:22+0530

AbsHwPos_Per4



Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.8 (Repeat Count = 1)

| Name | Input Value | | |
|--|--------------|----------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 6585 | | |
| k_KinmIntDiagMaxRackLimit_HwDeg_f32 | 0 | | |
| Name | Actual Value | Expected Value | Result |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 144 | 144 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.9 (Repeat Count = 1)

| Name | Input Value | | |
|--|--------------|----------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -7895.00977 | | |
| k_KinmIntDiagMaxRackLimit_HwDeg_f32 | 5000 | | |
| Name | Actual Value | Expected Value | Result |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 144 | 144 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.10 (Repeat Count = 1)

| Name | Input Value | | |
|--|--------------|----------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 100 | | |
| k_KinmIntDiagMaxRackLimit_HwDeg_f32 | 2100 | | |
| Name | Actual Value | Expected Value | Result |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 144 | 144 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_u08) | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:54:22+0530

AbsHwPos_Per4



Test Case 3: Path Test

Specification

"Performance Metrics
(With "None" Instrumentation and WithPS Environment"

CPU Cycles:

TS3.1 1354.00 Cycles
TS3.2 1341.00 Cycles

Description

Vector Description:

TS3.1 If (Abs_m(AbsHwPos_AlignedHwPos_HwDeg_M_f32) > k_KinmIntDiagMaxRackLimit_HwDeg_f32)=>True
TS3.2 If (Abs_m(AbsHwPos_AlignedHwPos_HwDeg_M_f32) > k_KinmIntDiagMaxRackLimit_HwDeg_f32)=>False

Test Step 3.1 (Repeat Count = 1)

| Name | Input Value | | |
|--|--------------|----------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -10125 | | |
| k_KinmIntDiagMaxRackLimit_HwDeg_f32 | 0 | | |
| Name | Actual Value | Expected Value | Result |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 144 | 144 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached | 1 | ✓ |

Test Step 3.2 (Repeat Count = 1)

| Name | Input Value | | |
|--|--------------|----------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 0 | | |
| k_KinmIntDiagMaxRackLimit_HwDeg_f32 | 647 | | |
| Name | Actual Value | Expected Value | Result |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 144 | 144 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per4_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per4_CP1_CheckpointReached | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:56:38+0530

AbsHwPos_SCom_NxtClearTrim



| | |
|-------------|----------------------------|
| Project | AbsHwPos |
| Module | AbsHwPos_Tcl2IVd |
| Test Object | AbsHwPos_SCom_NxtClearTrim |

Instrumentation: Test Object Only

| | |
|-------------------------|-------|
| Statement (C0) Coverage | 100 % |
| Branch (C1) Coverage | 100 % |

Statistics

| | |
|-----------------|-----|
| Total Testcases | 1 |
| Successful | 1 ✓ |
| Failed | 0 |
| Not Executed | 0 |

Module Properties

| | |
|------------------------|--|
| Project Root Directory | D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G |
| Configuration File | D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml |
| Target Environment | TI TMS 570 PLS UDE (Default) |
| Kind of Test | Unit Test |
| Linker Options | |
| Source File(s) | |
| File | \$(SOURCEROOT)\Ap_AbsHwPos.c |
| Compiler Options | -D_DATA_ACCESS=-Dconst=-I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap_AbsHwPos -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include |

Comments/Description/Specification

| Name | Text |
|---------------------------|---|
| Module 'AbsHwPos_Tcl2IVd' | *****Unit Test Description***** Name of Tester: Medha Sabale Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version: 10 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:11 Data Dictionary Version: 10 Unit Test Plan Version:3 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):3004 Total RAM Used (Bytes):64 Total CALS Used (Bytes):56 Special Test Requirements:NA Test Date:2/6/2017 Comments: NOTE 1:Inline functions defined in "Globalmacro.h" are not unit tested. NOTE 2: "CBD_Sandbox_dbg.map" file is embedded for reference. ***** |

Attributes

| Name | Value |
|-----------------------|---|
| Compiler Install Path | \$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5 |
| Float Precision | 9 |
| InitObjDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\obj |
| InitSrcDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\src |
| Linker File | \$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd |
| Makefile Template | \$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl |
| Target Install Path | \$(ProgramFiles)\pls\UDE 4.4 |
| Time Unit | Cycles |
| Timer Enabled | False |
| Timer Prescale | 0 |
| Timer Resolution | 1 |
| UDE Config File | \$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg |

TEST DETAILS REPORT

2017-02-06, 13:56:38+0530

AbsHwPos_SCom_NxtClearTrim



| Attributes | |
|----------------|--|
| Name | Value |
| Workspace File | D:\Synergy_Work_Area\AbsHwPos_TcI2cVd_FDD05G\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP |

TEST DETAILS REPORT

2017-02-06, 13:56:38+0530

AbsHwPos_SCom_NxtClearTrim



Test Case 1: Range Test

Specification "Performance Metrics
(With "None" Instrumentation and WithPS Environment")

CPU Cycles:

TS1.1 381.00 Cycles
TS1.2 381.00 Cycles
TS1.3 381.00 Cycles
TS1.4 381.00 Cycles
TS1.5 381.00 Cycles

Description Vector Description:

TS1.1 AbsHwPos_VehCntrOffset_HwDeg_M_f32=Min
TS1.2 AbsHwPos_VehCntrOffset_HwDeg_M_f32=Max
TS1.3 AbsHwPos_VehCntrOffset_HwDeg_M_f32=Mid
TS1.4 AbsHwPos_VehCntrOffset_HwDeg_M_f32=Pos
TS1.5 AbsHwPos_VehCntrOffset_HwDeg_M_f32=Neg

Test Step 1.1 (Repeat Count = 1)

| Name | Input Value | | |
|---|--------------------------|----------------|--------|
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1600 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |

Test Step 1.2 (Repeat Count = 1)

| Name | Input Value | | |
|---|--------------------------|----------------|--------|
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |

Test Step 1.3 (Repeat Count = 1)

| Name | Input Value | | |
|---|--------------------------|----------------|--------|
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 0 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |

Test Step 1.4 (Repeat Count = 1)

| Name | Input Value |
|------------------------------------|--------------------------|
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 12564.01 |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos |

TEST DETAILS REPORT

2017-02-06, 13:56:38+0530



AbsHwPos_SCom_NxtClearTrim

| Name | Input Value | | |
|---|--------------------------|----------------|--------|
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |

| Test Step 1.5 (Repeat Count = 1) | | | | |
|---|--------------------------|----------------|--------|--|
| Name | Input Value | | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1523.02 | | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | | |
| tgt_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | tgt_Pim_EOLVehCntrOffset | | | |
| Name | Actual Value | Expected Value | Result | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✓ | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ | |
| tgt_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ | |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:53:13+0530

AbsHwPos_Per3



| | |
|-------------|------------------|
| Project | AbsHwPos |
| Module | AbsHwPos_Tcl2IVd |
| Test Object | AbsHwPos_Per3 |

Instrumentation: Test Object Only

| | |
|-------------------------|-------|
| Statement (C0) Coverage | 100 % |
| Decision Coverage | 100 % |
| Branch (C1) Coverage | 100 % |
| MCC Coverage | 100 % |
| MC/DC Coverage | 100 % |

Statistics

| | |
|-----------------|-----|
| Total Testcases | 3 |
| Successful | 3 ✓ |
| Failed | 0 |
| Not Executed | 0 |

Module Properties

| | |
|------------------------|--|
| Project Root Directory | D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G |
| Configuration File | D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml |
| Target Environment | TI TMS 570 PLS UDE (Default) |
| Kind of Test | Unit Test |
| Linker Options | |
| Source File(s) | |
| File | \$(SOURCEROOT)\Ap_AbsHwPos.c |
| Compiler Options | -D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap_AbsHwPos -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include |

Comments/Description/Specification

| Name | Text |
|---------------------------|--|
| Module 'AbsHwPos_Tcl2IVd' | *****Unit Test Description***** Name of Tester: Medha Sabale Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version: 10 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:11 Data Dictionary Version: 10 Unit Test Plan Version:3 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):3004 Total RAM Used (Bytes):64 Total CALS Used (Bytes):56 Special Test Requirements:NA Test Date:2/6/2017 Comments: NOTE 1:Inline functions defined in "Globalmacro.h" are not unit tested. NOTE 2: "CBD_Sandbox_dbg.map" file is embedded for reference." ***** |

Attributes

| Name | Value |
|-----------------------|---|
| Compiler Install Path | \$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5 |
| Float Precision | 9 |
| InitObjDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\obj |
| InitSrcDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\src |
| Linker File | \$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd |
| Makefile Template | \$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl |
| Target Install Path | \$(ProgramFiles)\pls\UDE 4.4 |
| Time Unit | Cycles |

TEST DETAILS REPORT

2017-02-06, 13:53:13+0530

AbsHwPos_Per3



| Attributes | |
|------------------|--|
| Name | Value |
| Timer Enabled | False |
| Timer Prescale | 0 |
| Timer Resolution | 1 |
| UDE Config File | \$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_I2PIN_JTAG.cfg |
| Workspace File | D:\Synergy_Work_Area\AbsHwPos_TcI2cVd_FDD05G\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP |

TEST DETAILS REPORT

AbsHwPos_Per3

2017-02-06, 13:53:13+0530



Test Case 1: Metrics Test

| | |
|----------------------|---|
| Specification | Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles: TS1.1 969.00 Cycles TS1.2 1585.00 Cycles |
| Description | Vector Description: TS1.1"Shortest Execution Path: if (I2CHwAbsPosValid_Cnt_T_lgc == TRUE)=>False" TS1.2"Longest Execution Path: if (I2CHwAbsPosValid_Cnt_T_lgc == TRUE)=>True (FALSE == AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc)=True if (Abs_f32_m(I2CHwAbsPos_HwDeg_T_f32 - NewRelHwPos_HwDeg_T_f32) >= k_HWAtMtrADiffLimit_HwDeg_f32)=>False (AbsHwPos_HwAtMtrAFitAcc_Cnt_M_u16 == 0U)=True " |

Test Step 1.1 (Repeat Count = 1)

| Name | Input Value | | |
|--|--|----------------|--------|
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 0 | | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 0 | | |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | -11026 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -10125 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HwAtoMtrADiffLimit_HwDeg_f32 | 0 | | |
| k_HwAtoMtrAError_str.Threshold | 0 | | |
| k_HwAtoMtrAError_str.PStep | 0 | | |
| k_HwAtoMtrAError_str.NStep | 0 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | 0 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | -900 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 0 | 0 | ✔ |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 0 | 0 | ✔ |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | -11026 | -11026 ± 0.09 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

Test Step 1.2 (Repeat Count = 1)

| Name | Input Value | | |
|--|--|----------------------|--------|
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 125 | | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 0 | | |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | -9875.20996 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 420 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HWAtMtrADiffLimit_HwDeg_f32 | 332.5 | | |
| k_HwAtMtrAError_str.Threshold | 470 | | |
| k_HwAtMtrAError_str.PStep | 124 | | |
| k_HwAtMtrAError_str.NStep | 182 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | 666.200012 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 0 | 0 | ✓ |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | -246.200012 | -246.199997 ± 0.0009 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | 117 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:53:13+0530

AbsHwPos_Per3



Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

Test Case 2: Range Test

Specification Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cycles:

TS2.1 1029.00 Cycles
TS2.2 1585.00 Cycles
TS2.3 1575.00 Cycles
TS2.4 969.00 Cycles
TS2.5 1565.00 Cycles
TS2.6 1119.00 Cycles
TS2.7 1104.00 Cycles
TS2.8 1060.00 Cycles
TS2.9 1069.00 Cycles
TS2.10 1056.00 Cycles
TS2.11 1549.00 Cycles
TS2.12 1056.00 Cycles
TS2.13 1086.00 Cycles
TS2.14 1056.00 Cycles
TS2.15 1069.00 Cycles
TS2.16 1056.00 Cycles
TS2.17 1549.00 Cycles
TS2.18 1549.00 Cycles
TS2.19 1060.00 Cycles
TS2.20 1529.00 Cycles
TS2.21 1060.00 Cycles
TS2.22 1069.00 Cycles
TS2.23 1060.00 Cycles
TS2.24 1559.00 Cycles
TS2.25 1549.00 Cycles
TS2.26 1056.00 Cycles
TS2.27 1056.00 Cycles
TS2.28 1034.00 Cycles
TS2.29 1056.00 Cycles
TS2.30 949.00 Cycles
TS2.31 949.00 Cycles
TS2.32 1549.00 Cycles
TS2.33 1056.00 Cycles
TS2.34 1069.00 Cycles
TS2.35 1549.00 Cycles
TS2.36 1056.00 Cycles

Description Vector Description:

TS2.1All Min
TS2.2All Max
TS2.3I2CHwAbsPosValid_Cnt_Igc=>True
TS2.4I2CHwAbsPosValid_Cnt_Igc=>False
TS2.5I2CHwAbsPos_HwDeg_f32==>Min
TS2.6I2CHwAbsPos_HwDeg_f32==>Max
TS2.7I2CHwAbsPos_HwDeg_f32==>Zero
TS2.8I2CHwAbsPos_HwDeg_f32==>Pos
TS2.9I2CHwAbsPos_HwDeg_f32==>Neg
TS2.10AbsHwPos_RelHwPos_HwDeg_M_f32==>Min
TS2.11AbsHwPos_RelHwPos_HwDeg_M_f32==>Max
TS2.12AbsHwPos_RelHwPos_HwDeg_M_f32==>Zero
TS2.13AbsHwPos_RelHwPos_HwDeg_M_f32==>Pos
TS2.14AbsHwPos_RelHwPos_HwDeg_M_f32==>Neg
TS2.15AbsHwPos_HwAtoMtrAFItAcc_Cnt_M_u16==>Min
TS2.16AbsHwPos_HwAtoMtrAFItAcc_Cnt_M_u16==>Max
TS2.17AbsHwPos_HwAtoMtrAFItAcc_Cnt_M_u16==>Pos
TS2.18k_HwAtoMtrADiffLimit_HwDeg_f32==>Min
TS2.19k_HwAtoMtrADiffLimit_HwDeg_f32==>Max
TS2.20k_HwAtoMtrADiffLimit_HwDeg_f32==>Pos/Default
TS2.21k_HwAtoMtrAError_str.Threshold==>Min
TS2.22k_HwAtoMtrAError_str.Threshold==>Max
TS2.23k_HwAtoMtrAError_str.Threshold==>Pos/Default
TS2.24k_HwAtoMtrAError_str.PStep==>Min
TS2.25k_HwAtoMtrAError_str.PStep==>Max
TS2.26k_HwAtoMtrAError_str.PStep==>Pos/Default
TS2.27k_HwAtoMtrAError_str.NStep==>Min
TS2.28k_HwAtoMtrAError_str.NStep==>Max
TS2.29k_HwAtoMtrAError_str.NStep==>Pos/Default
TS2.30AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_Igc==>Min
TS2.31AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_Igc==>Max
TS2.32AbsHwPos_MtrToHwAOfst_HwDeg_M_f32==>Min
TS2.33AbsHwPos_MtrToHwAOfst_HwDeg_M_f32==>Max
TS2.34AbsHwPos_MtrToHwAOfst_HwDeg_M_f32==>Pos
TS2.35AbsHwPos_MtrToHwAOfst_HwDeg_M_f32==>Zero
TS2.36AbsHwPos_MtrToHwAOfst_HwDeg_M_f32==>Neg

Test Step 2.1 (Repeat Count = 1)

| Name | Input Value |
|------------------------------------|-------------|
| AbsHwPos_HwAtoMtrAFItAcc_Cnt_M_u16 | 0 |

TEST DETAILS REPORT

2017-02-06, 13:53:13+0530



AbsHwPos_Per3

| Name | Input Value | | |
|--|--|----------------|--------|
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 0 | | |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | -11026 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -10125 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HWAtoMtrADiffLimit_HwDeg_f32 | 0 | | |
| k_HwAtoMtrAError_str.Threshold | 0 | | |
| k_HwAtoMtrAError_str.PStep | 0 | | |
| k_HwAtoMtrAError_str.NStep | 0 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | 0 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | -900 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 0 | 0 | ✔ |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 0 | 0 | ✔ |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | -11026 | -11026 ± 0.09 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.2 (Repeat Count = 1) | | | | |
|--|--------------|--|--------|--|
| Name | | Input Value | | |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | | 65535 | | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | | 1 | | |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | | 11026 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | 10125 | | |
| Rte_Inst_Ap_AbsHwPos | | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HwAtoMtrADiffLimit_HwDeg_f32 | | 1600 | | |
| k_HwAtoMtrAError_str.Threshold | | 65535 | | |
| k_HwAtoMtrAError_str.PStep | | 255 | | |
| k_HwAtoMtrAError_str.NStep | | 255 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | | 1 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | | 900 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result | |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 65535 | 65535 | ✓ | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | 1 | ✓ | |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | 11026 | 11026 ± 0.09 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | 117 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ | |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.3 (Repeat Count = 1) | | |
|--|--------------------------|--|
| Name | Input Value | |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 125 | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 0 | |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | -9875.20996 | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 420 | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | |
| k_HwAtoMtrADiffLimit_HwDeg_f32 | 332.5 | |
| k_HwAtoMtrAError_str.Threshold | 470 | |
| k_HwAtoMtrAError_str.PStep | 124 | |
| k_HwAtoMtrAError_str.NStep | 182 | |
| tat_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | 1 | |

TEST DETAILS REPORT

2017-02-06, 13:53:13+0530



AbsHwPos_Per3

| Name | Input Value | | |
|--|--|----------------------|--------|
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | 666.200012 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 0 | 0 | ✔ |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_Igc | 1 | 1 | ✔ |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | -246.200012 | -246.199997 ± 0.0009 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | 117 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✔ |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.4 (Repeat Count = 1) | | | | |
|--|--------------|--|--------|--|
| Name | | Input Value | | |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | | 148 | | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | | 1 | | |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | | -6214.20996 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | -329.299988 | | |
| Rte_Inst_Ap_AbsHwPos | | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HwAtoMtrADiffLimit_HwDeg_f32 | | 355.600006 | | |
| k_HwAtoMtrAError_str.Threshold | | 493 | | |
| k_HwAtoMtrAError_str.PStep | | 48 | | |
| k_HwAtoMtrAError_str.NStep | | 173 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | | 0 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | | -457 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result | |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 0 | 0 | ✓ | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 0 | 0 | ✓ | |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | -6214.20996 | -6214.20996 ± 0.009 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | *none* | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✓ | |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.5 (Repeat Count = 1) | | | | |
|---|--|--|---------------------|--------|
| Name | | Input Value | | |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | | 1718 | | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | | 1 | | |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | | -2553.20996 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | 238.600006 | | |
| Rte_Inst_Ap_AbsHwPos | | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HwAtoMtrADiffLimit_HwDeg_f32 | | 378.700012 | | |
| k_HwAtoMtrAError_str.Threshold | | 516 | | |
| k_HwAtoMtrAError_str.PStep | | 66 | | |
| k_HwAtoMtrAError_str.NStep | | 164 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | | 1 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | | -900 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | |
| Name | | Actual Value | Expected Value | Result |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | | 516 | 516 | ✓ |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | | 1 | 1 | ✓ |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | | -2553.20996 | -2553.20996 ± 0.009 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | | 117 | 117 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | | 1 | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:53:13+0530



AbsHwPos_Per3

| Name | Actual Value | Expected Value | Result |
|--|--------------|----------------|--------|
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.6 (Repeat Count = 1) | | | | |
|--|--|----------------------|--------|--|
| Name | Input Value | | | |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 194 | | | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 0 | | | |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | 1107.79004 | | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 147.899994 | | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | | |
| k_HwAtoMtrADiffLimit_HwDeg_f32 | 401.799988 | | | |
| k_HwAtoMtrAError_str.Threshold | 5398 | | | |
| k_HwAtoMtrAError_str.PStep | 10 | | | |
| k_HwAtoMtrAError_str.NStep | 155 | | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | 900 | | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | | |
| Name | Actual Value | Expected Value | Result | |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 39 | 39 | ✓ | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | 1 | ✓ | |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | -752.099976 | -752.099976 ± 0.0009 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | *none* | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ | |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.7 (Repeat Count = 1) | | | | |
|--|--|--------------------|--------|--|
| Name | Input Value | | | |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 217 | | | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | | | |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | 4768.79004 | | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 57.2000008 | | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | | |
| k_HwAtoMtrADiffLimit_HwDeg_f32 | 424.899994 | | | |
| k_HwAtoMtrAError_str.Threshold | 562 | | | |
| k_HwAtoMtrAError_str.PStep | 77 | | | |
| k_HwAtoMtrAError_str.NStep | 146 | | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | 0 | | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | | |
| Name | Actual Value | Expected Value | Result | |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 294 | 294 | ✓ | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | 1 | ✓ | |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | 4768.79004 | 4768.79004 ± 0.009 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | *none* | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ | |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:53:13+0530

AbsHwPos_Per3



Test Step 2.8 (Repeat Count = 1)

| Name | Input Value |
|---|--|
| AbsHwPos_HwAtmtrAFitAcc_Cnt_M_u16 | 2840 |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 0 |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | 8429.79004 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -33.5 |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos |
| k_HwAtmtrADiffLimit_HwDeg_f32 | 448 |
| k_HwAtmtrAError_str.Threshold | 654 |
| k_HwAtmtrAError_str.PStep | 91 |
| k_HwAtmtrAError_str.NStep | 137 |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | 145.600006 |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 |

| Name | Actual Value | Expected Value | Result |
|--|--------------|----------------------|--------|
| AbsHwPos_HwAtmtrAFitAcc_Cnt_M_u16 | 2703 | 2703 | ✓ |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | -179.100006 | -179.100006 ± 0.0009 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.9 (Repeat Count = 1)

| Name | Input Value |
|---|--|
| AbsHwPos_HwAtmtrAFitAcc_Cnt_M_u16 | 263 |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | 1245.22998 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 1000 |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos |
| k_HwAtmtrADiffLimit_HwDeg_f32 | 471.100006 |
| k_HwAtmtrAError_str.Threshold | 677 |
| k_HwAtmtrAError_str.PStep | 105 |
| k_HwAtmtrAError_str.NStep | 128 |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | -558.299988 |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 |

| Name | Actual Value | Expected Value | Result |
|--|--------------|--------------------|--------|
| AbsHwPos_HwAtmtrAFitAcc_Cnt_M_u16 | 135 | 135 | ✓ |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | 1245.22998 | 1245.22998 ± 0.009 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.10 (Repeat Count = 1)

| Name | Input Value |
|---------------------------------------|--------------------------|
| AbsHwPos_HwAtmtrAFitAcc_Cnt_M_u16 | 286 |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 0 |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | -800.210022 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -10125 |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos |
| k_HwAtmtrADiffLimit_HwDeg_f32 | 494.200012 |
| k_HwAtmtrAError_str.Threshold | 7000 |
| k_HwAtmtrAError_str.PStep | 119 |

TEST DETAILS REPORT

2017-02-06, 13:53:13+0530







AbsHwPos_Per3

| Name | Input Value | | |
|--|--|--------------------|--------|
| k_HwAtoMtrAError_str.NStep | 119 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | 123.800003 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 167 | 167 | ✔ |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | -10248.7998 | -10248.7998 ± 0.09 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.11 (Repeat Count = 1) | | | | |
|--|--------------|--|--------|--|
| Name | | Input Value | | |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | | 3709 | | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | | 1 | | |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | | -2143.19995 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | 10125 | | |
| Rte_Inst_Ap_AbsHwPos | | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HwAtoMtrADiffLimit_HwDeg_f32 | | 517.299988 | | |
| k_HwAtoMtrAError_str.Threshold | | 125 | | |
| k_HwAtoMtrAError_str.PStep | | 133 | | |
| k_HwAtoMtrAError_str.NStep | | 110 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | | 1 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | | 756 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result | |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 125 | 125 | ✓ | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | 1 | ✓ | |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | -2143.19995 | -2143.19995 ± 0.009 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | 117 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ | |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.12 (Repeat Count = 1) | | | |  |
|---|--|--|----------------|---|
| Name | | Input Value | | |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | | 332 | | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | | 0 | | |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | | -3486.18994 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | 0 | | |
| Rte_Inst_Ap_AbsHwPos | | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HwAtoMtrADiffLimit_HwDeg_f32 | | 540.400024 | | |
| k_HwAtoMtrAError_str.Threshold | | 148 | | |
| k_HwAtoMtrAError_str.PStep | | 147 | | |
| k_HwAtoMtrAError_str.NStep | | 101 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | | 1 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | | -465.5 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | |
| Name | | Actual Value | Expected Value | Result |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | | 231 | 231 |  |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | | 1 | 1 |  |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | | 465.5 | 465.5 ± 0.0009 |  |

TEST DETAILS REPORT

2017-02-06, 13:53:13+0530



AbsHwPos_Per3

| Name | Actual Value | Expected Value | Result |
|--|--------------|----------------|--------|
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.13 (Repeat Count = 1)

| Name | Input Value | | |
|--|--|---------------------|--------|
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 355 | | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | | |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | -4829.18018 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 465.299988 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HWAtoMtrADiffLimit_HwDeg_f32 | 563.5 | | |
| k_HwAtoMtrAError_str.Threshold | 1718 | | |
| k_HwAtoMtrAError_str.PStep | 161 | | |
| k_HwAtoMtrAError_str.NStep | 92 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | 312.700012 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 516 | 516 | ✔ |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | -4829.18018 | -4829.18018 ± 0.009 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.14 (Repeat Count = 1)

| Name | Input Value | | |
|--|--|---------------------|--------|
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 378 | | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 0 | | |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | -6172.16992 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -88.9899979 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HwAtoMtrADiffLimit_HwDeg_f32 | 586.599976 | | |
| k_HwAtoMtrAError_str.Threshold | 194 | | |
| k_HwAtoMtrAError_str.PStep | 175 | | |
| k_HwAtoMtrAError_str.NStep | 83 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | -469.399994 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 295 | 295 | ✔ |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | 380.410004 | 380.410004 ± 0.0009 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:53:13+0530



AbsHwPos_Per3

Test Step 2.15 (Repeat Count = 1)

| Name | Input Value |
|---|--|
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 0 |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | -7515.16016 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 101.400002 |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos |
| k_HwAtoMtrADiffLimit_HwDeg_f32 | 212.399994 |
| k_HwAtoMtrAError_str.Threshold | 217 |
| k_HwAtoMtrAError_str.PStep | 189 |
| k_HwAtoMtrAError_str.NStep | 74 |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | 626.099976 |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 |

| Name | Actual Value | Expected Value | Result |
|--|--------------|---------------------|--------|
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 189 | 189 | ✓ |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | -7515.16016 | -7515.16016 ± 0.009 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.16 (Repeat Count = 1)

| Name | Input Value |
|---|--|
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 65535 |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 0 |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | -8858.15039 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -457 |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos |
| k_HwAtoMtrADiffLimit_HwDeg_f32 | 62.799992 |
| k_HwAtoMtrAError_str.Threshold | 2840 |
| k_HwAtoMtrAError_str.PStep | 203 |
| k_HwAtoMtrAError_str.NStep | 65 |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | -782.799988 |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 |

| Name | Actual Value | Expected Value | Result |
|--|--------------|---------------------|--------|
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 65470 | 65470 | ✓ |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | 325.799988 | 325.799988 ± 0.0009 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.17 (Repeat Count = 1)

| Name | Input Value |
|---------------------------------------|--------------------------|
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 4475 |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | -10201.1396 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 123.800003 |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos |
| k_HwAtoMtrADiffLimit_HwDeg_f32 | 655.900024 |
| k_HwAtoMtrAError_str.Threshold | 263 |

TEST DETAILS REPORT

2017-02-06, 13:53:13+0530



AbsHwPos_Per3

| Name | Input Value | | |
|--|--|--------------------|--------|
| k_HwAtoMtrAError_str.PStep | 217 | | |
| k_HwAtoMtrAError_str.NStep | 105 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | 692.099976 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 263 | 263 | ✔ |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | -10201.1396 | -10201.1396 ± 0.09 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | 117 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.18 (Repeat Count = 1) | | | | |
|--|--------------|--|--------|--|
| Name | | Input Value | | |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | | 470 | | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | | 1 | | |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | | 700.22998 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | 756 | | |
| Rte_Inst_Ap_AbsHwPos | | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HwAtoMtrADiffLimit_HwDeg_f32 | | 0 | | |
| k_HwAtoMtrAError_str.Threshold | | 286 | | |
| k_HwAtoMtrAError_str.PStep | | 231 | | |
| k_HwAtoMtrAError_str.NStep | | 119 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | | 1 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | | -601.400024 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result | |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 286 | 286 | ✓ | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | 1 | ✓ | |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | 700.22998 | 700.22998 ± 0.0009 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | 117 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ | |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.19 (Repeat Count = 1) | | | | |
|---|--|--|----------------|--------|
| Name | | Input Value | | |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | | 493 | | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | | 0 | | |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | | 1897.31995 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | -465.5 | | |
| Rte_Inst_Ap_AbsHwPos | | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HwAtoMtrADiffLimit_HwDeg_f32 | | 1600 | | |
| k_HwAtoMtrAError_str.Threshold | | 3709 | | |
| k_HwAtoMtrAError_str.PStep | | 245 | | |
| k_HwAtoMtrAError_str.NStep | | 133 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | | 1 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | | 510.700012 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | |
| Name | | Actual Value | Expected Value | Result |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | | 360 | 360 | |

TEST DETAILS REPORT

2017-02-06, 13:53:13+0530



AbsHwPos_Per3

| Name | Actual Value | Expected Value | Result |
|--|--------------|----------------------|--------|
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | -976.200012 | -976.200012 ± 0.0009 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.20 (Repeat Count = 1) | | | | |
|--|--|--------------------|--------|--|
| Name | Input Value | | | |
| AbsHwPos_HwAtmMtrAFitAcc_Cnt_M_u16 | 516 | | | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | | | |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | 3094.40991 | | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 312.700012 | | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | | |
| k_HwAtmMtrADiffLimit_HwDeg_f32 | 30 | | | |
| k_HwAtmMtrError_str.Threshold | 332 | | | |
| k_HwAtmMtrError_str.PStep | 236 | | | |
| k_HwAtmMtrError_str.NStep | 147 | | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | 420 | | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | | |
| Name | Actual Value | Expected Value | Result | |
| AbsHwPos_HwAtmMtrAFitAcc_Cnt_M_u16 | 332 | 332 | ✓ | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | 1 | ✓ | |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | 3094.40991 | 3094.40991 ± 0.009 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | 117 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ | |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.21 (Repeat Count = 1) | | | | |
|--|--|---------------------|--------|--|
| Name | Input Value | | | |
| AbsHwPos_HwAtmMtrAFitAcc_Cnt_M_u16 | 5398 | | | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 0 | | | |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | 4291.5 | | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 469.399994 | | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | | |
| k_HwAtmMtrADiffLimit_HwDeg_f32 | 564 | | | |
| k_HwAtmMtrError_str.Threshold | 0 | | | |
| k_HwAtmMtrError_str.PStep | 227 | | | |
| k_HwAtmMtrError_str.NStep | 161 | | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | -329.299988 | | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | | |
| Name | Actual Value | Expected Value | Result | |
| AbsHwPos_HwAtmMtrAFitAcc_Cnt_M_u16 | 5237 | 5237 | ✓ | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | 1 | ✓ | |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | 798.699951 | 798.700012 ± 0.0009 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | *none* | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ | |

TEST DETAILS REPORT

AbsHwPos_Per3

2017-02-06, 13:53:13+0530



Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.22 (Repeat Count = 1)

| Name | Input Value | | |
|--|--|--------------------|--------|
| AbsHwPos_HwAtoMtrAFItAcc_Cnt_M_u16 | 562 | | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | | |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | 5488.58984 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 1016.09998 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HWatoMtrADiffLimit_HwDeg_f32 | 1021.70001 | | |
| k_HwAtoMtrAError_str.Threshold | 65535 | | |
| k_HwAtoMtrAError_str.PStep | 218 | | |
| k_HwAtoMtrAError_str.NStep | 175 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | -238.600006 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_HwAtoMtrAFItAcc_Cnt_M_u16 | 780 | 780 | ✔ |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | 5488.58984 | 5488.58984 ± 0.009 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.23 (Repeat Count = 1)

| Name | Input Value | | |
|--|--|----------------------|--------|
| AbsHwPos_HwAtoMtrAFItAcc_Cnt_M_u16 | 585 | | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 0 | | |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | 6685.68018 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -782.799988 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HWAttoMtrADiffLimit_HwDeg_f32 | 179.399994 | | |
| k_HwAttoMtrAError_str.Threshold | 200 | | |
| k_HwAttoMtrAError_str.PStep | 182 | | |
| k_HwAttoMtrAError_str.NStep | 189 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | 147.899994 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_HwAtoMtrAFItAcc_Cnt_M_u16 | 396 | 396 | ✔ |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | -930.699951 | -930.700012 ± 0.0009 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:53:13+0530



AbsHwPos_Per3

Test Step 2.24 (Repeat Count = 1)

| Name | Input Value | | |
|--|--|----------------------|--------|
| AbsHwPos_HwAtoMtrAFItAcc_Cnt_M_u16 | 108 | | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 0 | | |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | 7882.77002 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 692.099976 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HWAtoMtrADiffLimit_HwDeg_f32 | 137.100006 | | |
| k_HwAtoMtrAError_str.Threshold | 489 | | |
| k_HwAtoMtrAError_str.PStep | 0 | | |
| k_HwAtoMtrAError_str.NStep | 203 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | 600 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_HwAtoMtrAFItAcc_Cnt_M_u16 | 0 | 0 | ✔ |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | 92.0999756 | 92.0999985 ± 0.00009 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | 117 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.25 (Repeat Count = 1)

| Name | Input Value | | |
|--|--|--------------------|--------|
| AbsHwPos_HwAtoMtrAFItAcc_Cnt_M_u16 | 6317 | | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | | |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | 9079.86035 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 756 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HWAtoMtrADiffLimit_HwDeg_f32 | 694.799988 | | |
| k_HwAtoMtrAError_str.Threshold | 646 | | |
| k_HwAtoMtrAError_str.PStep | 255 | | |
| k_HwAtoMtrAError_str.NStep | 217 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | -33.5 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_HwAtoMtrAFItAcc_Cnt_M_u16 | 646 | 646 | ✔ |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | 9079.86035 | 9079.86035 ± 0.009 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | 117 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.26 (Repeat Count = 1)

| Name | Input Value |
|---------------------------------------|--------------------------|
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 654 |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 0 |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | 10276.9502 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 819.900024 |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos |
| k_HWatoMtrADiffLimit_HwDeg_f32 | 123 |

TEST DETAILS REPORT

2017-02-06, 13:53:13+0530



AbsHwPos_Per3

| Name | Input Value | | |
|--|--|----------------|--------|
| k_HwAtmtrAEError_str.Threshold | 8974 | | |
| k_HwAtmtrAEError_str.PStep | 4 | | |
| k_HwAtmtrAEError_str.NStep | 199 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | -897.099976 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_HwAtmtrAFitAcc_Cnt_M_u16 | 455 | 455 | ✓ |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | 1717 | 1717 ± 0.009 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.27 (Repeat Count = 1) | | | | |
|--|--------------|--|--------|--|
| Name | | Input Value | | |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | | 677 | | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | | 0 | | |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | | -5478.31982 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | -883.799988 | | |
| Rte_Inst_Ap_AbsHwPos | | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HwAtoMtrADiffLimit_HwDeg_f32 | | 1310.19995 | | |
| k_HwAtoMtrAEError_str.Threshold | | 424 | | |
| k_HwAtoMtrAEError_str.PStep | | 173 | | |
| k_HwAtoMtrAEError_str.NStep | | 0 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | | 1 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | | 465.299988 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result | |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 677 | 677 | ✓ | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | 1 | ✓ | |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | -1349.09998 | -1349.09998 ± 0.009 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | *none* | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ | |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.28 (Repeat Count = 1) | | | | |
|---|--|--|----------------|--------|
| Name | | Input Value | | |
| AbsHwPos_HwAtoMtrAFItAcc_Cnt_M_u16 | | 7000 | | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | | 1 | | |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | | 1 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | 947.700012 | | |
| Rte_Inst_Ap_AbsHwPos | | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HwAtoMtrADiffLimit_HwDeg_f32 | | 1367.90002 | | |
| k_HwAtoMtrAError_str.Threshold | | 4477 | | |
| k_HwAtoMtrAError_str.PStep | | 164 | | |
| k_HwAtoMtrAError_str.NStep | | 255 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | | 1 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | | -88.9899979 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | |
| Name | | Actual Value | Expected Value | Result |
| AbsHwPos_HwAtoMtrAFItAcc_Cnt_M_u16 | | 6745 | 6745 | ✔ |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | | 1 | 1 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:53:13+0530



AbsHwPos_Per3

| Name | Actual Value | Expected Value | Result |
|--|--------------|----------------|--------|
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | 1 | 1 ± 0.000009 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.29 (Repeat Count = 1)

| Name | Input Value | | |
|--|--|---------------------|--------|
| AbsHwPos_HwAtoMtrAFItAcc_Cnt_M_u16 | 723 | | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 0 | | |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | 1457.31995 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -1011.59998 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HwAtoMtrADiffLimit_HwDeg_f32 | 1425.59998 | | |
| k_HwAtoMtrError_str.Threshold | 1470 | | |
| k_HwAtoMtrError_str.PStep | 155 | | |
| k_HwAtoMtrError_str.NStep | 1 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | 75.75 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_HwAtoMtrAFItAcc_Cnt_M_u16 | 722 | 722 | ✔ |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | -1087.34998 | -1087.34998 ± 0.009 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.30 (Repeat Count = 1)

| Name | Input Value | | |
|--|--|---------------------|--------|
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 516 | | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 0 | | |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | -1245.59998 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -465.5 | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HwAtoMtrADiffLimit_HwDeg_f32 | 378.700012 | | |
| k_HwAtoMtrAError_str.Threshold | 470 | | |
| k_HwAtoMtrAError_str.PStep | 124 | | |
| k_HwAtoMtrAError_str.NStep | 203 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | 0 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | 692.099976 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 0 | 0 | ✔ |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 0 | 0 | ✔ |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | -1245.59998 | -1245.59998 ± 0.009 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:53:13+0530

AbsHwPos_Per3



Test Step 2.31 (Repeat Count = 1) ✓

| Name | Input Value |
|---|--|
| AbsHwPos_HwAtoMtrAFItAcc_Cnt_M_u16 | 5398 |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | 1247 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 312.700012 |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos |
| k_HwAtoMtrADiffLimit_HwDeg_f32 | 401.799988 |
| k_HwAtoMtrAError_str.Threshold | 493 |
| k_HwAtoMtrAError_str.PStep | 32 |
| k_HwAtoMtrAError_str.NStep | 217 |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | -601.400024 |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 |

| Name | Actual Value | Expected Value | Result |
|--|--------------|----------------|--------|
| AbsHwPos_HwAtoMtrAFItAcc_Cnt_M_u16 | 0 | 0 | ✓ |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | 1247 | 1247 ± 0.009 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |

Test Step Call Trace ✓

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.32 (Repeat Count = 1) ✓

| Name | Input Value |
|---|--|
| AbsHwPos_HwAtoMtrAFItAcc_Cnt_M_u16 | 562 |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | -11026 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 469.399994 |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos |
| k_HwAtoMtrADiffLimit_HwDeg_f32 | 424.899994 |
| k_HwAtoMtrAError_str.Threshold | 516 |
| k_HwAtoMtrAError_str.PStep | 145 |
| k_HwAtoMtrAError_str.NStep | 231 |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | 510.700012 |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 |

| Name | Actual Value | Expected Value | Result |
|--|--------------|----------------|--------|
| AbsHwPos_HwAtoMtrAFItAcc_Cnt_M_u16 | 516 | 516 | ✓ |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | -11026 | -11026 ± 0.09 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | 117 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |

Test Step Call Trace ✓

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.33 (Repeat Count = 1) ✓

| Name | Input Value |
|---------------------------------------|--------------------------|
| AbsHwPos_HwAtoMtrAFItAcc_Cnt_M_u16 | 585 |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 0 |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | 11026 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 1016.09998 |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos |

TEST DETAILS REPORT

2017-02-06, 13:53:13+0530



AbsHwPos_Per3

| Name | Input Value | | |
|--|--|---------------------|--------|
| k_HwAtoMtrADiffLimit_HwDeg_f32 | 448 | | |
| k_HwAtoMtrAError_str.Threshold | 5398 | | |
| k_HwAtoMtrAError_str.PStep | 99 | | |
| k_HwAtoMtrAError_str.NStep | 245 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc.value | 1 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | 420 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_Igc | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 340 | 340 | ✔ |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_Igc | 1 | 1 | ✔ |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | 596.099976 | 596.099976 ± 0.0009 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.34 (Repeat Count = 1) | | | | |
|--|--------------|--|--------|--|
| Name | | Input Value | | |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | | 108 | | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | | 1 | | |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | | 2145.66992 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | -782.799988 | | |
| Rte_Inst_Ap_AbsHwPos | | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HwAtoMtrADiffLimit_HwDeg_f32 | | 471.100006 | | |
| k_HwAtoMtrAError_str.Threshold | | 562 | | |
| k_HwAtoMtrAError_str.PStep | | 87 | | |
| k_HwAtoMtrAError_str.NStep | | 236 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | | 1 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | | -329.299988 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result | |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 195 | 195 | ✓ | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | 1 | ✓ | |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | 2145.66992 | 2145.66992 ± 0.009 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | *none* | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ | |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.35 (Repeat Count = 1) | | | | |
|---|--|--|----------------|--------|
| Name | | Input Value | | |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | | 6317 | | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | | 1 | | |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | | 0 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | 692.099976 | | |
| Rte_Inst_Ap_AbsHwPos | | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HwAtoMtrADiffLimit_HwDeg_f32 | | 494.200012 | | |
| k_HwAtoMtrAError_str.Threshold | | 654 | | |
| k_HwAtoMtrAError_str.PStep | | 128 | | |
| k_HwAtoMtrAError_str.NStep | | 227 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | | 1 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | | -238.600006 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | |
| Name | | Actual Value | Expected Value | Result |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | | 654 | 654 | |

TEST DETAILS REPORT

2017-02-06, 13:53:13+0530



AbsHwPos_Per3

| Name | Actual Value | Expected Value | Result |
|--|--------------|----------------|--------|
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | 0 | 0 ± 0.000009 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | 117 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.36 (Repeat Count = 1) | | | | |
|--|--|---------------------|--------|--|
| Name | Input Value | | | |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 654 | | | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 0 | | | |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | -7894.12012 | | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 469.399994 | | | |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | | |
| k_HwAtoMtrADiffLimit_HwDeg_f32 | 517.299988 | | | |
| k_HwAtoMtrError_str.Threshold | 677 | | | |
| k_HwAtoMtrError_str.PStep | 214 | | | |
| k_HwAtoMtrError_str.NStep | 91 | | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | 100 | | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | | |
| Name | Actual Value | Expected Value | Result | |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 563 | 563 | ✓ | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | 1 | ✓ | |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | 369.399994 | 369.399994 ± 0.0009 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | *none* | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ | |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

| Test Case 3: Path Test | |
|------------------------|---|
| Specification | Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles: TS3.1 1029.00 Cycles TS3.2 1585.00 Cycles TS3.3 1575.00 Cycles TS3.4 1119.00 Cycles TS3.5 1104.00 Cycles |
| Description | Vector Description: TS3.1(I2CHwAbsPosValid_Cnt_T_lgc == TRUE)=False TS3.2"(I2CHwAbsPosValid_Cnt_T_lgc == TRUE)=True (FALSE == AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc)=False (Abs_f32_m(I2CHwAbsPos_HwDeg_T_f32 - NewRelHwPos_HwDeg_T_f32) >= k_HwAtoMtrADiffLimit_HwDeg_f32)=True (DiagFailed_m(AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16, k_HwAtoMtrError_str) == TRUE)=True" TS3.3'(FALSE == AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc)=True (Abs_f32_m(I2CHwAbsPos_HwDeg_T_f32 - NewRelHwPos_HwDeg_T_f32) >= k_HwAtoMtrADiffLimit_HwDeg_f32)=False (AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 == 0U)=True" TS3.4(AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 == 0U)=False TS3.5(DiagFailed_m(AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16, k_HwAtoMtrError_str) == TRUE)=False |

| Test Step 3.1 (Repeat Count = 1) | |
|---------------------------------------|-------------|
| Name | Input Value |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 0 |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 0 |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | -11026 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -10125 |

TEST DETAILS REPORT

2017-02-06, 13:53:13+0530



AbsHwPos_Per3

| Name | Input Value | | |
|--|--|----------------|--------|
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HwAtoMtrADiffLimit_HwDeg_f32 | 0 | | |
| k_HwAtoMtrAError_str.Threshold | 0 | | |
| k_HwAtoMtrAError_str.PStep | 0 | | |
| k_HwAtoMtrAError_str.NStep | 0 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | 0 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | -900 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 0 | 0 | ✓ |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | -11026 | -11026 ± 0.09 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

| Test Step 3.2 (Repeat Count = 1) | | | | |
|--|--------------|--|--------|--|
| Name | | Input Value | | |
| AbsHwPos_HwAtoMtrAFItAcc_Cnt_M_u16 | | 65535 | | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | | 1 | | |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | | 11026 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | 10125 | | |
| Rte_Inst_Ap_AbsHwPos | | tgt_Rte_Inst_Ap_AbsHwPos | | |
| k_HwAtoMtrADiffLimit_HwDeg_f32 | | 1600 | | |
| k_HwAtoMtrAError_str.Threshold | | 65535 | | |
| k_HwAtoMtrAError_str.PStep | | 255 | | |
| k_HwAtoMtrAError_str.NStep | | 255 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | | 1 | | |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | | 900 | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | | |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | | |
| Name | Actual Value | Expected Value | Result | |
| AbsHwPos_HwAtoMtrAFItAcc_Cnt_M_u16 | 65535 | 65535 | ✓ | |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | 1 | ✓ | |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | 11026 | 11026 ± 0.09 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | 117 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ | |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:53:13+0530



AbsHwPos_Per3

Test Step 3.3 (Repeat Count = 1)

| Name | Input Value |
|---|--|
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 125 |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 0 |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | -9875.20996 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 420 |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos |
| k_HWatoMtrADiffLimit_HwDeg_f32 | 332.5 |
| k_HwAtoMtrAError_str.Threshold | 470 |
| k_HwAtoMtrAError_str.PStep | 124 |
| k_HwAtoMtrAError_str.NStep | 182 |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | 666.200012 |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 |

| Name | Actual Value | Expected Value | Result |
|--|--------------|----------------------|--------|
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 0 | 0 | ✓ |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | -246.200012 | -246.199997 ± 0.0009 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | 117 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

Test Step 3.4 (Repeat Count = 1)

| Name | Input Value |
|---|--|
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 194 |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 0 |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | 1107.79004 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 147.899994 |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos |
| k_HWatoMtrADiffLimit_HwDeg_f32 | 401.799988 |
| k_HwAtoMtrAError_str.Threshold | 5398 |
| k_HwAtoMtrAError_str.PStep | 10 |
| k_HwAtoMtrAError_str.NStep | 155 |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | 900 |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 |

| Name | Actual Value | Expected Value | Result |
|--|--------------|----------------------|--------|
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 39 | 39 | ✓ |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | -752.099976 | -752.099976 ± 0.0009 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ |

Test Step 3.5 (Repeat Count = 1)

| Name | Input Value |
|---------------------------------------|--------------------------|
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 217 |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 |
| AbsHwPos_MtrtoHwAOfst_HwDeg_M_f32 | 4768.79004 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 57.2000008 |
| Rte_Inst_Ap_AbsHwPos | tgt_Rte_Inst_Ap_AbsHwPos |
| k_HWatoMtrADiffLimit_HwDeg_f32 | 424.899994 |
| k_HwAtoMtrAError_str.Threshold | 562 |

TEST DETAILS REPORT

2017-02-06, 13:53:13+0530



AbsHwPos_Per3

| Name | Input Value |
|---|--|
| k_HwAtoMtrAError_str.PStep | 77 |
| k_HwAtoMtrAError_str.NStep | 146 |
| tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32.value | 0 |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc | tgt_AbsHwPos_Per3_I2CHwAbsPosValid_Cnt_lgc |
| tgt_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 | tgt_AbsHwPos_Per3_I2CHwAbsPos_HwDeg_f32 |

| Name | Actual Value | Expected Value | Result |
|--|--------------|--------------------|--------|
| AbsHwPos_HwAtoMtrAFitAcc_Cnt_M_u16 | 294 | 294 | ✓ |
| AbsHwPos_MtrToHwAOfstLearnt_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_MtrToHwAOfst_HwDeg_M_f32 | 4768.79004 | 4768.79004 ± 0.009 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 117 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✓ |

| Test Step Call Trace | | | | | ✓ |
|--|-------|--|-------|--------|---|
| Actual Function | Count | Expected Function | Count | Result | |
| Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP0_CheckpointReached | 1 | ✓ | |
| Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per3_CP1_CheckpointReached | 1 | ✓ | |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530

AbsHwPos_Per2



| | |
|-------------|------------------|
| Project | AbsHwPos |
| Module | AbsHwPos_Tcl2IVd |
| Test Object | AbsHwPos_Per2 |

Instrumentation: Test Object Only

| | |
|-------------------------|-------|
| Statement (C0) Coverage | 100 % |
| Decision Coverage | 100 % |
| Branch (C1) Coverage | 100 % |
| MCC Coverage | 100 % |
| MC/DC Coverage | 100 % |

Statistics

| | |
|-----------------|-----|
| Total Testcases | 3 |
| Successful | 3 ✓ |
| Failed | 0 |
| Not Executed | 0 |

Module Properties

| | |
|------------------------|--|
| Project Root Directory | D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G |
| Configuration File | D:\Synergy_Work_Area\AbsHwPos_Tcl2cVd_FDD05G\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml |
| Target Environment | TI TMS 570 PLS UDE (Default) |
| Kind of Test | Unit Test |
| Linker Options | |
| Source File(s) | |
| File | \$(SOURCEROOT)\Ap_AbsHwPos.c |
| Compiler Options | -D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract -I\$(PROJECTROOT)\AbsHwPos_Tcl2cVd\utp\contract\Ap_AbsHwPos -I\$(PROJECTROOT)\NxtLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(Compiler Install Path)\include |

Comments/Description/Specification

| Name | Text |
|---------------------------|--|
| Module 'AbsHwPos_Tcl2IVd' | *****Unit Test Description***** Name of Tester: Medha Sabale Code File(s) Under Test:Ap_AbsHwPos.c Code File(s) Version: 10 Module Design Document:Absolute_Handwheel_Position_Tcl2cVd_MDD.doc Module Design Document Version:11 Data Dictionary Version: 10 Unit Test Plan Version:3 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):3004 Total RAM Used (Bytes):64 Total CALS Used (Bytes):56 Special Test Requirements:NA Test Date:2/6/2017 Comments: NOTE 1:Inline functions defined in "Globalmacro.h" are not unit tested. NOTE 2: "CBD_Sandbox_dbg.map" file is embedded for reference." ***** |

Attributes

| Name | Value |
|-----------------------|---|
| Compiler Install Path | \$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5 |
| Float Precision | 9 |
| InitObjDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\obj |
| InitSrcDir | \$(PROJECTROOT)\UnitTestEnv\static_build_files\src |
| Linker File | \$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd |
| Makefile Template | \$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl |
| Target Install Path | \$(ProgramFiles)\pls\UDE 4.4 |
| Time Unit | Cycles |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530

AbsHwPos_Per2



| Attributes | |
|------------------|--|
| Name | Value |
| Timer Enabled | False |
| Timer Prescale | 0 |
| Timer Resolution | 1 |
| UDE Config File | \$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_I2PIN_JTAG.cfg |
| Workspace File | D:\Synergy_Work_Area\AbsHwPos_TcI2cVd_FDD05G\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP |

TEST DETAILS REPORT

AbsHwPos_Per2

2017-02-06, 13:51:17+0530



Test Case 1: Metrics Test

| | |
|----------------------|---|
| Specification | Performance Metrics (With "None" Instrumentation and WithPS Environment) CPU Cycles: TS1.1 1341.00 Cycles TS1.2 2917.00 Cycles |
| Description | Vector Description: TS1.1"Shortest Execution Path: ((DiagStatusHwPosReducedPerf_Cnt_T_lgc == TRUE) (AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_INPUTINVALID))=>True (DiagStatusHwPosReducedPerf_Cnt_T_lgc == TRUE)=>False (AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc == TRUE)=>False (Abs_f32_m(AbsHwPos_TempHwPos_HwDeg_M_f32 - AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32) >= k_HwPosOutputLPFError_HwDeg_f32)=True" TS1.2"Longest Execution Path: ((DiagStatusHwPosReducedPerf_Cnt_T_lgc == TRUE) (AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_INPUTINVALID))=False (AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_INIT)=False (AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_SENSORVALID)=True (k_UseTurnsCntr_Cnt_lgc == FALSE)=False (TurnsCntrValidity_Cnt_u08 == D_TCVVALID_CNT_U08)=True (AbsHwPos_VehCntrValid_Cnt_M_lgc == TRUE)=False (SnsrlessAuthority_Uls_T_f32 >= k_MinSensorlessAuthority_Uls_f32)=True (SnsrlessAuthority_Uls_T_f32 >= k_MaxSensorlessAuthority_Uls_f32)=True (AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc == TRUE)=True (Abs_f32_m(VehCntrOffset_HwDeg_T_f32) <= k_VehCntrOffValidLimit_HwDeg_f32)=False (Abs_f32_m(Rte_Pim_EOLVehCntrOffset()->EOLVehCntrOffset_HwDeg_f32 - D_INVALIDOFFSET_HWDEG_F32)) >= FLT_EPSILON)=True if(NvMStatus_Cnt_T_enum != NVM_REQ_PENDING)=>True (Abs_f32_m(AbsHwPos_TempHwPos_HwDeg_M_f32 - AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32) >= k_HwPosOutputLPFError_HwDeg_f32)=False" |

Test Step 1.1 (Repeat Count = 1)

| Name | Input Value |
|--|---|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 9556.42969 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 1 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 204.899994 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.75999999 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1348 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 442.940002 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 442.940002 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 26817 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 1700.22998 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 1 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 5054.37012 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 8178.93018 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0 |
| k_HwPosOutputLPFError_HwDeg_f32 | 674.299988 |
| k_I2CHwAuthority_Uls_f32 | 0.75 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.839999974 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 822.799988 |
| k_MinSensorlessAuthority_Uls_f32 | 0.75 |
| k_TurnsCntrAuthority_Uls_f32 | 0.75 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 674.299988 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -291.100006 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.839999974 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 1587.5 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 128 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2325.1001 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 9299 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 3 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|---|--|---------------------|--------|
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 442.940002 | 442.940002 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 1 | 1 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 3890.49731 | 3890.49731 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1348 | 1348 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT | HWPOS_STATE_INIT | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 3890.49731 | 3890.49731 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 26817 | 26817 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 1700.22998 | 1700.22998 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 1 | 1 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 5054.37012 | 5054.37012 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 8178.93018 | 8178.93018 ± 0.009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 1 | 1 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 1348 | 1348 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 26817 | 26817 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2325.1001 | 2325.1001 ± 0.009 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 1.2 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 3787.70996 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.280000001 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1112.18005 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.280000001 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 296 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1207.09998 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 1207.09998 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 23721 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 1232.09998 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.280000001 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 8009.91016 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1132.57996 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.270000011 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1179.19995 |
| k_I2CHwAuthority_Uls_f32 | 0.270000011 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.340000004 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 882.200012 |
| k_MinSensorlessAuthority_Uls_f32 | 0.270000011 |
| k_TurnsCntrAuthority_Uls_f32 | 0.270000011 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 822.799988 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -271.399994 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.360000014 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 622.700012 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| target_AbsHwPos_Per2_TurnsCntValidity_Cnt_u08.value | 100 |
| target_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 875.5 |
| target_Pim_EOLVehCntOffset.EOLHwPosTrimPerformed_Cnt_u16 | 3347 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus_ErrorStatus | 3 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntOffset | target_Pim_EOLVehCntOffset |

| Name | Actual Value | Expected Value | Result |
|--|-------------------------|-------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 1207.09998 | 1207.09998 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.360000014 | 0.360000014 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 622.700012 | 622.700012 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3 | 3 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 622.700012 | 622.700012 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 1232.09998 | 1232.09998 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.360000014 | 0.360000014 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 622.700012 | 622.700012 ± 0.1 | ✓ |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | -1132.57996 | -1132.57996 ± 0.009 | ✓ |
| AbsHwPos_VehCntOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.360000014 | 0.360000014 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 622.700012 | 622.700012 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 3 | 3 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1232.09998 | 1232.09998 ± 0.1 | ✓ |
| target_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

| Test Step Call Trace | | | | | ✓ |
|---|-------|---|-------|--------|---|
| Actual Function | Count | Expected Function | Count | Result | |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus | 1 | ✓ | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_WriteBlock | 1 | ✓ | |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ | |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ | |

TEST DETAILS REPORT

AbsHwPos_Per2

2017-02-06, 13:51:17+0530



Test Case 2: Range Test



TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

Specification Performance Metrics (With "None" Instrumentation and WithPS Environment)

CPU Cycles:

TS2.1 1467.00 Cycles
TS2.2 2917.00 Cycles
TS2.3 1877.00 Cycles
TS2.4 1933.00 Cycles
TS2.5 1838.00 Cycles
TS2.6 1921.00 Cycles
TS2.7 1341.00 Cycles
TS2.8 1885.00 Cycles
TS2.9 1360.00 Cycles
TS2.10 1862.00 Cycles
TS2.11 1360.00 Cycles
TS2.12 1417.00 Cycles
TS2.13 1352.00 Cycles
TS2.14 1868.00 Cycles
TS2.15 1347.00 Cycles
TS2.16 1895.00 Cycles
TS2.17 1347.00 Cycles
TS2.18 1902.00 Cycles
TS2.19 1450.00 Cycles
TS2.20 1904.00 Cycles
TS2.21 1388.00 Cycles
TS2.22 1428.00 Cycles
TS2.23 1362.00 Cycles
TS2.24 1428.00 Cycles
TS2.25 1341.00 Cycles
TS2.26 1428.00 Cycles
TS2.27 1908.00 Cycles
TS2.28 1889.00 Cycles
TS2.29 1844.00 Cycles
TS2.30 1889.00 Cycles
TS2.31 1361.00 Cycles
TS2.32 1902.00 Cycles
TS2.33 1373.00 Cycles
TS2.34 1902.00 Cycles
TS2.35 1428.00 Cycles
TS2.36 1861.00 Cycles
TS2.37 1844.00 Cycles
TS2.38 1904.00 Cycles
TS2.39 1824.00 Cycles
TS2.40 1904.00 Cycles
TS2.41 1844.00 Cycles
TS2.42 1861.00 Cycles
TS2.43 2347.00 Cycles
TS2.44 2812.00 Cycles
TS2.45 1373.00 Cycles
TS2.46 2763.00 Cycles
TS2.47 1373.00 Cycles
TS2.48 1907.00 Cycles
TS2.49 1373.00 Cycles
TS2.50 1907.00 Cycles
TS2.51 1352.00 Cycles
TS2.52 1417.00 Cycles
TS2.53 1352.00 Cycles
TS2.54 1868.00 Cycles
TS2.55 1815.00 Cycles
TS2.56 1840.00 Cycles
TS2.57 1352.00 Cycles
TS2.58 1840.00 Cycles
TS2.59 1352.00 Cycles
TS2.60 1840.00 Cycles
TS2.61 1426.00 Cycles
TS2.62 2799.00 Cycles
TS2.63 2358.00 Cycles
TS2.64 1922.00 Cycles
TS2.65 1943.00 Cycles
TS2.66 1384.00 Cycles
TS2.67 1897.00 Cycles
TS2.68 1361.00 Cycles
TS2.69 1904.00 Cycles
TS2.70 1373.00 Cycles
TS2.71 1904.00 Cycles
TS2.72 1361.00 Cycles
TS2.73 1904.00 Cycles
TS2.74 1904.00 Cycles
TS2.75 1373.00 Cycles
TS2.76 1904.00 Cycles
TS2.77 1361.00 Cycles
TS2.78 1889.00 Cycles
TS2.79 1361.00 Cycles
TS2.80 1904.00 Cycles
TS2.81 1361.00 Cycles
TS2.82 1889.00 Cycles
TS2.83 1361.00 Cycles
TS2.84 1904.00 Cycles
TS2.85 1373.00 Cycles
TS2.86 1904.00 Cycles
TS2.87 1361.00 Cycles
TS2.88 1904.00 Cycles
TS2.89 1373.00 Cycles
TS2.90 1904.00 Cycles
TS2.91 1368.00 Cycles
TS2.92 1889.00 Cycles
TS2.93 1338.00 Cycles
TS2.94 1889.00 Cycles
TS2.95 1303.00 Cycles
TS2.96 1840.00 Cycles
TS2.97 1359.00 Cycles
TS2.98 1885.00 Cycles
TS2.99 1352.00 Cycles
TS2.100 1840.00 Cycles
TS2.101 2310.00 Cycles
TS2.102 2740.00 Cycles
TS2.103 2287.00 Cycles
TS2.104 2753.00 Cycles
TS2.105 2288.00 Cycles
TS2.106 1838.00 Cycles
TS2.107 1465.00 Cycles
TS2.108 1945.00 Cycles
TS2.109 1815.00 Cycles

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| | | |
|---------|---------|--------|
| TS2.110 | 1898.00 | Cycles |
| TS2.111 | 1386.00 | Cycles |
| TS2.112 | 1874.00 | Cycles |
| TS2.113 | 1862.00 | Cycles |
| TS2.114 | 1926.00 | Cycles |
| TS2.115 | 1379.00 | Cycles |
| TS2.116 | 1839.00 | Cycles |
| TS2.117 | 1451.00 | Cycles |
| TS2.118 | 2812.00 | Cycles |
| TS2.119 | 2358.00 | Cycles |
| TS2.120 | 2792.00 | Cycles |
| TS2.121 | 2306.00 | Cycles |
| TS2.122 | 2358.00 | Cycles |
| TS2.123 | 2792.00 | Cycles |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Description | Vector Description: |
|-------------|---|
| | TS2.1All Min |
| | TS2.2All Max |
| | TS2.3TurnsCntrValidity_Cnt_u08=>Min |
| | TS2.4TurnsCntrValidity_Cnt_u08=>Max |
| | TS2.5TurnsCntrValidity_Cnt_u08=>Pos |
| | TS2.6ManufMode_Cnt_enum=>Min |
| | TS2.7ManufMode_Cnt_enum=>Max |
| | TS2.8ManufMode_Cnt_enum=>Pos |
| | TS2.9DiagStatusHwPosReducedPerf_Cnt_lgc=>Min |
| | TS2.10DiagStatusHwPosReducedPerf_Cnt_lgc=>Max |
| | TS2.11I2CHwAbsPosValid_Cnt_lgc=>Min |
| | TS2.12I2CHwAbsPosValid_Cnt_lgc=>Max |
| | TS2.13SensorlessAuthority_Uls_f32=>Min |
| | TS2.14SensorlessAuthority_Uls_f32=>Max |
| | TS2.15SensorlessAuthority_Uls_f32=>Pos |
| | TS2.16SensorlessHwPos_HwDeg_f32=>Min |
| | TS2.17SensorlessHwPos_HwDeg_f32=>Max |
| | TS2.18SensorlessHwPos_HwDeg_f32=>Zero |
| | TS2.19SensorlessHwPos_HwDeg_f32=>Pos |
| | TS2.20SensorlessHwPos_HwDeg_f32=>Neg |
| | TS2.21I2CHwAbsPos_HwDeg_f32=>Min |
| | TS2.22I2CHwAbsPos_HwDeg_f32=>Max |
| | TS2.23I2CHwAbsPos_HwDeg_f32=>Zero |
| | TS2.24I2CHwAbsPos_HwDeg_f32=>Pos |
| | TS2.25I2CHwAbsPos_HwDeg_f32=>Neg |
| | TS2.26k_UseTurnsCntr_Cnt_lgc=>Min |
| | TS2.27k_UseTurnsCntr_Cnt_lgc=>Max |
| | TS2.28k_MinSensorlessAuthority_Uls_f32=>Min |
| | TS2.29k_MinSensorlessAuthority_Uls_f32=>Max/Default |
| | TS2.30k_MinSensorlessAuthority_Uls_f32=>Pos |
| | TS2.31k_MaxSensorlessAuthority_Uls_f32=>Min |
| | TS2.32k_MaxSensorlessAuthority_Uls_f32=>Max/Default |
| | TS2.33k_MaxSensorlessAuthority_Uls_f32=>Pos |
| | TS2.34k_VehCntrOffValidLimit_HwDeg_f32=>Min |
| | TS2.35k_VehCntrOffValidLimit_HwDeg_f32=>Max/Default |
| | TS2.36k_VehCntrOffValidLimit_HwDeg_f32=>Pos |
| | TS2.37Rte_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32=>Min |
| | TS2.38Rte_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32=>Max |
| | TS2.39Rte_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32=>Zero |
| | TS2.40Rte_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32=>Pos |
| | TS2.41Rte_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32=>Neg |
| | TS2.42k_MaxVehCntrOffDiff_HwDeg_f32=>Min |
| | TS2.43k_MaxVehCntrOffDiff_HwDeg_f32=>Max |
| | TS2.44k_MaxVehCntrOffDiff_HwDeg_f32=>Pos/Default |
| | TS2.45k_HwPosOutputLPFError_HwDeg_f32=>Min |
| | TS2.46k_HwPosOutputLPFError_HwDeg_f32=>Max |
| | TS2.47k_HwPosOutputLPFError_HwDeg_f32=>Pos/Default |
| | TS2.48k_HwPosAuthorityStep_Uls_f32=>Min |
| | TS2.49k_HwPosAuthorityStep_Uls_f32=>Max |
| | TS2.50k_HwPosAuthorityStep_Uls_f32=>Pos/Default |
| | TS2.51k_I2CHwAuthority_Uls_f32=>Min |
| | TS2.52k_I2CHwAuthority_Uls_f32=>Max/Default |
| | TS2.53k_I2CHwAuthority_Uls_f32=>Pos |
| | TS2.54k_TurnsCntrAuthority_Uls_f32=>Min |
| | TS2.55k_TurnsCntrAuthority_Uls_f32=>Max/Default |
| | TS2.56k_TurnsCntrAuthority_Uls_f32=>Pos |
| | TS2.57Rte_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16=>Min |
| | TS2.58Rte_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16=>Max |
| | TS2.59Rte_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16=>Pos |
| | TS2.60AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32=>Min |
| | TS2.61AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32=>Max |
| | TS2.62AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32=>Zero |
| | TS2.63AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32=>Pos |
| | TS2.64AbsHwPos_AlignedCumMtrPos_HwDeg_M_f32=>Neg |
| | TS2.65AbsHwPos_HandwheelAuthority_Uls_M_f32=>Min |
| | TS2.66AbsHwPos_HandwheelAuthority_Uls_M_f32=>Max |
| | TS2.67AbsHwPos_HandwheelAuthority_Uls_M_f32=>Pos |
| | TS2.68AbsHwPos_HwPosSource_Cnt_M_u16=>Min |
| | TS2.69AbsHwPos_HwPosSource_Cnt_M_u16=>Max |
| | TS2.70AbsHwPos_HwPosSource_Cnt_M_u16=>Pos |
| | TS2.71AbsHwPos_HwPosState_Cnt_M_enum=>HWPOS_STATE_SENSORVALID |
| | TS2.72AbsHwPos_HwPosState_Cnt_M_enum=>HWPOS_STATE_INPUTINVALID |
| | TS2.73AbsHwPos_HwPosState_Cnt_M_enum=>HWPOS_STATE_INIT |
| | TS2.74AbsHwPos_HwPosState_Cnt_M_enum=>HWPOS_STATE_SENSORINVALID |
| | TS2.75AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32=>Min |
| | TS2.76AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32=>Max |
| | TS2.77AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32=>Zero |
| | TS2.78AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32=>Pos |
| | TS2.79AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32=>Neg |
| | TS2.80AbsHwPos_RelHwPos_HwDeg_M_f32=>Min |
| | TS2.81AbsHwPos_RelHwPos_HwDeg_M_f32=>Max |
| | TS2.82AbsHwPos_RelHwPos_HwDeg_M_f32=>Zero |
| | TS2.83AbsHwPos_RelHwPos_HwDeg_M_f32=>Pos |
| | TS2.84AbsHwPos_RelHwPos_HwDeg_M_f32=>Neg |
| | TS2.85AbsHwPos_SrlComHwPos_HwDeg_M_f32=>Min |
| | TS2.86AbsHwPos_SrlComHwPos_HwDeg_M_f32=>Max |
| | TS2.87AbsHwPos_SrlComHwPos_HwDeg_M_f32=>Zero |
| | TS2.88AbsHwPos_SrlComHwPos_HwDeg_M_f32=>Pos |
| | TS2.89AbsHwPos_SrlComHwPos_HwDeg_M_f32=>Neg |
| | TS2.90AbsHwPos_SrlComHwPosStatus_Cnt_M_u16=>Min |
| | TS2.91AbsHwPos_SrlComHwPosStatus_Cnt_M_u16=>Max |
| | TS2.92AbsHwPos_SrlComHwPosStatus_Cnt_M_u16=>Pos |
| | TS2.93AbsHwPos_TargetHwAuthority_Uls_M_f32=>Min |
| | TS2.94AbsHwPos_TargetHwAuthority_Uls_M_f32=>Max |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

TS2.95AbsHwPos_TargetHwAuthority_Uls_M_f32=>Pos
 TS2.96AbsHwPos_TempHwPos_HwDeg_M_f32=>Min
 TS2.97AbsHwPos_TempHwPos_HwDeg_M_f32=>Max
 TS2.98AbsHwPos_TempHwPos_HwDeg_M_f32=>Zero
 TS2.99AbsHwPos_TempHwPos_HwDeg_M_f32=>Pos
 TS2.100AbsHwPos_TempHwPos_HwDeg_M_f32=>Neg
 TS2.101AbsHwPos_VehCntrOffset_HwDeg_M_f32=>Min
 TS2.102AbsHwPos_VehCntrOffset_HwDeg_M_f32=>Max
 TS2.103AbsHwPos_VehCntrOffset_HwDeg_M_f32=>Zero
 TS2.104AbsHwPos_VehCntrOffset_HwDeg_M_f32=>Pos
 TS2.105AbsHwPos_VehCntrOffset_HwDeg_M_f32=>Neg
 TS2.106AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc=>Min
 TS2.107AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc=>Max
 TS2.108AbsHwPos_VehCntrValid_Cnt_M_lgc=>Min
 TS2.109AbsHwPos_VehCntrValid_Cnt_M_lgc=>Max
 TS2.110AbsHwPos_HandwheelPositionLPF_M_str.SV=>Min
 TS2.111AbsHwPos_HandwheelPositionLPF_M_str.SV=>Max
 TS2.112AbsHwPos_HandwheelPositionLPF_M_str.SV=>Zero
 TS2.113AbsHwPos_HandwheelPositionLPF_M_str.SV=>Pos
 TS2.114AbsHwPos_HandwheelPositionLPF_M_str.SV=>Neg
 TS2.115AbsHwPos_HandwheelPositionLPF_M_str.K=>Min
 TS2.116AbsHwPos_HandwheelPositionLPF_M_str.K=>Max
 TS2.117AbsHwPos_HandwheelPositionLPF_M_str.K=>Pos
 TS2.118Rte_Call_EOLVehCntrOffset_GetErrorStatus=>NVM_REQ_OK
 TS2.119Rte_Call_EOLVehCntrOffset_GetErrorStatus=>NVM_REQ_NOT_OK
 TS2.120Rte_Call_EOLVehCntrOffset_GetErrorStatus=>NVM_REQ_PENDING
 TS2.121Rte_Call_EOLVehCntrOffset_GetErrorStatus=>NVM_REQ_INTEGRITY_FAILED
 TS2.122Rte_Call_EOLVehCntrOffset_GetErrorStatus=>NVM_REQ_BLOCK_SKIPPED
 TS2.123Rte_Call_EOLVehCntrOffset_GetErrorStatus=>NVM_REQ_NV_INVALIDATED

Test Step 2.1 (Repeat Count = 1)

| Name | Input Value |
|--|---|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -10125 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -11725 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -11725 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -10125 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -11725 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -11725 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1600 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0 |
| k_HwPosOutputLPFError_HwDeg_f32 | 0 |
| k_I2CHwAuthority_Uls_f32 | 0 |
| k_MaxSensorlessAuthority_Uls_f32 | 0 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 0 |
| k_MinSensorlessAuthority_Uls_f32 | 0 |
| k_TurnsCntrAuthority_Uls_f32 | 0 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 0 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -900 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -1600 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 0 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -1600 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 0 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|-----------------------------|---------------------------|--------|
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -10125 | -10125 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -11725 | -11725 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1 | 1 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -11725 | -11725 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -11725 | -11725 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -11725 | -11725 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1600 | -1600 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 0 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0 | 0 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -1600 | -1600 ± 0.009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.2 (Repeat Count = 1) | |
|--|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 10125 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 1 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 11725 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 1 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 11725 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 10125 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 11725 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 1 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 11725 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 1 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1620 |
| k_I2CHwAuthority_Uls_f32 | 1 |
| k_MaxSensorlessAuthority_Uls_f32 | 1 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1600 |
| k_MinSensorlessAuthority_Uls_f32 | 1 |
| k_TurnsCntrAuthority_Uls_f32 | 1 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1600 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 900 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 1 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 1600 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 255 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 43690 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 5 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|---|--|--------------------------|--------|
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 10125 | 10125 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 11725 | 11725 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 11725 | 11725 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 11725 | 11725 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0 | 0 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.3 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 128.470001 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.100000001 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 584.900024 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.100000001 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 88 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1487 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 1487 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21849 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 9026.62012 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.100000001 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 2802.53003 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 1328.5 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.100000001 |
| k_HwPosOutputLPFError_HwDeg_f32 | 407 |
| k_I2CHwAuthority_Uls_f32 | 0.100000001 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.100000001 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 110 |
| k_MinSensorlessAuthority_Uls_f32 | 0.100000001 |
| k_TurnsCntrAuthority_Uls_f32 | 0.100000001 |
| k_UseTurnsCntr_Cnt_lgc | 1 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| k_VehCntrOffValidLimit_HwDeg_f32 | 50.5999985 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 100.099998 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.100000001 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 100.099998 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 0 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 90.3000031 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 123 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 1 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|------------------|----------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 1487 | 1487 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 806.663025 | 806.663025 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT | HWPOS_STATE_INIT | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 806.663025 | 806.663025 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 9026.62012 | 9026.62012 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 2802.53003 | 2802.53003 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 1328.5 | 1328.5 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_Igc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0 | 0 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 806.663025 | 806.663025 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 90.3000031 | 90.3000031 ± 0.00009 | ✓ |

Test Step Call Trace ✓

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.4 (Repeat Count = 1) ✓

| Name | Input Value |
|--|------------------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 269.209991 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.200000003 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 605.179993 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.200000003 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 96 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1507.09998 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 1507.09998 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21921 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 8920.66016 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.200000003 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 202.529999 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 1358.69995 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|---------------------------|--------|
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0.200000003 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 436.700012 | | |
| k_I2CHwAuthority_Uls_f32 | 0.200000003 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.109999999 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 139.699997 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.200000003 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.200000003 | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 80.3000031 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 120.199997 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.109999999 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 120.199997 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 255 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 120.5 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 247 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 2 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 1507.09998 | 1507.09998 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 524.650024 | 524.650024 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 524.650024 | 524.650024 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 8920.66016 | 8920.66016 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 202.529999 | 202.529999 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 1358.69995 | 1358.69995 ± 0.009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0 | 0 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 524.650024 | 524.650024 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 120.5 | 120.5 ± 0.0009 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

Test Step 2.5 (Repeat Count = 1)

| Name | Input Value |
|--|---|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -409.950012 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.300000012 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 625.460022 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.300000012 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 104 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1527.19995 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 1527.19995 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21993 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 8814.7002 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.300000012 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 9802.53027 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 1388.90002 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.300000012 |
| k_HwPosOutputLPFError_HwDeg_f32 | 466.399994 |
| k_I2CHwAuthority_Uls_f32 | 0.300000012 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.119999997 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 169.399994 |
| k_MinSensorlessAuthority_Uls_f32 | 0.300000012 |
| k_TurnsCntrAuthority_Uls_f32 | 0.300000012 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 110 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 140.300003 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.119999997 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 140.300003 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 100 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 150.699997 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 371 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 3 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|-------------------------|-------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 1527.19995 | 1527.19995 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 3378.58105 | 3378.58105 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 3378.58105 | 3378.58105 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 8814.7002 | 8814.7002 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 9802.53027 | 9802.53027 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 1388.90002 | 1388.90002 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0 | 0 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|---------------------|--------|
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 150.699997 | 150.699997 ± 0.0009 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.6 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|--------------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 550.690002 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.400000006 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 645.73999 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.400000006 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 112 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | 5 | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 157.300003 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 157.300003 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 22065 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -8708.74023 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.400000006 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 3224.75 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 1419.09998 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0.400000006 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 496.100006 | | |
| k_I2CHwAuthority_Uls_f32 | 0.400000006 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.129999995 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 199.100006 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.400000006 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.400000006 | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 139.699997 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 160.399994 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.129999995 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 160.399994 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 10 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 180.899994 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 495 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 4 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 157.300003 | 157.300003 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1677.34399 | 1677.34399 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1677.34399 | 1677.34399 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|--------------------|--------|
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 3224.75 | 3224.75 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 1419.09998 | 1419.09998 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0 | 0 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.7 (Repeat Count = 1)

| Name | Input Value |
|--|---|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 691.429993 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.5 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -666.02002 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.5 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 120 |
| AbsHwPos_HwPosState_Cnt_M_enum | 6 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 567.400024 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 567.400024 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 22137 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 8602.78027 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.5 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 120.199997 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -980.599976 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.5 |
| k_HwPosOutputLPFError_HwDeg_f32 | 525.799988 |
| k_I2CHwAuthority_Uls_f32 | 0.5 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.140000001 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 228.800003 |
| k_MinSensorlessAuthority_Uls_f32 | 0.5 |
| k_TurnsCntrAuthority_Uls_f32 | 0.5 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 169.399994 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 180.5 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.140000001 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 180.5 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 14 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 211.100006 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 619 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 5 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|---|--|--------------------------|--------|
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 567.400024 | 567.400024 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.5 | 0.5 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 120.199997 | 120.199997 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 120 | 120 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 120.199997 | 120.199997 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 22137 | 22137 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 8602.78027 | 8602.78027 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.5 | 0.5 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 120.199997 | 120.199997 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -980.599976 | -980.599976 ± 0.0009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.5 | 0.5 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 120.199997 | 120.199997 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 120 | 120 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 22137 | 22137 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 211.100006 | 211.100006 ± 0.0009 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.8 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -832.169983 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.600000024 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 686.299988 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.600000024 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 128 |
| AbsHwPos_HwPosState_Cnt_M_enum | 7 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1587.5 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 1587.5 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 22209 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 8496.82031 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.600000024 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 140.300003 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -960.900024 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.600000024 |
| k_HwPosOutputLPFError_HwDeg_f32 | 555.5 |
| k_I2CHwAuthority_Uls_f32 | 0.600000024 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.150000006 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 258.5 |
| k_MinSensorlessAuthority_Uls_f32 | 0.600000024 |
| k_TurnsCntrAuthority_Uls_f32 | 0.600000024 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 199.100006 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 200.600006 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.150000006 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 200.600006 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 20 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 241.300003 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|--------------------------|--------|
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 743 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 0 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_u16 | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 1587.5 | 1587.5 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 358.699982 | 358.700012 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 358.699982 | 358.700012 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 140.300003 | 140.300003 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -960.900024 | -960.900024 ± 0.0009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0 | 0 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 358.699982 | 358.700012 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.9 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 972.909973 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.699999988 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 706.580017 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.699999988 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 136 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -1000.29999 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -1000.29999 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 22281 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -8390.86035 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.699999988 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 160.399994 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -941.200012 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.699999988 |
| k_HwPosOutputLPFError_HwDeg_f32 | 585.200012 |
| k_I2CHwAuthority_Uls_f32 | 0.699999988 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| k_MaxSensorlessAuthority_Uls_f32 | 0.159999999 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 288.200012 |
| k_MinSensorlessAuthority_Uls_f32 | 0.699999988 |
| k_TurnsCntrAuthority_Uls_f32 | 0.699999988 |
| k_UseTurnsCntr_Cnt_Igc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 228.800003 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 220.699997 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.159999999 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 220.699997 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 26 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 271.5 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 867 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 1 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|---------------------------|---------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -1000.29999 | -1000.29999 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 324.253998 | 324.253998 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 324.253998 | 324.253998 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -8390.86035 | -8390.86035 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 160.399994 | 160.399994 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -941.200012 | -941.200012 ± 0.0009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_Igc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0 | 0 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 324.253998 | 324.253998 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 271.5 | 271.5 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.10 (Repeat Count = 1) | |
|--|-------------|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 1113.65002 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.800000012 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -726.859985 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.800000012 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 144 |
| AbsHwPos_HwPosState_Cnt_M_enum | 9 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -980.599976 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -980.599976 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 22353 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|--------------------------|--------|
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 8284.88965 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.800000012 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 180.5 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 603.700012 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0.800000012 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 614.900024 | | |
| k_I2CHwAuthority_Uls_f32 | 0.800000012 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.170000002 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 317.899994 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.800000012 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.800000012 | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 258.5 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 240.800003 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.170000002 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 240.800003 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 32 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 301.700012 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 991 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 2 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -980.599976 | -980.599976 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -0.971984863 | -0.972000003 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -0.971984863 | -0.972000003 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 180.5 | 180.5 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 603.700012 | 603.700012 ± 0.0009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0 | 0 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -0.971984863 | -0.972000003 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 301.700012 | 301.700012 ± 0.0009 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Test Step 2.11 (Repeat Count = 1) | | | | ✓ |
|--|--|---|---------------------------|--------|
| Name | | Input Value | | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | -1254.39001 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | | 0.899999976 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | | 747.140015 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | | 0.899999976 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | | 152 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | | HWPOS_STATE_INIT | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | | -960.900024 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | -960.900024 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | | 22425 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | | 8178.93018 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | | 0.899999976 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | | 200.600006 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | 633.900024 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | | 0 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 0 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | | 0.899999976 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | | 644.599976 | | |
| k_I2CHwAuthority_Uls_f32 | | 0.899999976 | | |
| k_MaxSensorlessAuthority_Uls_f32 | | 0.180000007 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | | 347.600006 | | |
| k_MinSensorlessAuthority_Uls_f32 | | 0.899999976 | | |
| k_TurnsCntrAuthority_Uls_f32 | | 0.899999976 | | |
| k_UseTurnsCntr_Cnt_lgc | | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | | 288.200012 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | | 260.899994 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | | 2 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | | 0.180000007 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | | 260.899994 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | | 38 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | | 331.899994 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | | 1115 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | 3 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | | target_Pim_EOLVehCntrOffset | | |
| Name | | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | | -960.900024 | -960.900024 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | | 255.253998 | 255.253998 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | | 255.253998 | 255.253998 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | | 65534 | 65534 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | | 8178.93018 | 8178.93018 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | | 200.600006 | 200.600006 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | 633.900024 | 633.900024 ± 0.0009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | | 0 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | | 0 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | | 0 | 0 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | | 255.253998 | 255.253998 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | | 65534 | 65534 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|---------------------|--------|
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 331.899994 | 331.899994 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.12 (Repeat Count = 1) | | | | |
|--|-------------------------|---|--------|--|
| Name | | Input Value | | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | 1395.13 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | | 0.109999999 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | | 767.419983 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | | 0.109999999 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | | 160 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | | HWPOS_STATE_INIT | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | | 1547.30005 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | 1547.30005 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | | 22497 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | | -8072.97021 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | | 0.109999999 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | | 220.699997 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | 664.099976 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | | 1 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 1 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | | 0.100000001 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | | 674.299988 | | |
| k_I2CHwAuthority_Uls_f32 | | 0.100000001 | | |
| k_MaxSensorlessAuthority_Uls_f32 | | 0.189999998 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | | 377.299988 | | |
| k_MinSensorlessAuthority_Uls_f32 | | 0.100000001 | | |
| k_TurnsCntrAuthority_Uls_f32 | | 0.100000001 | | |
| k_UseTurnsCntr_Cnt_lgc | | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | | 317.899994 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | | 281 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | | 0 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | | 0.189999998 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | | 281 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | | 44 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | | 362.100006 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | | 1239 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | 4 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result | |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 1547.30005 | 1547.30005 ± 0.0009 | ✔ | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.100000001 | 0.100000001 ± 0.004 | ✔ | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 713.913757 | 713.913818 ± 0.1 | ✔ | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 2 | 2 | ✔ | |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✔ | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 713.913757 | 713.913818 ± 0.1 | ✔ | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✔ | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 281 | 281 ± 0.1 | ✔ | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.100000001 | 0.100000001 ± 0.004 | ✔ | |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|---------------------|--------|
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 281 | 281 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 664.099976 | 664.099976 ± 0.0009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.100000001 | 0.100000001 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 713.913757 | 713.913818 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 2 | 2 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 281 | 281 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

| Test Step Call Trace | | | | | ✓ |
|--|-------|--|-------|--------|---|
| Actual Function | Count | Expected Function | Count | Result | |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ | |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ | |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ | |

| Test Step 2.13 (Repeat Count = 1) | | ✓ |
|--|---|---|
| Name | Input Value | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 1535.87 | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.119999997 | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 787.700012 | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.119999997 | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 168 | |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1567.40002 | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 1567.40002 | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 22569 | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 7967.00977 | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.119999997 | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 240.800003 | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 694.299988 | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | |
| k_HwPosAuthorityStep_Uls_f32 | 0.109999999 | |
| k_HwPosOutputLPFError_HwDeg_f32 | 704 | |
| k_I2CHwAuthority_Uls_f32 | 0.109999999 | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.200000003 | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 407 | |
| k_MinSensorlessAuthority_Uls_f32 | 0.109999999 | |
| k_TurnsCntrAuthority_Uls_f32 | 0.109999999 | |
| k_UseTurnsCntr_Cnt_lgc | 0 | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 347.600006 | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 301.100006 | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0 | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 301.100006 | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 50 | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 392.299988 | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 1363 | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 5 | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|---|--|---------------------------|--------|
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 1567.40002 | 1567.40002 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.00999999791 | 0.0099999978 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 722.072021 | 722.072021 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 168 | 168 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 722.072021 | 722.072021 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 7967.00977 | 7967.00977 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 240.800003 | 240.800003 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 694.299988 | 694.299988 ± 0.0009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.00999999791 | 0.0099999978 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 722.072021 | 722.072021 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 168 | 168 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 392.299988 | 392.299988 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.14 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -1676.60999 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.129999995 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -807.97998 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.129999995 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 176 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -901.799988 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -901.799988 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 22641 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 7861.0498 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.129999995 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 260.899994 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 724.5 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.119999997 |
| k_HwPosOutputLPFError_HwDeg_f32 | 733.700012 |
| k_I2CHwAuthority_Uls_f32 | 0.119999997 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.209999993 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 436.700012 |
| k_MinSensorlessAuthority_Uls_f32 | 0.119999997 |
| k_TurnsCntrAuthority_Uls_f32 | 0.119999997 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 377.299988 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 321.200012 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 1 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 321.200012 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 100 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 422.5 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 1487 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|---|---|-------------------------|--------|
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 0 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_C | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -901.799988 | -901.799988 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.119999997 | 0.119999997 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -1015.08685 | -1015.08691 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1 | 1 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -1015.08685 | -1015.08691 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -2401.10986 | -2401.11011 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.119999997 | 0.119999997 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -2401.10986 | -2401.11011 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 724.5 | 724.5 ± 0.0009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.119999997 | 0.119999997 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -1015.08685 | -1015.08691 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 1 | 1 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.15 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 11535.87 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.119999997 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 787.700012 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.119999997 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 168 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1567.40002 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 1567.40002 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 22569 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 7967.00977 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.119999997 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 240.800003 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 694.299988 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.109999999 |
| k_HwPosOutputLPFError_HwDeg_f32 | 704 |
| k_I2CHwAuthority_Uls_f32 | 0.109999999 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.319999993 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|---------------------------|--------|
| k_MaxVehCntrOffDiff_HwDeg_f32 | 407 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.109999999 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.109999999 | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 347.600006 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 301.100006 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.200000003 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 301.100006 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 50 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 392.299988 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 1363 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 5 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 1567.40002 | 1567.40002 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.200000003 | 0.200000003 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 729.307983 | 729.307983 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3 | 3 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 729.307983 | 729.307983 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 301.100006 | 301.100006 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.200000003 | 0.200000003 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 301.100006 | 301.100006 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 694.299988 | 694.299988 ± 0.0009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.200000003 | 0.200000003 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 729.307983 | 729.307983 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 3 | 3 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 301.100006 | 301.100006 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 392.299988 | 392.299988 ± 0.0009 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.16 (Repeat Count = 1)

| Name | Input Value |
|--|-------------------------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 1958.08997 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.150000006 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 848.539978 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.150000006 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 192 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 126.699997 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 126.699997 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 22785 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 269.209991 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.150000006 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 301.100006 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 784.900024 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.140000001 |
| k_HwPosOutputLPFError_HwDeg_f32 | 793.099976 |
| k_I2CHwAuthority_Uls_f32 | 0.140000001 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.230000004 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 496.100006 |
| k_MinSensorlessAuthority_Uls_f32 | 0.140000001 |
| k_TurnsCntrAuthority_Uls_f32 | 0.140000001 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 436.700012 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 461.899994 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.230000004 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -1600 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 68 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 482.899994 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 1735 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 2 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|---------------------------|---------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 126.699997 | 126.699997 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.150000006 | 0.150000006 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 301.100006 | 301.100006 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 192 | 192 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 301.100006 | 301.100006 ± 0.1 | ✓ |
| AbsHwPos_SrComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_SrComHwPos_HwDeg_M_f32 | 269.209991 | 269.209991 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.150000006 | 0.150000006 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 301.100006 | 301.100006 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 784.900024 | 784.900024 ± 0.0009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.150000006 | 0.150000006 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 301.100006 | 301.100006 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 192 | 192 | ✓ |
| target_AbsHwPos_Per2_SrComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrComHwPos_HwDeg_f32.value | 269.209991 | 269.209991 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 482.899994 | 482.899994 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

Test Step 2.17 (Repeat Count = 1)

| Name | Input Value |
|--|---|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 2098.83008 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.159999996 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -868.820007 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.159999996 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 5200 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 139.100006 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 139.100006 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 22857 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -409.950012 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.159999996 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -6321.02979 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 815.099976 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.150000006 |
| k_HwPosOutputLPFError_HwDeg_f32 | 822.799988 |
| k_I2CHwAuthority_Uls_f32 | 0.150000006 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.239999995 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 525.799988 |
| k_MinSensorlessAuthority_Uls_f32 | 0.150000006 |
| k_TurnsCntrAuthority_Uls_f32 | 0.150000006 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 466.399994 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 482 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.239999995 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 1600 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 74 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 513.099976 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 1859 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 3 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|---------------------------|---------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 139.100006 | 139.100006 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.239999995 | 0.239999995 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -473.808807 | -473.808807 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3 | 3 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -473.808807 | -473.808807 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 1600 | 1600 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.239999995 | 0.239999995 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 1600 | 1600 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 815.099976 | 815.099976 ± 0.0009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.239999995 | 0.239999995 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -473.808807 | -473.808807 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 3 | 3 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|---------------------|--------|
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 513.099976 | 513.099976 ± 0.0009 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.18 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|---------------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -2239.57007 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.170000002 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 889.099976 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.170000002 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 208 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 151.5 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 151.5 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 22929 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 550.690002 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.170000002 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 6461.77002 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 845.299988 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0.159999996 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 852.5 | | |
| k_I2CHwAuthority_Uls_f32 | 0.159999996 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.25 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 555.5 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.159999996 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.159999996 | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 496.100006 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 502.100006 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.25 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 0 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 80 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 543.299988 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 1983 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 4 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 151.5 | 151.5 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.25 | 0.25 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3 | 3 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.25 | 0.25 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|----------------|--------|
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.25 | 0.25 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 3 | 3 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.19 (Repeat Count = 1) | |
|--|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 2380.31006 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.180000007 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 909.380005 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.180000007 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 216 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 163.899994 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 163.899994 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 23001 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 691.429993 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.180000007 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 6602.50977 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 875.5 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.170000002 |
| k_HwPosOutputLPFError_HwDeg_f32 | 882.200012 |
| k_I2CHwAuthority_Uls_f32 | 0.170000002 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.25999999 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 585.200012 |
| k_MinSensorlessAuthority_Uls_f32 | 0.170000002 |
| k_TurnsCntrAuthority_Uls_f32 | 0.170000002 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 525.799988 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 522.200012 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.25999999 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 421.700012 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 86 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 573.5 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 2107 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 5 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | Expected Value | Result |
|---|--|---------------------------|--------|
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 163.899994 | 163.899994 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.180000007 | 0.180000007 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1934.14343 | 1934.14343 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 216 | 216 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1934.14343 | 1934.14343 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 691.429993 | 691.429993 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.180000007 | 0.180000007 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 6602.50977 | 6602.50977 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 875.5 | 875.5 ± 0.0009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.180000007 | 0.180000007 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 216 | 216 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 691.429993 | 691.429993 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.20 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -2239.57007 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.170000002 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 889.099976 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.170000002 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 208 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 151.5 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 151.5 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 22929 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 550.690002 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.170000002 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 6461.77002 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 845.299988 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.159999996 |
| k_HwPosOutputLPFError_HwDeg_f32 | 852.5 |
| k_I2CHwAuthority_Uls_f32 | 0.159999996 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.25 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 555.5 |
| k_MinSensorlessAuthority_Uls_f32 | 0.159999996 |
| k_TurnsCntrAuthority_Uls_f32 | 0.159999996 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 496.100006 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 502.100006 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.25 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -500.600006 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 80 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 1983 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 4 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_u16 | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|---------------------------|---------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 151.5 | 151.5 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.25 | 0.25 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -500.600006 | -500.600006 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3 | 3 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -500.600006 | -500.600006 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -500.600006 | -500.600006 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.25 | 0.25 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -500.600006 | -500.600006 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.25 | 0.25 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -500.600006 | -500.600006 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 3 | 3 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | -500.600006 | -500.600006 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.21 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -2661.79004 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.200000003 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 949.940002 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.200000003 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 232 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -941.200012 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -941.200012 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 23145 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 972.909973 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.200000003 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 6883.99023 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 935.900024 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.189999998 |
| k_HwPosOutputLPFError_HwDeg_f32 | 941.599976 |
| k_I2CHwAuthority_Uls_f32 | 0.189999998 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.280000001 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| k_MaxVehCntrOffDiff_HwDeg_f32 | 644.599976 |
| k_MinSensorlessAuthority_Uls_f32 | 0.189999998 |
| k_TurnsCntrAuthority_Uls_f32 | 0.189999998 |
| k_UseTurnsCntr_Cnt_Igc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 585.200012 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -900 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.280000001 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 461.899994 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 98 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 633.900024 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 2355 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 1 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|-------------------------|-------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -941.200012 | -941.200012 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.189999998 | 0.189999998 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -900 | -900 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 2 | 2 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -900 | -900 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -900 | -900 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.189999998 | 0.189999998 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -900 | -900 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 935.900024 | 935.900024 ± 0.0009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_Igc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.189999998 | 0.189999998 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -900 | -900 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 2 | 2 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | -900 | -900 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 633.900024 | 633.900024 ± 0.0009 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.22 (Repeat Count = 1)

| Name | Input Value |
|--|---------------------------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 2802.53003 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.209999993 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 970.219971 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.209999993 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 240 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -921.5 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -921.5 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 23217 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 1113.65002 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.209999993 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 7024.72998 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 966.099976 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.200000003 |
| k_HwPosOutputLPFError_HwDeg_f32 | 971.299988 |
| k_I2CHwAuthority_Uls_f32 | 0.200000003 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.289999992 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 674.299988 |
| k_MinSensorlessAuthority_Uls_f32 | 0.200000003 |
| k_TurnsCntrAuthority_Uls_f32 | 0.200000003 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 614.900024 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 900 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.289999992 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 482 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 104 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 664.099976 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 2479 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 2 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|-------------------------|-------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -921.5 | -921.5 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.200000003 | 0.200000003 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 955.473755 | 955.473816 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 2 | 2 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 955.473755 | 955.473816 ± 0.1 | ✓ |
| AbsHwPos_SrComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✓ |
| AbsHwPos_SrComHwPos_HwDeg_M_f32 | 900 | 900 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.200000003 | 0.200000003 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 900 | 900 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 966.099976 | 966.099976 ± 0.0009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.200000003 | 0.200000003 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 955.473755 | 955.473816 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 2 | 2 | ✓ |
| target_AbsHwPos_Per2_SrComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ |
| target_AbsHwPos_Per2_SrComHwPos_HwDeg_f32.value | 900 | 900 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 664.099976 | 664.099976 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

Test Step 2.23 (Repeat Count = 1)

| Name | Input Value |
|--|---|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 2943.27002 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.219999999 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -990.5 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.219999999 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 248 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 324.5 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 324.5 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 23289 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -1254.39001 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.219999999 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 7165.47021 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 996.299988 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.209999993 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1001 |
| k_I2CHwAuthority_Uls_f32 | 0.209999993 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.300000012 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 704 |
| k_MinSensorlessAuthority_Uls_f32 | 0.209999993 |
| k_TurnsCntrAuthority_Uls_f32 | 0.209999993 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 644.599976 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 0 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.300000012 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 502.100006 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 110 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 694.299988 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 2603 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 3 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|-------------------------|-------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 324.5 | 324.5 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.209999993 | 0.209999993 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 2 | 2 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.209999993 | 0.209999993 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 996.299988 | 996.299988 ± 0.0009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.209999993 | 0.209999993 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 2 | 2 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|---------------------|--------|
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 694.299988 | 694.299988 ± 0.0009 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.24 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|-------------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -3084.01001 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.230000004 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1010.78003 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.230000004 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 256 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 471.600006 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 471.600006 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 23361 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 1395.13 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.230000004 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 7306.20996 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 1026.5 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0.219999999 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 1030.69995 | | |
| k_I2CHwAuthority_Uls_f32 | 0.219999999 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.310000002 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 733.700012 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.219999999 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.219999999 | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 674.299988 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 100.300003 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.310000002 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 522.200012 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 116 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 724.5 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 2727 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 4 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 471.600006 | 471.600006 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.219999999 | 0.219999999 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 100.300003 | 100.300003 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 2 | 2 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 100.300003 | 100.300003 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 100.300003 | 100.300003 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.219999999 | 0.219999999 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 100.300003 | 100.300003 ± 0.1 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|---------------------|--------|
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 1026.5 | 1026.5 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.219999999 | 0.219999999 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 100.300003 | 100.300003 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 2 | 2 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 100.300003 | 100.300003 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.25 (Repeat Count = 1) | |
|--|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 3224.75 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.239999995 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1031.06006 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.239999995 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 264 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -488.100006 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -488.100006 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 23433 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 1535.87 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.239999995 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -7446.9502 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 1056.69995 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.230000004 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1060.40002 |
| k_I2CHwAuthority_Uls_f32 | 0.230000004 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.319999993 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 763.400024 |
| k_MinSensorlessAuthority_Uls_f32 | 0.230000004 |
| k_TurnsCntrAuthority_Uls_f32 | 0.230000004 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 704 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -350.200012 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.319999993 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 542.299988 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 122 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 754.700012 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 2851 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 5 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|---|--|-------------------------|--------|
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -488.100006 | -488.100006 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.230000004 | 0.230000004 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -350.200012 | -350.200012 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 2 | 2 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -350.200012 | -350.200012 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -350.200012 | -350.200012 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.230000004 | 0.230000004 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -350.200012 | -350.200012 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 1056.69995 | 1056.69995 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.230000004 | 0.230000004 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -350.200012 | -350.200012 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 2 | 2 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | -350.200012 | -350.200012 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 754.700012 | 754.700012 ± 0.0009 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.26 (Repeat Count = 1)

| Name | Input Value |
|---|---|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 3365.48999 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.25 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1051.33997 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.25 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 272 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 765.799988 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 765.799988 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 23505 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -1676.60999 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.25 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 7587.68994 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 1086.90002 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.239999995 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1090.09998 |
| k_I2CHwAuthority_Uls_f32 | 0.239999995 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.330000013 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 793.099976 |
| k_MinSensorlessAuthority_Uls_f32 | 0.239999995 |
| k_TurnsCntrAuthority_Uls_f32 | 0.239999995 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 733.700012 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -330.5 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.330000013 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 562.400024 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 128 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 784.900024 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 2975 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 0 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_C | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|-------------------------|-------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 765.799988 | 765.799988 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.239999995 | 0.239999995 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 705.880005 | 705.880005 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 2 | 2 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 705.880005 | 705.880005 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -330.5 | -330.5 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.239999995 | 0.239999995 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -330.5 | -330.5 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 1086.90002 | 1086.90002 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.239999995 | 0.239999995 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 705.880005 | 705.880005 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 2 | 2 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | -330.5 | -330.5 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.27 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -2239.57007 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.170000002 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 889.099976 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.170000002 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 208 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 151.5 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 151.5 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 22929 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 550.690002 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.170000002 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 6461.77002 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 845.299988 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.159999996 |
| k_HwPosOutputLPFError_HwDeg_f32 | 852.5 |
| k_I2CHwAuthority_Uls_f32 | 0.159999996 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.25 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 555.5 |
| k_MinSensorlessAuthority_Uls_f32 | 0.159999996 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| k_TurnsCntrAuthority_Uls_f32 | 0.159999996 |
| k_UseTurnsCntr_Cnt_Igc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 496.100006 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 502.100006 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.25 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 0 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 80 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 543.299988 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 1983 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 2 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|---------------------------|---------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 151.5 | 151.5 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.25 | 0.25 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3 | 3 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.25 | 0.25 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_Igc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.25 | 0.25 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 3 | 3 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 543.299988 | 543.299988 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.28 (Repeat Count = 1) | |
|--|-------------------------|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 3646.96997 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.270000011 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1091.90002 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.270000011 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 2288 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1060 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 1060 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 23649 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 1958.08997 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.270000011 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -7869.16992 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 9238.54004 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.25999999 |
| k_HwPosOutputLPFEError_HwDeg_f32 | 1149.5 |
| k_I2CHwAuthority_Uls_f32 | 0.25999999 |
| k_MaxSensorlessAuthority_Uls_f32 | 0 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 852.5 |
| k_MinSensorlessAuthority_Uls_f32 | 0.25999999 |
| k_TurnsCntrAuthority_Uls_f32 | 0.25999999 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 793.099976 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -291.100006 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.349999994 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 602.599976 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 100 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 845.299988 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 3223 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 2 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrIComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrIComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrIComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrIComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|-------------------------|-------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 1060 | 1060 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.25999999 | 0.25999999 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -712.636963 | -712.636902 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1 | 1 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -712.636963 | -712.636902 ± 0.1 | ✓ |
| AbsHwPos_SrIComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✓ |
| AbsHwPos_SrIComHwPos_HwDeg_M_f32 | -5591.57031 | -5591.56982 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.25999999 | 0.25999999 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -5591.57031 | -5591.56982 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 9238.54004 | 9238.54004 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.25999999 | 0.25999999 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -712.636963 | -712.636902 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_SrIComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ |
| target_AbsHwPos_Per2_SrIComHwPos_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 845.299988 | 845.299988 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

Test Step 2.29 (Repeat Count = 1)

| Name | Input Value |
|--|---|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 3787.70996 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.280000001 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1112.18005 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.280000001 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 296 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1207.09998 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 1207.09998 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 23721 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 2098.83008 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.280000001 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 8009.91016 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1132.57996 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.270000011 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1179.19995 |
| k_I2CHwAuthority_Uls_f32 | 0.270000011 |
| k_MaxSensorlessAuthority_Uls_f32 | 1 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 882.200012 |
| k_MinSensorlessAuthority_Uls_f32 | 0.270000011 |
| k_TurnsCntrAuthority_Uls_f32 | 0.270000011 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 822.799988 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -271.399994 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.360000014 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 622.700012 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 100 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 875.5 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 3347 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 3 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|-------------------------|-------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 1207.09998 | 1207.09998 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.360000014 | 0.360000014 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 622.700012 | 622.700012 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3 | 3 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 622.700012 | 622.700012 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 2098.83008 | 2098.83008 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.360000014 | 0.360000014 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 622.700012 | 622.700012 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1132.57996 | -1132.57996 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.360000014 | 0.360000014 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 622.700012 | 622.700012 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 3 | 3 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|----------------|--------|
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 875.5 | 875.5 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.30 (Repeat Count = 1) | | | | ✔ |
|--|-------------------------|---|--------|---|
| Name | | Input Value | | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | -1676.60999 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | | 0.129999995 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | | -807.97998 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | | 0.129999995 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | | 176 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | | HWPOS_STATE_SENSORVALID | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | | -901.799988 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | -901.799988 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | | 22641 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | | 7861.0498 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | | 0.129999995 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | | 260.899994 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | 724.5 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | | 1 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 1 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | | 0.119999997 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | | 733.700012 | | |
| k_I2CHwAuthority_Uls_f32 | | 0.119999997 | | |
| k_MaxSensorlessAuthority_Uls_f32 | | 0.209999993 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | | 436.700012 | | |
| k_MinSensorlessAuthority_Uls_f32 | | 0.119999997 | | |
| k_TurnsCntrAuthority_Uls_f32 | | 0.119999997 | | |
| k_UseTurnsCntr_Cnt_lgc | | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | | 377.299988 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | | 321.200012 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | | 2 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | | 0.100000001 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | | 321.200012 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | | 100 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | | 422.5 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | | 1487 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | 0 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result | |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -901.799988 | -901.799988 ± 0.0009 | ✔ | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.119999997 | 0.119999997 ± 0.004 | ✔ | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -1015.08685 | -1015.08691 ± 0.1 | ✔ | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1 | 1 | ✔ | |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✔ | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -1015.08685 | -1015.08691 ± 0.1 | ✔ | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✔ | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -2401.10986 | -2401.11011 ± 0.1 | ✔ | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.119999997 | 0.119999997 ± 0.004 | ✔ | |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|---------------------|--------|
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -2401.10986 | -2401.11011 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 724.5 | 724.5 ± 0.0009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.119999997 | 0.119999997 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -1015.08685 | -1015.08691 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 422.5 | 422.5 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.31 (Repeat Count = 1) | |
|--|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 4069.18994 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.300000012 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1152.73999 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.300000012 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 4312 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1501.30005 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 1501.30005 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 23865 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 2380.31006 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.300000012 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 8920.66016 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 8920.66016 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.100000001 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1238.59998 |
| k_I2CHwAuthority_Uls_f32 | 0.100000001 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.379999995 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 941.599976 |
| k_MinSensorlessAuthority_Uls_f32 | 0 |
| k_TurnsCntrAuthority_Uls_f32 | 0.100000001 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 882.200012 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -232 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.379999995 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 662.900024 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 158 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 935.900024 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 3595 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 5 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|---------------------------|--------|
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntOffset | target_Pim_EOLVehCntOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 1501.30005 | 1501.30005 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.379999995 | 0.379999995 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 662.900024 | 662.900024 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3 | 3 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 662.900024 | 662.900024 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 662.900024 | 662.900024 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.379999995 | 0.379999995 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 662.900024 | 662.900024 ± 0.1 | ✔ |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | 8920.66016 | 8920.66016 ± 0.009 | ✔ |
| AbsHwPos_VehCntOfstLearn_Cnt_M_lgc | 0 | 0 | ✔ |
| AbsHwPos_VehCntValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.379999995 | 0.379999995 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 662.900024 | 662.900024 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 3 | 3 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 662.900024 | 662.900024 ± 0.1 | ✔ |
| target_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 935.900024 | 935.900024 ± 0.0009 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.32 (Repeat Count = 1) | |
|--|--|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 4209.93018 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.310000002 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1173.02002 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.310000002 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 320 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -448.700012 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -448.700012 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 23937 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 2521.05005 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.310000002 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 8814.7002 |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | 8814.7002 |
| AbsHwPos_VehCntOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.200000003 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1268.30005 |
| k_I2CHwAuthority_Uls_f32 | 0.200000003 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.389999986 |
| k_MaxVehCntOffDiff_HwDeg_f32 | 1060.40002 |
| k_MinSensorlessAuthority_Uls_f32 | 1 |
| k_TurnsCntAuthority_Uls_f32 | 0.200000003 |
| k_UseTurnsCnt_Cnt_lgc | 1 |
| k_VehCntOffValidLimit_HwDeg_f32 | 911.900024 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -212.300003 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.389999986 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 683 |
| target_AbsHwPos_Per2_TurnsCntValidity_Cnt_u08.value | 100 |
| target_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 966.099976 |
| target_Pim_EOLVehCntOffset.EOLHwPosTrimPerformed_Cnt_u16 | 3719 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus_ErrorStatus | 0 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|-------------------------|--------|
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -448.700012 | -448.700012 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.109999999 | 0.109999999 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 3541.94092 | 3541.94092 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 320 | 320 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 3541.94092 | 3541.94092 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 2521.05005 | 2521.05005 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 8814.7002 | 8814.7002 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 8814.7002 | 8814.7002 ± 0.009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.109999999 | 0.109999999 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 320 | 320 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.33 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -4350.66992 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.319999993 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1193.30005 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.319999993 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 328 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -429 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -429 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 24009 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -2661.79004 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.319999993 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -8708.74023 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1208.73999 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.310000002 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1298 |
| k_I2CHwAuthority_Uls_f32 | 0.310000002 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.400000006 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1090.09998 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|---------------------------|--------|
| k_MinSensorlessAuthority_Uls_f32 | 0.310000002 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.310000002 | | |
| k_UseTurnsCntr_Cnt_Igc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 941.599976 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -192.600006 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.400000006 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 703.099976 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 170 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 996.299988 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 3843 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 1 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -429 | -429 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.400000006 | 0.400000006 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 703.099976 | 703.099976 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3 | 3 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 703.099976 | 703.099976 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 703.099976 | 703.099976 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.400000006 | 0.400000006 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 703.099976 | 703.099976 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1208.73999 | -1208.73999 ± 0.009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc | 0 | 0 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_Igc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.400000006 | 0.400000006 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 703.099976 | 703.099976 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 3 | 3 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 703.099976 | 703.099976 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 996.299988 | 996.299988 ± 0.0009 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.34 (Repeat Count = 1) | |
|--|-------------|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 4491.41016 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.330000013 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1213.57996 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.330000013 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 336 |
| AbsHwPos_HwPosState_Cnt_M_enum | 7 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 147.300003 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 147.300003 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 24081 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 2802.53003 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.330000013 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 8602.78027 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 8602.78027 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.319999993 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1327.69995 |
| k_I2CHwAuthority_Uls_f32 | 0.319999993 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.409999996 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1119.80005 |
| k_MinSensorlessAuthority_Uls_f32 | 0.319999993 |
| k_TurnsCntrAuthority_Uls_f32 | 0.319999993 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 0 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -172.899994 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.409999996 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 723.200012 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 176 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 1026.5 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 3967 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 2 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|--------------------------|--------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 147.300003 | 147.300003 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.0100000203 | 0.00999999978 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 3652.01611 | 3652.01611 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 3652.01611 | 3652.01611 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 8602.78027 | 8602.78027 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 8602.78027 | 8602.78027 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.0100000203 | 0.00999999978 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 1026.5 | 1026.5 ± 0.009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

Test Step 2.35 (Repeat Count = 1)

| Name | Input Value |
|--|---|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 4632.1499 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.340000004 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1233.85999 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.340000004 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 344 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 156.899994 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 156.899994 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 24153 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 2943.27002 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.340000004 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 8496.82031 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 8496.82031 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.330000013 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1357.40002 |
| k_I2CHwAuthority_Uls_f32 | 0.330000013 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.419999987 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1149.5 |
| k_MinSensorlessAuthority_Uls_f32 | 0.330000013 |
| k_TurnsCntrAuthority_Uls_f32 | 0.330000013 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1600 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 461.899994 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.419999987 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 743.299988 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 182 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 1056.69995 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 4091 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 3 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|---------------------------|---------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 156.899994 | 156.899994 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.00999999046 | 0.00999999978 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 3703.2666 | 3703.26636 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 3703.2666 | 3703.26636 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 2943.27002 | 2943.27002 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 8496.82031 | 8496.82031 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 8496.82031 | 8496.82031 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.00999999046 | 0.00999999978 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|----------------|--------|
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.36 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|--------------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -4772.89014 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.349999994 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1254.14001 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.349999994 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 352 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | 9 | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 166.5 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 166.5 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 24225 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -3084.01001 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.349999994 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -8390.86035 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1390.85999 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0.340000004 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 1387.09998 | | |
| k_I2CHwAuthority_Uls_f32 | 0.340000004 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.430000007 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1179.19995 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.340000004 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.340000004 | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1030.69995 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 482 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.430000007 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 763.400024 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 188 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 1086.90002 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 4215 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 4 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 166.5 | 166.5 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.009999999046 | 0.00999999978 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -2121.60986 | -2121.61011 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -2121.60986 | -2121.61011 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|---------------|-----------------------|--------|
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -8390.86035 | -8390.86035 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1390.85999 | -1390.85999 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.00999999046 | 0.00999999978 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.37 (Repeat Count = 1)

| Name | Input Value |
|--|---|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 4913.62988 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.360000014 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1274.42004 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.360000014 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 360 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 176.100006 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 176.100006 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 24297 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 3224.75 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.360000014 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 8284.88965 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 8284.88965 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.100000001 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1416.80005 |
| k_I2CHwAuthority_Uls_f32 | 0.100000001 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.439999998 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1208.90002 |
| k_MinSensorlessAuthority_Uls_f32 | 0.100000001 |
| k_TurnsCntrAuthority_Uls_f32 | 0.100000001 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1060.40002 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 502.100006 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.439999998 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 783.5 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 194 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -1600 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 4339 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 5 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|---|--|
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|---------------------------|---------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 176.100006 | 176.100006 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.439999998 | 0.439999998 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 783.5 | 783.5 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3 | 3 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 783.5 | 783.5 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 783.5 | 783.5 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.439999998 | 0.439999998 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 783.5 | 783.5 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.439999998 | 0.439999998 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 783.5 | 783.5 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 3 | 3 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 783.5 | 783.5 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

| Test Step Call Trace | | | | | ✓ |
|--|-------|--|-------|--------|---|
| Actual Function | Count | Expected Function | Count | Result | |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ | |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ | |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ | |

| Test Step 2.38 (Repeat Count = 1) | | | ✓ |
|---|---|--|---|
| Name | Input Value | | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 5054.37012 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.370000005 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1294.69995 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.370000005 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 368 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | 11 | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 185.699997 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 185.699997 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 24369 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 3365.48999 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.370000005 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 8178.93018 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 8178.93018 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0.300000012 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 1446.5 | | |
| k_I2CHwAuthority_Uls_f32 | 0.300000012 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.449999988 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1238.59998 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.300000012 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.300000012 | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1090.09998 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 522.200012 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 | | |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.449999988 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 803.599976 |
| target_AbsHwPos_Per2_TurnsCntValidity_Cnt_u08.value | 200 |
| target_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 65535 |
| target_Pim_EOLVehCntOffset.EOLHwPosTrimPerformed_Cnt_u16 | 4463 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus_ErrorStatus | 0 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_u16 | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_u16 | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntOffset | target_Pim_EOLVehCntOffset |

| Name | Actual Value | Expected Value | Result |
|--|--------------------------|--------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 185.699997 | 185.699997 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.0699999928 | 0.0700000003 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 3841.86523 | 3841.86499 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 3841.86523 | 3841.86499 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 8178.93018 | 8178.93018 ± 0.1 | ✓ |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | 8178.93018 | 8178.93018 ± 0.009 | ✓ |
| AbsHwPos_VehCntOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntValid_Cnt_M_lgc | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.0699999928 | 0.0700000003 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.39 (Repeat Count = 1) | |
|--|--|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 5195.10986 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.379999995 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1314.97998 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.379999995 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 376 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 195.300003 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 195.300003 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 24441 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -3506.22998 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.379999995 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -8072.97021 |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | -1072.96997 |
| AbsHwPos_VehCntOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.370000005 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| k_HwPosOutputLPFError_HwDeg_f32 | 1476.19995 |
| k_I2CHwAuthority_Uls_f32 | 0.370000005 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.460000008 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1268.30005 |
| k_MinSensorlessAuthority_Uls_f32 | 0.370000005 |
| k_TurnsCntrAuthority_Uls_f32 | 0.370000005 |
| k_UseTurnsCntr_Cnt_Igc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1119.80005 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 542.299988 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.460000008 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 823.700012 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 206 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 0 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 4587 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 1 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|---------------------------|---------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 195.300003 | 195.300003 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.460000008 | 0.460000008 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 823.700012 | 823.700012 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3 | 3 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 823.700012 | 823.700012 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 823.700012 | 823.700012 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.460000008 | 0.460000008 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 823.700012 | 823.700012 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_Igc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.460000008 | 0.460000008 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 823.700012 | 823.700012 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 3 | 3 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 823.700012 | 823.700012 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

Test Step Call Trace ✓

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.40 (Repeat Count = 1) ✓

| Name | Input Value |
|--|-------------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -5335.8501 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.389999986 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1487 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.389999986 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|--------------------------|--------|
| AbsHwPos_HwPosSource_Cnt_M_u16 | 6384 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 204.899994 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 204.899994 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 24513 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 3646.96997 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.389999986 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 7967.00977 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 7967.00977 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0.379999995 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 1505.90002 | | |
| k_I2CHwAuthority_Uls_f32 | 0.379999995 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.469999999 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1298 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.379999995 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.379999995 | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1149.5 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 562.400024 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.469999999 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 843.799988 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 212 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 1000.59998 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 4711 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 2 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 204.899994 | 204.899994 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.00999999046 | 0.00999999978 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 4014.20361 | 4014.20386 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 4014.20361 | 4014.20386 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 7967.00977 | 7967.00977 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 7967.00977 | 7967.00977 ± 0.009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.00999999046 | 0.00999999978 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 1000.59998 | 1000.59998 ± 0.009 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.41 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|---------------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 5476.58984 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.400000006 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1507.09998 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.400000006 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 392 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 214.5 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 214.5 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 24585 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 3787.70996 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.400000006 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 7861.0498 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 7861.0498 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0.389999986 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 1535.59998 | | |
| k_I2CHwAuthority_Uls_f32 | 0.389999986 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.479999989 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1327.69995 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.389999986 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.389999986 | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1179.19995 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 582.5 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.479999989 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 863.900024 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 218 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -500.899994 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 4835 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 3 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 214.5 | 214.5 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.479999989 | 0.479999989 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 863.900024 | 863.900024 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3 | 3 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 863.900024 | 863.900024 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 863.900024 | 863.900024 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.479999989 | 0.479999989 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 863.900024 | 863.900024 ± 0.1 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|---------------------|--------|
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.479999989 | 0.479999989 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 863.900024 | 863.900024 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 3 | 3 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 863.900024 | 863.900024 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.42 (Repeat Count = 1) | |
|--|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 1617.32996 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.409999996 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1527.19995 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.409999996 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 400 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 224.100006 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 224.100006 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 24657 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -3928.44995 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.409999996 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -7755.08984 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1455.08997 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.400000006 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1565.30005 |
| k_I2CHwAuthority_Uls_f32 | 0.400000006 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.49000001 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 0 |
| k_MinSensorlessAuthority_Uls_f32 | 0.400000006 |
| k_TurnsCntrAuthority_Uls_f32 | 0.400000006 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1208.90002 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 602.599976 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.49000001 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 884 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 224 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 1268.09998 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 4959 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 4 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|---|--|--------------------------|--------|
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 224.100006 | 224.100006 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.00999999046 | 0.00999999978 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -2278.53882 | -2278.53882 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -2278.53882 | -2278.53882 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -7755.08984 | -7755.08984 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 733.329956 | 733.330017 ± 0.0009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.00999999046 | 0.00999999978 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 733.329956 | 733.330017 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.43 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -558.070007 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.419999987 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 157.300003 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.419999987 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 408 |
| AbsHwPos_HwPosState_Cnt_M_enum | 3 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -389.600006 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -389.600006 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 24729 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 4069.18994 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.419999987 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 269.209991 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 7649.14014 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.409999996 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1595 |
| k_I2CHwAuthority_Uls_f32 | 0.409999996 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.5 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1600 |
| k_MinSensorlessAuthority_Uls_f32 | 0.409999996 |
| k_TurnsCntrAuthority_Uls_f32 | 0.409999996 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1500.34998 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 622.700012 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.5 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 904.099976 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| target_AbsHwPos_Per2_TurnsCntValidity_Cnt_u08.value | 230 |
| target_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 1298.30005 |
| target_Pim_EOLVehCntOffset.EOLHwPosTrimPerformed_Cnt_u16 | 5083 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus_ErrorStatus | 5 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_u16 | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntOffset | target_Pim_EOLVehCntOffset |

| Name | Actual Value | Expected Value | Result |
|--|--------------------------|--------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -389.600006 | -389.600006 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.419999987 | 0.419999987 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 269.209991 | 269.209991 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 408 | 408 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 269.209991 | 269.209991 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 24729 | 24729 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 4069.18994 | 4069.18994 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.419999987 | 0.419999987 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 269.209991 | 269.209991 ± 0.1 | ✓ |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | -1462.16992 | -1462.17004 ± 0.009 | ✓ |
| AbsHwPos_VehCntOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntValid_Cnt_M_lgc | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.419999987 | 0.419999987 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 269.209991 | 269.209991 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 408 | 408 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 24729 | 24729 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | -1462.16992 | -1462.17004 ± 0.009 | ✓ |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.44 (Repeat Count = 1) | |
|--|--|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 898.809998 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.430000007 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 567.400024 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.430000007 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 416 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 243.300003 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 243.300003 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 24801 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 4209.93018 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.430000007 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -409.950012 |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | 7543.18018 |
| AbsHwPos_VehCntOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.419999987 |
| k_HwPosOutputLPFError_HwDeg_f32 | 50.5999985 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| k_I2CHwAuthority_Uls_f32 | 0.419999987 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.50999999 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 5 |
| k_MinSensorlessAuthority_Uls_f32 | 0.419999987 |
| k_TurnsCntrAuthority_Uls_f32 | 0.419999987 |
| k_UseTurnsCntr_Cnt_Igc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1268.30005 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 642.799988 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.50999999 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 924.200012 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 236 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 1328.5 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 5207 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 0 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|--------------------------|--------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 243.300003 | 243.300003 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.0100000203 | 0.0099999978 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 147.139496 | 147.139496 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 147.139496 | 147.139496 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -409.950012 | -409.950012 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -25.3900146 | -25.3999994 ± 0.00009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_Igc | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.0100000203 | 0.0099999978 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 147.139496 | 147.139496 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -25.3900146 | -25.3900146 ± 0.0009 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.45 (Repeat Count = 1)

| Name | Input Value |
|--|-------------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 6039.5498 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.439999998 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1587.5 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.439999998 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 424 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|--------------------------|--------|
| AbsHwPos_HwPosState_Cnt_M_enum | 5 | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 252.899994 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 252.899994 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 24873 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -4350.66992 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.439999998 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 550.690002 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1437.21997 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0.430000007 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 0 | | |
| k_I2CHwAuthority_Uls_f32 | 0.430000007 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.519999981 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1446.5 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.430000007 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.430000007 | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1298 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 662.900024 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.519999981 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 944.299988 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 242 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 1358.69995 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 5331 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 1 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 252.899994 | 252.899994 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.439999998 | 0.439999998 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1131.30359 | 1131.30359 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 424 | 424 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1131.30359 | 1131.30359 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 24873 | 24873 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -4350.66992 | -4350.66992 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.439999998 | 0.439999998 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 550.690002 | 550.690002 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1437.21997 | -1437.21997 ± 0.009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.439999998 | 0.439999998 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1131.30359 | 1131.30359 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 424 | 424 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 24873 | 24873 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 1358.69995 | 1358.69995 ± 0.009 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.46 (Repeat Count = 1)

| Name | Input Value |
|--|---|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 6180.29004 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.449999988 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -1000.29999 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.449999988 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 432 |
| AbsHwPos_HwPosState_Cnt_M_enum | 6 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 262.5 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 262.5 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 24945 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 4491.41016 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.449999988 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 691.429993 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 7331.25977 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.439999998 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1620 |
| k_I2CHwAuthority_Uls_f32 | 0.439999998 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.529999971 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1476.19995 |
| k_MinSensorlessAuthority_Uls_f32 | 0.439999998 |
| k_TurnsCntrAuthority_Uls_f32 | 0.439999998 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1327.69995 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 683 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.529999971 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 964.400024 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 248 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 1388.90002 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 5455 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 2 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|--------------------------|--------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 262.5 | 262.5 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.00999999046 | 0.00999999978 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 691.429993 | 691.429993 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 691.429993 | 691.429993 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 691.429993 | 691.429993 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 7331.25977 | 7331.25977 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|---------------|-----------------------|--------|
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.00999999046 | 0.00999999978 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 691.429993 | 691.429993 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 1388.90002 | 1388.90002 ± 0.009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

Test Step 2.47 (Repeat Count = 1)

| Name | Input Value |
|--|---|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -6321.02979 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.460000008 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -980.599976 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.460000008 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 6440 |
| AbsHwPos_HwPosState_Cnt_M_enum | 7 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 272.100006 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 272.100006 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 25017 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 4632.1499 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.460000008 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -832.169983 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 7225.2998 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.449999988 |
| k_HwPosOutputLPFError_HwDeg_f32 | 5 |
| k_I2CHwAuthority_Uls_f32 | 0.449999988 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.540000021 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1505.90002 |
| k_MinSensorlessAuthority_Uls_f32 | 0.449999988 |
| k_TurnsCntrAuthority_Uls_f32 | 0.449999988 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1357.40002 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 703.099976 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.540000021 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 984.5 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 254 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 1419.09998 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 5579 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 3 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|--------------------------|--------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 272.100006 | 272.100006 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.460000008 | 0.460000008 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -912.322144 | -912.322205 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 6440 | 6440 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -912.322144 | -912.322205 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 25017 | 25017 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 4632.1499 | 4632.1499 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.460000008 | 0.460000008 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -832.169983 | -832.169983 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 7225.2998 | 7225.2998 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.460000008 | 0.460000008 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -912.322144 | -912.322205 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 6440 | 6440 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 25017 | 25017 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|--------------------|--------|
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 1419.09998 | 1419.09998 ± 0.009 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.48 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|--------------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 6461.77002 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.469999999 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -960.900024 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.469999999 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 448 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 281.700012 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 281.700012 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 25089 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -4772.89014 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.469999999 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 972.909973 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1119.32996 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 169.399994 | | |
| k_I2CHwAuthority_Uls_f32 | 0.460000008 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.550000012 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1535.59998 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.460000008 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.460000008 | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1387.09998 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 723.200012 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.550000012 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 1004.59998 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 12 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -980.599976 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 5703 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 4 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 281.700012 | 281.700012 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.469999999 | 0.469999999 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -52.0092773 | -52.0093002 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -52.0092773 | -52.0093002 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 972.909973 | 972.909973 ± 0.1 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|---------------------|--------|
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1119.32996 | -1119.32996 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.469999999 | 0.469999999 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -52.0092773 | -52.0093002 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.49 (Repeat Count = 1) | |
|--|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 6602.50977 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.479999989 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1547.30005 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.479999989 |
| AbsHwPos_HwPosSource_Cnt_u16 | 456 |
| AbsHwPos_HwPosState_Cnt_M_enum | 9 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -350.200012 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -350.200012 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 25161 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 4913.62988 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.479999989 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 1113.65002 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 7013.37988 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 1 |
| k_HwPosOutputLPFError_HwDeg_f32 | 199.100006 |
| k_I2CHwAuthority_Uls_f32 | 0.469999999 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.560000002 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1565.30005 |
| k_MinSensorlessAuthority_Uls_f32 | 0.469999999 |
| k_TurnsCntrAuthority_Uls_f32 | 0.469999999 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1416.80005 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 743.299988 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.560000002 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 1024.69995 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 16 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -960.900024 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 5827 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 5 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | Expected Value | Result |
|---|--|--------------------------|--------|
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -350.200012 | -350.200012 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.479999989 | 0.479999989 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1339.14807 | 1339.14795 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 456 | 456 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1339.14807 | 1339.14795 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 25161 | 25161 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 4913.62988 | 4913.62988 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.479999989 | 0.479999989 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 1113.65002 | 1113.65002 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 7013.37988 | 7013.37988 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.479999989 | 0.479999989 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1339.14807 | 1339.14795 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 456 | 456 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 25161 | 25161 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -960.900024 | -960.900024 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.50 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -6743.25 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.49000001 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1567.40002 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.49000001 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 464 |
| AbsHwPos_HwPosState_Cnt_M_enum | 10 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 300.899994 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 300.899994 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 25233 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 5054.37012 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.49000001 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -1254.39001 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 6907.41992 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.20000003 |
| k_HwPosOutputLPFError_HwDeg_f32 | 228.800003 |
| k_I2CHwAuthority_Uls_f32 | 0.479999989 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.569999993 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1595 |
| k_MinSensorlessAuthority_Uls_f32 | 0.479999989 |
| k_TurnsCntrAuthority_Uls_f32 | 0.479999989 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1446.5 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 763.400024 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.569999993 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 1044.80005 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 20 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -941.200012 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 5951 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|---|---|--------------------------|--------|
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 0 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_ | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 300.899994 | 300.899994 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.290000021 | 0.290000021 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 184.7229 | 184.7229 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 184.7229 | 184.7229 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -1254.39001 | -1254.39001 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 6907.41992 | 6907.41992 ± 0.009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.290000021 | 0.290000021 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 184.7229 | 184.7229 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.51 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 6883.99023 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.5 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -901.799988 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.5 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 472 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 310.5 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 310.5 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 25305 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 5195.10986 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.5 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 1395.13 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 269.209991 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.49000001 |
| k_HwPosOutputLPFError_HwDeg_f32 | 258.5 |
| k_I2CHwAuthority_Uls_f32 | 0 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.579999983 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|-------------------------|--------|
| k_MaxVehCntrOffDiff_HwDeg_f32 | 50.5999985 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.49000001 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.49000001 | | |
| k_UseTurnsCntr_Cnt_Igc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1476.19995 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 783.5 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.579999983 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 1064.90002 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 24 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -921.5 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 6075 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 1 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 310.5 | 310.5 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.00999999046 | 0.00999999978 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -59.1499634 | -59.1500015 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 2 | 2 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -59.1499634 | -59.1500015 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 783.5 | 783.5 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 783.5 | 783.5 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 269.209991 | 269.209991 ± 0.0009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc | 0 | 0 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_Igc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.00999999046 | 0.00999999978 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -59.1499634 | -59.1500015 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 2 | 2 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 783.5 | 783.5 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -921.5 | -921.5 ± 0.0009 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.52 (Repeat Count = 1)

| Name | Input Value |
|--|-------------------------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 7024.72998 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.50999999 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 114.300003 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.50999999 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 480 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 320.100006 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 320.100006 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 25377 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -5335.8501 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.50999999 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 1535.87 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -409.950012 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.5 |
| k_HwPosOutputLPFError_HwDeg_f32 | 288.200012 |
| k_I2CHwAuthority_Uls_f32 | 1 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.589999974 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 80.3000031 |
| k_MinSensorlessAuthority_Uls_f32 | 0.5 |
| k_TurnsCntrAuthority_Uls_f32 | 0.5 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1505.90002 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 803.599976 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.589999974 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 1085 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 28 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -901.799988 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 6199 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 2 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|-------------------------|-------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 320.100006 | 320.100006 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 1 | 1 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 465.843018 | 465.842987 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 2 | 2 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 465.843018 | 465.842987 ± 0.1 | ✓ |
| AbsHwPos_SrComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✓ |
| AbsHwPos_SrComHwPos_HwDeg_M_f32 | 803.599976 | 803.599976 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 1 | 1 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 803.599976 | 803.599976 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -409.950012 | -409.950012 ± 0.0009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 1 | 1 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 465.843018 | 465.842987 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 2 | 2 | ✓ |
| target_AbsHwPos_Per2_SrComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ |
| target_AbsHwPos_Per2_SrComHwPos_HwDeg_f32.value | 803.599976 | 803.599976 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -901.799988 | -901.799988 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Test Step 2.53 (Repeat Count = 1) | | | | ✓ |
|--|-------------------------|---|--------|---|
| Name | | Input Value | | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | 7165.47021 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | | 0.519999981 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | | 126.699997 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | | 0.519999981 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | | 488 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | | HWPOS_STATE_SENSORINVALID | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | | -507.799988 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | -507.799988 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | | 25449 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | | 5476.58984 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | | 0.519999981 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | | -1676.60999 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | 550.690002 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | | 0 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 0 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | | 0.50999999 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | | 317.899994 | | |
| k_I2CHwAuthority_Uls_f32 | | 0.479999989 | | |
| k_MaxSensorlessAuthority_Uls_f32 | | 0.600000024 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | | 110 | | |
| k_MinSensorlessAuthority_Uls_f32 | | 0.50999999 | | |
| k_TurnsCntrAuthority_Uls_f32 | | 0.50999999 | | |
| k_UseTurnsCntr_Cnt_lgc | | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | | 1535.59998 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | | 823.700012 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | | 2 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | | 0.600000024 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | | 1105.09998 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | | 32 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | | -882.099976 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | | 6323 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | 3 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result | |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -507.799988 | -507.799988 ± 0.0009 | ✓ | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.479999989 | 0.479999989 ± 0.004 | ✓ | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 489.139954 | 489.140015 ± 0.1 | ✓ | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 2 | 2 | ✓ | |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✓ | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 489.139954 | 489.140015 ± 0.1 | ✓ | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✓ | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 823.700012 | 823.700012 ± 0.1 | ✓ | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.479999989 | 0.479999989 ± 0.004 | ✓ | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 823.700012 | 823.700012 ± 0.1 | ✓ | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 550.690002 | 550.690002 ± 0.0009 | ✓ | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ | |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.479999989 | 0.479999989 ± 0.004 | ✓ | |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 489.139954 | 489.140015 ± 0.1 | ✓ | |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 2 | 2 | ✓ | |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ | |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 823.700012 | 823.700012 ± 0.1 | ✓ | |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|----------------------|--------|
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -882.099976 | -882.099976 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.54 (Repeat Count = 1) | | | | ✔ |
|--|-------------------------|---|--------|---|
| Name | | Input Value | | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | -7306.20996 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | | 0.529999971 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | | 139.100006 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | | 0.529999971 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | | 9496 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | | HWPOS_STATE_INIT | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | | 389.200012 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | 389.200012 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | | 25521 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | | 5617.33008 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | | 0.529999971 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | | 1817.34998 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | 691.429993 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | | 1 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 1 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | | 0.519999981 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | | 347.600006 | | |
| k_I2CHwAuthority_Uls_f32 | | 0.519999981 | | |
| k_MaxSensorlessAuthority_Uls_f32 | | 0.610000014 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | | 139.699997 | | |
| k_MinSensorlessAuthority_Uls_f32 | | 0.519999981 | | |
| k_TurnsCntrAuthority_Uls_f32 | | 0 | | |
| k_UseTurnsCntr_Cnt_lgc | | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | | 1565.30005 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | | 843.799988 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | | 0 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | | 0.610000014 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | | 1125.19995 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | | 100 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | | -862.400024 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | | 6447 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | 4 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result | |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 389.200012 | 389.200012 ± 0.0009 | ✔ | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.00999999046 | 0.0099999978 ± 0.004 | ✔ | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -4173.37207 | -4173.37207 ± 0.1 | ✔ | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1 | 1 | ✔ | |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✔ | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -4173.37207 | -4173.37207 ± 0.1 | ✔ | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✔ | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -7997.64014 | -7997.64014 ± 0.1 | ✔ | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -7997.64014 | -7997.64014 ± 0.1 | ✔ | |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|---------------|----------------------|--------|
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 691.429993 | 691.429993 ± 0.0009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.00999999046 | 0.0099999978 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.55 (Repeat Count = 1) | |
|--|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 7446.9502 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.540000021 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 151.5 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.540000021 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 504 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -468.399994 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -468.399994 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 25593 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -5758.06982 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.540000021 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 1958.08997 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -832.169983 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.529999971 |
| k_HwPosOutputLPFError_HwDeg_f32 | 377.299988 |
| k_I2CHwAuthority_Uls_f32 | 0.529999971 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.620000005 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 169.399994 |
| k_MinSensorlessAuthority_Uls_f32 | 0.529999971 |
| k_TurnsCntrAuthority_Uls_f32 | 1 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1595 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 863.900024 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.620000005 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 1145.30005 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 100 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -842.700012 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 6571 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 5 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|---|--|-------------------------|--------|
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -468.399994 | -468.399994 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 1 | 1 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 4540.41504 | 4540.41504 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1 | 1 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 4540.41504 | 4540.41504 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 8279.12012 | 8279.12012 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 1 | 1 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 8279.12012 | 8279.12012 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -832.169983 | -832.169983 ± 0.0009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 1 | 1 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -842.700012 | -842.700012 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.56 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 7587.68994 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.550000012 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 163.899994 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.550000012 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 512 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 784.099976 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 7844.1001 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 25665 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 5898.81006 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.550000012 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 2098.83008 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 2521.05005 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.540000021 |
| k_HwPosOutputLPFError_HwDeg_f32 | 50.5999985 |
| k_I2CHwAuthority_Uls_f32 | 0.540000021 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.629999995 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 199.100006 |
| k_MinSensorlessAuthority_Uls_f32 | 0.540000021 |
| k_TurnsCntrAuthority_Uls_f32 | 0.479999989 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 50.5999985 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 884 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.629999995 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 1165.40002 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 100 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -823 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|-------------------------|--------|
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 6695 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 0 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_u16 | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 7844.1001 | 7844.1001 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.479999989 | 0.479999989 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 2860.40674 | 2860.40698 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1 | 1 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 2860.40674 | 2860.40698 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 5066.63965 | 5066.64014 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.479999989 | 0.479999989 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 5066.63965 | 5066.64014 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 2521.05005 | 2521.05005 ± 0.009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.479999989 | 0.479999989 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 1 | 1 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.57 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -7728.43018 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.560000002 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 176.300003 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.560000002 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 520 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 966.200012 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 9665.2002 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 25737 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 6039.5498 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.560000002 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -2239.57007 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1261.79004 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.550000012 |
| k_HwPosOutputLPFError_HwDeg_f32 | 80.3000031 |
| k_I2CHwAuthority_Uls_f32 | 0.550000012 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|---------------------------|--------|
| k_MaxSensorlessAuthority_Uls_f32 | 0.639999986 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 228.800003 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.550000012 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.550000012 | | |
| k_UseTurnsCntr_Cnt_Igc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 80.3000031 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 100.099998 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.639999986 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 1185.5 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 48 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -803.299988 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 1 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 9665.2002 | 9665.2002 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.639999986 | 0.639999986 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 741.452026 | 741.452026 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3 | 3 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 741.452026 | 741.452026 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 1185.5 | 1185.5 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.639999986 | 0.639999986 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 1185.5 | 1185.5 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1261.79004 | -1261.79004 ± 0.009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc | 0 | 0 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_Igc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.639999986 | 0.639999986 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 741.452026 | 741.452026 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 3 | 3 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1185.5 | 1185.5 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -803.299988 | -803.299988 ± 0.0009 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.58 (Repeat Count = 1) | |
|--|-------------|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 7869.16992 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.569999993 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -941.200012 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.569999993 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 528 |
| AbsHwPos_HwPosState_Cnt_M_enum | 5 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -409.299988 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -409.299988 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 25809 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|--------------------------|--------|
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 6180.29004 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.569999993 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 2380.31006 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 2802.53003 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0.560000002 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 110 | | |
| k_I2CHwAuthority_Uls_f32 | 0.560000002 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.649999976 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 258.5 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.560000002 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.560000002 | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 110 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 120.199997 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.649999976 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 1205.59998 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 52 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -783.599976 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 43690 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 2 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -409.299988 | -409.299988 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.00999999046 | 0.0099999978 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 952.06073 | 952.06073 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 952.06073 | 952.06073 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 2380.31006 | 2380.31006 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 2802.53003 | 2802.53003 ± 0.009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.00999999046 | 0.0099999978 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 952.06073 | 952.06073 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -783.599976 | -783.599976 ± 0.0009 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

TEST DETAILS REPORT

AbsHwPos_Per2

2017-02-06, 13:51:17+0530



| Test Step 2.59 (Repeat Count = 1) | | | | ✓ |
|--|--|---|--------------------------|--------|
| Name | | Input Value | | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | 8009.91016 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | | 0.579999983 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | | -921.5 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | | 0.579999983 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | | 536 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | | 6 | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | | 213 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | 213 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | | 25881 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | | -6321.02979 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | | 0.579999983 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | | 2521.05005 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | 2943.27002 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | | 0 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 0 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | | 0.569999993 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | | 139.699997 | | |
| k_I2CHwAuthority_Uls_f32 | | 0.569999993 | | |
| k_MaxSensorlessAuthority_Uls_f32 | | 0.660000026 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | | 288.200012 | | |
| k_MinSensorlessAuthority_Uls_f32 | | 0.569999993 | | |
| k_TurnsCntrAuthority_Uls_f32 | | 0.569999993 | | |
| k_UseTurnsCntr_Cnt_lgc | | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | | 139.699997 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | | 140.300003 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | | 2 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | | 0.660000026 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | | 1225.69995 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | | 56 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | | -763.900024 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | | 7067 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | 3 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | | target_Pim_EOLVehCntrOffset | | |
| Name | | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | | 213 | 213 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | | 0.579999983 | 0.579999983 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | | 1075.17896 | 1075.17896 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | | 536 | 536 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | | 1075.17896 | 1075.17896 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | | 25881 | 25881 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | | -6321.02979 | -6321.02979 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | | 0.579999983 | 0.579999983 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | | 2521.05005 | 2521.05005 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | 2943.27002 | 2943.27002 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | | 0 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | | 0 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | | 0.579999983 | 0.579999983 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | | 1075.17896 | 1075.17896 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | | 536 | 536 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | | 25881 | 25881 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|----------------------|--------|
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -763.900024 | -763.900024 ± 0.0009 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.60 (Repeat Count = 1)

| Name | Input Value |
|--|---|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -10125 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.589999974 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 324.5 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.589999974 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 544 |
| AbsHwPos_HwPosState_Cnt_M_enum | 7 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -369.899994 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -369.899994 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 25953 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 6461.77002 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.589999974 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -2661.79004 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1084.01001 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.579999983 |
| k_HwPosOutputLPFError_HwDeg_f32 | 169.399994 |
| k_I2CHwAuthority_Uls_f32 | 0.579999983 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.670000017 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 317.899994 |
| k_MinSensorlessAuthority_Uls_f32 | 0.579999983 |
| k_TurnsCntrAuthority_Uls_f32 | 0.579999983 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 169.399994 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 160.399994 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.670000017 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 1245.80005 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 60 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -744.200012 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 7191 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 4 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|--------------------------|--------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -369.899994 | -369.899994 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.00999999046 | 0.00999999978 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -1437.41101 | -1437.41113 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -1437.41101 | -1437.41113 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|---------------|----------------------|--------|
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -2661.79004 | -2661.79004 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1084.01001 | -1084.01001 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.00999999046 | 0.0099999978 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -1437.41101 | -1437.41113 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.61 (Repeat Count = 1)

| Name | Input Value |
|--|---|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 10125 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.600000024 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 471.600006 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.600000024 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 7552 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 120.199997 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 120.199997 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 26025 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 6602.50977 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.600000024 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 2802.53003 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 3224.75 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.589999974 |
| k_HwPosOutputLPFError_HwDeg_f32 | 199.100006 |
| k_I2CHwAuthority_Uls_f32 | 0.589999974 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.680000007 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 347.600006 |
| k_MinSensorlessAuthority_Uls_f32 | 0.589999974 |
| k_TurnsCntrAuthority_Uls_f32 | 0.589999974 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 199.100006 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 180.5 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.680000007 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 1265.90002 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 64 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -724.5 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 7315 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 5 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|---|--|---------------------------|--------|
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 120.199997 | 120.199997 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.0100000501 | 0.00999999978 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1870.15796 | 1870.15796 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1870.15796 | 1870.15796 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 6602.50977 | 6602.50977 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 2802.53003 | 2802.53003 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 3224.75 | 3224.75 ± 0.009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.0100000501 | 0.00999999978 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.62 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 0 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.610000014 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -488.100006 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.610000014 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 560 |
| AbsHwPos_HwPosState_Cnt_M_enum | 9 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 140.300003 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 140.300003 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 26097 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -6743.25 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.610000014 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 2943.27002 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 3365.48999 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.600000024 |
| k_HwPosOutputLPFError_HwDeg_f32 | 228.800003 |
| k_I2CHwAuthority_Uls_f32 | 0.600000024 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.689999998 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 377.299988 |
| k_MinSensorlessAuthority_Uls_f32 | 0.600000024 |
| k_TurnsCntrAuthority_Uls_f32 | 0.600000024 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 228.800003 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 200.600006 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.689999998 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 1286 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| target_AbsHwPos_Per2_TurnsCntValidity_Cnt_u08.value | 68 |
| target_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 1872.09998 |
| target_Pim_EOLVehCntOffset.EOLHwPosTrimPerformed_Cnt_u16 | 7439 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus_ErrorStatus | 0 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_u16 | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_u16 | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntOffset | target_Pim_EOLVehCntOffset |

| Name | Actual Value | Expected Value | Result |
|--|--------------------------|--------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 140.300003 | 140.300003 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.00999999046 | 0.00999999978 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1605.03577 | 1605.03564 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1605.03577 | 1605.03564 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 2943.27002 | 2943.27002 ± 0.1 | ✓ |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | 3365.48999 | 3365.48999 ± 0.009 | ✓ |
| AbsHwPos_VehCntOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.00999999046 | 0.00999999978 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.63 (Repeat Count = 1)

| Name | Input Value |
|--|--|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 8572.87012 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.620000005 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 765.799988 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.620000005 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 568 |
| AbsHwPos_HwPosState_Cnt_M_enum | 10 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 160.399994 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 160.399994 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 26169 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 6883.99023 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.620000005 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -3084.01001 |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | -1506.22998 |
| AbsHwPos_VehCntOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.610000014 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| k_HwPosOutputLPFError_HwDeg_f32 | 258.5 |
| k_I2CHwAuthority_Uls_f32 | 0.610000014 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.699999988 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 407 |
| k_MinSensorlessAuthority_Uls_f32 | 0.610000014 |
| k_TurnsCntrAuthority_Uls_f32 | 0.610000014 |
| k_UseTurnsCntr_Cnt_Igc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 258.5 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 220.699997 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.699999988 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 1306.09998 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 72 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 1902.30005 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 7563 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 1 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|--------------------------|--------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 160.399994 | 160.399994 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.620000005 | 0.620000005 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -1621.08228 | -1621.08215 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 568 | 568 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -1621.08228 | -1621.08215 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 26169 | 26169 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 6883.99023 | 6883.99023 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.620000005 | 0.620000005 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -3084.01001 | -3084.01001 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1506.22998 | -1506.22998 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_Igc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.620000005 | 0.620000005 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 568 | 568 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 26169 | 26169 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.64 (Repeat Count = 1) | |
|--|-------------|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -8713.61035 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.629999995 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 912.900024 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.629999995 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 576 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|--------------------------|--------|
| AbsHwPos_HwPosState_Cnt_M_enum | 11 | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 180.5 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 180.5 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 26241 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 7024.72998 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.629999995 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 3224.75 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 3646.96997 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0.620000005 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 288.200012 | | |
| k_I2CHwAuthority_Uls_f32 | 0.620000005 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.709999979 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 436.700012 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.620000005 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.620000005 | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 288.200012 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 240.800003 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.709999979 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 1326.19995 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 76 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 1932.5 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 7687 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 2 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 180.5 | 180.5 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.00999999046 | 0.00999999978 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 2369.36572 | 2369.36548 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 2369.36572 | 2369.36548 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 3224.75 | 3224.75 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 3646.96997 | 3646.96997 ± 0.009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.00999999046 | 0.00999999978 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 1932.5 | 1932.5 ± 0.009 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.65 (Repeat Count = 1) | | | | |
|--|--------------------------|---|--------|--|
| Name | | Input Value | | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | -9558.0498 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | | 0 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | | -429 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | | 0.689999998 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | | 624 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | | HWPOS_STATE_SENSORINVALID | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | | 301.100006 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | 301.100006 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | | 26313 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | | 1889.09998 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | | 0.689999998 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | | 4069.18994 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | 4491.41016 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | | 1 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 1 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | | 0.680000007 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | | 466.399994 | | |
| k_I2CHwAuthority_Uls_f32 | | 0.680000007 | | |
| k_MaxSensorlessAuthority_Uls_f32 | | 0.769999981 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | | 614.900024 | | |
| k_MinSensorlessAuthority_Uls_f32 | | 0.680000007 | | |
| k_TurnsCntrAuthority_Uls_f32 | | 0.680000007 | | |
| k_UseTurnsCntr_Cnt_lgc | | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | | 466.399994 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | | 461.899994 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | | 1 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | | 0.769999981 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | | 1446.80005 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | | 100 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | | 2113.69995 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | | 8431 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | 2 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result | |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 301.100006 | 301.100006 ± 0.0009 | ✔ | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 2674.75098 | 2674.75122 ± 0.1 | ✔ | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ | |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 2674.75098 | 2674.75122 ± 0.1 | ✔ | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✔ | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 4069.18994 | 4069.18994 ± 0.1 | ✔ | |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|--------------------|--------|
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 4491.41016 | 4491.41016 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0 | 0 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2113.69995 | 2113.69995 ± 0.009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.66 (Repeat Count = 1) | |
|--|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 9698.79004 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 1 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 147.300003 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.699999988 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 632 |
| AbsHwPos_HwPosState_Cnt_M_enum | 5 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 321.26001 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 3421.26001 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 26385 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 909.380005 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.699999988 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 4209.93018 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 4632.1499 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.689999998 |
| k_HwPosOutputLPFError_HwDeg_f32 | 496.100006 |
| k_I2CHwAuthority_Uls_f32 | 0.689999998 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.779999971 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 644.599976 |
| k_MinSensorlessAuthority_Uls_f32 | 0.689999998 |
| k_TurnsCntrAuthority_Uls_f32 | 0.689999998 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 496.100006 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 482 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.779999971 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 1466.90002 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 104 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2143.8999 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 8555 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 3 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|---|--|--------------------------|--------|
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 3421.26001 | 3421.26001 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.699999988 | 0.699999988 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 2991.14111 | 2991.14111 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 632 | 632 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 2991.14111 | 2991.14111 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 26385 | 26385 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 909.380005 | 909.380005 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.699999988 | 0.699999988 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 4209.93018 | 4209.93018 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 4632.1499 | 4632.1499 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.699999988 | 0.699999988 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 632 | 632 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 26385 | 26385 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 909.380005 | 909.380005 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2143.8999 | 2143.8999 ± 0.009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.67 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -9839.53027 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.709999979 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 156.899994 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.709999979 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 640 |
| AbsHwPos_HwPosState_Cnt_M_enum | 6 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 341.540009 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 341.540009 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 26457 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 929.659973 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.709999979 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -4350.66992 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1572.89001 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.699999988 |
| k_HwPosOutputLPFError_HwDeg_f32 | 525.799988 |
| k_I2CHwAuthority_Uls_f32 | 0.699999988 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.790000021 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 674.299988 |
| k_MinSensorlessAuthority_Uls_f32 | 0.699999988 |
| k_TurnsCntrAuthority_Uls_f32 | 0.699999988 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 525.799988 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 502.100006 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.790000021 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 1487 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 108 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2174.1001 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 8679 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 4 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_C | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|--------------------------|--------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 341.540009 | 341.540009 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.00999999046 | 0.00999999978 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -3043.47461 | -3043.47461 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -3043.47461 | -3043.47461 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -4350.66992 | -4350.66992 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1572.89001 | -1572.89001 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_Igc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.00999999046 | 0.00999999978 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.68 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 9980.26953 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.720000029 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 166.5 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.720000029 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1 |
| AbsHwPos_HwPosState_Cnt_M_enum | 7 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 361.820007 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 361.820007 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 26529 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 949.940002 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.720000029 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 4491.41016 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 4913.62988 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_Igc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.709999979 |
| k_HwPosOutputLPFError_HwDeg_f32 | 555.5 |
| k_I2CHwAuthority_Uls_f32 | 0.709999979 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.800000012 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 704 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|--------------------------|--------|
| k_MinSensorlessAuthority_Uls_f32 | 0.709999979 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.709999979 | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 555.5 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 522.200012 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.800000012 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 1507.09998 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 112 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2204.30005 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 8803 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 5 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 361.820007 | 361.820007 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.720000029 | 0.720000029 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 3280.43555 | 3280.4353 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1 | 1 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 3280.43555 | 3280.4353 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 26529 | 26529 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 949.940002 | 949.940002 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.720000029 | 0.720000029 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 4491.41016 | 4491.41016 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 4913.62988 | 4913.62988 ± 0.009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.720000029 | 0.720000029 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 1 | 1 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 26529 | 26529 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 949.940002 | 949.940002 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2204.30005 | 2204.30005 ± 0.009 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.69 (Repeat Count = 1) | |
|--|------------------|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 9874.30957 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.730000019 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 176.100006 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.730000019 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 382.100006 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 382.100006 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 26601 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 970.219971 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.730000019 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 4632.1499 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 5054.37012 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.720000029 |
| k_HwPosOutputLPFError_HwDeg_f32 | 585.200012 |
| k_I2CHwAuthority_Uls_f32 | 0.720000029 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.810000002 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 733.700012 |
| k_MinSensorlessAuthority_Uls_f32 | 0.720000029 |
| k_TurnsCntrAuthority_Uls_f32 | 0.720000029 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 585.200012 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 542.299988 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.810000002 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 1527.19995 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 116 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2234.5 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 8927 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 0 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|--------------------------|--------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 382.100006 | 382.100006 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.00999999046 | 0.00999999978 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_m_str.SV_Uls_f32 | 3429.0166 | 3429.0166 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 3429.0166 | 3429.0166 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 4632.1499 | 4632.1499 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 5054.37012 | 5054.37012 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.00999999046 | 0.00999999978 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Test Step 2.70 (Repeat Count = 1) | | | | ✓ |
|--|--------------------------|---|--------|---|
| Name | | Input Value | | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | -9768.34961 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | | 0.74000001 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | | 185.699997 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | | 0.74000001 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | | 1024 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | | 9 | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | | 402.380005 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | 402.380005 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | | 26673 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | | -990.5 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | | 0.74000001 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | | -4772.89014 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | 5195.10986 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | | 0 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 0 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | | 0.730000019 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | | 614.900024 | | |
| k_I2CHwAuthority_Uls_f32 | | 0.730000019 | | |
| k_MaxSensorlessAuthority_Uls_f32 | | 0.819999993 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | | 763.400024 | | |
| k_MinSensorlessAuthority_Uls_f32 | | 0.730000019 | | |
| k_TurnsCntrAuthority_Uls_f32 | | 0.730000019 | | |
| k_UseTurnsCntr_Cnt_lgc | | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | | 614.900024 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | | -330.5 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | | 0 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | | 0.819999993 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | | 1547.30005 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | | 120 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | | 2264.69995 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | | 9051 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | 1 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result | |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 402.380005 | 402.380005 ± 0.0009 | ✓ | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.74000001 | 0.74000001 ± 0.004 | ✓ | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -3483.65698 | -3483.65649 ± 0.1 | ✓ | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1024 | 1024 | ✓ | |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -3483.65698 | -3483.65649 ± 0.1 | ✓ | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 26673 | 26673 | ✓ | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -990.5 | -990.5 ± 0.1 | ✓ | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.74000001 | 0.74000001 ± 0.004 | ✓ | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -4772.89014 | -4772.89014 ± 0.1 | ✓ | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 5195.10986 | 5195.10986 ± 0.009 | ✓ | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ | |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.74000001 | 0.74000001 ± 0.004 | ✓ | |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✓ | |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 1024 | 1024 | ✓ | |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 26673 | 26673 | ✓ | |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | -990.5 | -990.5 ± 0.1 | ✓ | |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|--------------------|--------|
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2264.69995 | 2264.69995 ± 0.009 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.71 (Repeat Count = 1)

| Name | Input Value |
|--|---|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 9662.38965 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.75 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 195.300003 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.75 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1236 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 422.660004 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 422.660004 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 26745 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 1010.78003 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.75 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 4913.62988 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 8284.88965 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.74000001 |
| k_HwPosOutputLPFError_HwDeg_f32 | 644.599976 |
| k_I2CHwAuthority_Uls_f32 | 0.74000001 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.829999983 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 793.099976 |
| k_MinSensorlessAuthority_Uls_f32 | 0.74000001 |
| k_TurnsCntrAuthority_Uls_f32 | 0.74000001 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 644.599976 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -310.799988 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.829999983 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 1567.40002 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 124 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2294.8999 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 9175 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 2 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|---------------------------|---------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 422.660004 | 422.660004 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.75 | 0.75 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 3734.04761 | 3734.04761 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1236 | 1236 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 3734.04761 | 3734.04761 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 1010.78003 | 1010.78003 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.75 | 0.75 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 4913.62988 | 4913.62988 ± 0.1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|--------------------|--------|
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 8284.88965 | 8284.88965 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.75 | 0.75 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 1236 | 1236 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1010.78003 | 1010.78003 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2294.8999 | 2294.8999 ± 0.009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.72 (Repeat Count = 1) | |
|--|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 9556.42969 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.75999999 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 204.899994 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.75999999 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1348 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 442.940002 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 442.940002 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 26817 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 123.400002 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.75999999 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 5054.37012 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 8178.93018 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.75 |
| k_HwPosOutputLPFError_HwDeg_f32 | 674.299988 |
| k_I2CHwAuthority_Uls_f32 | 0.75 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.839999974 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 822.799988 |
| k_MinSensorlessAuthority_Uls_f32 | 0.75 |
| k_TurnsCntrAuthority_Uls_f32 | 0.75 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 674.299988 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -291.100006 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.839999974 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 1587.5 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 128 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2325.1001 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 9299 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 3 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|---------------------|--------|
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntOffset | target_Pim_EOLVehCntOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 442.940002 | 442.940002 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.75999999 | 0.75999999 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 3890.49731 | 3890.49731 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1348 | 1348 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT | HWPOS_STATE_INIT | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 3890.49731 | 3890.49731 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 26817 | 26817 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 123.400002 | 123.400002 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.75999999 | 0.75999999 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 5054.37012 | 5054.37012 ± 0.1 | ✓ |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | 8178.93018 | 8178.93018 ± 0.009 | ✓ |
| AbsHwPos_VehCntOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.75999999 | 0.75999999 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 1348 | 1348 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 26817 | 26817 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 123.400002 | 123.400002 ± 0.1 | ✓ |
| target_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 2325.1001 | 2325.1001 ± 0.009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.73 (Repeat Count = 1) | |
|--|--|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -9450.46973 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.769999981 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 214.5 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.769999981 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1460 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 463.220001 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 463.220001 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 26889 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -248.300003 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.769999981 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 5195.10986 |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | -1072.96997 |
| AbsHwPos_VehCntOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.75999999 |
| k_HwPosOutputLPFError_HwDeg_f32 | 704 |
| k_I2CHwAuthority_Uls_f32 | 0.75999999 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.850000024 |
| k_MaxVehCntOffDiff_HwDeg_f32 | 852.5 |
| k_MinSensorlessAuthority_Uls_f32 | 0.75999999 |
| k_TurnsCntAuthority_Uls_f32 | 0.75999999 |
| k_UseTurnsCnt_Cnt_lgc | 1 |
| k_VehCntOffValidLimit_HwDeg_f32 | 704 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -271.399994 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.850000024 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -1000.29999 |
| target_AbsHwPos_Per2_TurnsCntValidity_Cnt_u08.value | 132 |
| target_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 2355.30005 |
| target_Pim_EOLVehCntOffset.EOLHwPosTrimPerformed_Cnt_u16 | 9423 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus_ErrorStatus | 4 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_u16 | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_u16 | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|---------------------------|---------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 463.220001 | 463.220001 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.00999999046 | 0.00999999978 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 4049.56958 | 4049.56958 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 4049.56958 | 4049.56958 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -248.300003 | -248.300003 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 5195.10986 | 5195.10986 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1072.96997 | -1072.96997 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.00999999046 | 0.00999999978 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | -248.300003 | -248.300003 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.74 (Repeat Count = 1)

| Name | Input Value |
|---|---|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 9662.38965 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.75 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 195.300003 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.75 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1236 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 422.660004 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 422.660004 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 26745 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 1010.78003 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.75 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 4913.62988 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 8284.88965 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.74000001 |
| k_HwPosOutputLPFError_HwDeg_f32 | 644.599976 |
| k_I2CHwAuthority_Uls_f32 | 0.74000001 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.829999983 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 793.099976 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|---------------------------|--------|
| k_MinSensorlessAuthority_Uls_f32 | 0.74000001 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.74000001 | | |
| k_UseTurnsCntr_Cnt_Igc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 644.599976 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -310.799988 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.829999983 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 1567.40002 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 124 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2294.8999 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 9175 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 2 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 422.660004 | 422.660004 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.829999983 | 0.829999983 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1224.375 | 1224.375 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3 | 3 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1224.375 | 1224.375 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 1567.40002 | 1567.40002 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.829999983 | 0.829999983 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 1567.40002 | 1567.40002 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc | 0 | 0 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_Igc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.829999983 | 0.829999983 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1224.375 | 1224.375 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 3 | 3 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1567.40002 | 1567.40002 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2294.8999 | 2294.8999 ± 0.009 | ✔ |

| Test Step Call Trace | | | | | ✓ |
|--|-------|--|-------|--------|---|
| Actual Function | Count | Expected Function | Count | Result | |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ | |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ | |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ | |

| Test Step 2.75 (Repeat Count = 1) | | ✓ |
|--|-------------|---|
| Name | Input Value | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 9344.5 | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.779999971 | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 224.100006 | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.779999971 | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1572 | |
| AbsHwPos_HwPosState_Cnt_M_enum | 13 | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -11725 | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -600 | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 26961 | |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|--------------------------|--------|
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 1395.13 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.779999971 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -5335.8501 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 7967.00977 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0.769999981 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 733.700012 | | |
| k_I2CHwAuthority_Uls_f32 | 0.769999981 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.860000014 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 882.200012 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.769999981 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.769999981 | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 733.700012 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -251.699997 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.860000014 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -980.599976 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 136 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2385.5 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 9547 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 5 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -600 | -600 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.779999971 | 0.779999971 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -4112.66113 | -4112.66113 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1572 | 1572 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -4112.66113 | -4112.66113 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 26961 | 26961 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 1395.13 | 1395.13 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.779999971 | 0.779999971 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -5335.8501 | -5335.8501 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 7967.00977 | 7967.00977 ± 0.009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.779999971 | 0.779999971 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 1572 | 1572 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 26961 | 26961 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1395.13 | 1395.13 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2385.5 | 2385.5 ± 0.009 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

Test Step 2.76 (Repeat Count = 1)

| Name | Input Value |
|--|---|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 9238.54004 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.790000021 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -389.600006 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.790000021 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1684 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 11725 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 6008 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 27033 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 1535.87 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.790000021 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 5476.58984 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 7861.0498 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.779999971 |
| k_HwPosOutputLPFError_HwDeg_f32 | 763.400024 |
| k_I2CHwAuthority_Uls_f32 | 0.779999971 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.870000005 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 911.900024 |
| k_MinSensorlessAuthority_Uls_f32 | 0.779999971 |
| k_TurnsCntrAuthority_Uls_f32 | 0.779999971 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 763.400024 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -232 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.870000005 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -960.900024 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 140 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2415.69995 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 9671 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 0 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|--------------------------|--------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 6008 | 6008 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.0100000501 | 0.00999999978 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 4244.68994 | 4244.68994 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 4244.68994 | 4244.68994 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 5476.58984 | 5476.58984 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 7861.0498 | 7861.0498 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.0100000501 | 0.00999999978 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|----------------|--------|
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.77 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|---------------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -9132.58008 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.800000012 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 243.300003 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.800000012 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1796 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 0 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 7150 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 27105 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -1676.60999 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.800000012 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 5617.33008 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1455.08997 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0.790000021 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 793.099976 | | |
| k_I2CHwAuthority_Uls_f32 | 0.790000021 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.879999995 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 941.599976 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.790000021 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.790000021 | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 793.099976 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -212.300003 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.879999995 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -941.200012 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 144 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2445.8999 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 9795 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 1 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 7150 | 7150 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.800000012 | 0.800000012 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 4542.52393 | 4542.52393 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1796 | 1796 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 4542.52393 | 4542.52393 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|---------------------|--------|
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -1676.60999 | -1676.60999 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.800000012 | 0.800000012 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 5617.33008 | 5617.33008 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1455.08997 | -1455.08997 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.800000012 | 0.800000012 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 1796 | 1796 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2445.8999 | 2445.8999 ± 0.009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.78 (Repeat Count = 1) | |
|--|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 9026.62012 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.810000002 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 252.899994 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.810000002 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1908 |
| AbsHwPos_HwPosState_Cnt_M_enum | 3 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 544.340027 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 544.340027 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 27177 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 1817.34998 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.810000002 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -5758.06982 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 7649.14014 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.800000012 |
| k_HwPosOutputLPFError_HwDeg_f32 | 822.799988 |
| k_I2CHwAuthority_Uls_f32 | 0.800000012 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.889999986 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 971.299988 |
| k_MinSensorlessAuthority_Uls_f32 | 0.800000012 |
| k_TurnsCntrAuthority_Uls_f32 | 0.800000012 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 822.799988 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -192.600006 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.889999986 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -921.5 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 148 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2476.1001 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 9919 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 2 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|---|--|--------------------------|--------|
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 544.340027 | 544.340027 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.00999999046 | 0.0099999978 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -4615.98535 | -4615.98584 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -4615.98535 | -4615.98584 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -5758.06982 | -5758.06982 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 7649.14014 | 7649.14014 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.00999999046 | 0.0099999978 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2476.1001 | 2476.1001 ± 0.009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.79 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 8920.66016 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.819999993 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 262.5 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.819999993 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 2020 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -564.619995 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -564.619995 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 27249 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 1958.08997 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.819999993 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 5898.81006 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 7543.18018 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.810000002 |
| k_HwPosOutputLPFError_HwDeg_f32 | 852.5 |
| k_I2CHwAuthority_Uls_f32 | 0.810000002 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.899999976 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1001 |
| k_MinSensorlessAuthority_Uls_f32 | 0.810000002 |
| k_TurnsCntrAuthority_Uls_f32 | 0.810000002 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 852.5 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -172.899994 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.899999976 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -901.799988 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 152 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|---------------------------|--------|
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2506.30005 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 10043 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 3 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -564.619995 | -564.619995 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.899999976 | 0.899999976 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -901.799988 | -901.799988 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3 | 3 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -901.799988 | -901.799988 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -901.799988 | -901.799988 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.899999976 | 0.899999976 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -901.799988 | -901.799988 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 7543.18018 | 7543.18018 ± 0.009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.899999976 | 0.899999976 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -901.799988 | -901.799988 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 3 | 3 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | -901.799988 | -901.799988 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2506.30005 | 2506.30005 ± 0.009 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.80 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 8814.7002 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.829999983 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 272.100006 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.829999983 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 2132 |
| AbsHwPos_HwPosState_Cnt_M_enum | 5 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 584.900024 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -10125 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 27321 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 2098.83008 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.829999983 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 6039.5498 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1437.21997 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.819999993 |
| k_HwPosOutputLPFError_HwDeg_f32 | 882.200012 |
| k_I2CHwAuthority_Uls_f32 | 0.819999993 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.910000026 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1030.69995 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|--------------------------|--------|
| k_MinSensorlessAuthority_Uls_f32 | 0.819999993 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.819999993 | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 882.200012 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 461.899994 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.910000026 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -882.099976 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 156 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2536.5 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 10167 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 4 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -10125 | -10125 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.00999999046 | 0.0099999978 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 5059.0835 | 5059.0835 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 5059.0835 | 5059.0835 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 6039.5498 | 6039.5498 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1437.21997 | -1437.21997 ± 0.009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.00999999046 | 0.0099999978 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.81 (Repeat Count = 1) | |
|--|-------------|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -8708.74023 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.839999974 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 281.700012 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.839999974 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 2244 |
| AbsHwPos_HwPosState_Cnt_M_enum | 6 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 605.179993 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 10125 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 27393 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -2239.57007 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.839999974 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 6180.29004 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 7331.25977 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.829999983 |
| k_HwPosOutputLPFError_HwDeg_f32 | 911.900024 |
| k_I2CHwAuthority_Uls_f32 | 0.829999983 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.920000017 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1060.40002 |
| k_MinSensorlessAuthority_Uls_f32 | 0.829999983 |
| k_TurnsCntrAuthority_Uls_f32 | 0.829999983 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 911.900024 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 482 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.920000017 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -862.400024 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 160 |
| target_Pim_EOLVehCntrOffset_EOLVehCntrOffset_HwDeg_f32 | 2566.69995 |
| target_Pim_EOLVehCntrOffset_EOLHwPosTrimPerformed_Cnt_u16 | 10291 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 5 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|--------------------------|--------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 10125 | 10125 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.839999974 | 0.839999974 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 5236.51563 | 5236.51563 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 2244 | 2244 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 5236.51563 | 5236.51563 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 27393 | 27393 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -2239.57007 | -2239.57007 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.839999974 | 0.839999974 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 6180.29004 | 6180.29004 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 7331.25977 | 7331.25977 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.839999974 | 0.839999974 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 2244 | 2244 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 27393 | 27393 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset_EOLVehCntrOffset_HwDeg_f32 | 2566.69995 | 2566.69995 ± 0.009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

Test Step 2.82 (Repeat Count = 1)

| Name | Input Value |
|--|---|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 8602.78027 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.850000024 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -350.200012 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.850000024 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 2356 |
| AbsHwPos_HwPosState_Cnt_M_enum | 7 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 625.460022 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 0 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 27465 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 2380.31006 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.850000024 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -6321.02979 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 7225.2998 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.839999974 |
| k_HwPosOutputLPFError_HwDeg_f32 | 941.599976 |
| k_I2CHwAuthority_Uls_f32 | 0.839999974 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.930000007 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1090.09998 |
| k_MinSensorlessAuthority_Uls_f32 | 0.839999974 |
| k_TurnsCntrAuthority_Uls_f32 | 0.839999974 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 941.599976 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 502.100006 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.930000007 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -842.700012 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 164 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2596.8999 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 10415 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 0 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|--------------------------|--------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 0 | 0 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.0100000501 | 0.00999999978 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -5425.40527 | -5425.40527 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -5425.40527 | -5425.40527 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -6321.02979 | -6321.02979 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 7225.2998 | 7225.2998 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.0100000501 | 0.00999999978 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|----------------|--------|
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.83 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|---------------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 8496.82031 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.860000014 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 300.899994 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.860000014 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 2468 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 645.73999 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 645.73999 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 27537 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 2521.05005 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.860000014 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 6461.77002 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1119.32996 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0.850000024 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 1060.40002 | | |
| k_I2CHwAuthority_Uls_f32 | 0.850000024 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.939999998 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1119.80005 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.850000024 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.850000024 | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 971.299988 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 522.200012 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.939999998 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -823 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 168 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -488.100006 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 10539 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 1 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 645.73999 | 645.73999 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.00999999046 | 0.0099999978 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 5599.24854 | 5599.24805 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 5599.24854 | 5599.24805 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|---------------|-----------------------|--------|
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 2521.05005 | 2521.05005 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 6461.77002 | 6461.77002 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1119.32996 | -1119.32996 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.00999999046 | 0.00999999978 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -488.100006 | -488.100006 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.84 (Repeat Count = 1) | |
|--|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -8390.86035 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.870000005 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 310.5 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.870000005 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 2580 |
| AbsHwPos_HwPosState_Cnt_M_enum | 9 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -666.02002 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -666.02002 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 27609 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -2661.79004 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.870000005 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 6602.50977 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 7013.37988 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.860000014 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1090.09998 |
| k_I2CHwAuthority_Uls_f32 | 0.860000014 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.949999988 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1149.5 |
| k_MinSensorlessAuthority_Uls_f32 | 0.860000014 |
| k_TurnsCntrAuthority_Uls_f32 | 0.860000014 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1001 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 542.299988 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.949999988 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -803.299988 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 172 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -468.399994 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 10663 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 2 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|---|--|--------------------------|--------|
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -666.02002 | -666.02002 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.00999999046 | 0.0099999978 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 5784.54834 | 5784.54883 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 5784.54834 | 5784.54883 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 6602.50977 | 6602.50977 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 7013.37988 | 7013.37988 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.00999999046 | 0.0099999978 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -468.399994 | -468.399994 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.85 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 8284.88965 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.879999995 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 320.100006 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.879999995 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 2692 |
| AbsHwPos_HwPosState_Cnt_M_enum | 10 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 686.299988 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 686.299988 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 27681 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -11725 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.879999995 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -6743.25 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 6907.41992 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.870000005 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1119.80005 |
| k_I2CHwAuthority_Uls_f32 | 0.870000005 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.959999979 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1179.19995 |
| k_MinSensorlessAuthority_Uls_f32 | 0.870000005 |
| k_TurnsCntrAuthority_Uls_f32 | 0.870000005 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1030.69995 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 562.400024 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.959999979 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -783.599976 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 176 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|--------------------------|--------|
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -448.700012 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 10787 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 3 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 686.299988 | 686.299988 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.879999995 | 0.879999995 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -5895.64795 | -5895.64795 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 2692 | 2692 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -5895.64795 | -5895.64795 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 27681 | 27681 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -11725 | -11725 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.879999995 | 0.879999995 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -6743.25 | -6743.25 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 6907.41992 | 6907.41992 ± 0.009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.879999995 | 0.879999995 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 2692 | 2692 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 27681 | 27681 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -448.700012 | -448.700012 ± 0.0009 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.86 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 8178.93018 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.889999986 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -507.799988 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.889999986 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 2804 |
| AbsHwPos_HwPosState_Cnt_M_enum | 11 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 706.580017 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 706.580017 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 27753 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 11725 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.889999986 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 6883.99023 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 6801.45996 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.879999995 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1149.5 |
| k_I2CHwAuthority_Uls_f32 | 0.879999995 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.970000029 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1208.90002 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|--------------------------|--------|
| k_MinSensorlessAuthority_Uls_f32 | 0.879999995 | | |
| k_TurnsCntAuthority_Uls_f32 | 0.879999995 | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1060.40002 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 582.5 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.970000029 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -763.900024 | | |
| target_AbsHwPos_Per2_TurnsCntValidity_Cnt_u08.value | 180 | | |
| target_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | -429 | | |
| target_Pim_EOLVehCntOffset.EOLHwPosTrimPerformed_Cnt_u16 | 10911 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus_ErrorStatus | 4 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntOffset | target_Pim_EOLVehCntOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 706.580017 | 706.580017 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.00999999046 | 0.0099999978 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 6070.89307 | 6070.89307 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 6070.89307 | 6070.89307 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 6883.99023 | 6883.99023 ± 0.1 | ✔ |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | 6801.45996 | 6801.45996 ± 0.009 | ✔ |
| AbsHwPos_VehCntOfstLearn_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_VehCntValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.00999999046 | 0.0099999978 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✔ |
| target_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.87 (Repeat Count = 1) | |
|--|-------------|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -8072.97021 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.899999976 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 389.200012 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.899999976 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 2916 |
| AbsHwPos_HwPosState_Cnt_M_enum | 12 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -726.859985 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -726.859985 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|--------------------------|--------|
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 27825 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.899999976 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 7024.72998 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 6695.5 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0.889999986 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 1179.19995 | | |
| k_I2CHwAuthority_Uls_f32 | 0.889999986 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.980000019 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1238.59998 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.889999986 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.889999986 | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1090.09998 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 602.599976 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.980000019 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -744.200012 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 184 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -409.299988 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 11035 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 5 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -726.859985 | -726.859985 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.899999976 | 0.899999976 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 6361.17676 | 6361.17676 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 2916 | 2916 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 6361.17676 | 6361.17676 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 27825 | 27825 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.899999976 | 0.899999976 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 7024.72998 | 7024.72998 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 6695.5 | 6695.5 ± 0.009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.899999976 | 0.899999976 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 2916 | 2916 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 27825 | 27825 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -409.299988 | -409.299988 ± 0.0009 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

Test Step 2.88 (Repeat Count = 1)

| Name | Input Value |
|--|---|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 7967.00977 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.910000026 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -468.399994 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.910000026 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3028 |
| AbsHwPos_HwPosState_Cnt_M_enum | 13 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 747.140015 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 747.140015 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 27897 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 2943.27002 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.910000026 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 7165.47021 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1589.54004 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.899999976 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1208.90002 |
| k_I2CHwAuthority_Uls_f32 | 0.899999976 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.99000001 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1268.30005 |
| k_MinSensorlessAuthority_Uls_f32 | 0.899999976 |
| k_TurnsCntrAuthority_Uls_f32 | 0.899999976 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1119.80005 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 622.700012 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.99000001 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -724.5 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 188 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -389.600006 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 11159 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 0 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|--------------------------|--------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 747.140015 | 747.140015 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.0100000501 | 0.00999999978 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 6478.42188 | 6478.42188 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 6478.42188 | 6478.42188 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 7165.47021 | 7165.47021 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1589.54004 | -1589.54004 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.0100000501 | 0.00999999978 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|----------------|--------|
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.89 (Repeat Count = 1) | | | | |
|--|------------------|---|--------|--|
| Name | | Input Value | | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | 7861.0498 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | | 0.920000017 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | | 784.099976 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | | 0.920000017 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | | 3140 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | | HWPOS_STATE_INPUTINVALID | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | | 767.419983 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | 767.419983 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | | 27969 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | | -3084.01001 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | | 0.920000017 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | | -7306.20996 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | 6483.58008 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | | 0 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 0 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | | 0.910000026 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | | 1238.59998 | | |
| k_I2CHwAuthority_Uls_f32 | | 0.910000026 | | |
| k_MaxSensorlessAuthority_Uls_f32 | | 0.100000001 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | | 1298 | | |
| k_MinSensorlessAuthority_Uls_f32 | | 0.910000026 | | |
| k_TurnsCntrAuthority_Uls_f32 | | 0.910000026 | | |
| k_UseTurnsCntr_Cnt_lgc | | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | | 1149.5 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | | 642.799988 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | | 0 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | | 0.100000001 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | | -704.799988 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | | 192 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | | -369.899994 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | | 11283 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | 1 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result | |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 767.419983 | 767.419983 ± 0.0009 | ✓ | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.920000017 | 0.920000017 ± 0.004 | ✓ | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -6658.98535 | -6658.98535 ± 0.1 | ✓ | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3140 | 3140 | ✓ | |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT | HWPOS_STATE_INIT | ✓ | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -6658.98535 | -6658.98535 ± 0.1 | ✓ | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 27969 | 27969 | ✓ | |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|----------------------|--------|
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -3084.01001 | -3084.01001 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.920000017 | 0.920000017 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -7306.20996 | -7306.20996 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 6483.58008 | 6483.58008 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.920000017 | 0.920000017 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 3140 | 3140 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 27969 | 27969 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -369.899994 | -369.899994 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.90 (Repeat Count = 1) | |
|--|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -7755.08984 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.930000007 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 966.200012 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.930000007 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3252 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 787.700012 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 787.700012 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 2802.53003 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.930000007 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 5828.25977 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 6377.62012 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.920000017 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1268.30005 |
| k_I2CHwAuthority_Uls_f32 | 0.920000017 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.109999999 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1327.69995 |
| k_MinSensorlessAuthority_Uls_f32 | 0.920000017 |
| k_TurnsCntrAuthority_Uls_f32 | 0.920000017 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1179.19995 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 662.900024 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.109999999 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -685.099976 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 196 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -350.200012 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 11407 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 2 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|---|--|--------------------------|--------|
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 787.700012 | 787.700012 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.00999999046 | 0.0099999978 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 5487.91553 | 5487.91602 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 5487.91553 | 5487.91602 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 5828.25977 | 5828.25977 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 6377.62012 | 6377.62012 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.00999999046 | 0.0099999978 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -350.200012 | -350.200012 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.91 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 7649.14014 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.939999998 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -409.299988 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.939999998 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3364 |
| AbsHwPos_HwPosState_Cnt_M_enum | 3 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -807.97998 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -807.97998 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 202.529999 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.939999998 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 848.539978 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1271.65002 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.930000007 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1298 |
| k_I2CHwAuthority_Uls_f32 | 0.930000007 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.119999997 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1357.40002 |
| k_MinSensorlessAuthority_Uls_f32 | 0.930000007 |
| k_TurnsCntrAuthority_Uls_f32 | 0.930000007 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1208.90002 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 683 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.119999997 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -665.400024 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 200 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|--------------------------|--------|
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -330.5 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 11531 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 3 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -807.97998 | -807.97998 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.939999998 | 0.939999998 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 773.069519 | 773.06958 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3364 | 3364 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 773.069519 | 773.06958 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 202.529999 | 202.529999 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.939999998 | 0.939999998 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 848.539978 | 848.539978 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1271.65002 | -1271.65002 ± 0.009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.939999998 | 0.939999998 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 773.069519 | 773.06958 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 3364 | 3364 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 202.529999 | 202.529999 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -330.5 | -330.5 ± 0.0009 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.92 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 7543.18018 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.949999988 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 213 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.949999988 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3476 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 828.26001 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 5828.25977 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 28969 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 9802.53027 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.949999988 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -868.820007 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 6165.7002 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.939999998 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1327.69995 |
| k_I2CHwAuthority_Uls_f32 | 0.939999998 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.129999995 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1387.09998 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|--------------------------|--------|
| k_MinSensorlessAuthority_Uls_f32 | 0.939999998 | | |
| k_TurnsCntAuthority_Uls_f32 | 0.939999998 | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1238.59998 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 703.099976 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.129999995 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -645.700012 | | |
| target_AbsHwPos_Per2_TurnsCntValidity_Cnt_u08.value | 204 | | |
| target_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | -310.799988 | | |
| target_Pim_EOLVehCntOffset.EOLHwPosTrimPerformed_Cnt_u16 | 11655 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus_ErrorStatus | 4 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntOffset | target_Pim_EOLVehCntOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 5828.25977 | 5828.25977 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.00999999046 | 0.0099999978 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -814.729004 | -814.729004 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -814.729004 | -814.729004 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -868.820007 | -868.820007 ± 0.1 | ✔ |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | 6165.7002 | 6165.7002 ± 0.009 | ✔ |
| AbsHwPos_VehCntOfstLearn_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_VehCntValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.00999999046 | 0.0099999978 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -814.729004 | -814.729004 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✔ |
| target_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.93 (Repeat Count = 1) | |
|--|-------------|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -7437.22021 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.959999979 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -369.899994 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.959999979 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3588 |
| AbsHwPos_HwPosState_Cnt_M_enum | 5 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 848.539978 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 848.539978 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|--------------------------|--------|
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 29467 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 3224.75 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 1889.09998 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 6059.74023 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0.949999988 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 1357.40002 | | |
| k_I2CHwAuthority_Uls_f32 | 0.949999988 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.140000001 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1416.80005 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.949999988 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.949999988 | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1268.30005 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 723.200012 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.140000001 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -626 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 208 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -291.100006 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 11779 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 5 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 848.539978 | 848.539978 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.00999999046 | 0.0099999978 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1889.09998 | 1889.09998 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3588 | 3588 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1889.09998 | 1889.09998 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 29467 | 29467 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 3224.75 | 3224.75 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 1889.09998 | 1889.09998 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 6059.74023 | 6059.74023 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.00999999046 | 0.0099999978 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 3588 | 3588 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 29467 | 29467 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -291.100006 | -291.100006 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Test Step 2.94 (Repeat Count = 1) | | | | ✓ |
|--|--------------------------|---|--------|---|
| Name | | Input Value | | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | 7331.25977 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | | 0.970000029 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | | 120.199997 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | | 0.970000029 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | | 3700 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | | 6 | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | | -868.820007 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | -868.820007 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | | 29965 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | | 120.199997 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | | 1 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | | 909.380005 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | -1446.94995 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | | 1 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 1 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | | 0.959999979 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | | 1387.09998 | | |
| k_I2CHwAuthority_Uls_f32 | | 0.959999979 | | |
| k_MaxSensorlessAuthority_Uls_f32 | | 0.150000006 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | | 1446.5 | | |
| k_MinSensorlessAuthority_Uls_f32 | | 0.959999979 | | |
| k_TurnsCntrAuthority_Uls_f32 | | 0.959999979 | | |
| k_UseTurnsCntr_Cnt_lgc | | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | | 1298 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | | 743.299988 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | | 2 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | | 0.150000006 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | | -606.299988 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | | 212 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | | -271.399994 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | | 11903 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | 0 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result | |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -868.820007 | -868.820007 ± 0.0009 | ✓ | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.0100000501 | 0.0099999978 ± 0.004 | ✓ | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 885.704651 | 885.70459 ± 0.1 | ✓ | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ | |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 885.704651 | 885.70459 ± 0.1 | ✓ | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✓ | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 909.380005 | 909.380005 ± 0.1 | ✓ | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1446.94995 | -1446.94995 ± 0.009 | ✓ | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ | |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.0100000501 | 0.0099999978 ± 0.004 | ✓ | |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 885.704651 | 885.70459 ± 0.1 | ✓ | |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ | |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✓ | |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ | |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|----------------|--------|
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.95 (Repeat Count = 1) | | | | ✔ |
|--|--------------------------|---|--------|---|
| Name | | Input Value | | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | 7225.2998 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | | 0.980000019 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | | 140.300003 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | | 0.980000019 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | | 3812 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | | 7 | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | | 889.099976 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | 1889.09998 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | | 30463 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | | 140.300003 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | | 0.980000019 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | | 929.659973 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | 7587.68994 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | | 0 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 0 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | | 0.970000029 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | | 1416.80005 | | |
| k_I2CHwAuthority_Uls_f32 | | 0.970000029 | | |
| k_MaxSensorlessAuthority_Uls_f32 | | 0.159999996 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | | 1476.19995 | | |
| k_MinSensorlessAuthority_Uls_f32 | | 0.970000029 | | |
| k_TurnsCntrAuthority_Uls_f32 | | 0.970000029 | | |
| k_UseTurnsCntr_Cnt_lgc | | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | | 1327.69995 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | | 763.400024 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | | 0 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | | 0.159999996 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | | -586.599976 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | | 216 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | | -251.699997 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | | 12027 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | 1 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result | |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 1889.09998 | 1889.09998 ± 0.0009 | ✔ | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.980000019 | 0.980000019 ± 0.004 | ✔ | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 929.659973 | 929.659973 ± 0.1 | ✔ | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3812 | 3812 | ✔ | |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 929.659973 | 929.659973 ± 0.1 | ✔ | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 30463 | 30463 | ✔ | |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|----------------------|--------|
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 140.300003 | 140.300003 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.980000019 | 0.980000019 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 929.659973 | 929.659973 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 7587.68994 | 7587.68994 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.980000019 | 0.980000019 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 929.659973 | 929.659973 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 3812 | 3812 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 30463 | 30463 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 140.300003 | 140.300003 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -251.699997 | -251.699997 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.96 (Repeat Count = 1) | |
|--|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -7119.33008 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.99000001 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 160.399994 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.99000001 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3924 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 909.380005 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 909.380005 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 30961 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 160.399994 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.99000001 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -11725 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 7728.43018 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.980000019 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1446.5 |
| k_I2CHwAuthority_Uls_f32 | 0.980000019 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.17000002 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1505.90002 |
| k_MinSensorlessAuthority_Uls_f32 | 0.980000019 |
| k_TurnsCntrAuthority_Uls_f32 | 0.980000019 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1357.40002 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 783.5 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.17000002 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -566.900024 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 220 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 5000.6001 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 12151 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 2 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|---|--|--------------------------|--------|
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 909.380005 | 909.380005 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.00999999046 | 0.0099999978 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -11606.1465 | -11606.1465 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -11606.1465 | -11606.1465 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -11725 | -11725 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 7728.43018 | 7728.43018 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.00999999046 | 0.0099999978 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 5000.6001 | 5000.6001 ± 0.009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.97 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 7013.37988 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.100000001 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 180.5 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.100000001 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 4036 |
| AbsHwPos_HwPosState_Cnt_M_enum | 9 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 929.659973 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 929.659973 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 31459 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 180.5 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.100000001 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 11725 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1369.17004 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.99000001 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1476.19995 |
| k_I2CHwAuthority_Uls_f32 | 0.99000001 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.180000007 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1535.59998 |
| k_MinSensorlessAuthority_Uls_f32 | 0.99000001 |
| k_TurnsCntrAuthority_Uls_f32 | 0.99000001 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1387.09998 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 803.599976 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.180000007 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -547.200012 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 224 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|--------------------------|--------|
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 5120.3999 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 12275 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 3 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_u16 | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 929.659973 | 929.659973 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.100000001 | 0.100000001 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1334.95007 | 1334.94995 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 4036 | 4036 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1334.95007 | 1334.94995 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 31459 | 31459 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 180.5 | 180.5 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.100000001 | 0.100000001 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 11725 | 11725 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1369.17004 | -1369.17004 ± 0.009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.100000001 | 0.100000001 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1334.95007 | 1334.94995 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 4036 | 4036 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 31459 | 31459 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 180.5 | 180.5 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 5120.3999 | 5120.3999 ± 0.009 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.98 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 6907.41992 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.200000003 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 200.600006 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.200000003 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 4148 |
| AbsHwPos_HwPosState_Cnt_M_enum | 10 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 949.940002 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 949.940002 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 31957 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 200.600006 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.200000003 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 0 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 2804 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.100000001 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1505.90002 |
| k_I2CHwAuthority_Uls_f32 | 0.100000001 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.189999998 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1565.30005 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|--------------------------|--------|
| k_MinSensorlessAuthority_Uls_f32 | 0.100000001 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.100000001 | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1416.80005 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 823.700012 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.189999998 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -527.5 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 228 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 5240.2002 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 12399 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 4 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 949.940002 | 949.940002 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.100000001 | 0.100000001 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 2804 | 2804 ± 0.009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.100000001 | 0.100000001 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 0 | 0 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.99 (Repeat Count = 1) | |
|--|-------------|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 6801.45996 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.300000012 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 220.699997 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.300000012 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 4260 |
| AbsHwPos_HwPosState_Cnt_M_enum | 11 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 970.219971 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 970.219971 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 32455 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 220.699997 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.300000012 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 2943.27002 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 2916 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.109999999 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1535.59998 |
| k_I2CHwAuthority_Uls_f32 | 0.109999999 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.200000003 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1595 |
| k_MinSensorlessAuthority_Uls_f32 | 0.109999999 |
| k_TurnsCntrAuthority_Uls_f32 | 0.109999999 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1446.5 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 843.799988 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.200000003 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -507.799988 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 232 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 5360 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 12523 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 5 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|--------------------------|--------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 970.219971 | 970.219971 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.300000012 | 0.300000012 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1037.47107 | 1037.47095 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 4260 | 4260 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1037.47107 | 1037.47095 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 32455 | 32455 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 220.699997 | 220.699997 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.300000012 | 0.300000012 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 2943.27002 | 2943.27002 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 2916 | 2916 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.300000012 | 0.300000012 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1037.47107 | 1037.47095 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 4260 | 4260 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 32455 | 32455 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 220.699997 | 220.699997 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 5360 | 5360 ± 0.009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

Test Step 2.100 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|--------------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 6695.5 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.400000006 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 240.800003 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.400000006 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 4372 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | 12 | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -990.5 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -990.5 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 32953 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 240.800003 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.400000006 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -3084.01001 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 3028 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0.119999997 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 1565.30005 | | |
| k_I2CHwAuthority_Uls_f32 | 0.119999997 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.209999993 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 50.5999985 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.119999997 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.119999997 | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1476.19995 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 863.900024 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.209999993 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -488.100006 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 236 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 5479.7998 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 12647 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 0 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -990.5 | -990.5 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.280000001 | 0.280000001 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -1089.12402 | -1089.12402 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -1089.12402 | -1089.12402 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -3084.01001 | -3084.01001 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 3028 | 3028 ± 0.009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.280000001 | 0.280000001 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -1089.12402 | -1089.12402 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|----------------|--------|
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.101 (Repeat Count = 1) | | | | |
|--|--------------------------|---|--------|--|
| Name | | Input Value | | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | 200 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | | 0.5 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | | 260.899994 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | | 0.5 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | | 4484 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | | 13 | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | | 1010.78003 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | 1010.78003 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | | 33451 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | | 260.899994 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | | 0.5 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | | -248.300003 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | -1600 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | | 1 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 0 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | | 0.129999995 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | | 1595 | | |
| k_I2CHwAuthority_Uls_f32 | | 0.129999995 | | |
| k_MaxSensorlessAuthority_Uls_f32 | | 0.219999999 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | | 80.3000031 | | |
| k_MinSensorlessAuthority_Uls_f32 | | 0.129999995 | | |
| k_TurnsCntrAuthority_Uls_f32 | | 0.129999995 | | |
| k_UseTurnsCntr_Cnt_lgc | | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | | 1505.90002 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | | 884 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | | 0 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | | 0.219999999 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | | -468.399994 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | | 240 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | | 5599.6001 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | | 12771 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | 1 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result | |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 1010.78003 | 1010.78003 ± 0.0009 | ✔ | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.5 | 0.5 ± 0.004 | ✔ | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -248.300003 | -248.300003 ± 0.1 | ✔ | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 4484 | 4484 | ✔ | |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -248.300003 | -248.300003 ± 0.1 | ✔ | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 33451 | 33451 | ✔ | |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|---------------------|--------|
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 260.899994 | 260.899994 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.5 | 0.5 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -248.300003 | -248.300003 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 668.400024 | 668.400024 ± 0.0009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.5 | 0.5 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -248.300003 | -248.300003 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 4484 | 4484 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 33451 | 33451 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 260.899994 | 260.899994 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 668.400024 | 668.400024 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.102 (Repeat Count = 1) | |
|--|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 1000.25 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.600000024 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 281 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.600000024 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 4596 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1031.06006 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 123.400002 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 33949 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 281 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.600000024 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 1395.13 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.140000001 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1149.5 |
| k_I2CHwAuthority_Uls_f32 | 0.140000001 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.230000004 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 110 |
| k_MinSensorlessAuthority_Uls_f32 | 0.140000001 |
| k_TurnsCntrAuthority_Uls_f32 | 0.140000001 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1535.59998 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 100.099998 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.230000004 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -448.700012 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 244 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 5719.3999 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 12895 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 2 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|---|--|--------------------------|--------|
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 123.400002 | 123.400002 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.460000038 | 0.460000008 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1395.13 | 1395.13 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1395.13 | 1395.13 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 1395.13 | 1395.13 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 1448.94995 | 1448.94995 ± 0.009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.460000038 | 0.460000008 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1395.13 | 1395.13 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 5719.3999 | 5719.3999 ± 0.009 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.103 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 524.210022 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.699999988 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 301.100006 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.699999988 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 4708 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1051.33997 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -248.300003 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 34447 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 301.100006 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.699999988 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 1535.87 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 0 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.150000006 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1179.19995 |
| k_I2CHwAuthority_Uls_f32 | 0.150000006 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.239999995 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 139.699997 |
| k_MinSensorlessAuthority_Uls_f32 | 0.150000006 |
| k_TurnsCntrAuthority_Uls_f32 | 0.150000006 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1565.30005 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 120.199997 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.239999995 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -429 |
| target_AbsHwPos_Per2_TurnsCntValidity_Cnt_u08.value | 248 |
| target_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 5839.2002 |
| target_Pim_EOLVehCntOffset.EOLHwPosTrimPerformed_Cnt_u16 | 13019 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus_ErrorStatus | 3 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_u16 | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_u16 | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntOffset | target_Pim_EOLVehCntOffset |

| Name | Actual Value | Expected Value | Result |
|--|---------------------------|---------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -248.300003 | -248.300003 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.699999988 | 0.699999988 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1535.87 | 1535.87 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 4708 | 4708 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1535.87 | 1535.87 ± 0.1 | ✓ |
| AbsHwPos_SrComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_SrComHwPos_HwDeg_M_f32 | 301.100006 | 301.100006 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.699999988 | 0.699999988 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 1535.87 | 1535.87 ± 0.1 | ✓ |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | 953.210022 | 953.210022 ± 0.0009 | ✓ |
| AbsHwPos_VehCntOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntValid_Cnt_M_lgc | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.699999988 | 0.699999988 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1535.87 | 1535.87 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 4708 | 4708 | ✓ |
| target_AbsHwPos_Per2_SrComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrComHwPos_HwDeg_f32.value | 301.100006 | 301.100006 ± 0.1 | ✓ |
| target_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 953.210022 | 953.210022 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.104 (Repeat Count = 1) | |
|--|--|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -324.119995 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.800000012 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 321.26001 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.800000012 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 4820 |
| AbsHwPos_HwPosState_Cnt_M_enum | 3 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1071.62 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 147.300003 |
| AbsHwPos_SrComHwPosStatus_Cnt_M_u16 | 34945 |
| AbsHwPos_SrComHwPos_HwDeg_M_f32 | -6321.02979 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.800000012 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -1676.60999 |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | 1000.59998 |
| AbsHwPos_VehCntOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.159999996 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| k_HwPosOutputLPFError_HwDeg_f32 | 1208.90002 |
| k_I2CHwAuthority_Uls_f32 | 0.159999996 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.25 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 169.399994 |
| k_MinSensorlessAuthority_Uls_f32 | 0.159999996 |
| k_TurnsCntrAuthority_Uls_f32 | 0.159999996 |
| k_UseTurnsCntr_Cnt_Igc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1595 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 140.300003 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.25 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -409.299988 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 252 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 5959 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 13143 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 4 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|--------------------------|--------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 147.300003 | 147.300003 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.639999986 | 0.639999986 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -1277.03601 | -1277.03601 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -1277.03601 | -1277.03601 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -1676.60999 | -1676.60999 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 85.1799927 | 85.1800003 ± 0.00009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_Igc | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.639999986 | 0.639999986 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -1277.03601 | -1277.03601 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 85.1799927 | 85.1800003 ± 0.00009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.105 (Repeat Count = 1) | |
|--|-------------|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -400.209991 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.899999976 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 341.540009 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.899999976 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|---------------------------|--------|
| AbsHwPos_HwPosSource_Cnt_M_u16 | 4932 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1091.90002 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -991.700012 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 35443 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 6461.77002 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.899999976 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 1817.34998 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -500.899994 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0.170000002 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 1238.59998 | | |
| k_I2CHwAuthority_Uls_f32 | 0.170000002 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.259999999 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 199.100006 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.170000002 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.170000002 | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 50.5999985 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 160.399994 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.259999999 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -389.600006 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 10 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 6078.7998 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 13267 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 5 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -991.700012 | -991.700012 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.729999959 | 0.730000019 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -316.485992 | -316.485992 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3 | 3 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -316.485992 | -316.485992 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -389.600006 | -389.600006 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.259999999 | 0.25999999 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -389.600006 | -389.600006 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -10.6099854 | -10.6099997 ± 0.00009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.729999959 | 0.730000019 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -316.485992 | -316.485992 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 3 | 3 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | -389.600006 | -389.600006 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -10.6099854 | -10.6099997 ± 0.00009 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.106 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|--------------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 6059.74023 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.109999999 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 361.820007 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.109999999 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 5044 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | 5 | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1112.18005 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 894.5 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 35941 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 6602.50977 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.109999999 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 1958.08997 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 3700 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0.180000007 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 1268.30005 | | |
| k_I2CHwAuthority_Uls_f32 | 0.180000007 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.270000011 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 228.800003 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.180000007 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.180000007 | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 80.3000031 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 180.5 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.270000011 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -369.899994 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 20 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 6198.6001 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 13391 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 0 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 894.5 | 894.5 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1958.08997 | 1958.08997 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1958.08997 | 1958.08997 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 1958.08997 | 1958.08997 ± 0.1 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|-------------------|--------|
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 3700 | 3700 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0 | 0 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 6198.6001 | 6198.6001 ± 0.009 | ✓ |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.107 (Repeat Count = 1) | |
|--|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -5953.77881 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.119999997 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 382.100006 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.119999997 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 5156 |
| AbsHwPos_HwPosState_Cnt_M_enum | 6 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -1132.45996 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 5641 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 36439 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -6743.25 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.119999997 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 2098.83008 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 3812 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.189999998 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1298 |
| k_I2CHwAuthority_Uls_f32 | 0.189999998 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.280000001 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 258.5 |
| k_MinSensorlessAuthority_Uls_f32 | 0.189999998 |
| k_TurnsCntrAuthority_Uls_f32 | 0.189999998 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 110 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 200.600006 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.280000001 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -350.200012 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 30 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 6318.3999 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 13515 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 1 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|-----------------------------|--------------------------|--------|
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 5641 | 5641 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.119999997 | 0.119999997 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 588.107605 | 588.107605 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 5156 | 5156 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 588.107605 | 588.107605 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 36439 | 36439 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -6743.25 | -6743.25 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.119999997 | 0.119999997 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 2098.83008 | 2098.83008 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 3812 | 3812 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.119999997 | 0.119999997 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 588.107605 | 588.107605 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 5156 | 5156 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 36439 | 36439 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.108 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 5847.81982 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.129999995 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 402.380005 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.129999995 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 5268 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1152.73999 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 150.5 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 36937 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 6883.99023 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.129999995 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -2239.57007 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 3924 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.200000003 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1327.69995 |
| k_I2CHwAuthority_Uls_f32 | 0.200000003 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.289999992 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 288.200012 |
| k_MinSensorlessAuthority_Uls_f32 | 0.200000003 |
| k_TurnsCntrAuthority_Uls_f32 | 0.200000003 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 139.699997 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 220.699997 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.289999992 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -330.5 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 100 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 6438.2002 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 13639 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|-------------------------|--------|
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 2 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_u16 | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 150.5 | 150.5 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 58.9264832 | 58.9264984 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 58.9264832 | 58.9264984 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 6883.99023 | 6883.99023 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -2239.57007 | -2239.57007 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 3924 | 3924 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_Igc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0 | 0 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 58.9264832 | 58.9264984 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 6438.2002 | 6438.2002 ± 0.009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.109 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 5741.85986 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.140000001 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 422.6600001 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.140000001 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 5380 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1173.02002 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -620 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 37435 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 7024.72998 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.140000001 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 2380.31006 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 4036 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_Igc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.209999993 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1357.40002 |
| k_I2CHwAuthority_Uls_f32 | 0.209999993 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.300000012 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 317.899994 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|-------------------------|--------|
| k_MinSensorlessAuthority_Uls_f32 | 0.209999993 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.209999993 | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 169.399994 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 240.800003 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.300000012 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -310.799988 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 100 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 6558 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 13763 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 3 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -620 | -620 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.209999993 | 0.209999993 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1705.85986 | 1705.85999 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1 | 1 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1705.85986 | 1705.85999 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 1705.85986 | 1705.85999 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.209999993 | 0.209999993 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 1705.85986 | 1705.85999 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 4036 | 4036 ± 0.009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.209999993 | 0.209999993 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 1 | 1 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.110 (Repeat Count = 1) | |
|--|-------------|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 5635.8999 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.150000006 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -11725 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.150000006 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 5492 |
| AbsHwPos_HwPosState_Cnt_M_enum | 9 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1193.30005 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 201.199997 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|--------------------------|--------|
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 37933 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 7165.47021 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.150000006 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 2521.05005 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 4148 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0.219999999 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 1387.09998 | | |
| k_I2CHwAuthority_Uls_f32 | 0.219999999 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.310000002 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 347.600006 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.219999999 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.219999999 | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 199.100006 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 260.899994 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.310000002 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -291.100006 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 60 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 6677.7998 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 13887 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 4 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 201.199997 | 201.199997 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 2521.05005 | 2521.05005 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 2521.05005 | 2521.05005 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 2521.05005 | 2521.05005 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 4148 | 4148 ± 0.009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0 | 0 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |

TEST DETAILS REPORT

AbsHwPos_Per2

2017-02-06, 13:51:17+0530



Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.111 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|--------------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -5529.93018 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.159999996 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 11725 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.159999996 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 5604 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | 10 | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1213.57996 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 64.9800034 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 38431 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 7306.20996 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.159999996 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -2661.79004 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 4260 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0.230000004 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 1416.80005 | | |
| k_I2CHwAuthority_Uls_f32 | 0.230000004 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.319999993 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 377.299988 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.230000004 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.230000004 | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 228.800003 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 281 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.319999993 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -271.399994 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 70 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 6797.6001 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 14011 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 5 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 64.9800034 | 64.9800034 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.159999996 | 0.159999996 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 9423.11328 | 9423.11328 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 5604 | 5604 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 9423.11328 | 9423.11328 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 38431 | 38431 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 7306.20996 | 7306.20996 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.159999996 | 0.159999996 ± 0.004 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|---------------------|--------|
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -2661.79004 | -2661.79004 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 4260 | 4260 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.159999996 | 0.159999996 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 5604 | 5604 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 38431 | 38431 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 6797.6001 | 6797.6001 ± 0.009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.112 (Repeat Count = 1) | |
|--|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 5423.97998 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.170000002 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 0 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.170000002 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 5716 |
| AbsHwPos_HwPosState_Cnt_M_enum | 11 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1233.85999 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 8888 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 38929 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -7446.9502 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.170000002 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 949.940002 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 4372 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.239999995 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1446.5 |
| k_I2CHwAuthority_Uls_f32 | 0.239999995 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.330000013 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 407 |
| k_MinSensorlessAuthority_Uls_f32 | 0.239999995 |
| k_TurnsCntrAuthority_Uls_f32 | 0.239999995 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 258.5 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 301.100006 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.330000013 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -251.699997 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 80 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 6917.3999 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 14135 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 0 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|-----------------------------|--------------------------|--------|
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 8888 | 8888 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 949.940002 | 949.940002 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 949.940002 | 949.940002 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 949.940002 | 949.940002 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 4372 | 4372 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0 | 0 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 949.940002 | 949.940002 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.113 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 3787.70996 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.280000001 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1112.18005 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.280000001 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 296 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1207.09998 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 1207.09998 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 23721 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 2098.83008 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.280000001 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 8009.91016 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1132.57996 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.270000011 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1179.19995 |
| k_I2CHwAuthority_Uls_f32 | 0.270000011 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.340000004 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 882.200012 |
| k_MinSensorlessAuthority_Uls_f32 | 0.270000011 |
| k_TurnsCntrAuthority_Uls_f32 | 0.270000011 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 822.799988 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -271.399994 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.360000014 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 622.700012 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 100 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 875.5 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|-------------------------|--------|
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 3347 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 3 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_u16 | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 1207.09998 | 1207.09998 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.360000014 | 0.360000014 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 622.700012 | 622.700012 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3 | 3 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 622.700012 | 622.700012 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 2098.83008 | 2098.83008 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.360000014 | 0.360000014 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 622.700012 | 622.700012 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1132.57996 | -1132.57996 ± 0.009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.360000014 | 0.360000014 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 622.700012 | 622.700012 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 3 | 3 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.114 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -5212.05908 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.189999998 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -564.619995 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.189999998 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 5940 |
| AbsHwPos_HwPosState_Cnt_M_enum | 13 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1274.42004 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -471 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 39925 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 7728.43018 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.189999998 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -990.5 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 4596 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.25999999 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1505.90002 |
| k_I2CHwAuthority_Uls_f32 | 0.25999999 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|--------------------------|--------|
| k_MaxSensorlessAuthority_Uls_f32 | 0.349999994 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 466.399994 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.25999999 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.25999999 | | |
| k_UseTurnsCntr_Cnt_Igc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 317.899994 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 341.299988 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.349999994 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -212.300003 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 100 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 7157 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 14383 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 2 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -471 | -471 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -645.53717 | -645.53717 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -645.53717 | -645.53717 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -990.5 | -990.5 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 4596 | 4596 ± 0.009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc | 1 | 1 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_Igc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0 | 0 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -645.53717 | -645.53717 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 7157 | 7157 ± 0.009 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.115 (Repeat Count = 1) | |
|--|--------------------------|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 5106.08984 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.200000003 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 484 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 6052 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1294.69995 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 4649 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 40423 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -7869.16992 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.200000003 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 1010.78003 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 4708 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.270000011 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1535.59998 |
| k_I2CHwAuthority_Uls_f32 | 0.270000011 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.360000014 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 496.100006 |
| k_MinSensorlessAuthority_Uls_f32 | 0.270000011 |
| k_TurnsCntrAuthority_Uls_f32 | 0.270000011 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 347.600006 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 461.899994 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.360000014 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -192.600006 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 110 |
| target_Pim_EOLVehCntrOffset_EOLVehCntrOffset_HwDeg_f32 | 7276.7998 |
| target_Pim_EOLVehCntrOffset_EOLHwPosTrimPerformed_Cnt_u16 | 14507 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 3 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|------------------|---------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 4649 | 4649 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.200000003 | 0.200000003 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1010.78003 | 1010.78003 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 6052 | 6052 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT | HWPOS_STATE_INIT | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1010.78003 | 1010.78003 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 40423 | 40423 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -7869.16992 | -7869.16992 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.200000003 | 0.200000003 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 1010.78003 | 1010.78003 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 4708 | 4708 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.200000003 | 0.200000003 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1010.78003 | 1010.78003 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 6052 | 6052 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 40423 | 40423 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset_EOLVehCntrOffset_HwDeg_f32 | 7276.7998 | 7276.7998 ± 0.009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

TEST DETAILS REPORT

AbsHwPos_Per2

2017-02-06, 13:51:17+0530



| Test Step 2.116 (Repeat Count = 1) | | | | ✓ |
|--|--|---|--------------------------|--------|
| Name | | Input Value | | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | 5000.12988 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | | 0.209999993 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | | -564.299988 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | | 1 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | | 6164 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | | HWPOS_STATE_SENSORVALID | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | | 1314.97998 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | 234 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | | 40921 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | | 8009.91016 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | | 0.209999993 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | | 123.400002 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | 1687 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | | 1 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 1 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | | 0.280000001 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | | 1565.30005 | | |
| k_I2CHwAuthority_Uls_f32 | | 0.280000001 | | |
| k_MaxSensorlessAuthority_Uls_f32 | | 0.370000005 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | | 525.799988 | | |
| k_MinSensorlessAuthority_Uls_f32 | | 0.280000001 | | |
| k_TurnsCntrAuthority_Uls_f32 | | 0.280000001 | | |
| k_UseTurnsCntr_Cnt_lgc | | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | | 377.299988 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | | 482 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | | 0 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | | 0.370000005 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | | -172.899994 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | | 120 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | | 7396.6001 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | | 14631 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | 4 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | | target_Pim_EOLVehCntrOffset | | |
| Name | | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | | 234 | 234 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | | 123.400002 | 123.400002 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | | 123.400002 | 123.400002 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | | 65535 | 65535 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | | 123.400002 | 123.400002 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | 1687 | 1687 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | | 0 | 0 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | | 123.400002 | 123.400002 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | | 65535 | 65535 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|----------------|--------|
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.117 (Repeat Count = 1)

| Name | Input Value |
|--|---|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 9556.42969 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.75999999 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -941.200012 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.569999993 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1348 |
| AbsHwPos_HwPosState_Cnt_M_enum | 13 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -409.299988 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -409.299988 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 41419 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 6180.29004 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.569999993 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 2380.31006 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 2802.53003 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.75 |
| k_HwPosOutputLPFError_HwDeg_f32 | 674.299988 |
| k_I2CHwAuthority_Uls_f32 | 0.75 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.839999974 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 822.799988 |
| k_MinSensorlessAuthority_Uls_f32 | 0.75 |
| k_TurnsCntrAuthority_Uls_f32 | 0.75 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 674.299988 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -291.100006 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.839999974 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 1587.5 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 130 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2325.1001 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 9299 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 5 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|--------------------------|--------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -409.299988 | -409.299988 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.569999993 | 0.569999993 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 952.06073 | 952.06073 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1348 | 1348 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 952.06073 | 952.06073 ± 0.1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|---------------------|--------|
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 41419 | 41419 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 6180.29004 | 6180.29004 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.569999993 | 0.569999993 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 2380.31006 | 2380.31006 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 2802.53003 | 2802.53003 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.569999993 | 0.569999993 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 952.06073 | 952.06073 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 1348 | 1348 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 41419 | 41419 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.118 (Repeat Count = 1) | |
|--|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -514.320007 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.769999981 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -921.5 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.579999983 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1460 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 213 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 213 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 41917 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -6321.02979 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.579999983 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 2521.05005 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 2943.27002 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.75999999 |
| k_HwPosOutputLPFError_HwDeg_f32 | 704 |
| k_I2CHwAuthority_Uls_f32 | 0.75999999 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.850000024 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 852.5 |
| k_MinSensorlessAuthority_Uls_f32 | 0.75999999 |
| k_TurnsCntrAuthority_Uls_f32 | 0.75999999 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 704 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -271.399994 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.850000024 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -1000.29999 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 140 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2355.30005 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 9423 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 0 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|---|--|--------------------------|--------|
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntOffset | target_Pim_EOLVehCntOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 213 | 213 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.00999999046 | 0.00999999978 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1075.17896 | 1075.17896 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1075.17896 | 1075.17896 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 2521.05005 | 2521.05005 ± 0.1 | ✔ |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | 485.97998 | 485.980011 ± 0.0009 | ✔ |
| AbsHwPos_VehCntOfstLearn_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_VehCntValid_Cnt_M_lgc | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.00999999046 | 0.00999999978 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1075.17896 | 1075.17896 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✔ |
| target_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 485.97998 | 485.980011 ± 0.0009 | ✔ |

| Test Step Call Trace | | | | | ✓ |
|---|-------|---|-------|--------|---|
| Actual Function | Count | Expected Function | Count | Result | |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus | 1 | ✓ | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_WriteBlock | 1 | ✓ | |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ | |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ | |

| Test Step 2.119 (Repeat Count = 1) | | |
|---|---|--|
| Name | Input Value | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -874.22998 | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.779999971 | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 324.5 | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.589999974 | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1572 | |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -369.899994 | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -369.899994 | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 42415 | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 6461.77002 | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.589999974 | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -2661.79004 | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1084.01001 | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | |
| k_HwPosAuthorityStep_Uls_f32 | 0.769999981 | |
| k_HwPosOutputLPFError_HwDeg_f32 | 733.700012 | |
| k_I2CHwAuthority_Uls_f32 | 0.769999981 | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.860000014 | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 882.200012 | |
| k_MinSensorlessAuthority_Uls_f32 | 0.769999981 | |
| k_TurnsCntrAuthority_Uls_f32 | 0.769999981 | |
| k_UseTurnsCntr_Cnt_lgc | 0 | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 733.700012 | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -251.699997 | |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.860000014 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -980.599976 |
| target_AbsHwPos_Per2_TurnsCntValidity_Cnt_u08.value | 150 |
| target_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 2385.5 |
| target_Pim_EOLVehCntOffset.EOLHwPosTrimPerformed_Cnt_u16 | 9547 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus_ErrorStatus | 1 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_u16 | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntOffset | target_Pim_EOLVehCntOffset |

| Name | Actual Value | Expected Value | Result |
|--|---------------------------|---------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -369.899994 | -369.899994 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.589999974 | 0.589999974 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -1437.41101 | -1437.41113 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1572 | 1572 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -1437.41101 | -1437.41113 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 6461.77002 | 6461.77002 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.589999974 | 0.589999974 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -2661.79004 | -2661.79004 ± 0.1 | ✓ |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | 106.369995 | 106.370003 ± 0.0009 | ✓ |
| AbsHwPos_VehCntOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntValid_Cnt_M_lgc | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.589999974 | 0.589999974 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -1437.41101 | -1437.41113 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 1572 | 1572 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 106.369995 | 106.370003 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.120 (Repeat Count = 1) | |
|--|--|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -325.320007 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.790000021 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 471.600006 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.600000024 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1684 |
| AbsHwPos_HwPosState_Cnt_M_enum | 3 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 120.199997 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 120.199997 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 42913 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 6602.50977 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.600000024 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 2802.53003 |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | 3224.75 |
| AbsHwPos_VehCntOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus_ErrorStatus |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.779999971 |
| k_HwPosOutputLPFError_HwDeg_f32 | 763.400024 |
| k_I2CHwAuthority_Uls_f32 | 0.779999971 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.870000005 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 911.900024 |
| k_MinSensorlessAuthority_Uls_f32 | 0.779999971 |
| k_TurnsCntrAuthority_Uls_f32 | 0.779999971 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 763.400024 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -232 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.870000005 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -960.900024 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 160 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2415.69995 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 9671 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 2 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrIComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrIComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrIComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrIComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|--------------------------|--------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 120.199997 | 120.199997 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.0100000501 | 0.00999999978 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1870.15796 | 1870.15796 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1870.15796 | 1870.15796 ± 0.1 | ✓ |
| AbsHwPos_SrIComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✓ |
| AbsHwPos_SrIComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 2802.53003 | 2802.53003 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 635.580017 | 635.580017 ± 0.0009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.0100000501 | 0.00999999978 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrIComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✓ |
| target_AbsHwPos_Per2_SrIComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2415.69995 | 2415.69995 ± 0.009 | ✓ |

Test Step Call Trace ✓

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.121 (Repeat Count = 1) ✓

| Name | Input Value |
|--|-------------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -514.320007 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.769999981 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -921.5 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|--------------------------|--------|
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.579999983 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1460 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 213 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 213 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 41917 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -6321.02979 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.579999983 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 2521.05005 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 2943.27002 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0.75999999 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 704 | | |
| k_I2CHwAuthority_Uls_f32 | 0.75999999 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.850000024 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 852.5 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.75999999 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.75999999 | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 704 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -271.399994 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.850000024 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -1000.29999 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 140 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2355.30005 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 9423 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 3 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 213 | 213 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.00999999046 | 0.00999999978 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1075.17896 | 1075.17896 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1075.17896 | 1075.17896 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 2521.05005 | 2521.05005 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 485.97998 | 485.980011 ± 0.0009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.00999999046 | 0.00999999978 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1075.17896 | 1075.17896 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 485.97998 | 485.980011 ± 0.0009 | ✔ |

TEST DETAILS REPORT

AbsHwPos_Per2

2017-02-06, 13:51:17+0530



Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 2.122 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|---------------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -874.22998 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.779999971 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 324.5 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.589999974 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1572 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -369.899994 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -369.899994 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 42415 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 6461.77002 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.589999974 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -2661.79004 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1084.01001 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0.769999981 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 733.700012 | | |
| k_I2CHwAuthority_Uls_f32 | 0.769999981 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.860000014 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 882.200012 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.769999981 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.769999981 | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 733.700012 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -251.699997 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.860000014 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -980.599976 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 150 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2385.5 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 9547 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 4 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -369.899994 | -369.899994 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.589999974 | 0.589999974 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -1437.41101 | -1437.41113 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1572 | 1572 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -1437.41101 | -1437.41113 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 6461.77002 | 6461.77002 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.589999974 | 0.589999974 ± 0.004 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|---------------------|--------|
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -2661.79004 | -2661.79004 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 106.369995 | 106.370003 ± 0.0009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.589999974 | 0.589999974 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -1437.41101 | -1437.41113 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 1572 | 1572 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 106.369995 | 106.370003 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 2.123 (Repeat Count = 1) | |
|--|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -325.320007 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.790000021 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 471.600006 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.600000024 |
| AbsHwPos_HwPosSource_Cnt_u16 | 1684 |
| AbsHwPos_HwPosState_Cnt_M_enum | 3 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 120.199997 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 120.199997 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 42913 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 6602.50977 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.600000024 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 2802.53003 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 3224.75 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.779999971 |
| k_HwPosOutputLPFError_HwDeg_f32 | 763.400024 |
| k_I2CHwAuthority_Uls_f32 | 0.779999971 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.870000005 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 911.900024 |
| k_MinSensorlessAuthority_Uls_f32 | 0.779999971 |
| k_TurnsCntrAuthority_Uls_f32 | 0.779999971 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 763.400024 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -232 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.870000005 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -960.900024 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 160 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2415.69995 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 9671 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 5 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|---|--|--------------------------|--------|
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 120.199997 | 120.199997 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.0100000501 | 0.00999999978 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1870.15796 | 1870.15796 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1870.15796 | 1870.15796 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 2802.53003 | 2802.53003 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 635.580017 | 635.580017 ± 0.0009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.0100000501 | 0.00999999978 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 635.580017 | 635.580017 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |



Test Case 3: Path Test





| | |
|---------------|--|
| Specification | Performance Metrics (With "None" Instrumentation and WithPS Environment) |
| | CPU Cycles: |
| | TS3.1 1467.00 Cycles |
| | TS3.2 2915.00 Cycles |
| | TS3.3 1421.00 Cycles |
| | TS3.4 1965.00 Cycles |
| | TS3.5 2827.00 Cycles |
| | TS3.6 2340.00 Cycles |
| | TS3.7 2783.00 Cycles |
| | TS3.8 1341.00 Cycles |
| | TS3.9 1836.00 Cycles |
| | TS3.10 1374.00 Cycles |
| | TS3.11 1438.00 Cycles |
| | TS3.12 1850.00 Cycles |
| | TS3.13 1896.00 Cycles |
| | TS3.14 1860.00 Cycles |
| | TS3.15 1365.00 Cycles |
| | TS3.16 1895.00 Cycles |
| | TS3.17 1844.00 Cycles |
| | TS3.18 1915.00 Cycles |
| | TS3.19 1373.00 Cycles |
| | TS3.20 1426.00 Cycles |
| | TS3.21 1904.00 Cycles |
| | TS3.22 1861.00 Cycles |
| | TS3.23 1844.00 Cycles |
| | TS3.24 1904.00 Cycles |
| | TS3.25 1904.00 Cycles |
| | TS3.26 1373.00 Cycles |
| | TS3.27 1373.00 Cycles |
| | TS3.28 1910.00 Cycles |
| | TS3.29 1438.00 Cycles |
| | TS3.30 1904.00 Cycles |

| Description | Vector Description: |
|-------------|--|
| | <pre>TS3.1" if ((DiagStatusHwPosReducedPerf_Cnt_T_lgc == TRUE) (AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_INPUTINVALID))=>False if (AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc == TRUE)>False if (Abs_f32_m(AbsHwPos_TempHwPos_HwDeg_M_f32 - AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32) >= k_HwPosOutputLPFError_HwDeg_f32)>True else if (AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_INIT)>False else if (AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_SENSORVALID)>True if (k_UseTurnsCntr_Cnt_lgc == FALSE)>True if (I2CHwAbsPosValid_Cnt_T_lgc == TRUE)>False" TS3.2" if ((DiagStatusHwPosReducedPerf_Cnt_T_lgc == TRUE) (AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_INPUTINVALID))=>True if (DiagStatusHwPosReducedPerf_Cnt_T_lgc == TRUE)>True if (AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc == TRUE)>True if (Abs_f32_m(VehCntrOffset_HwDeg_T_f32) <= k_VehCntrOffValidLimit_HwDeg_f32)>False if ((Abs_f32_m(Rte_Pim_EOLVehCntrOffset()->EOLVehCntrOffset_HwDeg_f32 - D_INVALIDOFFSET_HWDEG_F32)) < FLT_EPSILON)>True if (NvMStatus_Cnt_T_enum != NVM_REQ_PENDING)=>True" TS3.3" if ((DiagStatusHwPosReducedPerf_Cnt_T_lgc == TRUE))=>False (AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_INPUTINVALID)>True) if (DiagStatusHwPosReducedPerf_Cnt_T_lgc == TRUE)>False" TS3.4" if (Abs_f32_m(VehCntrOffset_HwDeg_T_f32) <= k_VehCntrOffValidLimit_HwDeg_f32)>True if ((Abs_f32_m(Rte_Pim_EOLVehCntrOffset()->EOLVehCntrOffset_HwDeg_f32 - AbsHwPos_VehCntrOffset_HwDeg_M_f32) > k_MaxVehCntrOffDiff_HwDeg_f32) && (NvMStatus_Cnt_T_enum != NVM_REQ_PENDING))=>True" TS3.5" if ((Abs_f32_m(Rte_Pim_EOLVehCntrOffset()->EOLVehCntrOffset_HwDeg_f32 - AbsHwPos_VehCntrOffset_HwDeg_M_f32) > k_MaxVehCntrOffDiff_HwDeg_f32) && (NvMStatus_Cnt_T_enum != NVM_REQ_PENDING))=>False" TS3.6" if ((Abs_f32_m(Rte_Pim_EOLVehCntrOffset()->EOLVehCntrOffset_HwDeg_f32 - AbsHwPos_VehCntrOffset_HwDeg_M_f32) > k_MaxVehCntrOffDiff_HwDeg_f32)>True && (NvMStatus_Cnt_T_enum != NVM_REQ_PENDING))=>False" TS3.7" if (Abs_f32_m(Rte_Pim_EOLVehCntrOffset()->EOLVehCntrOffset_HwDeg_f32 - D_INVALIDOFFSET_HWDEG_F32)) < FLT_EPSILON)>False if (TurnsCntrValidity_Cnt_T_u08 == D_TCVCOMPUTING_CNT_U08)>False else if (TurnsCntrValidity_Cnt_T_u08 == D_TCVVALID_CNT_U08)>False" TS3.8"1.7 if (Abs_f32_m(AbsHwPos_TempHwPos_HwDeg_M_f32 - AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32) >= k_HwPosOutputLPFError_HwDeg_f32)>False else if (AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_SENSORVALID)>False else if (AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_SENSORINVALID)>False" TS3.9" else if (AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_INIT)>True if (k_UseTurnsCntr_Cnt_lgc == TRUE)>True if (TurnsCntrValidity_Cnt_T_u08 == D_TCVCOMPUTING_CNT_U08)>True" TS3.10" if (k_UseTurnsCntr_Cnt_lgc == TRUE)>False if (I2CHwAbsPosValid_Cnt_T_lgc == TRUE)>False" TS3.11" if (I2CHwAbsPosValid_Cnt_T_lgc == TRUE)>True" TS3.12" else if (TurnsCntrValidity_Cnt_T_u08 == D_TCVVALID_CNT_U08)>True if (AbsHwPos_VehCntrValid_Cnt_M_lgc == TRUE)>False" TS3.13" if (AbsHwPos_VehCntrValid_Cnt_M_lgc == TRUE)>True" TS3.14" if (k_UseTurnsCntr_Cnt_lgc == FALSE)>False if (TurnsCntrValidity_Cnt_T_u08 == D_TCVVALID_CNT_U08)>True if (AbsHwPos_VehCntrValid_Cnt_M_lgc == TRUE)>True if (SensorlessAuthority_Uls_T_f32 >= k_MaxSensorlessAuthority_Uls_f32)>True" TS3.15" if (I2CHwAbsPosValid_Cnt_T_lgc == TRUE)>True" TS3.16" if (TurnsCntrValidity_Cnt_T_u08 == D_TCVVALID_CNT_U08)>False" TS3.17" if (AbsHwPos_VehCntrValid_Cnt_M_lgc == TRUE)>False if (SensorlessAuthority_Uls_T_f32 >= k_MinSensorlessAuthority_Uls_f32)>True if (SensorlessAuthority_Uls_T_f32 >= k_MaxSensorlessAuthority_Uls_f32)>False" TS3.18" if (SensorlessAuthority_Uls_T_f32 >= k_MinSensorlessAuthority_Uls_f32)>False" TS3.19" else if (AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_SENSORINVALID)>True if ((k_UseTurnsCntr_Cnt_lgc == FALSE)>True && (I2CHwAbsPosValid_Cnt_T_lgc == TRUE)>False) if (k_UseTurnsCntr_Cnt_lgc == TRUE)>False if (SensorlessAuthority_Uls_T_f32 >= k_MinSensorlessAuthority_Uls_f32)>True if (SensorlessAuthority_Uls_T_f32 >= k_MaxSensorlessAuthority_Uls_f32)>True" TS3.20" if ((k_UseTurnsCntr_Cnt_lgc == FALSE) && (I2CHwAbsPosValid_Cnt_T_lgc == TRUE))=>True" TS3.21" if ((k_UseTurnsCntr_Cnt_lgc == FALSE) && (I2CHwAbsPosValid_Cnt_T_lgc == TRUE))=>False if (k_UseTurnsCntr_Cnt_lgc == TRUE)>True if (Abs_f32_m(Rte_Pim_EOLVehCntrOffset()->EOLVehCntrOffset_HwDeg_f32 - D_INVALIDOFFSET_HWDEG_F32)) < FLT_EPSILON)>False" TS3.22" if (SensorlessAuthority_Uls_T_f32 >= k_MaxSensorlessAuthority_Uls_f32)>False" TS3.23" if (SensorlessAuthority_Uls_T_f32 >= k_MaxSensorlessAuthority_Uls_f32)>True" TS3.24"</pre> |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

```
if ((Abs_f32_m(Rte_Pim_EOLVehCntrOffset()->EOLVehCntrOffset_HwDeg_f32 - D_INVALIDOFFSET_HWDEG_F32)) < FLT_EPSILON)=>True
if (NmMStatus_Cnt_T_enum != NVM_REQ_PENDING )=>True"
TS3.25if ( NmMStatus_Cnt_T_enum != NVM_REQ_PENDING )=>False
TS3.26"
if (SensorlessAuthority_Uls_T_f32 >= k_MinSensorlessAuthority_Uls_f32)=>False"
TS3.27"
if (SensorlessAuthority_Uls_T_f32 >= k_MaxSensorlessAuthority_Uls_f32)=>False"
TS3.28"
if ( NmMStatus_Cnt_T_enum != NVM_REQ_PENDING )=>False"
TS3.29"
(AbsHwPos_HandwheelAuthority_Uls_M_f32>=D_MAXAUTHORITY_ULS_F32)=>True"
TS3.30"(AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_SENSORINVALID)=False,(AbsHwPos_HwPosState_Cnt_M_enum ==
HWPOS_STATE_SENSORINVALID)=False,(AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_SENSORINVALID)=True,
(AbsHwPos_HwPosState_Cnt_M_enum == HWPOS_STATE_SENSORINVALID)=True( (k_UseTurnsCntr_Cnt_Igc == FALSE) &&
(I2CHwAbsPosValid_Cnt_T_Igc == TRUE) )=False,(k_UseTurnsCntr_Cnt_Igc == TRUE)=True,
((Abs_f32_m(Rte_Pim_EOLVehCntrOffset()->EOLVehCntrOffset_HwDeg_f32 - D_INVALIDOFFSET_HWDEG_F32)) >= FLT_EPSILON)=True,
( NmMStatus_Cnt_T_enum != NVM_REQ_PENDING )=False,(SensorlessAuthority_Uls_T_f32 >= k_MinSensorlessAuthority_Uls_f32)=True,
(SensorlessAuthority_Uls_T_f32 >= k_MaxSensorlessAuthority_Uls_f32)=True,(AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc == TRUE)=False"
```

Test Step 3.1 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|---------------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -10125 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -1600 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -1600 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -10125 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -11725 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -11725 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1600 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 0 | | |
| k_I2CHwAuthority_Uls_f32 | 0 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 0 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0 | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 0 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -900 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -1600 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 0 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -1600 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 0 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 0 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -10125 | -10125 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -1600 | -1600 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1 | 1 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -1600 | -1600 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|----------------|--------|
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -11725 | -11725 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -11725 | -11725 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1600 | -1600 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 0 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0 | 0 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -1600 | -1600 ± 0.009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 3.2 (Repeat Count = 1) | |
|--|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 10125 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 1 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1600 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 1 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1600 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 10125 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 11725 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 1 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 11725 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 1 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1620 |
| k_I2CHwAuthority_Uls_f32 | 1 |
| k_MaxSensorlessAuthority_Uls_f32 | 1 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1600 |
| k_MinSensorlessAuthority_Uls_f32 | 1 |
| k_TurnsCntrAuthority_Uls_f32 | 1 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1600 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 900 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 1 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 1600 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 255 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 43690 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 5 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|---|--|--------------------------|--------|
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 10125 | 10125 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 11725 | 11725 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 11725 | 11725 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 11725 | 11725 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0 | 0 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 3.3 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 9556.42969 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.75999999 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 204.899994 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.75999999 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1348 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 442.940002 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 442.940002 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 26817 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 123.400002 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.75999999 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 5054.37012 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 8178.93018 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.75 |
| k_HwPosOutputLPFError_HwDeg_f32 | 674.299988 |
| k_I2CHwAuthority_Uls_f32 | 0.75 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.839999974 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 822.799988 |
| k_MinSensorlessAuthority_Uls_f32 | 0.75 |
| k_TurnsCntrAuthority_Uls_f32 | 0.75 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 674.299988 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -291.100006 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.839999974 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 1587.5 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 128 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2325.1001 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|---------------------|--------|
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 9299 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 3 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_u16 | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 442.940002 | 442.940002 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.75999999 | 0.75999999 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 3890.49731 | 3890.49731 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1348 | 1348 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT | HWPOS_STATE_INIT | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 3890.49731 | 3890.49731 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 26817 | 26817 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 123.400002 | 123.400002 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.75999999 | 0.75999999 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 5054.37012 | 5054.37012 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 8178.93018 | 8178.93018 ± 0.009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.75999999 | 0.75999999 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 1348 | 1348 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 26817 | 26817 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 123.400002 | 123.400002 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 2325.1001 | 2325.1001 ± 0.009 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 3.4 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 1617.32996 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.409999996 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1527.19995 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.409999996 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 400 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 224.100006 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 224.100006 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 24657 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -3928.44995 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.409999996 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -7755.08984 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1555.08997 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.400000006 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1565.30005 |
| k_I2CHwAuthority_Uls_f32 | 0.400000006 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.49000001 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 0 |
| k_MinSensorlessAuthority_Uls_f32 | 0.400000006 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| k_TurnsCntrAuthority_Uls_f32 | 0.400000006 |
| k_UseTurnsCntr_Cnt_Igc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1208.90002 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 602.599976 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.49000001 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 884 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 224 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 1268.09998 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 4959 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 4 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|--------------------------|--------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 224.100006 | 224.100006 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.00999999046 | 0.00999999978 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -2278.53882 | -2278.53882 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -2278.53882 | -2278.53882 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -7755.08984 | -7755.08984 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 733.329956 | 733.330017 ± 0.0009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_Igc | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.00999999046 | 0.00999999978 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 733.329956 | 733.330017 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 3.5 (Repeat Count = 1) | |
|--|---------------------------|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 898.809998 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.430000007 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 567.400024 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.430000007 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 416 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 243.300003 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 243.300003 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 24801 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|--------------------------|--------|
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 4209.93018 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.430000007 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -409.950012 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 7543.18018 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0.419999987 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 50.5999985 | | |
| k_I2CHwAuthority_Uls_f32 | 0.419999987 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.50999999 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1416.80005 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.419999987 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.419999987 | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1268.30005 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 642.799988 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.50999999 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 924.200012 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 236 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 1328.5 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 5207 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 0 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 243.300003 | 243.300003 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.0100000203 | 0.00999999978 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 147.139496 | 147.139496 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 147.139496 | 147.139496 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -409.950012 | -409.950012 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -25.3900146 | -25.3899994 ± 0.00009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.0100000203 | 0.00999999978 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 147.139496 | 147.139496 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 1328.5 | 1328.5 ± 0.009 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

TEST DETAILS REPORT

AbsHwPos_Per2

2017-02-06, 13:51:17+0530



| Test Step 3.6 (Repeat Count = 1) | | | | ✓ |
|--|--|---|--------------------------|--------|
| Name | | Input Value | | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | 1000.25 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | | 0.600000024 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | | 281 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | | 0.600000024 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | | 4596 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | | HWPOS_STATE_INPUTINVALID | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | | 1031.06006 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | 123.400002 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | | 33949 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | | 281 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | | 0.600000024 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | | 1395.13 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | 65535 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | | 1 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 1 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | | 0.140000001 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | | 1149.5 | | |
| k_I2CHwAuthority_Uls_f32 | | 0.140000001 | | |
| k_MaxSensorlessAuthority_Uls_f32 | | 0.230000004 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | | 110 | | |
| k_MinSensorlessAuthority_Uls_f32 | | 0.140000001 | | |
| k_TurnsCntrAuthority_Uls_f32 | | 0.140000001 | | |
| k_UseTurnsCntr_Cnt_lgc | | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | | 1535.59998 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | | 100.099998 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | | 1 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | | 0.230000004 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | | -448.700012 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | | 244 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | | 5719.3999 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | | 12895 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | 2 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | | target_Pim_EOLVehCntrOffset | | |
| Name | | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | | 123.400002 | 123.400002 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | | 0.460000038 | 0.460000008 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | | 1395.13 | 1395.13 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | | 1395.13 | 1395.13 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | | 65535 | 65535 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | | 1395.13 | 1395.13 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | 1448.94995 | 1448.94995 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | | 0 | 0 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | | 0.460000038 | 0.460000008 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | | 1395.13 | 1395.13 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | | 65535 | 65535 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|-------------------|--------|
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 5719.3999 | 5719.3999 ± 0.009 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 3.7 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|---------------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 269.209991 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.200000003 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 605.179993 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.200000003 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 96 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1507.09998 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 1507.09998 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21921 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 8920.66016 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.200000003 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 202.529999 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 1358.69995 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0.200000003 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 436.700012 | | |
| k_I2CHwAuthority_Uls_f32 | 0.200000003 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.109999999 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 139.699997 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.200000003 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.200000003 | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 80.3000031 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 120.199997 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.109999999 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 120.199997 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 255 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 120.5 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 247 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 2 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 1507.09998 | 1507.09998 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 524.650024 | 524.650024 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 524.650024 | 524.650024 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|--------------------|--------|
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 8920.66016 | 8920.66016 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 202.529999 | 202.529999 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 1358.69995 | 1358.69995 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0 | 0 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 524.650024 | 524.650024 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 120.5 | 120.5 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 3.8 (Repeat Count = 1) | |
|--|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 691.429993 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.5 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -666.02002 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.5 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 120 |
| AbsHwPos_HwPosState_Cnt_M_enum | 6 |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 567.400024 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 567.400024 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 22137 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 8602.78027 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.5 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 120.199997 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -980.599976 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.5 |
| k_HwPosOutputLPFError_HwDeg_f32 | 525.799988 |
| k_I2CHwAuthority_Uls_f32 | 0.5 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.140000001 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 228.800003 |
| k_MinSensorlessAuthority_Uls_f32 | 0.5 |
| k_TurnsCntrAuthority_Uls_f32 | 0.5 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 169.399994 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 180.5 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.140000001 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 180.5 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 14 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 211.100006 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 619 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 5 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|---|--|--------------------------|--------|
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntOffset | target_Pim_EOLVehCntOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 567.400024 | 567.400024 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.5 | 0.5 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 120.199997 | 120.199997 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 120 | 120 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 120.199997 | 120.199997 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 22137 | 22137 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 8602.78027 | 8602.78027 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.5 | 0.5 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 120.199997 | 120.199997 ± 0.1 | ✔ |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | -980.599976 | -980.599976 ± 0.0009 | ✔ |
| AbsHwPos_VehCntOfstLearn_Cnt_M_lgc | 0 | 0 | ✔ |
| AbsHwPos_VehCntValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.5 | 0.5 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 120.199997 | 120.199997 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 120 | 120 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 22137 | 22137 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✔ |
| target_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 211.100006 | 211.100006 ± 0.0009 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 3.9 (Repeat Count = 1) | |
|--|--|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 128.470001 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.100000001 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 584.900024 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.100000001 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 88 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1487 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 1487 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21849 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 9026.62012 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.100000001 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 2802.53003 |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | 1328.5 |
| AbsHwPos_VehCntOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.100000001 |
| k_HwPosOutputLPFError_HwDeg_f32 | 407 |
| k_I2CHwAuthority_Uls_f32 | 0.100000001 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.100000001 |
| k_MaxVehCntOffDiff_HwDeg_f32 | 110 |
| k_MinSensorlessAuthority_Uls_f32 | 0.100000001 |
| k_TurnsCntAuthority_Uls_f32 | 0.100000001 |
| k_UseTurnsCnt_Cnt_lgc | 1 |
| k_VehCntOffValidLimit_HwDeg_f32 | 50.5999985 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 100.099998 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.100000001 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 100.099998 |
| target_AbsHwPos_Per2_TurnsCntValidity_Cnt_u08.value | 0 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 90.3000031 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 123 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 1 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|------------------|----------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 1487 | 1487 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 806.663025 | 806.663025 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT | HWPOS_STATE_INIT | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 806.663025 | 806.663025 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 9026.62012 | 9026.62012 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 2802.53003 | 2802.53003 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 1328.5 | 1328.5 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0 | 0 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 806.663025 | 806.663025 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 90.3000031 | 90.3000031 ± 0.00009 | ✓ |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 3.10 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 972.909973 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.699999988 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 706.580017 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.699999988 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 136 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -1000.29999 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -1000.29999 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 22281 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -8390.86035 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.699999988 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 160.399994 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -941.200012 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.699999988 |
| k_HwPosOutputLPFError_HwDeg_f32 | 585.200012 |
| k_I2CHwAuthority_Uls_f32 | 0.699999988 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.159999996 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|---------------------------|--------|
| k_MaxVehCntrOffDiff_HwDeg_f32 | 288.200012 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.699999988 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.699999988 | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 228.800003 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 220.699997 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.159999996 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 220.699997 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 26 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 271.5 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 867 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 1 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -1000.29999 | -1000.29999 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 324.253998 | 324.253998 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 324.253998 | 324.253998 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -8390.86035 | -8390.86035 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 160.399994 | 160.399994 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -941.200012 | -941.200012 ± 0.0009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0 | 0 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 324.253998 | 324.253998 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 271.5 | 271.5 ± 0.0009 | ✔ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 3.11 (Repeat Count = 1)

| Name | Input Value |
|--|------------------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 1395.13 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.109999999 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 767.419983 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.109999999 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 160 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1547.30005 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 1547.30005 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 22497 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -8072.97021 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.109999999 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 220.699997 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 664.099976 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.100000001 |
| k_HwPosOutputLPFError_HwDeg_f32 | 674.299988 |
| k_I2CHwAuthority_Uls_f32 | 0.100000001 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.189999998 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 377.299988 |
| k_MinSensorlessAuthority_Uls_f32 | 0.100000001 |
| k_TurnsCntrAuthority_Uls_f32 | 0.100000001 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 317.899994 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 281 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.189999998 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 281 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 44 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 362.100006 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 1239 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 4 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrIComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrIComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrIComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrIComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|-------------------------|-------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 1547.30005 | 1547.30005 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.100000001 | 0.100000001 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 713.913757 | 713.913818 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 2 | 2 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 713.913757 | 713.913818 ± 0.1 | ✓ |
| AbsHwPos_SrIComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✓ |
| AbsHwPos_SrIComHwPos_HwDeg_M_f32 | 281 | 281 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.100000001 | 0.100000001 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 281 | 281 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 664.099976 | 664.099976 ± 0.0009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.100000001 | 0.100000001 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 713.913757 | 713.913818 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 2 | 2 | ✓ |
| target_AbsHwPos_Per2_SrIComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ |
| target_AbsHwPos_Per2_SrIComHwPos_HwDeg_f32.value | 281 | 281 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

Test Step 3.12 (Repeat Count = 1)

| Name | Input Value |
|--|---|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -409.950012 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.300000012 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 625.460022 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.300000012 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 104 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1527.19995 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 1527.19995 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21993 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 8814.7002 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.300000012 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 9802.53027 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 1388.90002 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.300000012 |
| k_HwPosOutputLPFError_HwDeg_f32 | 466.399994 |
| k_I2CHwAuthority_Uls_f32 | 0.300000012 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.119999997 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 169.399994 |
| k_MinSensorlessAuthority_Uls_f32 | 0.300000012 |
| k_TurnsCntrAuthority_Uls_f32 | 0.300000012 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 110 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 140.300003 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.119999997 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 140.300003 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 100 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 150.699997 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 371 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 3 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|-------------------------|-------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 1527.19995 | 1527.19995 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 3378.58105 | 3378.58105 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 3378.58105 | 3378.58105 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 8814.7002 | 8814.7002 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 9802.53027 | 9802.53027 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 1388.90002 | 1388.90002 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0 | 0 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|---------------------|--------|
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 150.699997 | 150.699997 ± 0.0009 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|---|-------|---|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 3.13 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|-------------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -7306.20996 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.529999971 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 139.100006 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.529999971 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 9496 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INIT | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 389.200012 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 389.200012 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 25521 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 5617.33008 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.529999971 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 1817.34998 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 691.429993 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0.519999981 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 347.600006 | | |
| k_I2CHwAuthority_Uls_f32 | 0.519999981 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.610000014 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 139.699997 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.519999981 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0 | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1565.30005 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 843.799988 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.610000014 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 1125.19995 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 100 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -862.400024 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 6447 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 4 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 389.200012 | 389.200012 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.00999999046 | 0.00999999978 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -4173.37207 | -4173.37207 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1 | 1 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -4173.37207 | -4173.37207 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -7997.64014 | -7997.64014 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|---------------|----------------------|--------|
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -7997.64014 | -7997.64014 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 691.429993 | 691.429993 ± 0.0009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.00999999046 | 0.0099999978 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

Test Step Call Trace ✓

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 3.14 (Repeat Count = 1) ✓

| Name | Input Value |
|--|---|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -1676.60999 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.129999995 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -807.97998 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.129999995 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 176 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -901.799988 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -901.799988 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 22641 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 7861.0498 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.129999995 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 260.899994 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 724.5 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.119999997 |
| k_HwPosOutputLPFError_HwDeg_f32 | 733.700012 |
| k_I2CHwAuthority_Uls_f32 | 0.119999997 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.209999993 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 436.700012 |
| k_MinSensorlessAuthority_Uls_f32 | 0.119999997 |
| k_TurnsCntrAuthority_Uls_f32 | 0.119999997 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 377.299988 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 321.200012 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 1 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 321.200012 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 100 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 422.5 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 1487 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 0 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|---|--|-------------------------|--------|
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -901.799988 | -901.799988 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.119999997 | 0.119999997 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -1015.08685 | -1015.08691 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1 | 1 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -1015.08685 | -1015.08691 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -2401.10986 | -2401.11011 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.119999997 | 0.119999997 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -2401.10986 | -2401.11011 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 724.5 | 724.5 ± 0.0009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.119999997 | 0.119999997 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -1015.08685 | -1015.08691 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 3.15 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -2661.79004 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.200000003 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 949.940002 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.200000003 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 232 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -941.200012 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -941.200012 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 23145 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 972.909973 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.200000003 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 6883.99023 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 935.900024 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.189999998 |
| k_HwPosOutputLPFError_HwDeg_f32 | 941.599976 |
| k_I2CHwAuthority_Uls_f32 | 0.189999998 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.280000001 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 644.599976 |
| k_MinSensorlessAuthority_Uls_f32 | 0.189999998 |
| k_TurnsCntrAuthority_Uls_f32 | 0.189999998 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 585.200012 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -900 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.280000001 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 461.899994 |
| target_AbsHwPos_Per2_TurnsCntValidity_Cnt_u08.value | 98 |
| target_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 633.900024 |
| target_Pim_EOLVehCntOffset.EOLHwPosTrimPerformed_Cnt_u16 | 2355 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus_ErrorStatus | 1 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_u16 | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntOffset | target_Pim_EOLVehCntOffset |

| Name | Actual Value | Expected Value | Result |
|--|-------------------------|-------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -941.200012 | -941.200012 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.189999998 | 0.189999998 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -900 | -900 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 2 | 2 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -900 | -900 ± 0.1 | ✓ |
| AbsHwPos_SrComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✓ |
| AbsHwPos_SrComHwPos_HwDeg_M_f32 | -900 | -900 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.189999998 | 0.189999998 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -900 | -900 ± 0.1 | ✓ |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | 935.900024 | 935.900024 ± 0.0009 | ✓ |
| AbsHwPos_VehCntOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.189999998 | 0.189999998 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -900 | -900 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 2 | 2 | ✓ |
| target_AbsHwPos_Per2_SrComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ |
| target_AbsHwPos_Per2_SrComHwPos_HwDeg_f32.value | -900 | -900 ± 0.1 | ✓ |
| target_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 633.900024 | 633.900024 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 3.16 (Repeat Count = 1) | |
|--|--|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 1958.08997 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.150000006 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 848.539978 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.150000006 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 192 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 126.699997 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 126.699997 |
| AbsHwPos_SrComHwPosStatus_Cnt_M_u16 | 22785 |
| AbsHwPos_SrComHwPos_HwDeg_M_f32 | 269.209991 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.150000006 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 301.100006 |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | 784.900024 |
| AbsHwPos_VehCntOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.140000001 |
| k_HwPosOutputLPFError_HwDeg_f32 | 793.099976 |
| k_I2CHwAuthority_Uls_f32 | 0.140000001 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|---------------------------|--------|
| k_MaxSensorlessAuthority_Uls_f32 | 0.230000004 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 496.100006 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.140000001 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.140000001 | | |
| k_UseTurnsCntr_Cnt_Igc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 436.700012 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 461.899994 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.230000004 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | -1600 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 68 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 482.899994 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 1735 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 2 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 126.699997 | 126.699997 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.150000006 | 0.150000006 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 301.100006 | 301.100006 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 192 | 192 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 301.100006 | 301.100006 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 269.209991 | 269.209991 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.150000006 | 0.150000006 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 301.100006 | 301.100006 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 784.900024 | 784.900024 ± 0.0009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc | 1 | 1 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_Igc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.150000006 | 0.150000006 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 301.100006 | 301.100006 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 192 | 192 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 269.209991 | 269.209991 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 482.899994 | 482.899994 ± 0.0009 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 3.17 (Repeat Count = 1) | |
|--|-------------------------|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 3787.70996 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.280000001 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1112.18005 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.280000001 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 296 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1207.09998 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 1207.09998 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 23721 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 2098.83008 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.280000001 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 8009.91016 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1132.57996 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.270000011 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1179.19995 |
| k_I2CHwAuthority_Uls_f32 | 0.270000011 |
| k_MaxSensorlessAuthority_Uls_f32 | 1 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 882.200012 |
| k_MinSensorlessAuthority_Uls_f32 | 0.270000011 |
| k_TurnsCntrAuthority_Uls_f32 | 0.270000011 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 822.799988 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -271.399994 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.360000014 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 622.700012 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 100 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 875.5 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 3347 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 3 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|-------------------------|-------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 1207.09998 | 1207.09998 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.360000014 | 0.360000014 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 622.700012 | 622.700012 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3 | 3 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 622.700012 | 622.700012 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 2098.83008 | 2098.83008 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.360000014 | 0.360000014 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 622.700012 | 622.700012 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1132.57996 | -1132.57996 ± 0.009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.360000014 | 0.360000014 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 622.700012 | 622.700012 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 3 | 3 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 875.5 | 875.5 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Test Step 3.18 (Repeat Count = 1) | | | | ✓ |
|--|-------------------------|---|--------|---|
| Name | | Input Value | | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | | 4209.93018 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | | 0.310000002 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | | 1173.02002 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | | 0.310000002 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | | 320 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | | HWPOS_STATE_SENSORVALID | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | | -448.700012 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | | -448.700012 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | | 23937 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | | 2521.05005 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | | 0.310000002 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | | 8814.7002 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | | 8814.7002 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | | 1 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | | 0 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | | 0.200000003 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | | 1268.30005 | | |
| k_I2CHwAuthority_Uls_f32 | | 0.200000003 | | |
| k_MaxSensorlessAuthority_Uls_f32 | | 0.389999986 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | | 1060.40002 | | |
| k_MinSensorlessAuthority_Uls_f32 | | 1 | | |
| k_TurnsCntrAuthority_Uls_f32 | | 0.200000003 | | |
| k_UseTurnsCntr_Cnt_lgc | | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | | 911.900024 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | | -212.300003 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | | 2 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | | 0.389999986 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | | 683 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | | 100 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | | 966.099976 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | | 3719 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | 0 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result | |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -448.700012 | -448.700012 ± 0.0009 | ✓ | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.109999999 | 0.109999999 ± 0.004 | ✓ | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 3541.94092 | 3541.94092 ± 0.1 | ✓ | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 320 | 320 | ✓ | |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✓ | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 3541.94092 | 3541.94092 ± 0.1 | ✓ | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✓ | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 2521.05005 | 2521.05005 ± 0.1 | ✓ | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 8814.7002 | 8814.7002 ± 0.1 | ✓ | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 8814.7002 | 8814.7002 ± 0.009 | ✓ | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ | |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.109999999 | 0.109999999 ± 0.004 | ✓ | |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ | |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 320 | 320 | ✓ | |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|----------------|--------|
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 3.19 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|---------------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 1535.87 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.119999997 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 787.700012 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.119999997 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 168 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1567.40002 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 1567.40002 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 22569 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 7967.00977 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.119999997 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 240.800003 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 694.299988 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0.109999999 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 704 | | |
| k_I2CHwAuthority_Uls_f32 | 0.109999999 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.200000003 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 407 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.109999999 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.109999999 | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 347.600006 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 301.100006 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.200000003 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 301.100006 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 50 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 392.299988 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 1363 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 5 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 1567.40002 | 1567.40002 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.200000003 | 0.200000003 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 729.307983 | 729.307983 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3 | 3 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|---------------------|--------|
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 729.307983 | 729.307983 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 301.100006 | 301.100006 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.200000003 | 0.200000003 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 301.100006 | 301.100006 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 694.299988 | 694.299988 ± 0.0009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.200000003 | 0.200000003 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 729.307983 | 729.307983 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 3 | 3 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 301.100006 | 301.100006 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 392.299988 | 392.299988 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 3.20 (Repeat Count = 1) | |
|--|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 2802.53003 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.209999993 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 970.219971 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.209999993 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 240 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -921.5 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -921.5 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 23217 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 1113.65002 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.209999993 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 7024.72998 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 966.099976 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.200000003 |
| k_HwPosOutputLPFError_HwDeg_f32 | 971.299988 |
| k_I2CHwAuthority_Uls_f32 | 0.200000003 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.289999992 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 674.299988 |
| k_MinSensorlessAuthority_Uls_f32 | 0.200000003 |
| k_TurnsCntrAuthority_Uls_f32 | 0.200000003 |
| k_UseTurnsCntr_Cnt_lgc | 0 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 614.900024 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 900 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.289999992 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 482 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 104 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 664.099976 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 2479 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 2 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|---|--|-------------------------|--------|
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -921.5 | -921.5 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.200000003 | 0.200000003 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 955.473755 | 955.473816 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 2 | 2 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 955.473755 | 955.473816 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 900 | 900 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.200000003 | 0.200000003 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 900 | 900 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 966.099976 | 966.099976 ± 0.0009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.200000003 | 0.200000003 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 955.473755 | 955.473816 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 2 | 2 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 900 | 900 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 664.099976 | 664.099976 ± 0.0009 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 3.21 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -2239.57007 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.170000002 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 889.099976 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.170000002 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 208 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 151.5 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 151.5 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 22929 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 550.690002 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.170000002 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 6461.77002 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 845.299988 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.159999996 |
| k_HwPosOutputLPFError_HwDeg_f32 | 852.5 |
| k_I2CHwAuthority_Uls_f32 | 0.159999996 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.25 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 555.5 |
| k_MinSensorlessAuthority_Uls_f32 | 0.159999996 |
| k_TurnsCntrAuthority_Uls_f32 | 0.159999996 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 496.100006 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 502.100006 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.25 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 0 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| target_AbsHwPos_Per2_TurnsCntValidity_Cnt_u08.value | 80 |
| target_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 543.299988 |
| target_Pim_EOLVehCntOffset.EOLHwPosTrimPerformed_Cnt_u16 | 1983 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus_ErrorStatus | 4 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntOffset | target_Pim_EOLVehCntOffset |

| Name | Actual Value | Expected Value | Result |
|--|---------------------------|---------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 151.5 | 151.5 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.25 | 0.25 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3 | 3 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.25 | 0.25 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✓ |
| AbsHwPos_VehCntOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.25 | 0.25 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 3 | 3 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 3.22 (Repeat Count = 1) | |
|--|--|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -1676.60999 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.129999995 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -807.97998 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.129999995 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 176 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -901.799988 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | -901.799988 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 22641 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 7861.0498 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.129999995 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 260.899994 |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | 724.5 |
| AbsHwPos_VehCntOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.119999997 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| k_HwPosOutputLPFError_HwDeg_f32 | 733.700012 |
| k_I2CHwAuthority_Uls_f32 | 0.119999997 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.209999993 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 436.700012 |
| k_MinSensorlessAuthority_Uls_f32 | 0.119999997 |
| k_TurnsCntrAuthority_Uls_f32 | 0.119999997 |
| k_UseTurnsCntr_Cnt_Igc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 377.299988 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 321.200012 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.100000001 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 321.200012 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 100 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 422.5 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 1487 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 0 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|-------------------------|-------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | -901.799988 | -901.799988 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.119999997 | 0.119999997 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | -1015.08685 | -1015.08691 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 1 | 1 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | -1015.08685 | -1015.08691 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -2401.10986 | -2401.11011 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.119999997 | 0.119999997 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | -2401.10986 | -2401.11011 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 724.5 | 724.5 ± 0.0009 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_Igc | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.119999997 | 0.119999997 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | -1015.08685 | -1015.08691 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | -1600 | -1600 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 422.5 | 422.5 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 3.23 (Repeat Count = 1) | |
|--|-------------------------|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 3787.70996 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.280000001 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1112.18005 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.280000001 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 296 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|-------------------------|--------|
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1207.09998 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 1207.09998 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 23721 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 2098.83008 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.280000001 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 8009.91016 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1132.57996 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0.270000011 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 1179.19995 | | |
| k_I2CHwAuthority_Uls_f32 | 0.270000011 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.340000004 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 882.200012 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.270000011 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.270000011 | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 822.799988 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | -271.399994 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.360000014 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 622.700012 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 100 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 875.5 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 3347 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 3 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 1207.09998 | 1207.09998 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.360000014 | 0.360000014 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 622.700012 | 622.700012 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3 | 3 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 622.700012 | 622.700012 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 2098.83008 | 2098.83008 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.360000014 | 0.360000014 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 622.700012 | 622.700012 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -1132.57996 | -1132.57996 ± 0.009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.360000014 | 0.360000014 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 622.700012 | 622.700012 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 3 | 3 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |

TEST DETAILS REPORT

AbsHwPos_Per2

2017-02-06, 13:51:17+0530



Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_WriteBlock | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 3.24 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|---------------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -2239.57007 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.170000002 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 889.099976 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.170000002 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 208 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 151.5 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 151.5 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 22929 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 550.690002 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.170000002 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 6461.77002 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 845.299988 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0.159999996 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 852.5 | | |
| k_I2CHwAuthority_Uls_f32 | 0.159999996 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.25 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 555.5 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.159999996 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.159999996 | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 496.100006 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 502.100006 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.25 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 0 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 80 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 1983 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 4 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 151.5 | 151.5 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.25 | 0.25 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3 | 3 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos TargetHwAuthority Uls M f32 | 0.25 | 0.25 ± 0.004 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|----------------|--------|
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.25 | 0.25 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 3 | 3 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 3.25 (Repeat Count = 1) | |
|--|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -2239.57007 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.170000002 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 889.099976 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.170000002 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 208 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 151.5 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 151.5 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 22929 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 550.690002 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.170000002 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 6461.77002 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 845.299988 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.159999996 |
| k_HwPosOutputLPFError_HwDeg_f32 | 852.5 |
| k_I2CHwAuthority_Uls_f32 | 0.159999996 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.25 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 555.5 |
| k_MinSensorlessAuthority_Uls_f32 | 0.159999996 |
| k_TurnsCntrAuthority_Uls_f32 | 0.159999996 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 496.100006 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 502.100006 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.25 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 0 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 80 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 1983 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 2 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|---------------------------|--------|
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntOffset | target_Pim_EOLVehCntOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 151.5 | 151.5 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.25 | 0.25 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3 | 3 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.25 | 0.25 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |
| AbsHwPos_VehCntOfstLearn_Cnt_M_lgc | 0 | 0 | ✔ |
| AbsHwPos_VehCntValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.25 | 0.25 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 0 | 0 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 3 | 3 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✔ |
| target_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✔ |

| Test Step Call Trace | | | | |
|---|-------|---|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 3.26 (Repeat Count = 1) | |
|--|--|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 1535.87 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.119999997 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 787.700012 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.119999997 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 168 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1567.40002 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 1567.40002 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 22569 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 7967.00977 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.119999997 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 240.800003 |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | 694.299988 |
| AbsHwPos_VehCntOfstLearn_Cnt_M_lgc | 0 |
| AbsHwPos_VehCntValid_Cnt_M_lgc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.109999999 |
| k_HwPosOutputLPFError_HwDeg_f32 | 704 |
| k_I2CHwAuthority_Uls_f32 | 0.109999999 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.200000003 |
| k_MaxVehCntOffDiff_HwDeg_f32 | 407 |
| k_MinSensorlessAuthority_Uls_f32 | 0.409999996 |
| k_TurnsCntAuthority_Uls_f32 | 0.109999999 |
| k_UseTurnsCnt_Cnt_lgc | 0 |
| k_VehCntOffValidLimit_HwDeg_f32 | 347.600006 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 301.100006 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.200000003 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 301.100006 |
| target_AbsHwPos_Per2_TurnsCntValidity_Cnt_u08.value | 50 |
| target_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 392.299988 |
| target_Pim_EOLVehCntOffset.EOLHwPosTrimPerformed_Cnt_u16 | 1363 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|---|---|---------------------------|--------|
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 5 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_ | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 1567.40002 | 1567.40002 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.00999999791 | 0.0099999978 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 722.072021 | 722.072021 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 168 | 168 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 722.072021 | 722.072021 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65534 | 65534 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 7967.00977 | 7967.00977 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 240.800003 | 240.800003 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 694.299988 | 694.299988 ± 0.0009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc | 0 | 0 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_Igc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.00999999791 | 0.0099999978 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 722.072021 | 722.072021 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 168 | 168 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65534 | 65534 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 392.299988 | 392.299988 ± 0.0009 | ✔ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 3.27 (Repeat Count = 1) | |
|---|---|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 1535.87 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.119999997 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 787.700012 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.119999997 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 168 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1567.40002 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 1567.40002 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 22569 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 7967.00977 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.119999997 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 240.800003 |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 694.299988 |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_Igc | 0 |
| AbsHwPos_VehCntrValid_Cnt_M_Igc | 0 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 0.109999999 |
| k_HwPosOutputLPFError_HwDeg_f32 | 704 |
| k_I2CHwAuthority_Uls_f32 | 0.109999999 |
| k_MaxSensorlessAuthority_Uls_f32 | 0.319999993 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 407 |
| k_MinSensorlessAuthority_Uls_f32 | 0.109999999 |
| k_TurnsCntrAuthority_Uls_f32 | 0.109999999 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| k_UseTurnsCnt_Cnt_Igc | 0 |
| k_VehCntOffValidLimit_HwDeg_f32 | 347.600006 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc.value | 0 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 301.100006 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.200000003 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 301.100006 |
| target_AbsHwPos_Per2_TurnsCntValidity_Cnt_u08.value | 50 |
| target_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 392.299988 |
| target_Pim_EOLVehCntOffset.EOLHwPosTrimPerformed_Cnt_u16 | 1363 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntOffset_GetErrorStatus_ErrorStatus | 5 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_Igc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_Igc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntOffset | target_Pim_EOLVehCntOffset |

| Name | Actual Value | Expected Value | Result |
|--|---------------------------|---------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 1567.40002 | 1567.40002 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.200000003 | 0.200000003 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 729.307983 | 729.307983 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3 | 3 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 729.307983 | 729.307983 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 301.100006 | 301.100006 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.200000003 | 0.200000003 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 301.100006 | 301.100006 ± 0.1 | ✓ |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | 694.299988 | 694.299988 ± 0.0009 | ✓ |
| AbsHwPos_VehCntOfstLearn_Cnt_M_Igc | 0 | 0 | ✓ |
| AbsHwPos_VehCntValid_Cnt_M_Igc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | *none* | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | *none* | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.200000003 | 0.200000003 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 729.307983 | 729.307983 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 3 | 3 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 301.100006 | 301.100006 ± 0.1 | ✓ |
| target_Pim_EOLVehCntOffset.EOLVehCntOffset_HwDeg_f32 | 392.299988 | 392.299988 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

| Test Step 3.28 (Repeat Count = 1) | |
|--|--------------------------|
| Name | Input Value |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 10125 |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 1 |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 1600 |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 1 |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 1600 |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 10125 |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 11725 |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 1 |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 11725 |
| AbsHwPos_VehCntOffset_HwDeg_M_f32 | 65535 |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value |
|--|---|
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos |
| k_HwPosAuthorityStep_Uls_f32 | 1 |
| k_HwPosOutputLPFError_HwDeg_f32 | 1620 |
| k_I2CHwAuthority_Uls_f32 | 1 |
| k_MaxSensorlessAuthority_Uls_f32 | 1 |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 1600 |
| k_MinSensorlessAuthority_Uls_f32 | 1 |
| k_TurnsCntrAuthority_Uls_f32 | 1 |
| k_UseTurnsCntr_Cnt_lgc | 1 |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1600 |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 900 |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 2 |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 1 |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 1600 |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 255 |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 43690 |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 2 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset |

| Name | Actual Value | Expected Value | Result |
|--|--------------------------|--------------------------|--------|
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 10125 | 10125 ± 0.0009 | ✓ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 11725 | 11725 ± 0.1 | ✓ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 65534 | 65534 | ✓ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_INPUTINVALID | HWPOS_STATE_INPUTINVALID | ✓ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 11725 | 11725 ± 0.1 | ✓ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 65535 | 65535 | ✓ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✓ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0 | 0 ± 0.004 | ✓ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 11725 | 11725 ± 0.1 | ✓ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✓ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | 0 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0 | 0 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 1600 | 1600 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 65534 | 65534 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 65535 | 65535 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 65535 | 65535 ± 0.09 | ✓ |

| Test Step Call Trace | | | | | ✓ |
|---|-------|---|-------|--------|---|
| Actual Function | Count | Expected Function | Count | Result | |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ | |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ | |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ | |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ | |

| Test Step 3.29 (Repeat Count = 1) | | | | | ✓ |
|--------------------------------------|-------------|--|--|--|---|
| Name | Input Value | | | | |
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | 7024.72998 | | | | |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Input Value | | |
|--|---|-------------------------|--------|
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.50999999 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 114.300003 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.50999999 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 480 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 320.100006 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 320.100006 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 25377 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | -5335.8501 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.50999999 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 1535.87 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -409.950012 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0.5 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 288.200012 | | |
| k_I2CHwAuthority_Uls_f32 | 1 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.589999974 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 80.3000031 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.5 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.5 | | |
| k_UseTurnsCntr_Cnt_lgc | 0 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 1505.90002 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 803.599976 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 1 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.589999974 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 1085 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 28 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -901.799988 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 6199 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 2 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 320.100006 | 320.100006 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 1 | 1 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 465.843018 | 465.842987 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 2 | 2 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORVALID | HWPOS_STATE_SENSORVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 465.843018 | 465.842987 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 803.599976 | 803.599976 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 1 | 1 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 803.599976 | 803.599976 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | -409.950012 | -409.950012 ± 0.0009 | ✔ |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | 1 | ✔ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 0 | *none* | ✔ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 0 | *none* | ✔ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 1 | 1 ± 0.004 | ✔ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 465.843018 | 465.842987 ± 0.1 | ✔ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 2 | 2 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✔ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 803.599976 | 803.599976 ± 0.1 | ✔ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | -901.799988 | -901.799988 ± 0.0009 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

Test Step Call Trace

| Actual Function | Count | Expected Function | Count | Result |
|--|-------|--|-------|--------|
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |

Test Step 3.30 (Repeat Count = 1)

| Name | Input Value | | |
|--|---|---------------------------|--------|
| AbsHwPos_AlignedRelHwPos_HwDeg_M_f32 | -2239.57007 | | |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.170000002 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 889.099976 | | |
| AbsHwPos_HandwheelPositionLPF_M_str.K_Uls_f32 | 0.170000002 | | |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 208 | | |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | | |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 151.5 | | |
| AbsHwPos_RelHwPos_HwDeg_M_f32 | 151.5 | | |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 22929 | | |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 550.690002 | | |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.170000002 | | |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 6461.77002 | | |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 845.299988 | | |
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 1 | | |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 1 | | |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus(ErrorStatus) | target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | | |
| Rte_Inst_Ap_AbsHwPos | target_Rte_Inst_Ap_AbsHwPos | | |
| k_HwPosAuthorityStep_Uls_f32 | 0.159999996 | | |
| k_HwPosOutputLPFError_HwDeg_f32 | 852.5 | | |
| k_I2CHwAuthority_Uls_f32 | 0.159999996 | | |
| k_MaxSensorlessAuthority_Uls_f32 | 0.25 | | |
| k_MaxVehCntrOffDiff_HwDeg_f32 | 555.5 | | |
| k_MinSensorlessAuthority_Uls_f32 | 0.159999996 | | |
| k_TurnsCntrAuthority_Uls_f32 | 0.159999996 | | |
| k_UseTurnsCntr_Cnt_lgc | 1 | | |
| k_VehCntrOffValidLimit_HwDeg_f32 | 496.100006 | | |
| target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc.value | 0 | | |
| target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc.value | 1 | | |
| target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32.value | 502.100006 | | |
| target_AbsHwPos_Per2_ManufMode_Cnt_enum.value | 0 | | |
| target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32.value | 0.25 | | |
| target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32.value | 0 | | |
| target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08.value | 80 | | |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 543.299988 | | |
| target_Pim_EOLVehCntrOffset.EOLHwPosTrimPerformed_Cnt_u16 | 1983 | | |
| target_Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus_ErrorStatus | 2 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | target_AbsHwPos_Per2_DiagStatusHwPosReducedPerf_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_HwPosSource_Cnt_u16 | target_AbsHwPos_Per2_HwPosSource_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | target_AbsHwPos_Per2_I2CHwAbsPosValid_Cnt_lgc | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | target_AbsHwPos_Per2_I2CHwAbsPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_ManufMode_Cnt_enum | target_AbsHwPos_Per2_ManufMode_Cnt_enum | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | target_AbsHwPos_Per2_SensorlessAuthority_Uls_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SensorlessHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32 | | |
| target_Rte_Inst_Ap_AbsHwPos.AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | target_AbsHwPos_Per2_TurnsCntrValidity_Cnt_u08 | | |
| target_Rte_Inst_Ap_AbsHwPos.Pim_EOLVehCntrOffset | target_Pim_EOLVehCntrOffset | | |
| Name | Actual Value | Expected Value | Result |
| AbsHwPos_AlignedHwPos_HwDeg_M_f32 | 151.5 | 151.5 ± 0.0009 | ✔ |
| AbsHwPos_HandwheelAuthority_Uls_M_f32 | 0.25 | 0.25 ± 0.004 | ✔ |
| AbsHwPos_HandwheelPositionLPF_M_str.SV_Uls_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_HwPosSource_Cnt_M_u16 | 3 | 3 | ✔ |
| AbsHwPos_HwPosState_Cnt_M_enum | HWPOS_STATE_SENSORINVALID | HWPOS_STATE_SENSORINVALID | ✔ |
| AbsHwPos_PrevHandwheelPosition_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_SrlComHwPosStatus_Cnt_M_u16 | 21845 | 21845 | ✔ |
| AbsHwPos_SrlComHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_TargetHwAuthority_Uls_M_f32 | 0.25 | 0.25 ± 0.004 | ✔ |
| AbsHwPos_TempHwPos_HwDeg_M_f32 | 0 | 0 ± 0.1 | ✔ |
| AbsHwPos_VehCntrOffset_HwDeg_M_f32 | 65535 | 65535 ± 0.09 | ✔ |

TEST DETAILS REPORT

2017-02-06, 13:51:17+0530



AbsHwPos_Per2

| Name | Actual Value | Expected Value | Result |
|--|--------------|---------------------|--------|
| AbsHwPos_VehCntrOfstLearn_Cnt_M_lgc | 0 | 0 | ✓ |
| AbsHwPos_VehCntrValid_Cnt_M_lgc | 0 | 0 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) | 504 | 504 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) | 1 | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) | 1 | 1 | ✓ |
| target_AbsHwPos_Per2_HandwheelAuthority_Uls_f32.value | 0.25 | 0.25 ± 0.004 | ✓ |
| target_AbsHwPos_Per2_HandwheelPosition_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_AbsHwPos_Per2_HwPosSource_Cnt_u16.value | 3 | 3 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPosStatus_Cnt_u16.value | 21845 | 21845 | ✓ |
| target_AbsHwPos_Per2_SrlComHwPos_HwDeg_f32.value | 0 | 0 ± 0.1 | ✓ |
| target_Pim_EOLVehCntrOffset.EOLVehCntrOffset_HwDeg_f32 | 543.299988 | 543.299988 ± 0.0009 | ✓ |

| Test Step Call Trace | | | | |
|--|-------|--|-------|--------|
| Actual Function | Count | Expected Function | Count | Result |
| Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP0_CheckpointReached | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | Rte_Call_Ap_AbsHwPos_EOLVehCntrOffset_GetErrorStatus | 1 | ✓ |
| TrimNotPerfDiag | 1 | TrimNotPerfDiag | 1 | ✓ |
| Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | Rte_Call_Ap_AbsHwPos_NxtrDiagMgr_SetNTCStatus | 1 | ✓ |
| Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | Rte_Call_AbsHwPos_Per2_CP1_CheckpointReached | 1 | ✓ |