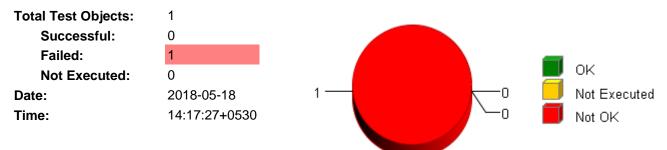


Summary

Overall Test Object Results (including Coverage)



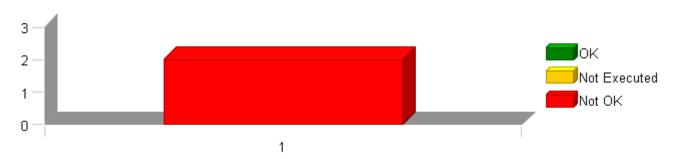
Selected Project Items

Test Object "CBD_Unittest/LoaMgr/LoaMgr_Per1"

Used Test Environments

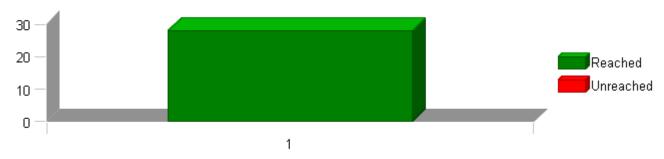
TI TMS 570 PLS UDE (Default)

Test Case Results for Each Test Object (without Coverage)



The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

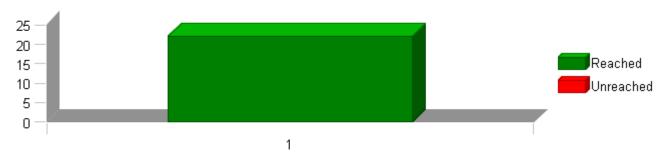
Statement (C0) Coverage: Total Statements for Each Test Object



The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

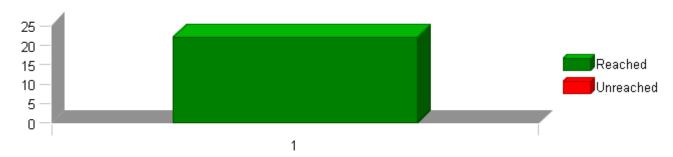


Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

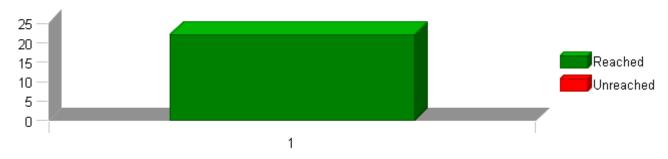
Decision Coverage: Total Decision Outcomes for Each Test Object



The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.

MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

TEST OVERVIEW REPORT

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Test Object List

Project LoaMgr

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	DC	MC/DC	Test Cases	Result
	LoaMgr	100 %	100 %	100 %	100 %	2 of 2 failed	×
	CBD_Unittest	100 %	100 %	100 %	100 %	2 of 2 failed	×
	LoaMgr	100 %	100 %	100 %	100 %	2 of 2 failed	×
1	LoaMgr_Per1	100 %	100 %	100 %	100 %	2 of 2 failed	×

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Project LoaMgr

LoaMgr

Test Object LoaMgr_Per1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MC/DC Coverage	100 %

Statistics

LoaMgr_Per1

Module

Total Testcases	2
Successful	0
Failed	2
Not Executed	0



Module Properties

Project Root Directory	D:\Synergy_Work_Area\LoaMgr_SF049A_4.3.0_0_NoUTP
Configuration File	D:\Synergy_Work_Area\LoaMgr_SF049A_4.3.0_0_NoUTP\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\LoaMgr\src\Ap_LoaMgr.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\LoaMgr\tools\contract -I\$(PROJECTROOT)\LoaMgr\tools\contract\Ap_LoaMgr -I\$ (PROJECTROOT)\StdDef\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(Compiler Install Path)\tinclude

Name	Text
lodule 'LoaMgr'	**************************************
•	Name of Tester:Pradnya Mhetre
	Code File(s) Under Test:Ap LoaMgr.c
	Code File(s) Version:11
	Module Design Version:9
	Requirement Document Version:NA
	Data Dictionary Version:8
	Unit Test Plan Version:5
	Optimization Level:Ogeneral
	Compiler (CodeGen) Version:TMS470_4.9.5
	Model Type:None
	Model Version:Nexteer EPS Unit Test Tool 1.0
	Total FLASH Used (Bytes):2244
	Total RAM Used (Bytes):27
	Total CALS Used (Bytes):76
	Special Test Requirements:NA
	Test Date:5/18/2018
	Comments:
	Note1:Inline functions defined in 'globalmacro.h' are not unit tested.
	Note2:The Expected output calculated from MATLAB model.
	Note3:Local functions ""LatchInputs()"", ""RqstRespConds()"", ""ArbResp()"", ""AssignScale()"",
	""RqstHwTqResp()"", ""ChooseFinalResp()"" and ""SwBasMtgtn()"" are not tested individually
	as they are tested in "LoaMgr_Per1" function.
	Please refer snapshot "LoaMgr_Per1_Coverage" for the same.
	Note4:In CSV_9 and CSV_10,value for boolean variable ""DiagcStslvtr1Inactv""
	given in "Simdata" is in float.
	Hence input values for "DiagcStslvtr1Inactv" are captured from Matlab design
	and given in Tessy for those test steps.
	Note5: Out of range value is given to the variable ""HwTqldptSig_Cnt_u08=5"" over the DD range {0,4},
	to cover the path at src line no.908 in TS5.1
	Note6: Out of range value is given to the variable ""MtrPosIdptSig_Cnt_u08=4"" over the DD range {0,3},
	to cover the path at src line no.918 in TS5.2
	Note7: Out of range value is given to the variable ""MotCurrIdptSig_Cnt_u08=3"" over the DD range {0,2},
	to cover the path at src line no.928 in TS5.3
	Note8:Output variable "LoaMgr_AgSV_Cnt_M_lgc" is failing
	because of Source Model Mismatch at src line no.1503
	Anomaly EA3#20083 is logged for the same.
	Mote9:Parameter of "Rte_Call_NxtrDiagMgr_SetNTCStatus" "Param_Cnt_T_u08" is failing
	because of Source Model Mismatch.
	Anomaly EA3#20083 is logged for the same.
	Note10:Output variable "LoaMgr_VltgMdSrcSV_Cnt_M_u08" is going out of range as [0 3]
	over DD range [0 1] in PRM test vector TS4.103
	Anomaly EA3#20083 is logged for the same.
	Note11:""CBD_Sandbox_dbg.map"" map file is embedded for reference.

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj</pre>
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	<pre>\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd</pre>
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	<pre>\$(ProgramFiles)\pls\UDE 4.4</pre>
Time Unit	cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\LoaMgr_SF049A_4.3.0_0_NoUTP\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP

LoaMgr_Per1



LoaMgr_Per1

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Test Case 1: PRM_R Test

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LoaMgr_Per1 Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles: CPU Cycles:

TS4.1 5037.00 Cycles
TS4.2 4978.00 Cycles
TS4.3 5037.00 Cycles
TS4.3 5037.00 Cycles
TS4.4 5026.00 Cycles
TS4.5 5003.00 Cycles
TS4.6 5017.00 Cycles
TS4.7 5052.00 Cycles
TS4.8 4969.00 Cycles
TS4.9 5023.00 Cycles
TS4.10 5056.00 Cycles
TS4.11 5015.00 Cycles
TS4.12 5022.00 Cycles
TS4.13 4963.00 Cycles
TS4.14 4970.00 Cycles
TS4.15 4957.00 Cycles
TS4.16 5059.00 Cycles
TS4.17 4989.00 Cycles
TS4.17 4989.00 Cycles
TS4.19 4976.00 Cycles
TS4.19 4976.00 Cycles TS4.18 4976.00 Cycles TS4.19 4993.00 Cycles TS4.20 4987.00 Cycles TS4.21 4951.00 Cycles TS4.22 5066.00 Cycles TS4.22 4997.00 Cycles TS4.24 4997.00 Cycles TS4.25 4965.00 Cycles TS4.26 5000.00 Cycles TS4.27 4990.00 Cycles TS4.28 4961.00 Cycles TS4.29 5022.00 Cycles TS4.30 4966.00 Cycles TS4.30 4966.00 Cycles TS4.31 5011.00 Cycles TS4.33 4998.00 Cycles TS4.33 4998.00 Cycles TS4.33 4999.00 Cycles TS4.33 4999.00 Cycles TS4.34 4999.00 Cycles TS4.35 4975.00 Cycles TS4.36 5469.00 Cycles TS4.37 4975.00 Cycles TS4.37 4975.00 Cycles TS4.38 4954.00 Cycles TS4.38 4935.00 Cycles TS4.40 4996.00 Cycles TS4.41 4934.00 Cycles TS4.42 5044.00 Cycles TS4.43 5006.00 Cycles TS4.43 5006.00 Cycles TS4.44 5011.00 Cycles TS4.46 5008.00 Cycles TS4.47 5012.00 Cycles TS4.48 5022.00 Cycles TS4.49 4950.00 Cycles TS4.50 5022.00 Cycles TS4.51 5015.00 Cycles TS4.52 4959.00 Cycles TS4.52 4959.00 Cycles TS4.53 4956.00 Cycles TS4.54 4933.00 Cycles TS4.55 5008.00 Cycles TS4.56 5012.00 Cycles TS4.57 5039.00 Cycles TS4.57 5039.00 Cycles TS4.58 4975.00 Cycles TS4.59 4987.00 Cycles TS4.60 4952.00 Cycles TS4.61 4916.00 Cycles TS4.62 5008.00 Cycles TS4.63 5012.00 Cycles TS4.64 5022.00 Cycles TS4.65 4958.00 Cycles TS4.65 4958.00 Cycles TS4.66 4987.00 Cycles TS4.67 4938.00 Cycles TS4.68 4916.00 Cycles TS4.70 4979.00 Cycles TS4.71 5022.00 Cycles TS4.71 5022.00 Cycles TS4.72 4952.00 Cycles TS4.73 4976.00 Cycles TS4.74 4938.00 Cycles TS4.75 4916.00 Cycles TS4.76 4997.00 Cycles TS4.77 4979.00 Cycles TS4.77 4979.00 Cycles TS4.77 4979.00 Cycles TS4.78 5022.00 Cycles TS4.78 5022.00 Cycles TS4.79 4952.00 Cycles TS4.80 4976.00 Cycles TS4.81 4938.00 Cycles TS4.82 4916.00 Cycles TS4.83 4997.00 Cycles TS4.84 4979.00 Cycles TS4.83 4997.00 Cycles
TS4.84 4979.00 Cycles
TS4.85 5022.00 Cycles
TS4.86 4921.00 Cycles
TS4.86 4921.00 Cycles
TS4.87 4916.00 Cycles
TS4.89 4979.00 Cycles
TS4.90 5022.00 Cycles
TS4.90 5022.00 Cycles
TS4.91 4976.00 Cycles
TS4.92 4938.00 Cycles
TS4.93 4916.00 Cycles
TS4.94 4997.00 Cycles
TS4.95 4979.00 Cycles
TS4.96 5022.00 Cycles
TS4.96 5022.00 Cycles
TS4.97 5039.00 Cycles
TS4.97 5039.00 Cycles
TS4.98 4975.00 Cycles
TS4.101 4916.00 Cycles
TS4.102 5008.00 Cycles
TS4.103 5022.00 Cycles
TS4.104 4952.00 Cycles
TS4.105 4976.00 Cycles
TS4.106 4938.00 Cycles
TS4.107 4989.00 Cycles
TS4.108 4976.00 Cycles
TS4.108 4976.00 Cycles
TS4.108 4976.00 Cycles
TS4.109 4993.00 Cycles

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TS4.109 4993.00 Cycles TS4.110 4987.00 Cycles TS4 109 4993 00

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LoaMgr_Per1



TS4.111 4938.00 Cycles





Description Vector Description:

TS4.1k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08==>Min TS4.2k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08==>Max TS4.3k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08==>Pos/default TS4.4k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08==>Min TS4.5k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08==>Max TS4.6k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08==>Pos/default TS4.7k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08==>Min TS4.8k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08==>Max TS4.8k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08==>Max
TS4.9k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08==>Pos/default
TS4.10k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08==>Min/default
TS4.11k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08==>Max
TS4.12k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08==>Pos
TS4.13k_LoaMgr_HwTqldptSig4Resp_Cnt_u08==>Min/default
TS4.14k_LoaMgr_HwTqldptSig4Resp_Cnt_u08==>Max
TS4.15k_LoaMgr_HwTqldptSig4Resp_Cnt_u08==>Pos
TS4.16k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08==>Min
TS4.17k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08==>Max
TS4.18k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08==>Pos/default
TS4.19k_LoaMgr_RedcdStSca_Uls_f32==>Min TS4.19k_LoaMgr_RedcdStSca_Uls_f32==>Min TS4.20k_LoaMgr_RedcdStSca_Uls_f32==>Max/default TS4.21k_LoaMgr_RedcdStSca_Uls_f32==>Pos TS4.21k_LoaMgr_RedcdStRate_UlspS_f32==>Min TS4.22k_LoaMgr_RedcdStRate_UlspS_f32==>Max TS4.23k_LoaMgr_RedcdStRate_UlspS_f32==>Defaut TS4.25k_LoaMgr_RedcdStRate_UlspS_f32==>Pos TS4.25k_LoaMgr_FadeOutStRate_UlspS_f32==>Min TS4.27k_LoaMgr_FadeOutStRate_UlspS_f32==>Min TS4.27k_LoaMgr_FadeOutStRate_UlspS_f32==>Max IS4.27k_LoaMgr_FadeOutstRate_UlspS_f32==>Max
TS4.28k_LoaMgr_FadeOutstRate_UlspS_f32==>Default
TS4.29k_LoaMgr_FadeOutstRate_UlspS_f32==>Pos
TS4.30k_LoaMgr_HwTqldptSigFltTnd_Cnt_u08==>Min
TS4.31k_LoaMgr_HwTqldptSigFltTnd_Cnt_u08==>Max
TS4.32k_LoaMgr_HwTqldptSigFltTnd_Cnt_u08==>Pos/default
TS4.33k_LoaMgr_MotAgldptSigFltTnd_Cnt_u08==>Min
TS4.34k_LoaMgr_MotAgldptSigFltTnd_Cnt_u08==>Max
TS4.34k_LoaMgr_MotAgldptSigFltTnd_Cnt_u08==>Max TS4.35k_LoaMgr_MotAgldptSigFltThd_Cnt_u08==>Pos/default TS4.36k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08==>Min/default TS4.37k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08==>Max TS4.38k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08==>Pos TS4.39k_LoaMgr_lvtrldptSigFltThd_Cnt_u08==>Min/default TS4.40k_LoaMgr_lvtrldptSigFltThd_Cnt_u08==>Max TS4.41k_LoaMgr_lvtrldptSigFitThd_Cnt_u08==>Pos TS4.41k_LoaMgr_lvtrldptSigFitThd_Cnt_u08==>Pos TS4.42k_LoaMgr_MotAgSmaAvailResp_Cnt_u08==>Min TS4.43k_LoaMgr_MotAgSmaAvailResp_Cnt_u08==>Max TS4.44k_LoaMgr_MotAgSmaAvailResp_Cnt_u08==>Pos/default TS4.45k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08==>Min TS4.46k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08==>Max/default TS4.47k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08==>Pos TS4.47k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08==>Pos TS4.48k_LoaMgr_MotAgldptSig2Resp_Cnt_u08==>Min TS4.49k_LoaMgr_MotAgldptSig2Resp_Cnt_u08==>Max TS4.50k_LoaMgr_MotAgldptSig2Resp_Cnt_u08==>Pos/default TS4.51k_LoaMgr_MotAgldptSig3Resp_Cnt_u08==>Min/default TS4.52k_LoaMgr_MotAgldptSig3Resp_Cnt_u08==>Pos TS4.53k_LoaMgr_MotAgldptSig3Resp_Cnt_u08==>Pos TS4.54k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08==>Min/default TS4.55k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08==>Max TS4.56k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08==>Pos TS4.57k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08==>Min/default TS4.58k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08==>Max TS4.59k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08==>Pos TS4.60k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08==>Min TS4.61k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08==>Max TS4.62k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08==>Pos/default TS4.63k_LoaMgr_lvtrldptSig1Resp_Cnt_u08==>Min/default TS4.64k_LoaMgr_lvtrldptSig1Resp_Cnt_u08==>Max TS4.65k_LoaMgr_lvtrldptSig1Resp_Cnt_u08==>Pos TS4.66k_LoaMgr_lvtrldptSig2Resp_Cnt_u08==>Min/default TS4.67k_LoaMgr_lvtrldptSig2Resp_Cnt_u08==>Max TS4.68k_LoaMgr_lvtrldptSig2Resp_Cnt_u08==>Pos TS4.69k_LoaMgr_HwTqMtgtnSca_Uls_f32==>Min TS4.70k_LoaMgr_HwTqMtgtnSca_Uls_f32==>Max/default TS4.71k_LoaMgr_HwTqMtgtnSca_Uls_f32==>Pos TS4.72k_LoaMgr_HwTqMtgtnRate_UlspS_f32==>Min TS4.73k_LoaMgr_HwTqMtgtnRate_UlspS_f32==>Max TS4.74k_LoaMgr_HwTqMtgtnRate_UlspS_f32==>Default TS4.75k_LoaMgr_HwTqMtgtnRate_UlspS_f32==>Pos TS4.76k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc==>Min TS4.77k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc==>Max TS4.78k_LoaMgr_MotAgMtgtnSca_Uls_f32==>Min TS4.78k_LoaMgr_MotAgMtgtnSca_Uls_f32==>Min
TS4.79k_LoaMgr_MotAgMtgtnSca_Uls_f32==>Max/default
TS4.80k_LoaMgr_MotAgMtgtnSca_Uls_f32==>Pos
TS4.81k_LoaMgr_MotAgMtgtnRate_UlspS_f32==>Min
TS4.82k_LoaMgr_MotAgMtgtnRate_UlspS_f32==>Max
TS4.83k_LoaMgr_MotAgMtgtnRate_UlspS_f32==>Default
TS4.84k_LoaMgr_MotAgMtgtnRate_UlspS_f32==>Pos
TS4.85k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc==>Min
TS4.86k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc=>Max
TS4.87k_LoaMgr_MotCurrMtgtnSca_Uls_f32==>Min
TS4.88k_LoaMgr_MotCurrMtgtnSca_Uls_f32==>Max/default
TS4.88k_LoaMgr_MotCurrMtgtnSca_Uls_f32==>Max/default
TS4.89k_LoaMgr_MotCurrMtgtnSca_Uls_f32==>Pos TS4.80k_LoaMgr_MotCurrMtgtnSca_Uls_132==>Max/defaul TS4.80k_LoaMgr_MotCurrMtgtnSca_Uls_132==>Pos TS4.90k_LoaMgr_MotCurrMtgtnRate_UlspS_f32==>Min TS4.91k_LoaMgr_MotCurrMtgtnRate_UlspS_f32==>Max TS4.92k_LoaMgr_MotCurrMtgtnRate_UlspS_f32==>Default TS4.93k_LoaMgr_MotCurrMtgtnRate_UlspS_f32==>Pos TS4.94k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_Igc==>Min



TS4.95k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc==>Max
TS4.96k_LoaMgr_lvtrMtgtnSca_Uls_f32==>Min
TS4.97k_LoaMgr_lvtrMtgtnSca_Uls_f32==>Max/default
TS4.97k_LoaMgr_lvtrMtgtnSca_Uls_f32==>Pos
TS4.99k_LoaMgr_lvtrMtgtnRate_UlspS_f32==>Min
TS4.100k_LoaMgr_lvtrMtgtnRate_UlspS_f32==>Max
TS4.101k_LoaMgr_lvtrMtgtnRate_UlspS_f32==>Pos
TS4.101k_LoaMgr_lvtrMtgtnRate_UlspS_f32==>Pos
TS4.103k_LoaMgr_lvtrMtgtnRate_UlspS_f32==>Pos
TS4.103k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc==>Min
TS4.104k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc==>Max
TS4.105k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32==>Max/Default
TS4.105k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32==>Max/Default
TS4.107k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32==>Pos
TS4.108k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32==>Max
TS4.109k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32==>Max
TS4.109k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32==>Max
TS4.110k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32==>Default
TS4.111k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32==>Default

Test Step 1.1 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotApMetaPlate_UlanS_623	2
k_LoaMgr_MotAgMtgtnSeqZepEn_Cot_loa	0
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k LoaMgr MotCurrldptSig2Resp Cnt u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k LoaMgr MotCurrMtgtnSca Uls f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value	0
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3
JJ	





Name	Input Value			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1			
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0			
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum			
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi	_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoa	MtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrld	ptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLo	oaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldp	otSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod	_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_	_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cr	nt_lgc		
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	0	0	~	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	_	
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	~	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1		
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	✓	
LoaMgr_lvtrRespSV_Cnt_M_u08	2	2		
LoaMgr_LOASt_State_M_enum	5	5	•	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500		
LoaMgr LoaScaleFctr Uls M f32	0	0	~	
LoaMgr MaxMtgtnResp Cnt M u08	5	5	_	
LoaMgr MotAgldptSigSV Cnt M u08	3	3	~	
LoaMgr MotAgLoaMtgtnEn Cnt M Igc	1	1	_	
LoaMgr MotAgRespSV Cnt M u08	4	4	✓	
LoaMgr MotCurrldptSigSV Cnt M u08	0	0	_	
LoaMgr MotCurrLoaMtgtnEn Cnt M lgc	0	0	•	
LoaMgr MotCurrRespSV Cnt M u08	3	3	_	
LoaMgr TloaAvail Cnt M lgc	1	1	•	
LoaMgr_TloaDi_Cnt_M_lgc	0	0	_	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 174}	{170, 172, 174}	_	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	_	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	✓	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	·	
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1		
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	*	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1		
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	·	
a.got_coa.ngi_i or i_motouricountguicii_orit_igo.value	•	•		

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
CalclvtrState	2	CalclvtrState	2	~	
LatchInputs	1	LatchInputs	1	~	
RqstRespConds	1	RqstRespConds	1	~	
RqstHwTqResp	1	RqstHwTqResp	1	~	
ArbResp	1	ArbResp	1	~	
ChooseFinalResp	1	ChooseFinalResp	1	~	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	-	
AssignScale	1	AssignScale	1	~	
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~	
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	V	

Test Step 1.2 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4





Name	Input Value		
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1		
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0		
LoaMgr_lvtrRespSV_Cnt_M_u08	5		
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2		
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0		
LoaMgr_MotAgRespSV_Cnt_M_u08	1		
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1		
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0		
LoaMgr_MotCurrRespSV_Cnt_M_u08	5		
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1		
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1		
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr		
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	5		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_Igc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k LoaMgr MotCurrldptSig1Resp Cnt u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSigFltThd Cnt u08	0		
k LoaMgr MotCurrMtgtnRate UlspS f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target LoaMgr Per1 DiagcStslvtr1Inactv Cnt Igc.value	1		
target_LoaMgr_Per1_DiagcStsIvtr1Mactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	1		
target_Loamgr_Per1_Diagcststvti2mactv_Crit_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
· - · · · · · ·	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value			
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0	to stand for more than 10 met 10 met	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsI		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsI		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldpt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoa		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_St		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoa		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrld	lptSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLo	oaMtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldp	otSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod	_Cnt_enum	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Ci	nt_lgc	
Name	Actual Value	Expected Value	Resu





Name	Actual Value	Expected Value	Result
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	✓
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	✓
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	✓
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	✓
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	✓
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	✓
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_MotAgRespSV_Cnt_M_u08	1	1	✓
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	✓
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	5	5	✓
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 175}	{170, 172, 175}	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	✓
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	✓
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	✓

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	V

Test Step 1.3 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2





Name	Input Value		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_UIs_f32	1		
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k LoaMgr lvtrMtgtnRate UlspS f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k LoaMgr lvtrMtgtnSca Uls f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1		
k LoaMgr MotAgSmaAvailResp Cnt u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSig2Resp Cnt u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k LoaMgr MotCurrMtgtnRate UlspS f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_UIs_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
	2		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value		nt lan	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target LoaMgr Per1 HwTqldptSig Cnt u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lot target_LoaMgr_Per1_LOASt_State_enum	yc .	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	' - '		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	t lao	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	_ :	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrloaMtatpEp_0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0	0	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	l=	
Name	Actual Value	Expected Value	Result

target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08	3	3	•
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	•
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	•
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓

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LoaMgr_Per1



Name	Actual Value	Expected Value	Result
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	•
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	•
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	•
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.4 (Repeat Count = 1)	□
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1

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LoaMgr_Per1

LoaMgr_Per1		MACH	
Name	Input Value		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSigFltThd Cnt u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lo	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Der1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	_,	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	I=	I
Name	Actual Value		
		Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08	1 0	1 0	*
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1 0 0	0 0	· ·
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	1 0 0 4	1 0 0 4	· · · · · · · · · · · · · · · · · · ·
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08	1 0 0 4 0	1 0 0 4 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1 0 0 4 0	1 0 0 4 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08	1 0 0 4 0 0 5	1 0 0 4 0 0 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	1 0 0 4 0 0 5 5	1 0 0 4 0 0 5 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UIspS_M_f32	1 0 0 4 0 0 5	1 0 0 4 0 0 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32	1 0 0 4 0 0 5 5 5 500	1 0 0 4 0 0 0 5 5 5 500	· · · · · · · · · · · · · · · · · · ·
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 0 0 4 0 0 0 5 5 5 5 500	1 0 0 4 0 0 0 5 5 5 5 500 1 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_ivtrIdptSigSV_Cnt_M_u08 LoaMgr_ivtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_ivtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	1 0 0 4 0 0 0 5 5 5 5 500 1 5	1 0 0 4 0 0 0 5 5 5 5 500 1 5	· · · · · · · · · · · · · · · · · · ·
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LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqlcoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LoaState_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1 0 0 0 4 0 0 0 5 5 5 5 5 5 5 5 5 5 5 5 5	1 0 0 4 0 0 0 5 5 5 500 1 5 0 0 0 5 0 1 2 1 1 1 {170, 172, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqlcoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	1 0 0 0 4 0 0 0 0 5 5 5 5 5 500 1 1 5 5 0 0 0 1 1 2 2 1 1 1 1 1 (170, 172, 173, 175) {1, 1, 1, 1, 1} {1, 1, 1, 1, 1} 500 1	1 0 0 0 4 0 0 5 5 5 5 500 1 5 0 0 1 1 2 1 1 1 {170, 172, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LoaStaleLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1 0 0 0 4 0 0 0 0 5 5 5 5 5 5 5 5 0 0 0 1 1 2 2 1 1 1 1 1 1 1 1 1 1 1 1 1	1 0 0 0 4 0 0 5 5 5 5 500 1 5 0 0 1 1 2 1 1 1 {170, 172, 173, 175} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IwtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1 0 0 0 4 0 0 5 5 5 5 500 1 5 0 0 1 1 2 1 1 1 {170, 172, 173, 175} {1, 1, 1, 1} {1, 1, 1, 1} 500 1 0 0	1 0 0 0 4 0 0 5 5 5 5 500 1 5 0 0 1 1 2 1 1 1 {170, 172, 173, 175} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 0 0	· · · · · · · · · · · · · · · · · · ·



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	✓

Test Step 1.5 (Repeat Count = 1)	Σ
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr MotAgldptSigSV Cnt M u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr MotCurrLoaMtgtnEn Cnt M lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	5
k LoaMgr HwTqldptSig1TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnRcaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2 0
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurridptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_UIs_f32	
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target LoaMgr Per1 HwTqldptSig Cnt u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target LoaMgr Per1 SteerMod Cnt enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target LoaMgr Per1 DiagcStslvtr1I	nactv Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2l	nactv Cnt Igc	
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqldptSig Cnt u08	target_LoaMgr_Per1_HwTqldptSig_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgt	_	
target Rte Inst Ap LoaMgr.LoaMgr Per1 lvtrLoaMtgtnEn Cnt lgc	target_LoaMgr_Per1_lvtrLoaMtgtnE		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtg		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSi		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaM		
target Rte Inst Ap LoaMgr.LoaMgr Per1 MtrPosldptSig Cnt u08	target_LoaMgr_Per1_MtrPosldptSig		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cn		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaAvail Cnt Igc	target_LoaMgr_Per1_TloaAvail_Cnt		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lg		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	*
LoaMgr HwTqldptSigSV Cnt M u08	1	1	✓
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	_
LoaMgr HwTqRespSV Cnt M u08	0	0	✓
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	_
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	_
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	_
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	_
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	_
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	✓
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	_
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	_
LoaMgr TloaAvail Cnt M lgc	1	1	~
LoaMgr TloaDi Cnt M lgc	0	0	_
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 173}	{170, 173}	_
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	{1, 1}	{1, 1}	
Rte Write Ap LoaMgr LoaRateLimit UlspS f32(data)	500	500 ± 0.01	·
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	
target LoaMgr Per1 HwTqLoaMtgtnEn Cnt Igc.value	0	0	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	·
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	·
and a second a second and a second a second and a second a second and a second and a second and			

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	✓
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	✓





Name	Test Step 1.6 (Repeat Count = 1)	3
Looking Lever Le		
Looking Fund Geological Count Jul 98 Looking Fund Geological Count Jul 99 Looking Fun		·
Looking Family County Looking		
Loading - Intrologisty, Crit M, Usile Loading - Intrologisty, Tabulanian, Crit Usile Loading - Intrologisty, Crit Loading - Intrologisty, Crit Loading - Intrologisty		
Landary Medicality Count May 6 Landary Medicality Count May 7 Landary Medicality Medicality Count Medicality Medicality Count Medicality		
Landary Land	LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
Looking / MAGAGINESS V. COIL M. USB 1	LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
Coating Management Cort Up	LoaMgr_lvtrRespSV_Cnt_M_u08	4
Leading J. MacCarding (SV) Cost M. J. 108 Rin J. J. Leading C. Cost M. J. 108 Rin J. Leading J. Cost M. J. 108 Leading J. MacCarding (SV) Cost M. J. 108 Leading J. MacCarding Cost M. 108 Leading J. MacCarding M. 108 Leading J. MacCa	LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
Loading McCurnordispiders Ord M 19	LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	
Looking Amount Processor Amount Looking Amount Processor Amoun	LoaMgr_MotAgRespSV_Cnt_M_u08	
Condings Seminarity Seminarity Condings Seminarity Seminar	LoaMgr_MotCurrldptSigSV_Cnt_M_u08	
Loading		
Loading / Uniford Space Loading / Unifor		
Name	· - · · ·	
L. Loadey_Contributingscales_Cont_USe_		·
Loading-Invitings Syll-base-landersp. Cnt_UBS 2		
L. Lookings / Marcifolds (19	k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	
Loading-Introducting-Ziman-Resp. Cnt. U88	k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	
		2
	k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
	k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
	k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
Loading-InvirightignSeau_Dis_1232	k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
Loading_NutrippisigResp_Cnt_u08	k_LoaMgr_HwTqMtgtnRate_UlspS_f32	
L. Lookligr / hvfridptSig2Resp_Crt u/08 0 L. Lookligr / hvfridptSig2Resp_Crt u/08 0 L. Lookligr / hvfridptSig2Resp_Crt u/08 0 L. Lookligr / hvfridptSig2Resp_Crt u/08 5 L. Lookligr / hvfridptSig2Resp_Crt u/08 1 L. Lookligr / hvfridptSig2Resp_Crt u/08 0 L. Lookligr / hvfridptSig3Resp_Crt u/08 2 L. Lookligr / hvfridptSig3Resp_Crt u/08 0 L. Lookligr / hvfridptSig3Re		
Loaking IntridptSigPitThet Cnt_u08		
		1
LoaMgr_MortAghdShaZeu_Us_152		
k_LoaMgr_MotAgldplSig1NoSmaResp_Cnt_u08 5 k_LoaMgr_MotAgldpSig3Resp_Cnt_u08 0 k_LoaMgr_MotAgldpSig3Resp_Cnt_u08 0 k_LoaMgr_MotAgldpSig1Resp_Cnt_u08 2 k_LoaMgr_MotAgldpSig1Rest_UispS_172 1 k_LoaMgr_MotAgldpSig3Resp_Cnt_u08 2 k_LoaMgr_MotAgldpSig3Resp_Cnt_u08 2 k_LoaMgr_MotAgldpSig3Resp_Cnt_u08 2 k_LoaMgr_MotCurridptSig0Resp_Cnt_u08 2 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_Pert_DiagcSistvtrInactv_Cnt_lgc value 1 LoaMgr_Pert_DiagcSistvtrInactv_Cnt_lgc value 0 LoaMgr_Pert_LoaScaD_LoaMgr_Pert_LoaScaD_LoaMgr_Pert_LoaScaD_LoaMgr_Pert_LoaMgr_Pert_LoaMgr_Pert_LoaMgr_Pert_LoaMgr_Pert_LoaMgr_Pert_LoaMgr_Pert_LoaMgr_Pert_LoaMgr_Pert_LoaMgr_Pert_L		
LoaMgr_MotAgidptSig2Resp_Cnt_u08		
k_Loakingr_MorkajdiptSigsResp_Cnt_u08 2 k_Loakingr_MorkajdiptSigsRiThd_Cnt_u08 2 k_Loakingr_MorkajdiptSigsRiThd_Cnt_u08 1 k_Loakingr_MorkajdiptSigs_Cnt_u01gc 0 k_Loakingr_MorkajdiptSigs_Disp_Cnt_u08 2 k_Loakingr_MorkajdiptSigs_Dresp_Cnt_u08 2 k_Loakingr_MorturidptSigsResp_Cnt_u08 0 k_Loakingr_MorturidptSigsResp_Cnt_u08 1 k_Loakingr_MorturidptSigsResp_Cnt_u08 1 k_Loakingr_MorturidptSigsResp_Cnt_u08 1 k_Loakingr_MorturidptSigsResp_Cnt_u08 1 k_Loakingr_MorturidptSigs_Sidstritinactv_Cnt_lgc 1 k_Loakingr_RedodSissa_ubis_Sids_Us_Sids_Sids_Us_Sids_Sids_Cnt_u08 1 k_Loakingr_Loakingr_Cnt_u08_Vsalue 0 target_Loakingr_Pert_IndptSigs_		
K_LoaMgr_MotAgMtgmRate_UlspS_f32 1 K_LoaMgr_MotAgMtgmSazeroEn_Cnt_lgc 0 K_LoaMgr_MotAgMtgmSazeroEn_Cnt_lgc 1 K_LoaMgr_MotAgMtgmSaz_UlspS_g 1 K_LoaMgr_MotCurridpsIsigsGResp_Cnt_u08 2 K_LoaMgr_MotCurridpsIsigsCResp_Cnt_u08 0 K_LoaMgr_MotCurridpsIsigsCResp_Cnt_u08 0 K_LoaMgr_MotCurridpsIsigsCResp_Cnt_u08 0 K_LoaMgr_MotCurridpsIsigsCResp_Cnt_u08 0 K_LoaMgr_MotCurridpsIsigsCaseroEn_Cnt_lgc 1 K_LoaMgr_MotCurridpsIsigsCaseroEn_Cnt_lgc 1 K_LoaMgr_MotCurridpsIsigsCaseroEn_Cnt_lgc 1 K_LoaMgr_RedCdSISrate_ulspS_f32 1 K_LoaMgr_RedCdSISrate_ulspS_f32 1 K_LoaMgr_Pert_pollogScistwitilinactv_Cnt_lgc_value 0 target_LoaMgr_Pert_loagCistwitilinactv_Cnt_lgc_value 0 target_LoaMgr_Pert_loagCistwitilinactv_Cnt_lgc_value 1 target_LoaMgr_Pert_loagCistwitilinactv_Cnt_lgc_value 0 target_LoaMgr_Pert_MotCoalQustilinactv_Cnt_lgc_value 0 target_LoaMgr_Pert_MotCoalQustilinactv_Cnt_lgc_value 0 target_LoaMgr_Pert_MotCoalQustilinactv_Cnt_lgc_value 0	k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
K_LoaMgr_MotAgMtginSca_ZeroEn_Cnt_Igc 0 K_LoaMgr_MotAgMtginSca_Uis_132 1 K_LoaMgr_MotCurridptSigGresp_Cnt_u08 2 K_LoaMgr_MotCurridptSigGresp_Cnt_u08 2 K_LoaMgr_MotCurridptSigGresp_Cnt_u08 0 K_LoaMgr_MotCurridptSigGresp_Cnt_u08 0 K_LoaMgr_MotCurridptSigGresp_Cnt_u08 0 K_LoaMgr_MotCurridptSigGresp_Cnt_u08 0 K_LoaMgr_MotCurridptSigGresp_Cnt_u08 0 K_LoaMgr_MotCurridptSigGresp_Cnt_u08 0 K_LoaMgr_MotCurridptSigGresp_Cnt_u08 1 K_LoaMgr_MotCurridptSigGresp_Cnt_u08 1 K_LoaMgr_MotCurridptSig_Cnt_u1gc 1 K_LoaMgr_RedodSiRae_UispS_32 1 K_LoaMgr_RedodSiRae_UispS_32 1 K_LoaMgr_RedodSiRae_UispS_32 1 K_LoaMgr_Per1_DiagcStslvtrIinactv_Cnt_Igc.value 0 target_LoaMgr_Per1_DiagcStslvtrIinactv_Cnt_Igc.value 0 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value 1 target_LoaMgr_Per1_MrotCurridptSig_Cnt_u08 value 2 target_LoaMgr_Per1_SteerMod_Cnt_enum value 0 target_LoaMgr_Per1_ToaDi_Cnt_Igc.value 1	k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
K_LoaMgr_MotAgMtgInSca_Uls_f32 1 K_LoaMgr_MotAgSmaAvailResp_Cnt_u08 2 K_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 K_LoaMgr_MotCurridptSig2Resp_Cnt_u08 1 K_LoaMgr_MotCurridptSig2Cnt_u08 1 K_LoaMgr_MotCurridptSig2Cnt_u08 1 K_LoaMgr_RedcdSitRes_Ulsp_S122 1 K_LoaMgr_RedcdSitRes_Ulsp_S122 1 K_LoaMgr_Per1_DiagcStstvtTinactv_Cnt_lgcvalue 0 target_LoaMgr_Per1_MratiqotSig_Cnt_u08 value 4 target_LoaMgr_Per1_MratiqotSig_Cnt_u08 value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum value 0 target_LoaMgr_Per1_SteerMod_Cnt_enum value 0 target_LoaMgr_Per1_ToaApi_Cnt_lgc value 1	k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotOurdpSigoResp_Cnt_u08 2 k_LoaMgr_MotCurridptSigOResp_Cnt_u08 2 k_LoaMgr_MotCurridptSigOResp_Cnt_u08 0 k_LoaMgr_MotCurridptSigOResp_Cnt_u08 0 k_LoaMgr_MotCurridptSigOREsp_Cnt_u08 0 k_LoaMgr_MotCurridptSigORThd_Cnt_u08 0 k_LoaMgr_MotCurridptSigORThd_Cnt_u08 1 k_LoaMgr_MotCurridptSigORThd_Cnt_u08 1 k_LoaMgr_MotCurridptSigORThd_Cnt_u0gC 1 target_LoaMgr_Per1_MotCurridptSigORThd_U0gC 4 target_LoaMgr_Per1_MotCurridptSigORThd_U0gC 1 target_LoaMgr_Per1_MotCurridptSigORThd_U0gC 2 target_LoaMgr_Per1_MotCurridptSigORThd_U0gC 1 target_LoaMgr_Per1_MotCurridptSigORThd_U0gC 1 target_LoaMgr_Per1_MotCurridptSigORThd_U0gC	k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotCurridptSig0Resp_Cnt_u08 2 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_MotCurridptSig2Sig1Sig2 1 k_LoaMgr_MotCurridptSig2Sig2Sig2 1 k_LoaMgr_RedcdStRate_UispS_732 1 k_LoaMgr_RedcdStSca_Uis_f32 1 k_LoaMgr_RedcdStSca_Uis_f32 1 k_LoaMgr_Per1_biagcStsivtr1inactv_Cnt_lgc_value 0 target_LoaMgr_Per1_biagcStsivtr2inactv_Cnt_lgc_value 1 target_LoaMgr_Per1_sicerMod_Cnt_enum_value 0 target_LoaMgr_Per1_sicerMod_Cnt_enum_value 0 target_LoaMgr_Per1_sicerMod_Cnt_enum_value 0 target_LoaMgr_Per1_loaAlg_Cnt_ugc_value 1 target_LoaMgr_Per1_biagcStsivtr1inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStsivtr1inactv_Cnt_lgc target_LoaMgr_Per1_b	k_LoaMgr_MotAgMtgtnSca_Uls_f32	
k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig1RtThd_Cnt_u08 0 k_LoaMgr_MotCurridtgtinsEqt_UispS_f32 1 k_LoaMgr_MotCurridtgtinsCa_Uis_532 1 k_LoaMgr_MotCurridtgtinsCa_Uis_532 1 k_LoaMgr_MotCurridtgtinsCa_Uis_532 1 k_LoaMgr_ReddStRate_UispS_f32 1 k_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc.value 0 target_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc.value 0 target_LoaMgr_Perd_HortJdptSig_Cnt_u08 value 4 target_LoaMgr_Perd_MotCurridptSig_Cnt_u08 value 0 target_LoaMgr_Perd_MotCurridptSig_Cnt_u08 value 0 target_LoaMgr_Perd_SteenMod_Cnt_enum_value 0 target_LoaMgr_Perd_TioaAyail_Cnt_lgc.value 1 target_LoaMgr_Perd_TioaDi_Cnt_lgc.value 1 target_Re_Inst_Ap_LoaMgr_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc target_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc target_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Perd_HwTqLoaMtgnEn_Cnt_lgc target_LoaMgr_Perd_HwTqLoaMtgnEn_Cnt_lgc target_Re_Ins		
k_LoaMgr_MolCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MolCurridptSigFitThd_Cnt_u08 0 k_LoaMgr_MolCurrMtgtnScaZeroEn_Cnt_lgc 1 k_LoaMgr_MolCurrMtgtnScaZeroEn_Cnt_lgc 1 k_LoaMgr_MolCurrMtgtnSca_Uls_f32 1 k_LoaMgr_RedcdStRate_UlspS_f32 1 k_LoaMgr_RedcdStRate_UlspS_f32 1 target_LoaMgr_Per1_DiagcStslvtrInactv_Cnt_lgc.value 0 target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc.value 0 target_LoaMgr_Per1_LHVTqldptSig_Cnt_u08.value 4 target_LoaMgr_Per1_MolCurridptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MrPosldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MrPosldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_Re_Inst_Ap_LoaMgr_Per1_DiagcStslvtrInactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtrInactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtrZlnactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc targ		
k_LoaMgr_MotCurridptSigFitThd_Cnt_u08 0 k_LoaMgr_MotCurridptRate_UlspS_f32 1 k_LoaMgr_MotCurrMtgtnScazeroEn_Cnt_lgc 1 k_LoaMgr_MotCurrMtgtnSca_Uls_f32 1 k_LoaMgr_RedcdStRate_UlspS_f32 1 k_LoaMgr_RedcdStRate_UlspS_f32 1 k_LoaMgr_Per1_DiagcStsivtr1lnactv_Cnt_lgc.value 0 target_LoaMgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc.value 0 target_LoaMgr_Per1_HvTqldptSig_Cnt_u08.value 4 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_ToaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_ToaDi_Cnt_lgc.value 0 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsivtr1nactv_Cnt_lgc target_LoaMgr_Per1_DiagcStsivtr2nactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsivtr2nactv_Cnt_lgc target_LoaMgr_Per1_DiagcStsivtr2nactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMgtnEn_Cnt_lgc target_LoaMgr_Per1_HvTqLoaMgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMstnEn_Cnt_lgc target_LoaMgr_Per1_Int		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc 1 k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc 1 k_LoaMgr_MotCurrMtgtnSca_Uls_f32 1 k_LoaMgr_RedcdStRae_UlspS_f32 1 k_LoaMgr_Perd_DiagcStslvtr1Inactv_Cnt_lgc.value 0 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value 0 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value 0 target_LoaMgr_Per1_HVTqldptSig_Cnt_u08.value 4 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_ToaAvail_Cnt_lgc value 1 target_LoaMgr_Per1_ToaAvail_Cnt_lgc value 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_InvTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_InvTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cn		
k_LoaMgr_MotCurrMtgtnSca_Uis_f32 k_LoaMgr_MotCurrMtgtnSca_Uis_f32 k_LoaMgr_RedcdStRate_UispS_f32 k_LoaMgr_RedcdStSca_Uis_f32 target_LoaMgr_Perl_DiagcStslvtr1Inactv_Cnt_lgc.value 0 target_LoaMgr_Perl_DiagcStslvtr2Inactv_Cnt_lgc.value 0 target_LoaMgr_Perl_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Perl_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Perl_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Perl_MotCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Perl_MotCurrldptSig_Cnt_u08.value 2 target_LoaMgr_Perl_MotCurrldptSig_Cnt_u08.value 2 target_LoaMgr_Perl_MotCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Perl_ToaDi_Cnt_lgc.value 1 target_LoaMgr_Perl_ToaDi_Cnt_lgc.value 1 target_LoaMgr_Perl_ToaDi_Cnt_lgc.value 1 target_LoaMgr_Perl_ToaDi_Cnt_lgc.value 1 target_LoaMgr_Perl_ToaDi_Cnt_lgc.value 1 target_LoaMgr_Perl_ToaOmgr_Perl_DiagcStslvtr1Inactv_Cnt_lgc target_Rle_Inst_Ap_LoaMgr_LoaMgr_Perl_DiagcStslvtr2Inactv_Cnt_lgc target_Rle_Inst_Ap_LoaMgr_LoaMgr_Perl_DiagcStslvtr2Inactv_Cnt_lgc target_Rle_Inst_Ap_LoaMgr_LoaMgr_Perl_MotGldptSig_Cnt_u08 target_Rle_Inst_Ap_LoaMgr_LoaMgr_Perl_HwTqldptSig_Cnt_u08 target_Rle_Inst_Ap_LoaMgr_LoaMgr_Perl_HwTqloaMtgtnEn_Cnt_lgc target_Rle_Inst_Ap_LoaMgr_LoaMgr_Perl_IvtrLoaMtgtnEn_Cnt_lgc target_Rle_Inst_Ap_LoaMgr_LoaMgr_Perl_LoaScaDi_Cnt_lgc target_Rle_Inst_Ap_LoaMgr_LoaMgr_Perl_LoaScaDi_Cnt_lgc target_Rle_Inst_Ap_LoaMgr_LoaMgr_Perl_LoaScaDi_Cnt_lgc target_Rle_Inst_Ap_LoaMgr_LoaMgr_Perl_LoaScaDi_Cnt_lgc target_Rle_Inst_Ap_LoaMgr_LoaMgr_Perl_LoaScaDi_Cnt_lgc target_Rle_Inst_Ap_LoaMgr_LoaMgr_Perl_LoaScaDi_Cnt_lgc target_Rle_Inst_Ap_LoaMgr_LoaMgr_Perl_NotAgloaMtgtnEn_Cnt_lgc target_Rle_Inst_Ap_LoaMgr_LoaMgr_Perl_NotAgloaMtgtnEn_Cnt_lgc target_Rle_Inst_Ap_LoaMgr_LoaMgr_Perl_NotAgloaMtgtnEn_Cnt_lgc target_Rle_Inst_Ap_LoaMgr_LoaMgr_Perl_NotAgloaMtgtnEn_Cnt_lgc target_Rle_Inst_Ap_LoaMgr_LoaMgr_Perl_NotAgloaMtgtnEn_Cnt_lgc target_LoaMgr_Perl_NotAgloaMtgtnEn_Cnt_lgc target_LoaMgr_Perl_NotAgloaMtgtnEn_Cnt_lgc target_LoaMgr_Perl_NotAgloaMtgtnEn_Cnt_lgc target_LoaMgr_Perl_NotAgloaMtgtnEn_Cnt_lgc target_LoaMgr_Perl_NotAgloaMtgtn		1
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStRate_UlspS_f32 target_LoaMgr_Per1_DiagcStslvtr1lnactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MrtPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MrtPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MrtPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MrtPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MrtPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1lnactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaSt_State_enum target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaSt_State_enum target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaSt_State_enum target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgloaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_Per1_MotAgloaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_Per1_MotAgloaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_Per1_MotAgloaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_Per1_MotAgloaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_Per1_MotAgloaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
k_LoaMgr_RedcdStRate_UlspS_132		
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target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvTLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc		





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	✓

Test Step 1.7 (Repeat Count = 1)	12
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Input Value



Name



Name	Input value			
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1			
k_LoaMgr_FadeOutStRate_UlspS_f32	1	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2			
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	0			
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0			
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0			
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1			
k LoaMgr HwTqMtgtnRate UlspS f32	1			
	1			
k_LoaMgr_HwTqMtgtnSca_UIs_f32				
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0			
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0			
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1			
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1			
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5			
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1			
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0			
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2			
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1			
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2			
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2			
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0			
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0			
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1			
	1			
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1			
k_LoaMgr_RedcdStRate_UlspS_f32	1			
k_LoaMgr_RedcdStSca_Uls_f32				
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1			
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value	0			
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1			
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0			
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2			
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	0			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1			
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_	lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target LoaMgr Per1 MotAgLoaMtgtnEn C	int lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target LoaMgr Per1 MtrPosldptSig Cnt u			
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target_LoaMgr_Per1_SteerMod_Cnt_enum			
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	w	1	
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	0	0	~	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~	
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	~	
LoaMgr_LOASt_State_M_enum	4	4	•	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	4	4	-	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~	
LoaMar MotAal oaMtatnEn, Cot, M. Jac			•	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr MotAgRespSV Cnt M u08	0	0	V	

LoaMgr_MotAgRespSV_Cnt_M_u08

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LoaMgr_Per1



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	✓
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{171, 172, 173}	{171, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	✓
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	4	4	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.8 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	5
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





N	In most Welling		
Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k LoaMgr MotAgMtgtnRate UlspS f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Ont_Igc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target LoaMgr Per1 LoaScaDi Cnt Igc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inac	tv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inac		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enu		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgr target_LoaMgr_Per1_MotAgLoaMtgtnEr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtn	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_er	_	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc	:	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	✓
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	✓
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	Y
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	V
LoaMgr_LOASt_State_M_enum	5 500	5 500	
LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr LoaScaleFctr Uls M f32	0	0	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	_
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	_
LoaMgr MotAgRespSV Cnt M u08	4	4	✓
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
	F00	E00 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0 ± 0.001	Ž
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0 1 0	0 ± 0.001 1 0	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_LOASt_State_enum.value	0 1 0 5	0 ± 0.001 1 0 5	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0 1 0	0 ± 0.001 1 0	~



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	✓
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

- 101 10 10 10 10 10 10 10 10 10 10 10 10	
Test Step 1.9 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	1
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1 0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	-	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0	Ont Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	0	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	✓
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	_
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	✓
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	_
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	_
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	✓
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	✓
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	✓
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	✓
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	✓
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 174}	{170, 174}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target LoaMgr Per1 MotAgLoaMtgtnEn Cnt Igc.value	0	0	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	•
	·		

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~



Test Step 1.10 (Repeat Count = 1)	3
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAglopMtgtpEp_Cot_M_uo8	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV Cnt M u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1.
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	4 2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1 0
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k LoaMgr MotAgMtgtnSca Uls f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_UIs_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_DiagcStsIvtr1IIIactv_Crit_igc.value target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Crit_igc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LoASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
2 1 _ 0 0	, 5 = 5 = 5 = 5 = 5 = 5



LoaMgr_Per1

Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	✓
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 175}	{170, 173, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	✓

Test Step Call Trace ✓				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	✓
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	✓
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	✓

Test Step 1.11 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1 5		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k LoaMgr HwTqMtgtnRate UlspS f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1 0		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target LoaMgr Per1 MotCurrldptSig Cnt u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
target LoaMgr Per1 SteerMod Cnt enum.value	0		
target LoaMgr Per1 TloaAvail Cnt Igc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C	:nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	:nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_le	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_(
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum	JU	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_SteetMod_Crit_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	✓
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	2	2	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	✓
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	_

LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc.value	0	0	✓

Test Step Call Trace	est Step Call Trace				
Actual Function	Count	Expected Function	Count	Result	
CalclvtrState	2	CalclvtrState	2	~	
LatchInputs	1	LatchInputs	1	~	
RqstRespConds	1	RqstRespConds	1	~	
RqstHwTqResp	1	RqstHwTqResp	1	~	
ArbResp	1	ArbResp	1	~	
ChooseFinalResp	1	ChooseFinalResp	1	~	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~	
AssignScale	1	AssignScale	1	✓	
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~	
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~	

Test Step 1.12 (Repeat Count = 1)		
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_lvtrRespSV_Cnt_M_u08	4	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0	
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1	
k_LoaMgr_FadeOutStRate_UlspS_f32	1	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2	
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4	
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2	
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1	
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	3	
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0	
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1	
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1	
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1	
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0	
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0	
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0	
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1	
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0	
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1	
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5	





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k LoaMgr MotAqMtqtnSca Uls f32	1		
_ 0_ 0			
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value			
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inac		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inac	tv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqIdptSig_Cnt	_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_C	cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enu	ım	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lge		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_C		
		_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnl		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cr	_	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_er	num	
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc			
	1	1	✓
			✓
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0 1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	0 1 4	0 1 4	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08	0 1 4 0	0 1 4 0	· ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	0 1 4 0	0 1 4 0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08	0 1 4 0	0 1 4 0	· ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0 1 4 0	0 1 4 0	· ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08	0 1 4 0 0 5	0 1 4 0 0 5	· ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	0 1 4 0 0 5 5	0 1 4 0 0 5 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	0 1 4 0 0 5 5 5 5 500	0 1 4 0 0 5 5 5 500	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	0 1 4 0 0 5 5 5 500 1 5	0 1 4 0 0 5 5 5 500	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	0 1 4 0 0 0 5 5 5 5 500 1 5	0 1 4 0 0 5 5 5 500 1 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0 1 4 0 0 0 5 5 5 5 500 1 5 0	0 1 4 0 0 5 5 5 5 500 1 5 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	0 1 4 0 0 0 5 5 5 5 500 1 5 0 0	0 1 4 0 0 5 5 5 5 500 1 5 0 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0 1 4 0 0 0 5 5 5 5 500 1 5 0	0 1 4 0 0 5 5 5 5 500 1 5 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	0 1 4 0 0 0 5 5 5 5 500 1 5 0 0	0 1 4 0 0 5 5 5 5 500 1 5 0 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08	0 1 4 0 0 0 5 5 5 5 500 1 5 0 0	0 1 4 0 0 5 5 5 5 500 1 5 0 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgCoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc	0 1 4 0 0 0 5 5 5 500 1 5 0 0 0 5	0 1 4 0 0 5 5 5 5 500 1 5 0 0 0 1 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaState_Limit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglCoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc	0 1 4 0 0 0 5 5 5 5 500 1 1 5 0 0 0 5	0 1 4 0 0 5 5 5 500 1 5 0 0 0 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlCaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc	0 1 4 0 0 0 5 5 5 500 1 5 0 0 0 0 5	0 1 4 0 0 5 5 5 500 1 5 0 0 0 5 0	· · · · · · · · · · · · · · · · · · ·
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LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0 1 4 0 0 0 5 5 5 500 1 5 0 0 1 3 1 1 1 1 {170, 173, 174, 175}	0 1 4 0 0 5 5 5 500 1 5 0 0 0 5 0 1 3 1 1 1 1 1 1 170, 173, 174, 175}	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0 1 4 0 0 0 5 5 5 5 0 0 1 3 1 1 1 1 (170, 173, 174, 175) {1, 1, 1, 1}	0 1 4 0 0 0 5 5 5 500 1 5 0 0 1 3 1 1 1 {170, 173, 174, 175}	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 1 4 0 0 0 5 5 5 5 0 0 1 3 1 1 1 1 {170, 173, 174, 175} {1, 1, 1, 1} {1, 1, 1, 1}	0 1 4 0 0 0 5 5 5 500 1 5 0 0 1 3 1 1 1 {170, 173, 174, 175} {0, 0, 0, 0, 0} {1, 1, 1, 1}	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 1 4 0 0 0 5 5 5 5 0 0 1 1 3 1 1 1 {170, 173, 174, 175} {1, 1, 1, 1} {1, 1, 1, 1} 500	0 1 4 0 0 0 5 5 5 500 1 5 0 0 1 1 3 1 1 1 {170, 173, 174, 175} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 1 4 0 0 0 5 5 5 5 500 1 5 0 0 1 1 1 1 1 1 1	0 1 4 0 0 0 5 5 5 500 1 5 0 0 1 3 1 1 1 {170, 173, 174, 175} {0, 0, 0, 0, 0} {1, 1, 1, 1}	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 1 4 0 0 0 5 5 5 5 0 0 1 1 3 1 1 1 {170, 173, 174, 175} {1, 1, 1, 1} {1, 1, 1, 1} 500	0 1 4 0 0 0 5 5 5 500 1 5 0 0 1 1 3 1 1 1 {170, 173, 174, 175} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01	
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LoaMgr_HwTqloptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgltoftSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_StltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0 1 4 0 0 0 5 5 5 5 500 1 5 0 0 1 1 1 1 1 {170, 173, 174, 175} {1, 1, 1, 1} {1, 1, 1, 1} 500 1 1 1	0 1 4 0 0 0 5 5 5 5 500 1 5 0 0 1 1 3 1 1 1 {170, 173, 174, 175} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 1	
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Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.13 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	4
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr MotAgldptSigSV Cnt M u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1.
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Ci	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target LoaMgr Per1 LoaScaDi Cnt Igc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target LoaMgr Per1 MtrPosldptSig Cnt u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr HwTqldptSigSV Cnt M u08	0	0	_
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	_
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	_
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	
LoaMgr MotCurrLoaMtgtnEn Cnt M Igc	1	1	
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	-
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	
LoaMgr_TloaDi_Cnt_M_lgc	1	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 172}	{170, 172}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_endin)	{1, 1}	{0, 0, 0, 0, 0}	X
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	{1, 1} {1, 1}	{1, 1}	~
Rte Write Ap LoaMgr LoaRateLimit UlspS f32(data)	500	500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	-
target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	
target_LoaMgr_Per1_LOASt_State_enum.value target LoaMgr_Per1_MotAgLoaMtgtnEn Cnt lgc.value	1	1	
		1	-
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	I I	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	✓
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Total Part Total Total Total	Test Step 1.14 (Repeat Count = 1)	
Loading_Invitediant Sept Cort Murble		
Leading		·
Londing_HardingsRepC_CM_HA_SB 1 Londing_HardingsRepC_CM_HA_SB 2 Londing_HardingsRepC_CM_HA_SB 3 Londing_HardingsRepC_CM_HA_SB 4 Londing_HardingsRepC_CM_HA_SB 5 Londing_HardingsRepC_CM_HA_SB 6 Londing_HardingsRepC_CM_HA		
Loading_Interfeleps(Cort_M_Ups)		
Loading		
Loading / Info@dispS / Cort M, US		2
Loading Load	LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
Leading JubiAngles (PM Mg) 9	LoaMgr_lvtrRespSV_Cnt_M_u08	5
Landing Makhaghesay Crit M, Ur38	LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
Landery McCurrens(SSS) CM, ML-98 Landery Currens(SSS) CM, ML-98 Landery Currens(SSS) CM, ML-98 Landery Currens(SSSS) CM, ML-98 Landery Currens(SSSS) CM, ML-98 Landery Landery CM, ML-98 Landery Landery CM, ML-98 Landery Landery SSS CM, ML-98 Landery Landery SSS (ML-98) CM, Landery Landers L	LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
Landing IndOurnesWarsEn, Crit M, US		
Leading: ShrietSharkyanil.Crit. M_UB		
Leading Shrifskotokykani Chr. M. ge		
Loading		
Bits_Inst_A_Loaklycr	· - · · ·	
		, · · - ·
K.LoMayl. Print(spiss) (1)		
K_Loading - HvTolptiSign Throat AvailResp_Cnt_U08 2 K_Loading - HvTolptiSign Throat AvailResp_Cnt_U08 2 K_Loading - HvTolptiSign Throat AvailResp_Cnt_U08 1 K_Loading - HvTolptiSign Throat AvailResp_Cnt_U08 5 K_Loading - HvTolptiSign Teacher Dect_U08 5 K_Loading - HvTolptiSign Resp_Cnt_U08 1 K_Loading - HvTolptiSign Resp_Cnt_U08 1 K_Loading - HvTolptiSign Resp_Cnt_U08 1 K_Loading - HvTolptiSign Resp_Cnt_U08 0 K_Loading - HvTolptiSize Zeroein, Cnt_Ug 0 K_Loading - HvTolptiSize Zeroein, Cnt_Ug 0 K_Loading - MvTolptiSize Zeroein, Cnt_Ug 1 K_Loading - MvTolptiSize Zeroein, Cnt_Ug 1 K_Loading - MvTolptiSize Zeroein, Cnt_Ug 2 K_Loading - MvTolptiSize Zeroein, Cnt_Ug 0 K_Loading - MvTolptiSize Zeroein, Cnt_Ug 0 K_Loading - MvTolptiSize Zeroein, Cnt_Ug 0		
K_LOMING / HVTGIPES/STERNA/RISE, COT. U/08 2 K_LOMING / HVTGIPES/STERNERS, COT. U/08 0 K_LOMING / HVTGIPES/STERNERS, COT. U/08 0 K_LOMING / HVTGIPES/STERNERS, COT. U/08 1 K_LOMING / HVTGIPES/STERNER, COT. U/08 1 K_LOMING / HVTGIPES/STERNER, COT. U/08 0 K_LOMING / HVTGIPES/STERNER, COT. U/08 1 K_LOMING / HVTGIPES/STERNER, COT. U/08 5 K_LOMING / HVTGIPES/STERNER, COT. U/08 1 K_LOMING / MVTGIPES/STERNER, COT. U/08 2 K_LOMING / MVTGIPES/STERNER, COT. U/08 <td></td> <td></td>		
		2
K_LoaMgr_HvTqlqpSig4Resp_Cnt_u08		1
k_Loakig_HwTqdqsispliftInd_Cnt_Uo8 1 k_Loakig_HwTqdqsingRate_UispS_132 1 k_Loakig_HwTqdsingRate_UispS_132 1 k_Loakig_HwTqdsisgResp_Cnt_Uo8 0 k_Loakig_HwTqdsisgResp_Cnt_Uo8 0 k_Loakig_HwTqdsisgResp_Cnt_Uo8 0 k_Loakig_HwTqdsisgResp_Cnt_Uo8 0 k_Loakig_HwTqdsisgResp_Cnt_Uo8 0 k_Loakig_HwTqdsiss_Carefic_Cnt_Uge 0 k_Loakig_HwTqdsiss_Carefic_Cnt_Uo8 5 k_Loakig_MwTqdsissisgResp_Cnt_Uo8 5 k_Loakig_MwTqdsissisgResp_Cnt_Uo8 1 k_Loakig_MwTqdsissisgResp_Cnt_Uo8 0 k_Loakig_MwTqdsissisgResp_Cnt_Uo8 2 k_Loakig_MwTqdsissisgResp_Cnt_Uo8 2 k_Loakig_MwTqdsissisgResp_Cnt_Uo8 2 k_Loakig_MwTqdsissisgResp_Cnt_Uo8 2 k_Loakig_MwTqdsissisgResp_Cnt_Uo8 2 k_Loakig_MwTqdsissisgResp_Cnt_Uo8 2 k_Loakig_MwTqdcurridgsisgresp_Cnt_Uo8 2 k_Loakig_MwTqdcurridgsisgresp_Cnt_Uo8 2 k_Loakig_MwTqdcurridgsisgresp_Cnt_Uo8 0 k_Loakig_MwCurridgsisgresp_Cnt_Uo8 0 <td>k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08</td> <td>0</td>	k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_Loakigr_HwTqMtginRate_UlspS_I32 1 k_Loakigr_HwTqMtginScz_Uls_I32 1 k_Loakigr_HwTqMtginScz_Uls_I32 0 k_Loakigr_HwTqMtginScz_Den_UlspS_I32 0 k_Loakigr_HwTqMtginScz_Den_Crt_UlspS_I32 1 k_Loakigr_IwTqMtginScz_Uls_I32 1 k_Loakigr_IwTqMtginScz_Uls_I32 1 k_Loakigr_IwTqqqtpScz_Uls_I32 1 k_Loakigr_MotQiqtpSigRes_Crt_Uls_I32 1 k_Loakigr_MotQiqtpSigRes_Crt_Uls_I32 1 k_Loakigr_MotQiqtpSigRes_Crt_Uls_I32 1 k_Loakigr_MotQiqtsigSigRes_Crt_Uls_I32 1 k_Loakigr_MotQiqtsigSigRes_Crt_Uls_I32 1 k_Loakigr_MotQiqtsigSigRes_Crt_Uls_I32 1 k_Loakigr_MotQiqtsigRis_Crt_Uls_I32 1 k_Loakigr_MotQiqtsigRis_Crt_Uls_I32 1 k_Loakigr_MotQiqtsigRis_Crt_Uls_I32 1 k_Loakigr_MotQiqtsigRis_Crt_Uls_I32 2 k_Loakigr_MotQiqtsigRis_Crt_Uls_I32 2 k_Loakigr_MotQiqtsigRis_Crt_Uls_I32 1 k_Loakigr_MotQiqtsigRis_Crt_Uls_I32 1 k_Loakigr_MotQiqtsigRis_Crt_Uls_I32 1 k_Loakigr_MotQiqtsigRis_Crt_Uls	k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	5
k_LoaMgr_HvTqMtghtSq1Resp_Cnt_U08 0 k_LoaMgr_HvtqhtSg1Resp_Cnt_U08 0 k_LoaMgr_HvtqhtSg1Resp_Cnt_U08 0 k_LoaMgr_HvtqhtSg1Resp_Cnt_U08 0 k_LoaMgr_IvtMtghtSq2Resp_Cnt_U08 0 k_LoaMgr_IvtMtghtSq2Resp_Cnt_U19C 0 k_LoaMgr_IvtMtghtSq1Ds1Resp_Cnt_U08 5 k_LoaMgr_MotAglotSq1Sq1Resp_Cnt_U08 5 k_LoaMgr_MotAglotSq1Sq1Resp_Cnt_U08 1 k_LoaMgr_MotAglotSq1Sg1Resp_Cnt_U08 2 k_LoaMgr_MotAglotSq1Sg1Resp_Cnt_U08 2 k_LoaMgr_MotAglotSq1Resp_Cnt_U08 2 k_LoaMgr_MotAglotSq1Resp_Cnt_U08 2 k_LoaMgr_MotAglotSq1Resp_Cnt_U08 2 k_LoaMgr_MotAglotSq1Resp_Cnt_U08 2 k_LoaMgr_MotCq1Resp_Cnt_U08 2 k_LoaMgr_MotCq1Resp_Cnt_U08 2 k_LoaMgr_MotCuntdptSq1Resp_Cnt_U08 0 k_LoaMgr_MotCuntdptSq1Resp_Cnt_U08 0 k_LoaMgr_MotCuntdptSq1Resp_Cnt_U08 0 k_LoaMgr_MotCuntdptSq1Resp_Cnt_U08 0 k_LoaMgr_MotCuntdptSq1Resp_Cnt_U08 0 k_LoaMgr_MotCuntdptSq2Resp_Cnt_U08 0 <tr< td=""><td>k_LoaMgr_HwTqldptSigFltThd_Cnt_u08</td><td></td></tr<>	k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
k_Loaklgr_IntridptSig1Resp_Cnt_U08 0 k_Loaklgr_IntridptSig2Resp_Cnt_U08 0 k_Loaklgr_IntridptSig2Resp_Cnt_U08 0 k_Loaklgr_IntridptSig2Resp_Cnt_U08 0 k_Loaklgr_IntridptSig2Resp_Cnt_U08 1 k_Loaklgr_IntridptSig2Resp_Cnt_U08 5 k_Loaklgr_MotAjdiptSig2Resp_Cnt_U08 1 k_Loaklgr_MotAjdiptSig2Resp_Cnt_U08 2 k_Loaklgr_MotCurridptSig1Resp_Cnt_U08 2 k_Loaklgr_MotCurridptSig1Resp_Cnt_U08 2 k_Loaklgr_MotCurridptSig1Resp_Cnt_U08 0 k_Loaklgr_MotCurridptSig1Resp_Cnt_U08 0 k_Loaklgr_MotCurridptSig1Resp_Cnt_U08 0 k_Loaklgr_MotCurridptSig1Resp_Cnt_U08 0 k_Loaklgr_MotCurridptSig1Resp_Cnt_U08 0 k_Loaklgr_MotCurridptSig1Sig1Resp_Cnt_U08 1 k_Loakl		
k_LoaMgr_IvridptSig/Resp_Cnt_u08 0 k_LoaMgr_IvridptSig/Rithd_Cnt_u08 0 k_LoaMgr_IvriMgtReate_UlspS_G22 1 k_LoaMgr_IvriMgthSeaZeroEn_Cnt_lgc 0 k_LoaMgr_IvriMgthSeaZeroEn_Cnt_lgc 1 k_LoaMgr_MotAglotptSigrNoSmaResp_Cnt_u08 5 k_LoaMgr_MotAglotptSigrResp_Cnt_u08 1 k_LoaMgr_MotAglotptSigrResp_Cnt_u08 0 k_LoaMgr_MotAglotptSigrResp_Cnt_u08 2 k_LoaMgr_MotAglotptSigrResp_Cnt_u08 2 k_LoaMgr_MotAglotptSigrResp_Cnt_u08 2 k_LoaMgr_MotAglotptSigrResp_Cnt_u08 2 k_LoaMgr_MotOglotptSigrResp_Cnt_u08 2 k_LoaMgr_MotOurridptSigrResp_Cnt_u08 2 k_LoaMgr_MotOurridptSigrResp_Cnt_u08 2 k_LoaMgr_MotOurridptSigrResp_Cnt_u08 0 k_LoaMgr_MotOurrid		
k_Loak/gr_Ivrit/pts/SpiFitThd_Cnt_U08 0 k_Loak/gr_Ivrit/ginScazeroEn_Cnt_U08 1 k_Loak/gr_Ivrit/ginScazeroEn_Cnt_U08 5 k_Loak/gr_Mot/gipSigiTNoSmaResp_Cnt_U08 5 k_Loak/gr_Mot/gipSigiRsep_Cnt_U08 1 k_Loak/gr_Mot/gipSigiRsep_Cnt_U08 1 k_Loak/gr_Mot/gipSigiRsep_Cnt_U08 2 k_Loak/gr_Mot/gipSigiRsep_Cnt_U08 2 k_Loak/gr_Mot/gipSigiRsep_Cnt_U08 2 k_Loak/gr_Mot/gipSigiRsep_Cnt_U08 2 k_Loak/gr_Mot/gipSigiRsep_Cnt_U08 2 k_Loak/gr_Mot/gipSigiRsep_Cnt_U08 2 k_Loak/gr_Mot/curridpSigiRsep_Cnt_U08 2 k_Loak/gr_Mot/CurridpSigiRsep_Cnt_U08 2 k_Loak/gr_Mot/CurridpSigiRsep_Cnt_U08 2 k_Loak/gr_Mot/CurridpSigiRsep_Cnt_U08 0 k_Loak/gr		
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k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32		
k_LoaMgr_MolCurrMtgtnSca_Uls_f32 k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1lnactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc		
k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc targe		0
target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 1 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value 1 target_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value 2 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.Per1_HwTqLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc		
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.15 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

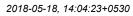
2018-05-18, 14:04:23+0530





Name	Input Value			
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1			
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08		2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4			
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	1	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0			
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	2			
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1			
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1			
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1			
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0			
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0			
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1			
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1			
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5			
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1			
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0			
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2			
k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1			
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2			
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2			
k LoaMgr MotCurrldptSig1Resp Cnt u08	0			
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0			
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1			
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1			
k_LoaMgr_RedcdStRate_UlspS_f32	1			
k_LoaMgr_RedcdStSca_Uls_f32	1			
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1			
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0			
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1			
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0			
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1			
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	Cnt lac		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target LoaMgr Per1 DiagcStsIvtr2Inactv			
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target LoaMgr Per1 HwTqLoaMtgtnEn 0			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_	Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt	_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enur	n		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		1-	
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	0	0	· ·	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	· ·	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0 4	0 4		
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1		
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0		
LoaMgr_lvtrRespSV_Cnt_M_u08	1	1		
LoaMgr_LOASt_State_M_enum	5	5		
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500		
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	•	
	5	5		

LoaMgr_MotAgRespSV_Cnt_M_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt lgc.value	1	1	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Name Input Value	
LoaMgr_AgSV_Cnt_M_lgc 0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc 0	
LoaMgr_HwTqRespSV_Cnt_M_u08 5	
LoaMgr_IvtrldptSigSV_Cnt_M_u08 2	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc 1	
LoaMgr_IvtrRespSV_Cnt_M_u08 1	
LoaMgr_MotAgldptSigSV_Cnt_M_u08 3	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc 1	
LoaMgr_MotAgRespSV_Cnt_M_u08 3	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08 1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 0	
LoaMgr_MotCurrRespSV_Cnt_M_u08 4	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc 1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08 0	
Rte_Inst_Ap_LoaMgr target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32 1	
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc 0	
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32 1	
k_LoaMgr_FadeOutStRate_UlspS_f32 1	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08 0	
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 4	
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 2	
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 0	
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 0	
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
k_LoaMgr_HwTqMtgtnRate_UlspS_f32 1	
k_LoaMgr_HwTqMtgtnSca_Uls_f32	
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08 0	
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08 0	
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc 0	
k_LoaMgr_lvtrMtgtnSca_Uls_f32	
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 5	





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1	0.1.1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqIdptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_	_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_	_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u	ه08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enun	1	
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
		•	
	0	0	✓
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTgldptSigSV_Cnt_M_u08	0	0	Y
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	*
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	0 0 5	0 0 5	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08	0 0 5 1	0 0 5	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0 0 5 1	0 0 5 1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08	0 0 5 1 1	0 0 5 1 1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	0 0 5 1 1 1 5	0 0 5 1 1 1 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	0 0 5 1 1 1 5 500	0 0 5 1 1 1 5 5	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	0 0 5 1 1 1 5 500	0 0 5 1 1 1 5 500	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	0 0 5 1 1 1 5 500 1	0 0 5 1 1 1 5 500 1 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	0 0 5 1 1 1 1 5 500 1 5 2	0 0 5 1 1 1 5 500 1 5 2	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0 0 5 1 1 1 1 5 500 1 5 2	0 0 5 1 1 1 5 500 1 5 2	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08	0 0 5 1 1 1 1 5 5 500 1 1 5 2 1	0 0 5 1 1 1 5 500 1 5 2 1 3	· · · · · · · · · · · · · · · · · · ·
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LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaSt_atate_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc	0 0 5 1 1 1 1 5 500 1 5 2 1 3 1	0 0 5 1 1 1 5 500 1 5 2 1 3 1	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaSt_atate_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	0 0 5 1 1 1 1 5 5 500 1 1 5 2 1 3 1 0 4	0 0 5 1 1 1 1 5 500 1 5 2 1 3 1 0	
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LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LoaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	0 0 5 1 1 1 5 500 1 5 2 1 3 1 0 4 1	0 0 5 1 1 1 1 5 500 1 5 2 1 3 1 0 4 1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MoxAgtloptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	0 0 5 1 1 1 5 500 1 5 2 1 3 1 0 4 1 1	0 0 5 1 1 1 1 5 500 1 5 2 1 3 1 0 4 1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaStaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0 0 5 1 1 1 5 5 500 1 5 2 1 1 3 1 0 4 1 1 0 {170, 173}	0 0 5 1 1 1 1 5 500 1 5 2 1 3 1 0 4 1 1 1	
LoaMgr_HwTqldptSigSY_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaStaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgNespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0 0 5 1 1 1 1 5 5 500 1 5 2 1 1 3 1 0 4 1 1 0 {170, 173}	0 0 5 1 1 1 1 5 500 1 5 2 1 3 1 0 4 1 1 1 0 {170, 173}	
LoaMgr_HwTqldptSigSY_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotOurrlodptSigSV_Cnt_M_u08 LoaMgr_MotOurrlotptSigSV_Cnt_M_u08 LoaMgr_MotOurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 0 5 1 1 1 1 5 5 500 1 5 2 1 1 3 1 0 4 1 1 0 {170, 173}	0 0 5 1 1 1 1 5 500 1 5 2 1 3 1 0 4 4 1 1 0 {170, 173} {0, 0, 0, 0, 0, 0} {1, 1}	
LoaMgr_HwTqldptSigSY_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotOurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotOurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotOurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotOurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 0 5 1 1 1 1 5 5 500 1 5 2 1 1 3 1 0 4 1 1 0 {170, 173} {1, 1} {1, 1} 500	0 0 5 1 1 1 1 5 500 1 5 2 1 1 3 1 0 4 1 1 0 {170, 173} {0, 0, 0, 0, 0, 0} {1, 1} 5500 ± 0.01	
LoaMgr_HwTqldptSigSY_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotOcurrldptSigSV_Cnt_M_u08 LoaMgr_MotOcurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0 0 5 1 1 1 1 5 5 500 1 5 2 1 1 3 1 0 4 1 1 0 {1,1} {1,1} 500 1	0 0 5 1 1 1 1 5 5 500 1 5 2 1 1 3 1 0 4 1 1 0 {170, 173} {0, 0, 0, 0, 0} {1, 1} 5500 ± 0.01 1 ± 0.001	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0 0 5 1 1 1 1 5 5 500 1 5 2 1 1 3 1 0 4 1 1 0 4 1 1 1 0 {1,1} {1,1} 500 1	0 0 5 1 1 1 1 5 5 500 1 5 2 1 1 3 1 0 4 1 1 0 {170, 173} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	0 0 5 1 1 1 1 5 5 500 1 5 2 1 1 3 1 0 4 1 1 0 {1,1} {1,1} {1,1} 500 1 0 1 5	0 0 5 1 1 1 1 5 5 500 1 5 2 1 1 3 1 0 4 1 1 1 0 {170, 173} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0 1 5	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0 0 5 1 1 1 1 5 5 500 1 5 2 1 1 3 1 0 4 1 1 0 {1,1} {1,1} 500 1	0 0 5 1 1 1 1 5 5 500 1 5 2 1 1 3 1 0 4 1 1 0 {170, 173} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0 1	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	-
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.17 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	5
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr MotAgldptSigSV Cnt M u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	5
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1.
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u		
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	1	1	
LoaMgr HwTqldptSigSV Cnt M u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	5	5	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	_
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 172, 173, 174, 175}	{170, 172, 173, 174, 175}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cht_1_endff)	{1, 1, 1, 1, 1}	{0, 0, 0, 0, 0}	1 12 12 12 12 12 12 12 12 12 12 12 12 12
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1, 1} {1, 1, 1, 1, 1}	{1, 1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc.value	0	0	-
target_LoaMgr_Per1_IvtiLoaMigtriEr1_Crit_igc.value target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
· - ·	0	0	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_Loawigi_Ferri_wotourrLoawitgtriEff_Offt_lgc.value	U	0	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	✓
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	-





Test Step 1.18 (Repeat Count = 1)	3
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV Cnt M u08	1
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 k LoaMgr HwTqldptSig1TloaAvailResp Cnt u08	4 2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_UIs_f32	5
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 k LoaMgr MotCurrMtgtnScaZeroEn Cnt lgc	1
k LoaMgr MotCurrMtgtnSca Uls f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1lnactv_Cnt_lgc target_Rte_inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1mactv_Cnt_lgc target_LoaMgr_Per1_DiagcStsIvtr2lnactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_blagcsistvt2Infactv_Ont_igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc



Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	~
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	•
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

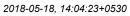
Test Step 1.19 (Repeat Count = 1)		×
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	3	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_lvtrRespSV_Cnt_M_u08	0	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

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Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	1		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08 k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_IvtrIdptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	0		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target Rte Inst Ap LoaMgr.LoaMgr Per1 MotAgLoaMtgtnEn Cnt lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_	Cnt. lac	
target Rte Inst Ap LoaMgr.LoaMgr Per1 MotCurrldptSig Cnt u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_		
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target_LoaMgr_Per1_SteerMod_Cnt_enur		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaAvail Cnt Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	✓
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	v
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	V
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	✓
	5		





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	✓
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	✓
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	✓
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt lgc.value	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.20 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Nama	Input Value		
Name k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08 k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	0		
k_LoaMgr_MotCurrMtgtnNate_otspo_joz	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0 2		
target_LoaMgr_Per1_SteenMod_Crit_enum.value target_LoaMgr_Per1_TloaAvail_Crit_lgc.value	0		
target LoaMgr Per1 TloaDi Cnt Igc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1In	actv Cnt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2In		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_C	Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtn	En_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_e		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrIdptSig target_LoaMgr_Per1_MotCurrLoaMtg		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	;	
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	· ·
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr LOASt State M enum	5	5	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	*
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08			
		1	✓
	1	1 {170, 173}	· ·
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)		1 {170, 173} {0, 0, 0, 0, 0}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	1 {170, 173}	{170, 173}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1 {170, 173} {1, 1}	{170, 173} {0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1 {170, 173} {1, 1} {1, 1} 500 1	{170, 173} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001	¥
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1 {170, 173} (1, 1) {1, 1} 500 1 0	{170, 173} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1 {170, 173} {1, 1} {1, 1} 500 1	{170, 173} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0	¥
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	1 {170, 173} {1, 1} {1, 1} 500 1 0 1 5	{170, 173} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0 1	¥
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1 {170, 173} {1, 1} {1, 1} 500 1	{170, 173} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0	**************************************



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.21 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_Igc	0
k_LoaMgr_IvtrMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k LoaMgr MotAgMtgtnSca UIs f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k LoaMgr MotCurrldptSigFltThd Cnt u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_UIs_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	0.258599997
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
a.gocogi_i or ibiagootoret imacte_ont_igo.eatac	l*





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	1	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lg	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target LoaMgr Per1 LoaScaDi Cnt Igc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	/ /
LoaMgr HwTqldptSigSV Cnt M u08	2	2	-
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
	0	0	-
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	-
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	5	5	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	-
LoaMgr_LOASt_State_M_enum			
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	-
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	V
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	Y
LoaMgr_TloaDi_Cnt_M_lgc	0	0	Y
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~



Test Step 1.22 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32 k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	5
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1.
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32	0.0099999978
	0
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u(08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	✓
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	✓
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	✓
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	✓
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	✓
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace			✓	
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.23 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	0		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k LoaMgr MotCurrldptSig1Resp Cnt u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	500		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	Det In-	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	From a stand Walton	D 14
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	· ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	V
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1 2	· ·
LoaMgr_HwTqRespSV_Cnt_M_u08	2		Ž
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	1 0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	Ž
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	-
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaRateLimit_UlspS_M_f32	300		
LoaMar LoaScaleEctr Lile M f22	1	1	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	· ·
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	✓
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	✓
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc.value	0	0	✓

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.24 (Repeat Count = 1)	N .
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Innut Value		
Name k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	Input Value		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k LoaMgr MotAgMtgtnRate UlspS f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32 k_LoaMgr_RedcdStRate_UlspS_f32	1		
k LoaMgr RedcdStSca Uls f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Ina		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Ina		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cr	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnE		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_IvtrLoaMtgtnEn_ target_LoaMgr_Per1_LOASt_State_er		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_l		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnl		
target_Rte_Inst_Ap_LoaMgr_Der1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgt	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_e	num	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lg	c	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	
Landan Hartalan Mata Far Out Million			✓
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08	4	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	4 1 0	4 1 0	~
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08	4 1 0 5	4 1 0 5	•
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	4 1 0 5 5	4 1 0 5 5	• • • • • • • • • • • • • • • • • • •
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	4 1 0 5 5 5	4 1 0 5 5 5	, , , , , , , , , , , , , , , , , , ,
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	4 1 0 5 5 5 500	4 1 0 5 5 5 500	• • • • • • • • • • • • • • • • • • •
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	4 1 0 5 5 5 500 0	4 1 0 5 5 5 500 0 5	• • • • • • • • • • • • • • • • • • •
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	4 1 0 5 5 5 500	4 1 0 5 5 5 500	• • • • • • • • • • • • • • • • • • •
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	4 1 0 5 5 5 500 0 5	4 1 0 5 5 5 500 0 5	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	4 1 0 5 5 5 500 0 5	4 1 0 5 5 5 500 0 5 0	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	4 1 0 5 5 5 500 0 0 5	4 1 0 5 5 5 500 0 5 0 0 5	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08	4 1 0 5 5 5 500 0 5 0 0 5	4 1 0 5 5 5 500 0 5 0 0 5 1	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc	4 1 0 5 5 5 500 0 5 0 0 5 1	4 1 0 5 5 5 500 0 5 0 0 5 1	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlCaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	4 1 0 5 5 5 500 0 0 5 0 0 5 1 0 3 1	4 1 0 5 5 5 500 0 0 5 0 0 5 1 0 0 3 1	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LVtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlCaAMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	4 1 0 5 5 5 5 500 0 0 5 0 0 5 1 0 0 3 1	4 1 0 5 5 5 5 500 0 0 5 0 0 5 1 0 0 3 1 1	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	4 1 0 5 5 5 500 0 0 5 0 0 5 1 0 3 1 1 0 4 170}	4 1 0 5 5 5 5 500 0 0 5 0 0 5 1 0 0 3 1 1 1 0 4 170}	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4 1 0 5 5 5 5 0 0 0 5 1 0 3 1 1 0 (170)	4 1 0 5 5 5 500 0 5 0 0 5 1 0 3 1 1 0 {170} {0, 0, 0, 0, 0}	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	4 1 0 5 5 5 5 0 0 0 5 1 0 0 1 1 0 3 1 1 0 {170}	4 1 0 5 5 5 5 500 0 0 5 0 0 5 1 0 0 3 1 1 0 {170} {0, 0, 0, 0, 0, 0} {1}	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvaii_Cnt_M_lgc LoaMgr_TloaAvaii_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	4 1 0 5 5 5 5 0 0 0 5 1 0 0 1 1 0 3 1 1 0 {170}	4 1 0 5 5 5 5 5 0 0 0 5 1 1 0 3 1 1 1 0 {170} {0, 0, 0, 0, 0, 0} {1} 5 500 ± 0.01	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_NotCurrRespSV_Cnt_M_u08 LoaMgr_NotCurrRespSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	4 1 0 5 5 5 5 0 0 0 5 1 0 0 3 1 1 0 (170) (1) (1) 5 0 0	4 1 0 5 5 5 5 500 0 0 5 0 0 5 1 0 0 3 1 1 0 {170} {0,0,0,0,0} {1} 500 ± 0.01 0 ± 0.001	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgtloptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VttgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	4 1 0 5 5 5 5 0 0 0 5 1 0 0 1 1 0 3 1 1 0 {170}	4 1 0 5 5 5 5 5 0 0 0 5 1 1 0 3 1 1 1 0 {170} {0, 0, 0, 0, 0, 0} {1} 5 500 ± 0.01	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgtloptSigSV_Cnt_M_u08 LoaMgr_MotAgtloaNtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrloptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	4 1 0 5 5 5 5 0 0 0 5 1 0 0 3 1 1 0 (170) (1) (1) 5 0 0 1	4 1 0 5 5 5 5 500 0 0 5 0 0 5 1 0 0 3 1 1 0 {170} {0,0,0,0,0} {1} 500 ± 0.01 0 ± 0.001 1	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgtloptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VttgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	4 1 0 5 5 5 5 0 0 0 5 1 0 0 3 1 1 0 {170} {1} 5 0 0 1 0 1 0	4 1 0 5 5 5 5 500 0 0 5 0 0 5 1 0 0 3 1 1 1 0 {170} {0,0,0,0,0,0} {1} 500 ± 0.01 0 ± 0.001 1 0	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_u08) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	4 1 0 5 5 5 5 0 0 0 5 1 0 0 3 1 1 1 0 {170} {1} 5 0 1 0 5 1 0 0 5 1 1 0 5 0 5 1 1 0 5 0 6 1 1 0 6 5 1 1 0 6 5 6 6 6 6 6 6 6 6 7 7 8 6 7 8 7 8 8 8 8 8	4 1 0 5 5 5 5 500 0 0 5 0 0 5 1 0 0 3 1 1 1 0 {170} {0,0,0,0,0,0} {1} 500 ± 0.01 0 ± 0.001 1 0 5	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Input Value	
0	
0	
4	
1	
0	
3	
0	
0	
target_Rte_Inst_Ap_LoaMgr	
1	
0	
· ·	
1	
0	
1	
0	
1	
2	
2	
0	
0	
0	
1	
1	
1	
	0 1 1 1 1 1 1 1 1 1 0 5 0 0 4 1 1 0 3 3 0 0 target_Rte_inst_Ap_LoaMgr 1 0 1 1 2 4 2 1 1 0 0 0 1 1 1 1 1 0 0 0 0 1 1 1 1 1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	.lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt	u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum	1	
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaAvail Cnt Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr HwTqldptSigSV Cnt M u08	1	1	V
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	•
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	-
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	✓
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	✓
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	-
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	-
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	✓
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170}	{170}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1}	{1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~
v = =v			

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•





September Sept	Test Step 1.26 (Repeat Count = 1)	3
anding Junifording Style (Circ Mu Julio		
Acadeg		·
ausleg - Interfolespie Cert M, 2000 ausleg - In	LoaMgr_HwTqldptSigSV_Cnt_M_u08	
analog _ Michaelphor_Coff_Misson	LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
analog Junio solution (C. M. voll6 analog Junio solution (C. M. v	LoaMgr_HwTqRespSV_Cnt_M_u08	1
analog _	LoaMgr_lvtrldptSigSV_Cnt_M_u08	
200489_MANQUASHER COLUNDS 0 0 0 0 0 0 0 0 0	LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	
. analogy . Mondag contaggres C. pt. 1. 1909		
auditeg MacCarellagis Cont. Usil auditeg MacCarellagis Usil audit		
2.004/gr MADCUTARGESPS / CPL M 1/98		
. coading "Senderfacing will County" (1996) . coading "Senderfacing will get . coading "Senderfacing will get . coading "Senderfacing will get . (Loading "County Angeleria Lights 122 . (Loading "County Angeleria Lights 122 . (Loading "County Angeleria Lights 122 . (Loading "The Virtual Sender Lights 22 . (Loading "The Virtual Sender Lights 23 . (Loading "In Virtual Sender Lights 23 . (Loading "In Virtual Sender Lights 24 . (Loading "In Virtua		
Loading _ Linching _	LoaMgr_MotCurrRespSV_Cnt_M_u08	
	LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
Landarg_CurvindingsSau_Fish_Cit_Ups 0	LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Clashign Currich Mighign Care Currich	Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
Laaking / Index State Lipids 12 1 1 1 1 1 1 1 1	k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	
Laadig Mid-Nightspin Talevailless Cit Laadig Mid-Nightspin Talevailless Cit Laadig Mid-Nightspin Talevailless Cit Laadig Mid-Nightspin Talevailless Cit Ci		
Leading Harticipitis (Introduce)		
Loading Interfoliation Change Child		
Loading Mind gloss 20		
Loading Mind policy Stational Reso, Crit UBS		
Loading: https://displayseps.com/.u08	k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	
Loaking MarToMorginate Usips 132	k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
Loaking MrVTqMightRaine LugsS 132	k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
CLoaking WirtightSig Resp_Cnt_U08	k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
Clasking Invitigistig Resp. Cmt Loss	k_LoaMgr_HwTqMtgtnRate_UlspS_f32	
Cloaking_Ministrage_Crit_uo8	k_LoaMgr_HwTqMtgtnSca_Uls_f32	
Loaking WritightSigiFiTINd Crit USB 0		
Cloakkgr WrthkighRate_UlspS_R2		
Cloaking		
Cloaking		
Loakig_MoXajdptSig3Resp_Cnt_u08		
LoaMgr_MotAgldptSig3Resp_Cnt_u08	k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
CLoaMgr_MotAglidpiSigFitThd_Cnt_u08	k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
LoaMgr_MotAgMtghrRate_UlspS_132	k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
CLoaMgr_MotAgMiginScaZeroEn_Cnt_lgc	k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	
CLoaMgr_MotAgSmaAvailResp_Cnt_u08	k_LoaMgr_MotAgMtgtnRate_UlspS_f32	
LoaMgr MotQurridptSigOResp_Cnt_u08		
LoaMgr_MotCurridptSig0Resp_Cnt_u08 LoaMgr_MotCurridptSig1Resp_Cnt_u08 LoaMgr_MotCurridptSig1Resp_Cnt_u08 LoaMgr_MotCurridptSig1Resp_Cnt_u08 LoaMgr_MotCurridptSig1Resp_Cnt_u08 LoaMgr_MotCurridptSig1Resp_Cnt_u08 LoaMgr_MotCurridptSig1Resp_Cnt_u08 LoaMgr_MotCurridptSig1Resp_Cnt_u08 LoaMgr_MotCurridptSig2Resp_Cnt_u08 LoaMgr_MotCurridptSig2Resp_Cnt_u08 LoaMgr_RedcdSiRate_UispS_f32 LoaMgr_RedcdSiRate_UispS_f32 LoaMgr_RedcdSiRate_UispS_f32 LoaMgr_RedcdSiRate_UispS_f32 LoaMgr_RedcdSiRate_UispS_f32 LoaMgr_RedcdSiRate_UispS_f32 LoaMgr_RedcdSiRate_UispS_f32 LoaMgr_RedcdSiRate_UispS_f32 LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc_value 1 larget_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc_value 1 larget_LoaMgr_Per1_DiagcSitslot_Cnt_lgc_value 1 larget_LoaMgr_Per1_LoaScaD_Cnt_lgc_value 1 larget_LoaMgr_Per1_MotCurridptSig_Cnt_u08_value 1 larget_LoaMgr_Per1_MotCurridptSig_Cnt_u08_value 1 larget_LoaMgr_Per1_MotCurridptSig_Cnt_u08_value 1 larget_LoaMgr_Per1_SteenMod_Cnt_enum_value 0 larget_LoaMgr_Per1_ToaDi_Cnt_lgc_value 1 larget_LoaMgr_Per1_ToaDi_Cnt_lgc_value 1 larget_LoaMgr_Per1_ToaDi_Cnt_lgc_value 1 larget_LoaMgr_Per1_ToaDi_Cnt_lgc_value 1 larget_LoaMgr_Per1_ToaDi_Cnt_lgc_value 1 larget_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc_larget_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc_larget_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc_larget_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc_larget_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc_larget_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc_larget_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc_larget_LoaMgr_Per1_HwTqldptSig_Cnt_u08 larget_Rie_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqloaMtglnEn_Cnt_lgc_larget_LoaMgr_Per1_HwTqldptSig_Cnt_u08 larget_Rie_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqloaMtglnEn_Cnt_lgc_larget_LoaMgr_Per1_HwTqloaMtglnEn_Cnt_lgc_larget_Rie_Inst_Ap_LoaMgr_DadMgr_Per1_LoaScaD_Cnt_lgc_larget_LoaMgr_Per1_MotCurridptSig_Cnt_u08 larget_Rie_Inst_Ap_LoaMgr_DadMgr_Per1_LoaScaD_Cnt_lgc_larget_LoaMgr_Per1_MotCurridptSig_Cnt_u08 larget_Rie_Inst_Ap_LoaMgr_DadMgr_Per1_MotCurridptSig_Cnt_u08 larget_Rie_Inst_Ap_LoaMgr_DadMgr_Per1_MotCurri		
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\LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc \\ \LoaMgr_MotCurrMtgtnSca_Uls_f32 \\ \LoaMgr_RedcdStRate_UlspS_f32 \\ \LoaMgr_RedcdStRate_UlspS_f32 \\ \tau_{anget_LoaMgr_Perd_DiagcStstvtr1Inactv_Cnt_lgc.value \\ \tau_{arget_LoaMgr_Perd_DiagcStstvtr2Inactv_Cnt_lgc.value \\ \tau_{arget_LoaMgr_Perd_DiagcStstvtr2Inactv_Cnt_lgc.value \\ \tau_{arget_LoaMgr_Perd_DiagcStstvtr2Inactv_Cnt_lgc.value \\ \tau_{arget_LoaMgr_Perd_DiagcStstvtr2Inactv_Cnt_lgc.value \\ \tau_{arget_LoaMgr_Perd_DiagcSts_Dl_Cnt_u08.value \\ \tau_{arget_LoaMgr_Perd_MotCurrldptSig_Cnt_u08.value \\ \tau_{arget_LoaMgr_Perd_MotCurrldptSig_Cnt_u08.value \\ \tau_{arget_LoaMgr_Perd_MotCurrldptSig_Cnt_u08.value \\ \tau_{arget_LoaMgr_Perd_MotCurrldptSig_Cnt_u08.value \\ \tau_{arget_LoaMgr_Perd_DiagcSts_Under_Not_under_Not	k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
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target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target LoaMgr Per1 MotCurrldptSig Cnt u08.value	
target_LoaMgr_Per1_SteerMod_Cnt_enum.value arget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value arget_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 arget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 0 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_loaMgr_Per1_LoaScaDi_Cnt_lgc target_loaMgr_Per1_LoaScaDi_Cnt_lgc target_loaMgr_Per1_LoaScaDi_Cnt_lgc target_loaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_loaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_loaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_loaMgr_Per1_MotCurrldptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
	target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_MotCurrIdptSig_Cnt_u08	
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	



Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174, 175}	{170, 172, 173, 174, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1, 1}	{1, 1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•

Test Step 1.27 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Input Value





k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	500		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgIdptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0 4		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	4		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	4 0 1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	4 0 1 2 1 1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	4 0 1 2 1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	4 0 1 2 1 1	nt_lgc	
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	4 0 1 2 1 1 0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	4 0 1 2 1 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C	nt_lgc	
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1lnactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc	4 0 1 2 1 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C	nt_lgc	
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	4 0 1 2 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	nt_lgc ; t_lgc	
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target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TioaDi_Cnt_lgc.value target_LoaMgr_Per1_TioaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASC_aDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	4 0 1 2 1 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cr_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cr_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cr_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cr_target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 1 2 1 3 5	Expected Value	***
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_lgc.value target_LoaMgr_Per1_TioaDi_Cnt_lgc.value target_LoaMgr_Per1_TioaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	4 0 1 2 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_LoaMgr_Per1_MotQurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaDi_Cnt_lgc actual Value 0 1 1 2 1 1 3 5 5000	Expected Value	***
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_lgc.value target_LoaMgr_Per1_TioaDi_Cnt_lgc.value target_LoaMgr_Per1_TioaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqlcaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaScaleFctr_Uis_M_f32 LoaMgr_LoaScaleFctr_Uis_M_f32 LoaMgr_LoaScaleFctr_Uis_M_f32	4 0 1 2 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgt target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_LoaMgr_Per1_MotQurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 1 2 1 3 5 500 0	Expected Value	***
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsivtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaNtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaNtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaNtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaNtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaNtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaNtgtnEn_Cnt_M_lgc LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_008	4 0 1 2 1 1 2 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_log target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_log target_LoaMgr_Per1_MotQurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrloaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Ctarget_LoaMgr_Per1_TloaAvail_Cnt_logc target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaDi_Cnt_logc Actual Value 0 1 1 1 2 1 3 5 500 0 5	Expected Value	***





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	•
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	•
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.28 (Repeat Count = 1)	3
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





	I		
Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k LoaMgr MotAgMtgtnSca Uls f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08			
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqIdptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	ıt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_l	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cl	nt lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	J8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr HwTqldptSigSV Cnt M u08	1	1	~
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr lvtrldptSigSV Cnt M u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	-
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	✓
LoaMgr MotAgLoaMtgtnEn Cnt M lgc	1	1	-
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	✓
LoaMgr MotCurrldptSigSV Cnt M u08	1	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	
	0	0	
LoaMgr_TloaAvail_Cnt_M_lgc			
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
	1	1	-
target LoaMgr Per1 lytrLoaMtgtnEn Cnt lgc.value			
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value			V
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	V
			V



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.29 (Repeat Count = 1)	Input Value
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	
LoaMgr_lvtrRespSV_Cnt_M_u08	5 2
LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
	0
LoaMgr_MotAgRespSV_Cnt_M_u08	
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	3
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	target Rte Inst Ap LoaMgr
Rte_Inst_Ap_LoaMgr	target_Rte_inst_Ap_coamgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	0
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_CurrIvtrMtgtnSca_Uls_f32 k LoaMgr FadeOutStRate UlspS f32	159.248901
	2
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrIdptSig1Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnVatc_Otspo_Toz	0
k_LoaMgr_IvtrMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k LoaMgr MotCurrMtgtnSca Uls f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	18	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	-	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target LoaMgr Per1 LoaScaDi Cnt Igc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	ent Igc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt Igc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target LoaMgr Per1 MtrPosldptSig Cnt u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	✓ ×
LoaMgr HwTqldptSigSV Cnt M u08	1	1	·
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	·
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	·
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	·
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	·
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	
LoaMgr MotCurrLoaMtgtnEn Cnt M Igc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 173}	{170, 173}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_CITE_1_endIII)	{1, 1}	{0, 0, 0, 0, 0}	X
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	
target LoaMgr Per1 HwTqLoaMtgtnEn Cnt Igc.value	0	0 ± 0.001	
target_LoaMigr_Per1_Intr_LoaMigtriEn_Cnt_lgc.value target_LoaMigr_Per1_lvtrLoaMigtriEn_Cnt_lgc.value	1	1	
target LoaMgr Per1 LOASt State enum.value	5	5	
target_LoaMgr_Peri_LOASt_State_enum.value target LoaMgr_Peri MotAgLoaMtgtnEn Cnt lgc.value	1	1	.,
		1	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	✓
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	✓





Test Step 1.30 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1 0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Inst_Ap_LoaMgr	target Rte Inst Ap LoaMgr
k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	0
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08 k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0 0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	2 0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k LoaMgr MotCurrMtgtnRate UlspS f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_UIs_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1 0
target_LoaMgr_Per1_TloaAvail_Cnt_igc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_real_noabl_cnt_gc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1mactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc
target_re_met_re_comgriceanign_rent_metal_ent_general	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_Loaningt_Ferr_Loadt_State_enum
	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170}	{170}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1}	{1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.31 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Input Value



Name



Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08			
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	4		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
	5		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08			
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
	0		
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08			
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C	int lac	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqldptSig Cnt u08			
· · · · · · · · · · · · · · · · ·	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_load	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_(
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	-
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	
	1	1	
LoaMgr_lvtrldptSigSV_Cnt_M_u08			
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
		4	~
LoaMgr_lvtrRespSV_Cnt_M_u08	4		
	5	5	~
LoaMgr_lvtrRespSV_Cnt_M_u08		5 500	*
LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	5		· ·
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	5 500	500	~
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	5 500 0 5	500 0 5	· ·
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	5 500 0 5 1	500 0 5 1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	5 500 0 5	500 0 5	· ·

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Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	✓
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	0	0 ± 0.001	✓
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	✓

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.32 (Repeat Count = 1)	N .
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Ina		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Ina	ctv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cn	t_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnE		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_en		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lo		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnE		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_t	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_C	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lg	C	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	I=	
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	· ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08			
LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	
·	2	2	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr LoaScaleFctr Uls M f32	0	0	
LoaMgr MaxMtgtnResp Cnt M u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	_
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	_
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170}	{170}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1}	{1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	✓



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.33 (Repeat Count = 1)	3
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	5
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr MotAgRespSV Cnt M u08	3
LoaMgr MotCurrldptSigSV Cnt M u08	0
LoaMgr MotCurrLoaMtgtnEn Cnt M Igc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	5
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrlytrMtgtnScaZeroEn Cnt lgc	0
k LoaMgr CurrlytrMtgtnSca Uls f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	int lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	0	0	/ v
LoaMgr HwTqldptSiqSV Cnt M u08	1	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr lvtrldptSigSV Cnt M u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08	1	1	
LoaMgr_LOASt_State_M_enum	5	5	
	500	500	
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	
LoaMgr_LoaScaleFctr_Uls_M_f32	5	5	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	0	0	~
LoaMgr_MotAglaptSigSV_Cnt_M_u08		1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	3	3	~
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	5	5	
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	
LoaMgr_TloaAvail_Cnt_M_lgc			
LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08		-	V
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{170, 172, 173}	{170, 172, 173}	✓
	{1, 1, 1}	{0, 0, 0, 0, 0}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1} 500 + 0.01	· ·
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	•
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	1	1	
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	V
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	Y
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<u> </u>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 1.34 (Repeat Count = 1)	3
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	2 4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32 k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	1 0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	3
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosioptSig_Cnt_uos.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrlopMitthEn_Cnt_loc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	3	3	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit An LoaMor FalloaScaleAndRate	1	Rte Exit An LoaMar FalloaScaleAndRate	1	V

Test Step 1.35 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Input Value



Name



Name	Input value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k LoaMgr HwTqMtgtnRate UlspS f32	1		
	1		
k_LoaMgr_HwTqMtgtnSca_UIs_f32	0		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08			
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32			
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_i		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target LoaMgr Per1 MtrPosldptSig Cnt u		
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_TloaAvail_Cnt_Igc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	Function Value	Den II
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
	10		
Loaniar Motagiantsiasy Cnt M u08			V
LoaMgr_MotAgloptSigSV_Cnt_M_u08 LoaMgr_MotAglopMtatnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08		0	

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Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.36 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value			
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1			
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0			
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2			
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0			
k LoaMgr MotAgMtgtnSca Uls f32	1			
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2			
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2			
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0			
k LoaMgr MotCurrldptSig2Resp Cnt u08	0			
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1			
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1			
k_LoaMgr_RedcdStRate_UlspS_f32	1			
k_LoaMgr_RedcdStSca_Uls_f32	1			
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1			
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0			
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2			
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0			
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1			
	1			
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value				
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1			
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_	Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_	Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u	08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt	Igc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	•		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_(ant lac		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enun	1		
		target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc	_		
		Expected Value	Result	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	Expected Value	Result	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	· ·	Result	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0	0	~	
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target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrlOaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MoxAmtgtnResp_Cnt_M_u08 LoaMgr_MoxAmtgtnResp_Cnt_M_u08 LoaMgr_MoxAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 1 2 1 0 5 5 500 0 5 1 1 0 5 1 0 5 1 0 5 1 0 5 1 0 5 1 0 5 1 0 5 1 0 5 1 0 5 1 0 5 1 0 5 1 0 5 1 0 5 1 0 5 1 0 0 5 1 0 0 0	0 1 1 1 2 1 0 5 5 5 5 500 0 0 5 1 1 0 5 1		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgGloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlotSigSV_Cnt_M_u08 LoaMgr_MotCurrlotSigSV_Cnt_M_u08 LoaMgr_MotCurrlotSigSV_Cnt_M_u08 LoaMgr_MotCurrlotSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 1 2 1 0 5 5 5 00 0 5 1 1 0 5 1 0 5 1 0 5 1 0 5 1 0 5 1 1 1 1 1 1 1 1 1 1 1 1	0 1 1 1 2 1 0 5 5 5 5 500 0 0 5 1 1 0 5 5 1 1 0 0 5 1		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LoASt_State_M_enum LoaMgr_LoaSt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgCaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 1 2 1 0 5 5 5 500 0 5 1 1 0 5 1 0 5 1 1 1 1 1 1 1 1 1 1 1 1	0 1 1 1 2 1 0 5 5 5 500 0 5 1 1 0 5 1 1 1 3 1 0 0 {170, 173} {0, 0, 0, 0, 0, 0}		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgGloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlotSigSV_Cnt_M_u08 LoaMgr_MotCurrlotSigSV_Cnt_M_u08 LoaMgr_MotCurrlotSigSV_Cnt_M_u08 LoaMgr_MotCurrlotSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 1 2 1 0 5 5 5 00 0 5 1 1 0 5 1 0 5 1 0 5 1 0 5 1 0 5 1 1 1 1 1 1 1 1 1 1 1 1	0 1 1 1 2 1 0 5 5 5 5 500 0 0 5 1 1 0 5 5 1 1 0 0 5 1		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LoASt_State_M_enum LoaMgr_LoaSt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgCaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 1 2 1 0 5 5 5 500 0 5 1 1 0 5 1 0 5 1 1 1 1 1 1 1 1 1 1 1 1	0 1 1 1 2 1 0 5 5 5 500 0 5 1 1 0 5 1 1 1 3 1 0 0 {170, 173} {0, 0, 0, 0, 0, 0}		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LoaSt_State_M_enum LoaMgr_LoaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgHostgSV_Cnt_M_u08 LoaMgr_MotCurrlotSigSV_Cnt_M_u08 LoaMgr_MotCurrlotptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 1 2 1 0 5 5 5 500 0 5 1 1 0 5 1 0 5 1 1 1 2 1 1 1 3 1 0 0 (170, 173) {1, 1} {1, 1}	0 1 1 1 2 1 0 5 5 5 500 0 5 1 1 0 5 1 1 1 3 1 0 0 {170, 173} {0, 0, 0, 0, 0, 0} {1, 1}		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotOurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 1 2 1 0 5 5 5 500 0 5 1 1 0 5 1 1 0 5 1 1 1 1 1 1 1 1 1 1 1 1	0 1 1 1 2 1 0 5 5 5 500 0 5 1 1 0 5 1 1 0 0 (170, 173) {0, 0, 0, 0, 0, 0} {1, 1} 500 ± 0.01		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIcdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIcdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 1 2 1 0 5 5 5 5 00 0 5 1 1 0 0 5 1 0 5 1 1 1 1	0 1 1 1 2 1 0 5 5 5 500 0 5 1 1 0 5 1 1 0 6 5 1 1 1 3 1 0 0 0 {170, 173} {0,0,0,0,0} {1,1} 500 ± 0.01 0 ± 0.001		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 1 2 1 0 5 5 5 5 00 0 0 5 1 1 0 0 5 1 1 1 2 1 1 0 0 5 1 1 1 1 1 3 1 1 1 3 1 1 0 0 0 {170, 173} {1, 1} {1, 1} 500 0 1	0 1 1 1 2 1 0 5 5 5 5 500 0 0 5 1 1 0 5 1 1 0 0 {170, 173} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 0 ± 0.001 1 0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcsV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_LOASt_State_enum.value	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 1 2 1 0 5 5 5 500 0 5 1 1 0 5 1 1 1 1 1 1 1 1 1 1 1 1	0 1 1 1 2 1 0 5 5 5 5 500 0 0 5 1 1 0 5 1 1 0 0 {170, 173} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 0 ± 0.001 1 0 5		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 1 2 1 0 5 5 5 5 00 0 0 5 1 1 0 0 5 1 1 1 2 1 1 0 0 5 1 1 1 1 1 3 1 1 1 3 1 1 0 0 0 {170, 173} {1, 1} {1, 1} 500 0 1	0 1 1 1 2 1 0 5 5 5 5 500 0 0 5 1 1 0 5 1 1 0 0 {170, 173} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 0 ± 0.001 1 0		



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	✓
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.37 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr MotAgldptSigSV Cnt M u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnRsca_Uls_f32	1
k_LoaMgr_IvtrIdptSig1Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrIdptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_Igc	0
k_LoaMgr_IvtrMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
	1
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
	1
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	0
	2
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0 2
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	1
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0

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LoaMgr_Per1 Input Value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 0 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 $target_LoaMgr_Per1_TloaDi_Cnt_lgc.value$ n target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc $target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc$ target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 $target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc$ target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target LoaMgr Per1 LOASt State enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc $target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc$ target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc $target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08$ target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc $target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08$ target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc

target_Rte_inst_Ap_Loawgr.Loawgr_Ferr_noabt_Crit_igc	target_Loalvigt_Fer 1_110aDi	_Crit_igc	
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	✓
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	✓
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_lvtrRespSV_Cnt_M_u08	3	3	✓
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	✓
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	✓
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	✓
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	✓
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	✓
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	✓
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	✓
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	✓
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	~





Test Step 1.38 (Repeat Count = 1)	x
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlytrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08 k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k LoaMgr IvtrMtgtnRate UlspS f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	0
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	1
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedodStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	0
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3
target LoaMgr Per1 LoaScaDi Cnt Igc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr_Dead_Res_Dead_Res_Rte_Inst_Ap_LoaMgr_Dead_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_A	target_LoaMgr_Per1_DiagcStslvtr1lnactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_inst_Ap_LoaMgr.LoaMgr_Per1_ivirLoaMtgtnEn_Cnt_igc target_Rte_inst_Ap_LoaMgr.LoaMgr_Per1_ivirLoaMtgtnEn_Cnt_igc	target_LoaMgr_Perl_IntrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_lvtrRespSV_Cnt_M_u08	3	3	~
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.39 (Repeat Count = 1)	12
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

LoaMgr_Per1

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Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08 k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2 2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
target LoaMgr Per1 SteerMod Cnt enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inac	ctv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inac	ctv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cn	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnE		
target_Rte_Inst_Ap_LoaMgr_Derd_LoaMgr_Derd_LoaMgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_loa		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaUi_Cnt_igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lg target_LoaMgr_Per1_MotAgLoaMtgtnE		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_(
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtr	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_e	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lg	c	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	<u> </u>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4 0	0	- J
LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	Ž
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	_
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	✓

0

5

LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08

LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc

LoaMgr_MotAgRespSV_Cnt_M_u08

0

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Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174, 175}	{170, 172, 173, 174, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1, 1}	{1, 1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.40 (Repeat Count = 1)	3
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	2
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0		
k LoaMgr MotCurrMtgtnRate UlspS f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value			
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_loan	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	Kesuit
LoaMgr HwTqldptSigSV Cnt M u08	0	0	
	0		•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc			
LoaMgr_HwTqRespSV_Cnt_M_u08		0	*
LoaMgr_IvtrldptSigSV_Cnt_M_u08	4	4	•
	4	4	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1 1	1 1	
LoaMgr_lvtrRespSV_Cnt_M_u08	4 1 1 3	4 1 1 3	
LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	4 1 1 3 5	4 1 1 3 5	
LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	4 1 1 3 5 500	4 1 1 3 5 500	
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	4 1 1 3 5 500 0	4 1 1 3 5 500 0	
LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	4 1 1 3 5 500 0 5	4 1 1 3 5 500 0 5	
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	4 1 1 3 5 500 0	4 1 1 3 5 500 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	4 1 1 3 5 500 0 5	4 1 1 3 5 500 0 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	4 1 1 3 5 500 0 5 0	4 1 1 3 5 500 0 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	4 1 1 3 5 500 0 5 0	4 1 1 3 5 500 0 0 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	4 1 1 3 5 5 500 0 5 0 0 5 5	4 1 1 3 5 500 0 0 5 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	4 1 1 3 5 5 500 0 5 0 0 5 0	4 1 1 3 5 500 0 0 5 0 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	4 1 1 3 5 5 500 0 5 0 0 5 0 0	4 1 1 3 5 5 500 0 0 5 0 0 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaSt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc	4 1 1 3 5 500 0 5 0 0 5 0 0 1 2	4 1 1 3 5 5000 0 5 0 0 5 0 0 1	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	4 1 1 3 5 5 500 0 5 0 0 5 0 0 1 2 0	4 1 1 3 5 5000 0 5 0 0 5 0 0 1 2	
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	4 1 1 3 5 5 500 0 0 5 0 0 5 0 0 1 2 0 0	4 1 1 3 5 5000 0 5 0 0 5 0 1 1 2 0 0	
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaAst_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	4 1 1 3 5 500 0 5 0 0 5 0 1 2 0 0 0 1 2 0 0 1 2 0 0 1 2 0 0 1 1 2 0 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1	4 1 1 3 5 5000 0 5 0 0 5 0 1 2 0 0 0 (1 2 0 0 0 (1 2 0 0 0 1 2 0 0 0 1 1 2 0 0 0 0 0 0 0 0 0 0 0 0 0	
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaSt_State_M_enum LoaMgr_LoaCaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4 1 1 3 5 500 0 5 0 0 5 0 1 2 0 0 0 (170, 172, 173, 174, 175) {1, 1, 1, 1, 1}	4 1 1 3 5 500 0 5 0 0 5 0 1 2 0 0 0 {170, 172, 173, 174, 175} {0, 0, 0, 0, 0, 0}	
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaAst_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	4 1 1 3 5 5 500 0 5 0 0 1 2 0 0 {170, 172, 173, 174, 175} {1, 1, 1, 1, 1} {1, 1, 1, 1, 1}	4 1 1 3 5 500 0 5 0 0 0 5 0 0 1 2 0 0 0 {170, 172, 173, 174, 175} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1, 1}	
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	4 1 1 3 5 5 500 0 5 0 0 5 0 1 2 0 0 {170, 172, 173, 174, 175} {1, 1, 1, 1, 1} {1, 1, 1, 1, 1} 500	4 1 1 3 5 5 500 0 0 5 0 0 1 2 0 0 (170, 172, 173, 174, 175) {0, 0, 0, 0, 0} {1, 1, 1, 1, 1} 500 ± 0.01	
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	4 1 1 3 5 5 500 0 5 0 0 5 0 1 2 0 0 {170, 172, 173, 174, 175} {1, 1, 1, 1, 1} {1, 1, 1, 1, 1} 500 0	4 1 1 1 3 5 5 500 0 0 5 0 0 1 2 0 0 {170, 172, 173, 174, 175} {0, 0, 0, 0, 0} {1, 1, 1, 1, 1} 500 ± 0.001	
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Unite_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	4 1 1 3 5 5 500 0 5 0 0 5 0 1 2 0 0 {170, 172, 173, 174, 175} {1, 1, 1, 1, 1} {1, 1, 1, 1, 1} 500 0 0	4 1 1 1 3 5 5 500 0 0 5 0 0 1 2 0 0 {170, 172, 173, 174, 175} {0, 0, 0, 0, 0} {1, 1, 1, 1, 1} 500 ± 0.01 0 ± 0.001	
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_igc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_igc LoaMgr_TloaDi_Cnt_M_igc LoaMgr_VitgMdSrcsV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	4 1 1 3 5 5 500 0 5 0 0 5 0 1 2 0 0 {170, 172, 173, 174, 175} {1, 1, 1, 1, 1, 1} {1, 1, 1, 1, 1} 500 0 1	4 1 1 3 5 5 500 0 5 0 0 5 0 1 2 0 0 {170, 172, 173, 174, 175} (0, 0, 0, 0, 0) {1, 1, 1, 1, 1} 500 ± 0.01 0 1	
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lotrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	4 1 1 3 5 5 500 0 5 0 0 5 0 1 2 0 0 {170, 172, 173, 174, 175} {1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1	4 1 1 3 5 5 500 0 0 5 0 0 1 2 0 0 {170, 172, 173, 174, 175} (0, 0, 0, 0, 0) {1, 1, 1, 1, 1, 1} 500 ± 0.01 0 1 5	
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_igc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_igc LoaMgr_TloaDi_Cnt_M_igc LoaMgr_VitgMdSrcsV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	4 1 1 3 5 5 500 0 5 0 0 5 0 1 2 0 0 {170, 172, 173, 174, 175} {1, 1, 1, 1, 1, 1} {1, 1, 1, 1, 1} 500 0 1	4 1 1 3 5 5 500 0 5 0 0 5 0 1 2 0 0 {170, 172, 173, 174, 175} (0, 0, 0, 0, 0) {1, 1, 1, 1, 1} 500 ± 0.01 0 1	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•

Test Step 1.41 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrlvtrMtgtnScaZeroEn Cnt lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_UIs_f32	0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	1
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_load	ус	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	0	0	_
LoaMgr HwTqldptSigSV Cnt M u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr HwTqRespSV Cnt M u08	4	4	✓
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	3	3	
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	✓
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	✓
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	-
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174, 175}	{170, 172, 173, 174, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1, 1}	{1, 1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	-
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	✓
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	~
AssignScale	1	AssignScale	1	✓
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 1.42 (Repeat Count = 1)	3
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	2 4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32 k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	1 0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosioptSig_Cnt_uos.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrlopMitthEn_Cnt_loc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_lvtrRespSV_Cnt_M_u08	2	2	~
LoaMgr_LOASt_State_M_enum	4	4	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	4	4	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotAgRespSV_Cnt_M_u08	3	3	✓
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{171, 172, 173}	{171, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	4	4	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.43 (Repeat Count = 1)	12
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	5
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	5
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Input Value



Name



Name	Input value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	5		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08			
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_UIs_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value			
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value	1 2		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1 0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value		Ont Inc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0 target_LoaMgr_Per1_DiagcStslvtr2Inactv_0		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqldptSig Cnt u08	target LoaMgr Per1 HwTqldptSig Cnt u0		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqLoaMtgtnEn Cnt Igc	target LoaMgr Per1 HwTqLoaMtgtnEn Ci		
	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cl	_ ·	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_ target_LoaMgr_Per1_LOASt_State_enum	igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Peri_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Peri_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Crit_igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	ent lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrIoptSig_Crit_ target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u		
target_Rte_inst_Ap_LoaMgr.LoaMgr_Per1_MitPosiaptsig_Crit_uoo target_Rte_inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Crit_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteenMod_Crit_Endini target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Larget_Loaningt_t of t_thoabt_offt_lgc	Expected Value	D 14
	Actual Value		
LOWING AGOV CHEIVERC	Actual Value	· ·	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	Kesuit
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1 2	1 2	Kesuit
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1 2 0	1 2 0	v v
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	1 2 0 5	1 2 0 5	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08	1 2 0 5	1 2 0 5	\rightarrow \right
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1 2 0 5 0	1 2 0 5 0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08	1 2 0 5 0 0 5 5 0 5 5 0 0 5 5 0 5 5 0 6 7 6 7 6 7 6 7 6 7 6 7 6 7 6 7 6 7 6	1 2 0 5 0 0 5	***
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	1 2 0 5 0 0 5 5 5 5	1 2 0 5 0 0 5 5 5	***
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	1 2 0 5 0 0 5 5 5 5 5 500	1 2 0 5 0 0 5 5 5 5 5 5 5 5	***
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	1 2 0 5 0 5 5 5 5 5 5 5 5 1 1 1 1 1 1 1 1 1	1 2 0 5 0 0 0 5 5 5 5 5 5 5 1	***
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 2 0 5 0 0 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5	1 2 0 5 0 0 0 5 5 5 5 5 5 5 5 5	***
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	1 2 0 5 0 0 5 5 5 5 5 5 5 5 5 5 2	1 2 0 5 0 0 0 5 5 5 5 5 5 500 1 5	***************************************
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 2 0 5 0 0 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5	1 2 0 5 0 0 0 5 5 5 5 5 5 5 5 5	***

LoaMgr_MotAgRespSV_Cnt_M_u08

2018-05-18, 14:04:23+0530



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	5	5	✓
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 175}	{170, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	✓
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.44 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k LoaMgr MotCurrldptSig1Resp Cnt u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSigFltThd Cnt u08	0		
	1		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32			
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value			
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inact		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inact		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_	_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enu	m	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	;	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Ci		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnB	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cn		
		_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_en		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	Expected Value	Result
		Expected Value	Result
Name	Actual Value		~
Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08	Actual Value	0	~
Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc	Actual Value 0 1	1 1	~
Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08	Actual Value 0 1 1 2	0 1 1 2	· ·
Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08	Actual Value 0 1 1 1 1	0 1 1 2 1	•
Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	Actual Value 0 1 1 2 1 0	0 1 1 2 1 0	• • • • • • • • • • • • • • • • • • •
Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08	Actual Value 0 1 1 2 1 0 1	0 1 1 2 1 0	• • • • • • • • • • • • • • • • • • •
Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	Actual Value 0 1 1 2 1 0 1 5	0 1 1 2 1 0 1 5	
Name LoaMgr_AgsV_Cnt_M_lgc LoaMgr_HwTqldptSigsV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigsV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	Actual Value 0 1 1 2 1 0 1 5 500	0 1 1 2 1 0 1 5 500	
Name LoaMgr_AgsV_Cnt_M_Igc LoaMgr_HwTqIdptSigsV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigsV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	Actual Value 0 1 1 2 1 0 1 5 500	0 1 1 2 1 0 1 5 500	
Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	Actual Value 0 1 1 2 1 0 1 5 500 0 5	0 1 1 2 1 0 1 5 500 0 5	
Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08	Actual Value 0 1 1 2 1 0 1 5 500	0 1 1 2 1 0 1 5 500	
Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	Actual Value 0 1 1 2 1 0 1 5 500 0 5	0 1 1 2 1 0 1 5 500 0 5	
Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08	Actual Value 0 1 1 1 2 1 0 1 5 500 0 5 2	0 1 1 2 1 0 1 5 500 0 5 2	
Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc	Actual Value 0 1 1 1 2 1 0 1 5 500 0 5 2 1	0 1 1 2 1 0 1 5 500 0 5 2	
Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	Actual Value 0 1 1 1 2 1 0 1 5 500 0 5 2 1 3	0 1 1 2 1 0 1 5 500 0 5 2 1 3	
Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	Actual Value 0 1 1 1 2 1 0 1 5 500 0 5 2 1 3 1	0 1 1 2 1 0 1 5 500 0 5 2 1 3	
Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	Actual Value 0 1 1 1 2 1 0 1 5 500 0 5 2 1 3 1 1	0 1 1 1 2 1 0 1 5 500 0 5 2 1 3 1 1 1 2	
Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08	Actual Value 0 1 1 1 2 1 0 1 5 500 0 5 2 1 3 1 1 1 2 1	0 1 1 1 2 1 0 1 5 500 0 5 2 1 3 1 1 2 1	
Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MotAgdtpSigSV_Cnt_M_u08 LoaMgr_MotAgdLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_u08 LoaMgr_MotAgCaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	Actual Value 0 1 1 1 2 1 0 1 5 500 0 5 2 1 1 3 1 1 1 2 1 0	0 1 1 1 2 1 0 1 5 500 0 5 2 1 3 1 1 1 2 1 0	
Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoASc_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	Actual Value 0 1 1 1 2 1 0 1 5 500 0 5 2 1 1 3 1 1 1 2 1 0 0 0	0 1 1 1 2 1 0 1 5 500 0 5 2 1 3 1 1 1 2 1 0 0 0	
Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrtdptSigSV_Cnt_M_u08 LoaMgr_MotCurrtLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrtLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrtRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VttgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	Actual Value 0 1 1 1 2 1 0 1 5 500 0 5 2 1 1 3 1 1 1 2 1 0 0 (170, 172, 173}	0 1 1 1 2 1 0 1 5 500 0 5 2 1 1 3 1 1 2 1 0 0 {170, 172, 173}	
LoaMgr_AgsV_Cnt_M_lgc LoaMgr_HwTqldptSigsV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IwtrldptSigsV_Cnt_M_u08 LoaMgr_lvtrldptSigsV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_Ulsps_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigsV_Cnt_M_u08 LoaMgr_MotAgldptSigsV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigsV_Cnt_M_u08 LoaMgr_MotCurrldptSigsV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VttgMdSrcsV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	Actual Value 0 1 1 1 2 1 0 1 5 500 0 5 2 1 1 3 1 1 1 2 1 0 0 0 {170, 172, 173} {1, 1, 1}	0 1 1 1 2 1 0 1 5 500 0 5 2 1 1 3 1 1 2 1 0 0 (170, 172, 173) {0, 0, 0, 0, 0}	
Name LoaMgr_AgsV_Cnt_M_lgc LoaMgr_HwTqldptSigsV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lwtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VttgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	Actual Value 0 1 1 1 2 1 0 1 5 500 0 5 2 1 1 3 1 1 1 2 1 0 0 (170, 172, 173) {1, 1, 1} {1, 1, 1}	0 1 1 1 2 1 0 1 5 500 0 5 2 1 1 3 1 1 2 1 0 0 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1}	
LoaMgr_AgsV_Cnt_M_lgc LoaMgr_HwTqldptSigsV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigsV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VttgMdSrcsV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	Actual Value 0 1 1 1 2 1 0 1 5 500 0 5 2 1 1 3 1 1 1 2 1 0 0 (170, 172, 173) {1, 1, 1} {1, 1, 1} 500	0 1 1 1 2 1 0 1 5 500 0 5 2 1 1 3 1 1 2 1 0 0 {170, 172, 173} {0, 0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01	
Name LoaMgr_AgsV_Cnt_M_lgc LoaMgr_HwTqldptSigsV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HvTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaRcaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_StgMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRcaleFctr_Uls_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	Actual Value 0 1 1 1 2 1 0 1 5 500 0 5 2 1 1 3 1 1 1 2 1 0 0 (170, 172, 173) {1, 1, 1} {1, 1, 1} 500 0	0 1 1 1 2 1 0 1 5 500 0 5 2 1 1 3 1 1 2 1 0 0 (170, 172, 173) {0, 0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001	
LoaMgr_AgsV_Cnt_M_lgc LoaMgr_HwTqldptSigsV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigsV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VttgMdSrcsV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	Actual Value 0 1 1 1 2 1 0 1 5 500 0 5 2 1 1 3 1 1 1 2 1 0 0 (170, 172, 173) {1, 1, 1} {1, 1, 1} 500	0 1 1 1 2 1 0 1 5 500 0 5 2 1 1 3 1 1 2 1 0 0 {170, 172, 173} {0, 0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01	
Name LoaMgr_AgsV_Cnt_M_lgc LoaMgr_HwTqldptSigsV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HvTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaRcaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_StgMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRcaleFctr_Uls_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	Actual Value 0 1 1 1 2 1 0 1 5 500 0 5 2 1 1 3 1 1 1 2 1 0 0 (170, 172, 173) {1, 1, 1} {1, 1, 1} 500 0	0 1 1 1 2 1 0 1 5 500 0 5 2 1 1 3 1 1 2 1 0 0 (170, 172, 173) {0, 0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001	
Name LoaMgr_AgsV_Cnt_M_lgc LoaMgr_HwTqldptSigsV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigsV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	Actual Value 0 1 1 1 2 1 0 1 5 5 500 0 5 2 1 1 3 1 1 1 2 1 0 0 (170, 172, 173) {1, 1, 1} {1, 1, 1} 500 0 1	0 1 1 1 2 1 0 1 5 500 0 5 2 1 1 3 1 1 1 2 1 0 0 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001 1	
Name LoaMgr_AgsV_Cnt_M_lgc LoaMgr_HwTqldptSigsV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigsV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VttgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	Actual Value 0 1 1 1 2 1 0 1 5 5 500 0 5 2 1 1 3 1 1 2 1 0 0 {170, 172, 173} {1, 1, 1} {1, 1, 1} 5 500 0 1 0	0 1 1 1 2 1 0 1 5 500 0 5 2 1 1 3 1 1 1 2 1 0 0 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001 1 0	
Name LoaMgr_AgsV_Cnt_M_lgc LoaMgr_HwTqldptSigsV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigsV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VtgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	Actual Value 0 1 1 1 2 1 0 1 5 5 500 0 5 2 1 1 3 1 1 1 2 1 0 0 {170, 172, 173} {1, 1, 1} {1, 1, 1} 500 0 1 0 5	0 1 1 1 2 1 0 1 5 5 500 0 5 2 1 1 3 1 1 2 1 0 0 (170, 172, 173) {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001 1 0 5	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.45 (Repeat Count = 1)	C
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr MotAgldptSigSV Cnt M u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_Igc	0
	1
k_LoaMgr_lvtrMtgtnSca_Uls_f32	0
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
	0
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	2
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	1
k_LoaMgr_MotAgMtgtnScaZosoFn_Cat.line	0
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotAgMtgtnSca_Uls_f32	2
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0 0
k_LoaMgr_MotCurrNdptSigFltThd_Cnt_u08	1
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target LoaMgr Per1 HwTqldptSig Cnt u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target LoaMgr Per1 MtrPosldptSig Cnt u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target Rte Inst Ap LoaMgr.LoaMgr Per1 DiagcStslvtr1Inactv Cnt Igc	target_LoaMgr_Per1_DiagcStslvtr1	Inactv Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2	Inactv Cnt Igc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target LoaMgr Per1 HwTqLoaMtg		
target Rte Inst Ap LoaMgr.LoaMgr Per1 lvtrLoaMtgtnEn Cnt lgc	target_LoaMgr_Per1_lvtrLoaMtgtnl	En Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Ci		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptS		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaN		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSi		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cr		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc	target LoaMgr Per1 TloaAvail Cn		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_I	gc	
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	_
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	·
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	_
LoaMgr HwTqRespSV Cnt M u08	0	0	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	_
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	_
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	_
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	_
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	_
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	1	1	✓
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	_
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	_
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	_
LoaMgr_TloaDi_Cnt_M_lgc	1	1	_
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	_
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 172, 175}	{170, 172, 175}	_
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	
Rte Write Ap LoaMgr LoaRateLimit UlspS f32(data)	500	500 ± 0.01	·
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	
target LoaMgr Per1 HwTqLoaMtgtnEn Cnt Igc.value	0	0	•
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~
2			

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	✓





Test Step 1.46 (Repeat Count = 1)	3
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32 k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	1 2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08 k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2 2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	4
target_LoaMgr_Per1_LoaScaDi_Cnt_igc.value target LoaMgr Per1 MotCurrldptSig Cnt u08.value	0
target LoaMgr Per1 MtrPosldptSig Cnt u08.value	3
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
5	00





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 174, 175}	{170, 174, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.47 (Repeat Count = 1)	2
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Input Value



Name



Name	Input value			
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1			
k_LoaMgr_FadeOutStRate_UlspS_f32	1			
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2			
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4			
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2			
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1			
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0			
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0			
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1			
k LoaMgr HwTqMtgtnRate UlspS f32	1			
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1			
	0			
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0			
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08				
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1			
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1			
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	3			
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1			
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0			
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2			
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1			
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2			
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2			
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0			
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0			
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1			
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1			
k_LoaMgr_RedcdStRate_UlspS_f32	1			
k_LoaMgr_RedcdStSca_Uls_f32	1			
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc.value	1			
	1			
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value				
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4			
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1			
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0			
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0			
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_	_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_	_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u	08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_0	Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt	_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_	Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn			
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_			
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enur			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc	 		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc			
		Expected Volume	Desuit	
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	1	1	~	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	-	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~	
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	~	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~	
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	•	
LoaMgr_LOASt_State_M_enum	5	5	~	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	•	
	500			
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	✓	
		1 5	*	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 5	5		
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	1		~	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 5 0	5	·	

LoaMgr_MotAgRespSV_Cnt_M_u08

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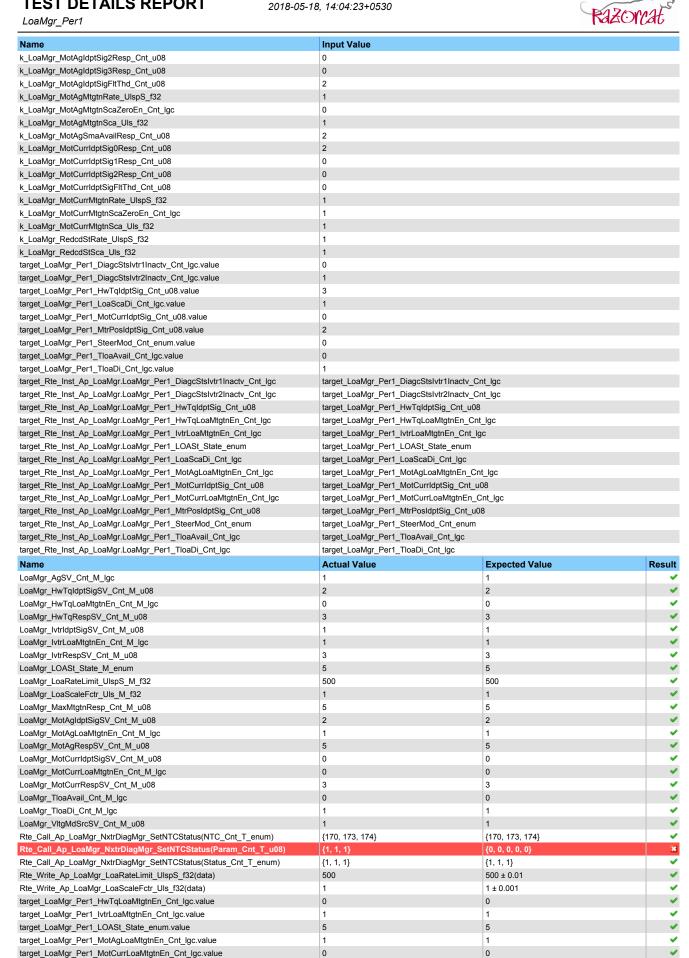
Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	✓
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	✓
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 174, 175}	{170, 174, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	✓
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc.value	0	0	✓

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.48 (Repeat Count = 1)	N .
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5

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Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•

Test Step 1.49 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr MotAgldptSigSV Cnt M u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	5
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0

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Name	Input Value			
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1			
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4			
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0			
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0			
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	0			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1			
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1	Inactv_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2	!Inactv_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtg			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnB			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cr			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMto			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptS			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaM			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target LoaMgr Per1 SteerMod Cr			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cn	_		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_l			
Name	Actual Value	Expected Value	Result	
		· · · · · · · · · · · · · · · · · · ·	Result	
LoaMgr_AgSV_Cnt_M_lgc	1	1		
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1		
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	V	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	Y	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	V	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5		
LoaMgr_LOASt_State_M_enum	5	5	~	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~	
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	~	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~	
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~	
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓	
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)		{170, 173, 174}	~	
	{170, 173, 174}		· ·	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{170, 173, 174} {1, 1, 1}	{0, 0, 0, 0, 0}		
		{0, 0, 0, 0, 0} {1, 1, 1}		
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}		~	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	{1, 1, 1} {1, 1, 1}	{1, 1, 1}	~	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	{1, 1, 1} {1, 1, 1} 500	{1, 1, 1} 500 ± 0.01	~	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	(1, 1, 1) {1, 1, 1} 500 0	{1, 1, 1} 500 ± 0.01 0 ± 0.001	~	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Igc.value	(1, 1, 1) {1, 1, 1} 500 0	{1, 1, 1} 500 ± 0.01 0 ± 0.001		
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	(1, 1, 1) {1, 1, 1} 500 0 0	{1, 1, 1} 500 ± 0.01 0 ± 0.001 0	•	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	✓



Sealing Justice Just	Test Step 1.50 (Repeat Count = 1)	8
Landing Fart Contact (Cent M Lips) Landing Fart Contact (Cent M L		
Leading In-Info@sequent Crit M 196 adding In-Info@sequent Crit M 196 adding Info@sequent Cri	LoaMgr_AgSV_Cnt_M_lgc	
Leading Interliging Com M, June 2 Leading Interliging Com M, June 0 Leading Interlig	LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
Landary Junifordings Coult M, 100 Londary Junifordings Transference Coult M, 100 Londary Junifordings Coult M, 100 Londary Junifordings Coult M, 100 Londary Junifording Coult M, 100 Londary Junifordings C	LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
Loading - Michagles (CM - M. u) dis Coulding - M	LoaMgr_HwTqRespSV_Cnt_M_u08	
Landary Mandigardageria, Cert. My per Landary Mandigardageria, La	LoaMgr_lvtrldptSigSV_Cnt_M_u08	
Loading MonAge parties Co. M. 195		
Loading Manufage (1987) C. M. Loading Loading Loa		·
Landary, MacContrack Stigner, Cort. M. 198 Looking, MacContrack Stigner, Librop, Ed2 Looking, Contrack Stigner, Librop, Ed2 Looking, Contrack Stigner, Librop, Ed2 Looking, Contrack Stigner, Librop, Ed2 Looking, Looking, Librop, Ed2 Looking, Looking, Looking, Librop, Ed2 Lo		
Loading / MoCardinagesty Crit M, 108		
Loading Journal of the Control of th		
Loading - Christophora - Christophor	LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1.
Loading	LoaMgr_MotCurrRespSV_Cnt_M_u08	0
Insert I	LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
Landary Controllingtonia Unit	LoaMgr_VltgMdSrcSV_Cnt_M_u08	
Landing	Rte_Inst_Ap_LoaMgr	V
Loading Failed Shiften Users 1		
Leading / Mini Charles Care		
Landary Hardfords (No Tealines) Cort. USB Landary Hardfords (No Tealines) Cort. USB Landary Hardfords (No Tealines) Cort. USB Landary Hardfords (SZT handars) Cort. USB Landary Hardfords (USB handars) Cort. USB Landary Mandaffords (U		
Loading Mr Options The Tourism Loading Loading Loading Mr Options The Tourism The T		
Loading Mindignis Ziman Loading Loading Loading Loading Mindignis Ziman Loading Mindignis Ziman Zima		
Loading Introduction Loading Interduction Loading Introduction	k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	
Loaking-Hw7qidptiSig4ffeep_Cnt_UBS	k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1.
Loading-HwTqMgtgnSpFTTMC_Crt_U0S	k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
Loaking-IntrofigingRise_UsingS-132	k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	
Loaking Introduction Loaking	k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
Loaking_invirigisigiResp_Crit_u08		
Loaklgr_IntriptSig2Resp_Crt_u08		
Loaking NrtiopisSigFiTTM c Crit USB 0		
L Loaking / Intrinspirsoca/ Line Loaking / L		
Loading		
Loakly_MoVAglightSig1NoSmaResp_Cnt_u08	k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	
LoaMgr_MotAgloptSig2Resp_Cnt_u08	k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
LoaMgr_MotAgloptSig3Resp_Cnt_u08	k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
LoaMgr_MotAgliqhisigFitThd_Cnt_u08	k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	
LoaMgr_MotAgMtghtRate_UlspS_132	k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	
LoaMgr_MotAgMiginScaZeroEn_Cnt_lgc		
LoaMgr_MotAgSmaAvailResp_Cnt_u08		
LoaMgr_MotOurridptSigOResp_Cnt_u08		1
k_LoaMgr_MotCurridptSig0Resp_Cnt_u08 2 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_Ugc 1 k_LoaMgr_MotCurridptSig2SaZeroEn_Cnt_Ugc 1 k_LoaMgr_MotCurridptSig.SaZeroEn_Cnt_Ugc 1 k_LoaMgr_RedcdSiStade_Uls_732 1 k_LoaMgr_RedcdSiStade_Uls_732 1 k_LoaMgr_Perd_DiagcStstvfr1Inactv_Cnt_Ugc.value 0 larget_LoaMgr_Perd_DiagcStstvfr2Inactv_Cnt_Ugc.value 0 larget_LoaMgr_Per1_DiagcStstvfr2Inactv_Cnt_Ugc.value 1 larget_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value 1 larget_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value 1 larget_LoaMgr_Per1_MirPosidptSig_Cnt_u08.value 3 larget_LoaMgr_Per1_ToaAvail_Cnt_Ugc.value 0 larget_LoaMgr_Per1_ToaDi_Cnt_Ugc.value 1 larget_LoaMgr_Per1_ToaDi_Cnt_Ugc.value 1 larget_LoaMgr_Per1_ToaDi_Cnt_Ugc.value 1 larget_LoaMgr_Per1_ToaDi_Cnt_Ugc.value 1 larget_Lo		
k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2FitThd_Cnt_u08 1 k_LoaMgr_MotCurridptSig2FitThd_Cnt_u08 1 k_LoaMgr_MotCurridptSig2SerpEn_Cnt_ligc 1 k_LoaMgr_RedcdSitSea_Uis_32 1 k_LoaMgr_RedcdSitSea_Uis_32 1 k_LoaMgr_Per1_DiagcStsivtrInactv_Cnt_ligc value 0 target_LoaMgr_Per1_DiagcStsivtrInactv_Cnt_ligc value 1 target_LoaMgr_Per1_SitecrMod_Cnt_enum_value 1 target_LoaMgr_Per1_ToaAvail Cnt_ligc value 0 target_LoaMgr_Per1_ToaAvail Cnt_ligc value 1 target_LoaMgr_Per1_ToaAvail Cnt_ligc value 1 target_LoaMgr_Per1_DiagcStsivtrInactv_Cnt_ligc target_LoaMgr_Per1_DiagcStsivtrInactv_Cnt_ligc <td></td> <td></td>		
k_LoaMgr_MotCurrIdptSigFitThd_CnL_u08 0 k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 1 k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 1 k_LoaMgr_MotCurrMtgtnSca_Uls_f32 1 k_LoaMgr_RedcdStRate_UlspS_f32 1 k_LoaMgr_RedcdStRate_UlspS_f32 1 k_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc.value 0 target_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc.value 0 target_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc.value 1 target_LoaMgr_Perd_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Perd_MotCurrIdptSig_Cnt_u08.value 1 target_LoaMgr_Perd_MotCurrIdptSig_Cnt_u08.value 3 target_LoaMgr_Perd_SteerMod_Cnt_enum.value 1 target_LoaMgr_Perd_TioaAvail_Cnt_lgc.value 0 target_Net_Inst_Ap_LoaMgr_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc target_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc target_Ret_Inst_Ap_LoaMgr_LoaMgr_Perd_DiagcStsIvtrZinactv_Cnt_lgc target_LoaMgr_Perd_DiagcStsIvtrZinactv_Cnt_lgc target_Ret_Inst_Ap_LoaMgr_LoaMgr_Perd_HwTqloaMtgtnEn_Cnt_lgc target_LoaMgr_Perd_HwTqloaMtgtnEn_Cnt_lgc target_Ret_Inst_Ap_LoaMgr_LoaMgr_Perd_HwTqloaMtgtnEn_Cnt_lgc target_LoaMgr_Perd_HwTqloaMtgtnEn_Cnt_lgc target_Ret_Inst_Ap_LoaMgr_LoaMg	k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	
LoaMgr_MotCurrMtgthRate_UlspS_f32	k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
LoaMgr_MotCurrMigtnScaZercEn_Cnt_lgc	k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
LoaMgr_MotCurrMigtnSca_Uls_f32	k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	
K_LoaMgr_RedcdStRate_UispS_f32	k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	
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target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_SteerMod_Cnt_enum.value	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc			
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	0	0	~	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	2	~	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	✓	
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~	
LoaMgr_LOASt_State_M_enum	5	5	✓	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	•	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	•	
LoaMgr_MotAgRespSV_Cnt_M_u08	1	1	✓	
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓	
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~	
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•	
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172}	{170, 172}	~	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓	
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	✓
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.51 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	3
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Name	Input Value			
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1			
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2			
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4			
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2			
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1			
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0			
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0			
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1			
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1			
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1			
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0			
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0			
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1			
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1			
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5			
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1			
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0			
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2			
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1			
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2			
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2			
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0			
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0			
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1			
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1			
k_LoaMgr_RedcdStRate_UlspS_f32	1			
k_LoaMgr_RedcdStSca_Uls_f32	1			
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0			
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0			
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4			
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0			
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1			
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0			
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv	_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv	_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u	108		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_	Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cn	t_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	1		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_	_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt			
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEr	_		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enu			
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc			
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	0	0		
LoaMgr_HwTqldptSigSV_Cnt_M_u08	3	3		
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	0	0		
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1		
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0		
LoaMgr_IvtrRespSV_Cnt_M_u08	1	1		
LoaMgr_LOASt_State_M_enum	4	4		
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500		
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0		
LoaMgr_MaxMtgtnResp_Cnt_M_u08	4	4		
LoaMgr_MaxwiginResp_Cnt_iw_uo6 LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1		
LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc	1	1		
LoaMgr_MotAgRespSV Cnt M u08	2	2		
Loanigi MolAgitespov Oni M 400	4	4	· ·	

LoaMgr_MotAgRespSV_Cnt_M_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	✓
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{171, 173}	{171, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	✓
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	4	4	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.52 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	5		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Ina	actv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Ina	actv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_C	nt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnl	En_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_e	num	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtn	En_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_	enum	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_I		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc			
	target_LoaMgr_Per1_TloaAvail_Cnt_I		Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_l target_LoaMgr_Per1_TloaDi_Cnt_lgc	gc	Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name	target_LoaMgr_Per1_TloaAvail_Cnt_l target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	gc Expected Value	Result
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_l target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	Expected Value	~
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_l target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 2	Expected Value 0 2	7
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_l target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 2 1	Expected Value 0 2 1	•
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_l target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 2 1 3	Expected Value 0 2 1 3	•
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_l target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 2 1 3 1 1 4	Expected Value 0 2 1 3 1	•
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_l target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 2 1 3 1	Expected Value 0 2 1 3 1	• • • • • • • • • • • • • • • • • • •
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_l target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 2 1 3 1 1 4	Expected Value 0 2 1 3 1 4	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	target_LoaMgr_Per1_TloaAvail_Cnt_l target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 2 1 3 1 1 4 5	Expected Value 0 2 1 3 1 1 4 5	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	target_LoaMgr_Per1_TloaAvail_Cnt_l target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 2 1 3 1 1 4 5 500	Expected Value 0 2 1 3 1 1 4 5 500	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOaSt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	target_LoaMgr_Per1_TloaAvail_Cnt_l target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 2 1 3 1 1 4 5 500 0	Expected Value 0 2 1 3 1 1 4 5 500	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LoaSt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_l target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 2 1 3 1 1 4 5 500 0 5 1 1	Expected Value 0 2 1 3 1 1 4 5 500 0 5 1 1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_l target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 2 1 3 1 1 4 5 500 0 5 1 1 1 5	Expected Value 0 2 1 3 1 1 4 5 500 0 5 1 1 1 5	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LoaSt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_l target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 2 1 3 1 1 4 5 500 0 5 1 1 1 5 1	Expected Value 0 2 1 3 1 1 4 5 500 0 5 1 1 1 5 1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_l target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 2 1 3 1 1 4 5 500 0 5 1 1 1 5 1 0	Expected Value 0 2 1 3 1 1 4 5 500 0 5 1 1 1 5 1 0	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_I target_LoaMgr_Per1_TloaDi_Cnt_Igc Actual Value 0 2 1 3 1 1 4 5 500 0 5 1 1 1 5 1 0 1	Expected Value 0 2 1 3 1 4 5 500 0 0 5 1 1 1 1 5 1 0 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_StateM_enum LoaMgr_LoASateLief_uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_I target_LoaMgr_Per1_TloaDi_Cnt_Igc Actual Value 0 2 1 3 1 1 4 5 500 0 5 1 1 1 5 1 0 1	Expected Value 0 2 1 3 1 1 4 5 500 0 5 1 1 1 5 1 0 1 0	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgltoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_I target_LoaMgr_Per1_TloaDi_Cnt_Igc Actual Value 0 2 1 3 1 1 4 5 500 0 5 1 1 1 5 1 0 1	Expected Value 0 2 1 3 1 1 4 5 500 0 5 1 1 1 5 1 0 1 0 0	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgltoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VtgMdSrcSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_I target_LoaMgr_Per1_TloaDi_Cnt_Igc Actual Value 0 2 1 3 1 1 4 5 500 0 5 1 1 1 5 1 0 1 0 0	Expected Value 0 2 1 3 1 1 4 5 500 0 5 1 1 1 5 1 0 0 0 0 0	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrltoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrltoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	target_LoaMgr_Per1_TloaAvail_Cnt_I target_LoaMgr_Per1_TloaDi_Cnt_Igc Actual Value 0 2 1 3 1 1 4 5 500 0 5 1 1 1 0 1 0 0 1 0 1 1 0 0 0 {170, 173}	Expected Value 0 2 1 3 1 1 4 5 500 0 5 1 1 1 0 1 0 1 0 0 1 170, 173}	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrtLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrtLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrtLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrtLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	target_LoaMgr_Per1_TloaAvail_Cnt_I target_LoaMgr_Per1_TloaDi_Cnt_Igc Actual Value 0 2 1 3 1 1 4 5 500 0 5 1 1 1 0 1 0 0 1 0 1 1 0 0 0 {170, 173}	Expected Value 0 2 1 3 1 1 4 5 500 0 5 1 1 1 0 1 0 0 1 0 0 {170, 173}	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LoaStaleLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_LoaMgr_Per1_TloaAvail_Cnt_I target_LoaMgr_Per1_TloaDi_Cnt_Igc Actual Value 0 2 1 3 1 1 4 5 500 0 5 1 1 1 0 0 1 0 0 {170, 173} {1, 1} {1, 1}	Expected Value 0 2 1 3 1 1 4 5 500 0 5 1 1 1 0 0 1 0 0 {170, 173} {0, 0, 0, 0, 0} {1, 1}	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_NotCurrLoaMtgnEn_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_LoaMgr_Per1_TloaAvail_Cnt_I target_LoaMgr_Per1_TloaDi_Cnt_Igc Actual Value 0 2 1 3 1 1 4 5 500 0 5 1 1 1 0 0 1 0 0 1 0 0 1 1 1 5 1 1 1 5 1 1 5 1 1 1 5 1 1 1 5 1 1 1 5 1 1 1 5 1 1 1 5 1 1 1 5 1 1 1 5 1 1 1 5 1 1 1 5 1 1 1 5 1 1 1 5 1 1 1 5 1 1 1 5 1 1 1 5 1 1 1 1 5 1 1 1 1 5 1 1 1 1 5 1 1 1 5 1 1 1 1 5 1 1 1 5 1 1 1 1 5 1 1 1 5 1 1 1 1 5 1 1 1 1 5 1 1 1 1 5 1 1 1 1 5 1 1 1 1 5 1 1 1 1 5 1 1 1 1 5 1 1 1 1 5 1 1 1 1 5 1 1 1 1 5 1 1 1 1 5 1 1 1 1 1 5 1	Expected Value 0 2 1 3 1 1 4 5 500 0 5 1 1 1 0 0 1 0 0 1 0 0 1 1 0 0 1 0 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_StgMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRcaleFctr_Uls_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	target_LoaMgr_Per1_TloaAvail_Cnt_I target_LoaMgr_Per1_TloaDi_Cnt_Igc Actual Value 0 2 1 3 1 1 4 5 500 0 5 1 1 1 0 0 1 0 0 (170, 173) {1, 1} {1, 1} 500 0	Expected Value 0 2 1 3 1 1 4 5 500 0 5 1 1 1 0 0 1 0 0 {170, 173} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 0 ± 0.001	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaStateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_StyDespt StyDespt StyDespt Status(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	target_LoaMgr_Per1_TloaAvail_Cnt_I target_LoaMgr_Per1_TloaDi_Cnt_Igc Actual Value 0 2 1 3 1 1 4 5 500 0 5 1 1 1 0 0 1 0 0 (170, 173) {1, 1} {1, 1} 500 0 1	Expected Value 0 2 1 3 1 1 4 5 500 0 5 1 1 1 0 0 1 0 0 {170, 173} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 0 ± 0.001 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LoaStaleEtimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	target_LoaMgr_Per1_TloaAvail_Cnt_I target_LoaMgr_Per1_TloaDi_Cnt_Igc Actual Value 0 2 1 3 1 1 4 5 500 0 5 1 1 0 0 0 {170,173} {1,1} {1,1} 500 0 1 1	Expected Value 0 2 1 3 1 1 4 5 500 0 5 1 1 1 5 1 0 0 1 0 0 {170, 173} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 0 ± 0.001 1 1 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LoaSt_State_M_enum LoaMgr_LoaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_loASt_State_enum.value	target_LoaMgr_Per1_TloaAvail_Cnt_I target_LoaMgr_Per1_TloaDi_Cnt_Igc Actual Value 0 2 1 3 1 1 4 5 500 0 5 1 1 0 0 0 {170,173} {1,1} {1,1} 500 0 1 1 5	Expected Value 0 2 1 3 1 1 4 5 500 0 5 1 1 1 5 1 0 0 1 0 0 (170, 173) {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 0 ± 0.001 1 1 1 5	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LoaStaleEtimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	target_LoaMgr_Per1_TloaAvail_Cnt_I target_LoaMgr_Per1_TloaDi_Cnt_Igc Actual Value 0 2 1 3 1 1 4 5 500 0 5 1 1 0 0 0 {170,173} {1,1} {1,1} 500 0 1 1	Expected Value 0 2 1 3 1 1 4 5 500 0 5 1 1 1 5 1 0 0 1 0 0 {170, 173} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 0 ± 0.001 1 1 1	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.53 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr MotAgldptSigSV Cnt M u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	3
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_le	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	_
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	~
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	1	1	_
LoaMgr HwTqRespSV Cnt M u08	3	3	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	_
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	_
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	_
LoaMgr_LoaScaleFctr_UIs_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	_
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	_
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	_
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	_
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	_
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	_
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(Param Cnt T u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	{1, 1}	{1, 1}	
Rte Write Ap LoaMgr LoaRateLimit UlspS f32(data)	500	500 ± 0.01	-
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	_
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	_
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	-
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~
gg	-	1-	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	✓
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	✓



Test Step 1.54 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1 torret Dte last An Laster
Rte_Inst_Ap_LoaMgr k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnNcaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08 k_LoaMgr_lvtrMtgtnRate_UlspS_f32	0 1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k LoaMgr RedcdStRate UlspS f32	1
k_LoaMgr_RedcdStSca_UIs_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
	,





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	3	3	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170}	{170}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1}	{1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.55 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Name	Input Value			
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1			
k_LoaMgr_FadeOutStRate_UlspS_f32	1			
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2			
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4			
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2			
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1			
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0			
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	1			
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08 k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1			
k_LoaMgr_HwTqMtgtnSca_UIs_f32	1			
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0			
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0			
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1			
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1			
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5			
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1			
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0			
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2			
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc		0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1			
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2			
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2			
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	5			
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08 k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1			
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1			
k_LoaMgr_RedcdStRate_UlspS_f32	1			
k_LoaMgr_RedcdStSca_Uls_f32	1			
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc.value	1			
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1			
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3			
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0			
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2			
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1			
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc		target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc		target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08		target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Igc	target LoaMgr Per1 IvtrLoaMtgtnEn Cnt	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_Loamgr_Per1_ivitLoamiginEn_Gnt_igc target LoaMgr Per1_LOASt State enum			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_(
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc			
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enun	n		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc			
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	1	1	~	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	~	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0		
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	V	
LoaMgr_LOASt_State_M_enum	5	5	V	
LoaMgr_LoaScoloFetr_Ulo_M_f32	500	500	· ·	
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	0	0		
LUGOVIII MATRIMUM BESU LUI IVI 1000	5	5		
	5	5	· ·	
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_igc	5 2 0	5 2 0	, , , , , , , , , , , , , , , , , , ,	

LoaMgr_MotAgRespSV_Cnt_M_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	✓
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 175}	{170, 173, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc.value	1	1	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.56 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc.value	1		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value			
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	80	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_	.lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0	ent lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
		From a start Walter	D14
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	500	500	~
LoaMgr MaxMtgtnResp Cnt M u08	0	0	~
Loawigi_waxivitgtirkesp_Crit_ivi_uoo			\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	_
	0 5	0 5	_
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0 5 2	0 5 2	_
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_igc LoaMgr_MotAgRespSV_Cnt_M_u08	0 5 2 0	0 5 2 0	_
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0 5 2 0 1	0 5 2 0 1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_igc	0 5 2 0 1 0	0 5 2 0 1 0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_igc LoaMgr_MotCurrRespSV_Cnt_M_u08	0 5 2 0 1 0 1 3	0 5 2 0 1 0 1 3	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc	0 5 2 0 1 0 1 3	0 5 2 0 1 0 1 3	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurridptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_igc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_igc LoaMgr_TloaDi_Cnt_M_igc	0 5 2 0 1 0 1 3 1	0 5 2 0 1 0 1 3 1	· · · · · · · · · · · · · · · · · · ·
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	0 5 2 0 1 0 1 3 1 1	0 5 2 0 1 0 1 3 1 1	***************************************
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0 5 2 0 1 0 1 3 1 1 1 1 1 1(170, 173, 175)	0 5 2 0 1 0 1 3 1 1 1 1 1 1(70, 173, 175)	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0 5 2 0 1 0 1 3 1 1 1 1 1(170, 173, 175) {1, 1, 1}	0 5 2 0 1 0 1 3 1 1 1 1 {170, 173, 175} {0, 0, 0, 0, 0}	
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 5 2 0 1 0 1 3 1 1 1 1 (170, 173, 175) {1, 1, 1}	0 5 2 0 1 0 1 3 1 1 1 1 {170, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1}	
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	0 5 2 0 1 0 1 3 1 1 1 1 (170, 173, 175) {1, 1, 1} {1, 1, 1}	0 5 2 0 1 0 1 3 1 1 1 1 {170, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01	
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rto_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0 5 2 0 1 0 1 3 1 1 1 1 {170, 173, 175} {1, 1, 1} {1, 1, 1} 500 0	0 5 2 0 1 0 1 1 3 1 1 1 {170, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001	
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rto_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0 5 2 0 1 0 1 3 1 1 1 1 {170, 173, 175} {1, 1, 1} {1, 1, 1} 500 0 1	0 5 2 0 1 0 1 1 3 1 1 1 {170, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001 1	
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0 5 2 0 1 0 1 1 0 1 3 1 1 1 {170, 173, 175} {1, 1, 1} {1, 1, 1} 500 0 1 1	0 5 2 0 1 0 1 3 1 1 1 {170, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001 1	
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	0 5 2 0 1 0 1 3 1 1 1 {170, 173, 175} {1, 1, 1} {1, 1, 1} 500 0 1 1 1 5	0 5 2 0 1 0 1 1 0 1 3 1 1 1 {170, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001 1 1 1 5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0 5 2 0 1 0 1 1 0 1 3 1 1 1 {170, 173, 175} {1, 1, 1} {1, 1, 1} 500 0 1 1	0 5 2 0 1 0 1 3 1 1 1 {170, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001 1	· · · · · · · · · · · · · · · · · · ·



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.57 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	1.
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	5
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrlvtrMtgtnScaZeroEn Cnt lgc	0
k LoaMgr CurrlvtrMtgtnSca Uls f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrIdptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
	1
k_LoaMgr_lvtrMtgtnSca_Uls_f32	5
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtstaScoZorsEn_Cot_lag	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	-	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0	Ont Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	0	0	_
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	✓
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	✓
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	✓
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	✓
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	1	1	✓
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr MotCurrLoaMtgtnEn Cnt M lgc	0	0	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	5	5	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	✓
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172}	{170, 172}	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	-
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target LoaMgr Per1 MotAgLoaMtgtnEn Cnt Igc.value	0	0	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	✓
	·		

Test Step Call Trace ✓				
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 1.58 (Repeat Count = 1)	XII
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	
LoaMgr_IvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_UIs_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08 k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k LoaMgr lvtrMtgtnRate UlspS f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	0
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	5
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_UIs_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0
target LoaMgr Per1 LoaScaDi Cnt Igc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1lnactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_InvTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 175}	{170, 172, 173, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.59 (Repeat Count = 1)	2
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08 k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k LoaMgr MotCurrldptSig0Resp Cnt u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target Rie Inst An LoaMgr LoaMgr Peri DiagoStsivtrilnacty Cnt Igo	target LoaMgr Per1 DiagcStslytr1Inacty Cn	nt lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cn target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_DiagcStsivtr1Inactv_Cnt_igc target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_DiagcStsivtr2Inactv_Cnt_igc target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cn target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cn target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cn	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cr target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	nt_lgc _lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cntarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgtarget_LoaMgr_Per1_LOASt_State_enum	nt_lgc _lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cntarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgtarget_LoaMgr_Per1_LOASt_State_enumtarget_LoaMgr_Per1_LoaScaDi_Cnt_lgc	nt_lgc _lgc c	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cntarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgtarget_LoaMgr_Per1_LOASt_State_enumtarget_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_loaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_loaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotAgloaMgr_Per1_MotAgloaMgr_Per1_MotAgloaMgr_Per1_MotAgloaMgr_Per1_MotAgloaMgr_Per1_MotAgloaMgr_Per1_MotAgloaMgr_Per1_MotAgloaMgr_P	nt_lgc c t_lgc	
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target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cntarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgtarget_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08target_LoaMgr_Per1_SteerMod_Cnt_enum	nt_lgc c t_lgc b8 nt_lgc	
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target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_vtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cntarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_gtarget_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotQurrldptSig_Cnt_u02 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u02 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u02 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u02 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1 0 0 1	t_lgc c t_lgc s t_lgc	***
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_vtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_ItvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IVtrRespSV_Cnt_M_u08 LoaMgr_IOASt_State_M_enum	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cntarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_gtarget_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrloaMtgtnEn_C target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1 0 1 5 5	t_lgc c t_lgc s8 nt_lgc 8 t_lgc 8 t_lgc 1 0 1 0 0 1 5 5	*****
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_vtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotQurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqlAcaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cntarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgtarget_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotQurrldptSig_Cnt_u0ttarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0ttarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0ttarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0ttarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgc **Actual Value** 1	L	***************************************
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_vtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotQurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cntarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_gtarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igctarget_LoaMgr_Per1_LoaScaDi_Cnt_Igctarget_LoaMgr_Per1_MotQurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_Igctarget_LoaMgr_Per1_TloaDi_Cnt_Igc Actual Value 1 0 1 5 5 5 500 0	L Igc C C C C C C C C C	*****

LoaMgr_MotAgRespSV_Cnt_M_u08

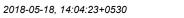
2018-05-18, 14:04:23+0530



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	✓
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 175}	{170, 172, 173, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	✓
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.60 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





N	In and Malana		
Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k LoaMgr MotAgMtgtnRate UlspS f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k LoaMgr MotAgMtgtnSca Uls f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	0		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target LoaMgr Per1 LoaScaDi Cnt Igc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Ina	actv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Ina	actv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqIdptSig_C	nt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnl		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_e		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_	-	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrIdptSig_ target_LoaMgr_Per1_MotCurrLoaMtgr	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_	_	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_l		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target LoaMgr Per1 TloaDi Cnt Igc	3 -	
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	✓
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	✓
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	✓
LoaMgr_LOASt_State_M_enum	-	5	
LoaMgr_LoaRateLimit_UlspS_M_f32	5		~
	500	500	· ·
LoaMgr_LoaScaleFctr_Uls_M_f32	500	500	~
LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	500 0 5	500 0 5	
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	500 0 5 2	500 0 5 2	~
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	500 0 5 2 1	500 0 5 2	· · · · · · · · · · · · · · · · · · ·
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	500 0 5 2 1	500 0 5 2 1	· · · · · · · · · · · · · · · · · · ·
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	500 0 5 2 1 2	500 0 5 2 1 2	, , , , , , , , , , , , , , , , , , ,
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	500 0 5 2 1	500 0 5 2 1 2 0	~
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	500 0 5 2 1 2 0	500 0 5 2 1 2	•
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	500 0 5 2 1 2 0 1	500 0 5 2 1 2 0 1	•
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc	500 0 5 2 1 2 0 1 1	500 0 5 2 1 2 0 1 1 1	
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaOi_Cnt_M_lgc	500 0 5 2 1 2 0 1 1 1	500 0 5 2 1 2 0 1 1 1 1	
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	500 0 5 2 1 2 0 1 1 1 0	500 0 5 2 1 2 0 1 1 1 0 0	
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	500 0 5 2 1 2 0 1 1 1 0 0 {170, 173, 174} {1, 1, 1} {1, 1, 1}	500 0 5 2 1 2 0 1 1 1 0 0 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1}	
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NataleLimit_UlspS_f32(data)	500 0 5 2 1 2 0 1 1 1 1 0 0 {170, 173, 174} {1, 1, 1} {1, 1, 1} 500	500 0 5 2 1 2 0 1 1 1 1 0 0 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01	
LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	500 0 5 2 1 2 0 1 1 1 1 0 0 {170, 173, 174} {1, 1, 1} {1, 1, 1} 500 0	500 0 5 2 1 2 0 1 1 1 1 0 0 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001	
LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	500 0 5 2 1 2 0 1 1 1 1 0 0 {170, 173, 174} {1, 1, 1} {1, 1, 1} 500 0 1	500 0 5 2 1 2 0 1 1 1 1 0 0 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001 1	
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_u08) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	500 0 5 2 1 2 0 1 1 1 1 0 0 {170, 173, 174} {1, 1, 1} 500 0 1 1	500 0 5 2 1 2 0 1 1 1 1 0 0 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 0 ± 0.001 1 1	
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	500 0 5 2 1 2 0 1 1 1 1 0 0 {170, 173, 174} {1, 1, 1} 500 0 1 1 1 5	500 0 5 2 1 2 0 1 1 1 0 0 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001 1 1 5	
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_u08) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	500 0 5 2 1 2 0 1 1 1 1 0 0 {170, 173, 174} {1, 1, 1} 500 0 1 1	500 0 5 2 1 2 0 1 1 1 1 0 0 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 0 ± 0.001 1 1	**************************************



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•

Test Step 1.61 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr MotAgldptSigSV Cnt M u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrIdptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnKate_Oisp3_132	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k LoaMgr MotAgMtgtnSca Uls f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
	5
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08 k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	
k_LoaMgr_RedodStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C	cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C	cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	ıt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_l	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ci	nt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaAvail Cnt Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	- 100 and
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	· ·
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr HwTqRespSV Cnt M u08	4	4	· ·
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	_
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	· ·
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	5	5	
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	
LoaMgr VltgMdSrcSV Cnt M u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(Param Cnt T u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	_
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	-
target LoaMgr Per1 LOASt State enum.value	5	5	✓
target LoaMgr Per1 MotAgLoaMtgtnEn Cnt Igc.value	1	1	
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc.value	0	0	~
2. 2. 2. 3. 3. 2. 2. 2. 2. 2. 2. 2. 2. 2. 2. 2. 2. 2.			

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~



Test Step 1.62 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlytrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08 k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k LoaMgr IvtrMtgtnRate UlspS f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	0
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedodStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	0
target LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr_Dead_Res_Dead_Rts_Inst_Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1lnactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_inst_Ap_LoaMgr.LoaMgr_Per1_ivirLoaMtgtnEn_Cnt_igc target_Rte_inst_Ap_LoaMgr.LoaMgr_Per1_ivirLoaMtgtnEn_Cnt_igc	target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_IvtrRespSV_Cnt_M_u08	4	4	~
LoaMgr_LOASt_State_M_enum	4	4	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	4	4	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{171, 173}	{171, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	4	4	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.63 (Repeat Count = 1)		×
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_lvtrRespSV_Cnt_M_u08	4	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	4	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

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Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k LoaMgr HwTqMtgtnSca Uls f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0		
k LoaMgr IvtrMtgtnRate UlspS f32	1		
	0		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_lvtrMtgtnSca_Uls_f32			
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Ont lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0		
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target LoaMgr Per1 HwTqldptSig Cnt u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Ci		
target Rte Inst Ap LoaMgr.LoaMgr Per1 lvtrLoaMtgtnEn Cnt lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_		
target Rte Inst Ap LoaMgr.LoaMgr Per1 LOASt State enum	target LoaMgr Per1 LOASt State enum	.90	
target Rte Inst Ap LoaMgr.LoaMgr Per1 LoaScaDi Cnt Igc	target LoaMgr Per1 LoaScaDi Cnt Igc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 LoaScaDI_Cnt_igc target Rte Inst Ap LoaMgr.LoaMgr Per1 MotAgLoaMtgtnEn Cnt Igc	target LoaMgr Per1 MotAgLoaMtgtnEn C	nt lac	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	✓
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	~
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	_
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	_
LoaMgr MotAgldptSigSV Cnt M u08	1	1	•
LoaMgr MotAqLoaMtgtnEn Cnt M lqc			

LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08

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Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	✓
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc.value	0	0	✓

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.64 (Repeat Count = 1)	3
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	5
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k LoaMgr MotCurrldptSig1Resp Cnt u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSigFltThd Cnt u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32			
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv	Cnt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target LoaMgr Per1 DiagcStslvtr2Inactv		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqldptSig Cnt u08	target LoaMgr Per1 HwTqldptSig Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cr		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enur		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cn	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnE		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt	_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enu	ım	
	target_LoaMgr_Per1_SteerMod_Cnt_enutarget_LoaMgr_Per1_TloaAvail_Cnt_lgc	ım	
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum		um	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc	Expected Value	Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc		Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	Expected Value	~
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	Expected Value	~
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1	Expected Value 1	~
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 4	Expected Value 1 1 1 4	· ·
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IwtrIqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 0	Expected Value 1 1 1 4 0	· ·
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 4 0 1	Expected Value 1 1 1 4 0 1	• • • • • • • • • • • • • • • • • • •
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 4 0 1 5	Expected Value 1 1 1 4 0 1 5	• • • • • • • • • • • • • • • • • • •
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 5 5	Expected Value 1 1 1 4 0 1 5 5	•
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 4 0 1 5 5 5	Expected Value 1 1 1 4 0 1 5 5 5	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 4 0 1 5 5 500 0	Expected Value 1 1 1 4 0 1 5 5 0 0 0	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaSt_state_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 4 0 1 5 5 500 0 5	Expected Value 1 1 1 4 0 1 5 5 5 500 0 5	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrQaMtgtnEn_Cnt_M_Igc LoaMgr_LoaSt_State_M_enum LoaMgr_LoaScaleFctr_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 4 0 1 5 5 500 0 5 1	Expected Value 1 1 1 4 0 1 5 5 5 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdoaMtgtnEn_Cnt_M_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 4 0 1 5 5 5 500 0 5 1 0	Expected Value 1 1 1 4 0 1 5 5 5 1 0	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_ivtrloaMtgtnEn_Cnt_M_lgc LoaMgr_ivtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_ivtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 4 0 1 5 5 500 0 5 1 0 5	Expected Value 1 1 1 4 0 1 5 5 5 1 0 5	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_ivtrloaMtgtnEn_Cnt_M_lgc LoaMgr_ivtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_ivtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurridptSigSV_Cnt_M_u08 LoaMgr_MotCurridptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 4 0 1 5 5 500 0 5 1 0 5 0	Expected Value 1 1 1 4 0 1 5 5 5 1 0 5 0	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaAsateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 4 0 1 5 5 5 500 0 0 5 1 0 5	Expected Value 1 1 1 4 0 1 5 5 5 1 0 5 0 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoASt_State_M_enum LoaMgr_LoaCastelFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 4 0 1 5 5 500 0 5 1 0 5 0 1	Expected Value 1 1 1 4 0 1 5 5 5 500 0 5 1 0 5 0 1 2	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaAsateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 4 0 1 5 5 5 500 0 0 5 1 0 5	Expected Value 1 1 1 4 0 1 5 5 5 5 0 1 2 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoASt_State_M_enum LoaMgr_LoaCastelFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 4 0 1 5 5 500 0 5 1 0 5 0 1	Expected Value 1 1 1 4 0 1 5 5 5 500 0 5 1 0 5 0 1 2	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotAglcaMtgtnEn_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 4 0 1 5 5 500 0 0 5 1 0 1 2 1	Expected Value 1 1 1 4 0 1 5 5 5 5 0 1 2 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LoASt_State_M_enum LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 4 0 1 5 5 5 500 0 5 1 0 5 0 1 2 1 1	Expected Value 1 1 1 4 0 1 5 5 5 0 1 2 1 1 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LoASt_State_M_enum LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VtgMdSrcSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 4 0 1 5 5 5 500 0 5 1 0 5 0 1 2 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Expected Value 1 1 1 4 0 1 5 5 5 0 1 2 1 1 1 1 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgCaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgNespSV_Cnt_M_u08 LoaMgr_MotAgNespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_NtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 4 0 1 5 5 5 500 0 5 1 0 5 0 1 2 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Expected Value 1 1 1 1 4 0 1 5 5 5 500 0 5 1 0 1 2 1 1 1 1 {170, 172, 173}	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LOASt_State_M_enum LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgCaMtgtnEn_Cnt_M_u08 LoaMgr_MotAgCaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrtLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrtLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrtLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrtLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_NtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 4 0 1 5 5 5 500 0 1 2 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Expected Value 1 1 1 1 4 0 1 5 5 5 500 0 5 1 0 1 2 1 1 1 1 {170, 172, 173} {0, 0, 0, 0, 0}	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgloaMtgtnEn_Cnt_M_log LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_NotCurrlapMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 4 0 1 5 5 5 500 0 1 2 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Expected Value 1 1 1 1 4 0 1 5 5 5 500 0 1 2 1 1 1 1 1 11 1 170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1}	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgCoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrtCadMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrtCadMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrtCadMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrtCadMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrtCadMtgtnEn_Cnt_M_Igc LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_StgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 4 0 1 5 5 5 500 0 1 2 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Expected Value 1 1 1 1 4 0 1 5 5 5 500 0 1 2 1 1 1 1 1 1 170, 172, 173} {0, 0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_StyltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 4 0 1 5 5 5 500 0 1 2 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Expected Value 1 1 1 1 4 0 1 5 5 5 500 0 1 2 1 1 1 1 1 1 170, 172, 173} {0, 0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_StyD_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 4 0 1 5 5 5 500 0 1 2 1 1 1 1 {170, 172, 173} {1, 1, 1} {1, 1, 1} 500 0 1	Expected Value 1 1 1 4 0 1 5 5 5 500 0 1 2 1 1 1 (170, 172, 173) {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_U08 LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaCall_Ap_LoaMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatu	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 4 0 1 5 5 5 500 0 0 5 1 1 2 1 1 1 1 {170, 172, 173} {1, 1, 1} {1, 1, 1} 500 0 1 1 5 6 7 6 7 7 8 7 8 7 8 8 8 8 8 8 8 8 8 8 8	Expected Value 1 1 1 4 0 1 5 5 5 500 0 5 1 1 0 1 2 1 1 1 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001 1 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_StyD_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 4 0 1 5 5 5 500 0 5 1 1 2 1 1 1 1 4 170, 172, 173} {1, 1, 1} {1, 1, 1} 500 0 1 1 1	Expected Value 1 1 1 4 0 1 5 5 5 500 0 5 1 0 5 1 1 1 1 1 1 1 1	



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	✓
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	-
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.65 (Repeat Count = 1)	Σ
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr MotAgldptSigSV Cnt M u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	3
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
	0
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_lvtrMtgtnSca_Uls_f32	5
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
	0
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	2
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	1
k_LoaMgr_MotAgMtgtnScaZgasEn_Cat_lag	0
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotAgMtgtnSca_Uls_f32	2
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0 0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	1
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target LoaMgr Per1 HwTqldptSig Cnt u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target LoaMgr Per1 MtrPosldptSig Cnt u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target Rte Inst Ap LoaMgr.LoaMgr Per1 DiagcStslvtr1Inactv Cnt Igc	target_LoaMgr_Per1_DiagcStslvtr1l	nactv Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target LoaMgr Per1 DiagcStslvtr2l		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_	Cnt u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target LoaMgr Per1 HwTqLoaMtgt	_	
target Rte Inst Ap LoaMgr.LoaMgr Per1 lvtrLoaMtgtnEn Cnt Igc	target_LoaMgr_Per1_IvtrLoaMtgtnE		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtg		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cni		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc	target LoaMgr Per1 TloaAvail Cnt		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lg		
Name	Actual Value	Expected Value	Result
	1	1	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	4	4	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	5	5	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum			
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	Ž
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	· ·
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	Ž
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	V
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	•
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	V
LoaMgr_TloaDi_Cnt_M_lgc	1	1	•
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<u> </u>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	•
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	✓



Test Step 1.66 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08 k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k LoaMgr lvtrMtgtnRate UlspS f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	0
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_UIs_f32	1
k_LoaMgr_RedodStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	0
target LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0
target LoaMgr Per1 LoaScaDi Cnt Igc.value	1
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_InvTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc



Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	2	2	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	•
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•

Test Step 1.67 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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LoaMgr_Per1



Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
	5		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08			
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k LoaMgr MotAgldptSig3Resp Cnt u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k LoaMgr MotAgMtgtnRate UlspS f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc			
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target LoaMgr Per1 LoaScaDi Cnt Igc.value	0		
target LoaMgr Per1 MotCurrldptSig Cnt u08.value	1		
target LoaMgr Per1 MtrPosIdptSig Cnt u08.value	1		
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target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqLoaMtgtnEn Cnt Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_k		
target Rte Inst Ap LoaMgr.LoaMgr Per1 LOASt State enum	target_LoaMgr_Per1_LOASt_State_enum	5-	
	target LoaMgr Per1 LoaScaDi Cnt Igc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc		at Igo	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
		-	result
LoaMgr_AgSV_Cnt_M_lgc	1	1	Y
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	V
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	-
	0	0	-
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc		4	-
	4		
LoaMgr_lvtrRespSV_Cnt_M_u08			-
LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	5	5	
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	5 500	5 500	-
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	5 500 0	5 500 0	7
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	5 500 0 5	5 500 0 5	~
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	5 500 0	5 500 0	~
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	5 500 0 5	5 500 0 5	





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	Ī	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	✓
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	✓
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.68 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	2
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





N.			
Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqIdptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_loaning	дс	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	
LoaMgr HwTqldptSigSV Cnt M u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	-
	4	4	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4		
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	1	-
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc		0	
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	-
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	_
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	
target_LoaMgr_Per1_LOASt_State_enum.value target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	
		134	. •
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.69 (Repeat Count = 1)	0
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr MotAgldptSigSV Cnt M u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
	0
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	· -	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	ent Ige	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum	1	
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaAvail Cnt Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	_
LoaMgr HwTqRespSV Cnt M u08	2	2	✓
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	✓
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	✓
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	_
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	✓
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	_
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	_
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	•
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target LoaMgr Per1 MotAgLoaMtgtnEn Cnt Igc.value	1	1	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 1.70 (Repeat Count = 1)	13
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	1 4
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	4 2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_UIs_f32	1 5
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_NotCurrintgthSca_UIs_t32 k_LoaMgr_RedcdStRate_UIspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1 target LeaMar Perf DiagoStelytr/Inacty Cnt Inc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
· · · · · · · · · · · · · · · · ·	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc



Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	✓
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	✓
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	•
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	✓
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	✓
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	✓

Test Step 1.71 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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LoaMgr_Per1



Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0.589399993		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	0		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k LoaMgr MotAgldptSig2Resp Cnt u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08 k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_UIs_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target LoaMgr Per1 TloaDi Cnt Igc.value	1		
target Rte Inst Ap LoaMgr.LoaMgr Per1 DiagcStslvtr1Inactv Cnt Igc	target LoaMgr Per1 DiagcStslvtr1Inactv Ci	nt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	toract LooMan Dord LluxTaldatCia Cat u00		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt		
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lo	_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LOASt_State_enum	_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Ig target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_Igc	_igc ic	
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target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_MrLoaMtgtnEn_Cnt_lgtarget_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cntarget_LoaMgr_Per1_MotCurrIdptSig_Cnt_utarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgc	_lgc lc t_lgc lc	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_tgtarget_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrIotAgttgnEn_Cnt_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cttarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaDi_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 0 4 0 0 5	_lgc lcc t_lgc lcs t_lgc lcs Expected Value 0 0 0 4 0 0 5	***
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HvTqLoaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igtarget_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrIotAgtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cttarget_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0ttarget_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_Igc target_LoaMgr_Per1_TloaDi_Cnt_Igc Actual Value 0 0 0 4 0 0 5 5	_lgc lc t_lgc 08 8cht_lgc 8 Expected Value 0 0 0 4 0 0 5 5	***
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HvTqLoaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaSt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgtarget_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cntarget_LoaMgr_Per1_MotCurrIdptSig_Cnt_utarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 0 0 4 0 0 5 5 5 500	lgc lc t_lgc 08 cnt_lgc 8 Expected Value 0 0 0 4 0 0 5 5 5 500	***
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIoptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqltpSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgtarget_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cntarget_LoaMgr_Per1_MotCurrlotptSig_Cnt_utarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaDi_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 0 0 4 0 0 5 5 5 500 1	lgc lc tlgc	***
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIoptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_IvtrIoptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgtarget_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cntarget_LoaMgr_Per1_MotCurrIdptSig_Cnt_utarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 0 0 4 0 0 5 5 5 500	lgc lc t_lgc 08 cnt_lgc 8 Expected Value 0 0 0 4 0 0 5 5 5 500	*****
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIoptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIoptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrlottSig_Cnt_utarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Ctarget_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 0 5 5 500 1	igc ic tigc 08 cntigc 8 Expected Value 0 0 0 0 4 0 0 5 5 5 5000 1 5	*****

LoaMgr_MotAgRespSV_Cnt_M_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	✓
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt lgc.value	1	1	✓

Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.72 (Repeat Count = 1)	13
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	0.0099999978
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5

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Nama	Input Value		
Name k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08 k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	0		
k_LoaMgr_MotCurrMtgtnNate_otspo_joz	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1 2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target LoaMgr Per1 TloaDi Cnt Igc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inac	tv Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inac		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt	_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enu		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lg		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEi		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_er	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	2	· ·
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08	1 4	4	
LoaMgr LOASt State M enum	4	4	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	4	4	✓
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	
		{171, 172, 173}	
Rie Call Ab Loawig Nxirdiagwig Seint Cstatus(NTC Chi i enum)	{171, 172, 173}		
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{171, 172, 173} {1, 1, 1}	{0, 0, 0, 0, 0}	
			×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1} {1, 1, 1} 500	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	(1, 1, 1) {1, 1, 1} 500 0	{0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001	¥
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	(1, 1, 1) {1, 1, 1} 500 0	{0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	(1, 1, 1) {1, 1, 1} 500 0 0	{0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001 0	¥
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	(1, 1, 1) {1, 1, 1} 500 0 0 1	{0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001 0	¥
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	(1, 1, 1) {1, 1, 1} 500 0 0	{0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001 0	¥



est Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.73 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	500
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k LoaMgr MotCurrldptSig0Resp Cnt u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k LoaMgr MotCurrldptSig2Resp Cnt u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k LoaMgr RedcdStSca Uls f32	1
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	0
<u> </u>	

LoaMgr_Per1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	ıt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ci	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	✓
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_IvtrRespSV_Cnt_M_u08	4	4	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	· ·
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	Y
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	✓

Fest Step Call Trace ✓				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	✓
RqstHwTqResp	1	RqstHwTqResp	1	✓
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 1.74 (Repeat Count = 1)	x
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08 k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k LoaMgr IvtrMtgtnRate UlspS f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	0
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedodStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_Invr_QLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IntrLoaMtgtnEn_Crit_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.75 (Repeat Count = 1)	E Company of the Comp
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	250.478897		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k LoaMgr MotAgldptSig1NoSmaResp Cnt u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k LoaMgr MotAgldptSig3Resp Cnt u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k LoaMgr MotAgMtgtnRate UlspS f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca2eroEn_Cnt_gc k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
	0		
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08			
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	t_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I	gc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	*
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	_
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	-
Loamgr_Loascalercti_Ois_M_i52 Loamgr_MaxMtgtnResp_Cnt_M_u08	5	5	_
Loawigi_maximigiii\csp_oni_ivi_uoo	1 J		T
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	-

LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08

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Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	✓
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc.value	1	1	✓

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.76 (Repeat Count = 1)	N .
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSigFltThd Cnt u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
	1		
k_LoaMgr_RedcdStSca_Uls_f32	0		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value			
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqldptSig Cnt u08	target LoaMgr Per1 HwTqldptSig Cnt u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_		
target_Rte_Inst_Ap_LoaMgr_Der1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	.90	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
		Nat les	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum	1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	A stud Malus	Expected Value	
	Actual Value	The state of the s	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	Result
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08		· ·	Result
	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	0 1 1 1 0 0	0 1 1 0	· ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08	0 1 1 0 1	0 1 1 0	· ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0 1 1 0 1	0 1 1 0 1 1	· ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08	0 1 1 0 1 1 2	0 1 1 0 1 1 1 2	· ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	0 1 1 0 1 1 1 2	0 1 1 0 1 1 1 2 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	0 1 1 0 1 1 1 2 5	0 1 1 0 1 1 1 2 5 500	· ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lVtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	0 1 1 0 1 1 2 5 500	0 1 1 0 1 1 1 2 5 500	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lOASt_State_M_enum LoaMgr_LoaState_Mign_S_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	0 1 1 0 1 1 1 2 5 5 500	0 1 1 0 1 1 2 5 500 1 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lOASt_State_M_enum LoaMgr_LoaState_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	0 1 1 0 1 1 1 2 5 5 500 1 5	0 1 1 0 1 1 1 2 5 5 500 1 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaState_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0 1 1 0 1 1 1 2 5 5 500 1 5 1	0 1 1 0 1 1 1 2 5 5 500 1 5 1	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaState_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	0 1 1 0 1 1 1 2 5 5 500 1 5 1	0 1 1 0 1 1 2 5 500 1 5 1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaState_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0 1 1 0 1 1 1 2 5 5 500 1 5 1 0 5	0 1 1 0 1 1 2 5 5 500 1 5 1 0 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaState_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	0 1 1 0 1 1 1 2 5 5 500 1 5 1	0 1 1 0 1 1 2 5 500 1 5 1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaState_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08	0 1 1 0 1 1 1 2 5 5 500 1 5 1 0 5	0 1 1 0 1 1 2 5 5 500 1 5 1 0 5	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaState_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc	0 1 1 0 1 1 1 2 5 5 500 1 1 5 1 0 5	0 1 1 0 1 1 1 2 5 5 500 1 5 1 0 5 2	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaState_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	0 1 1 0 1 1 1 2 5 5 500 1 5 1 0 5 2	0 1 1 1 0 1 1 1 2 5 5 500 1 5 1 0 5 2 0 3	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaState_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlCaAMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlCaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc	0 1 1 0 1 1 1 2 5 5 500 1 1 5 1 0 5 2 0 3 1	0 1 1 1 0 1 1 1 2 5 5 500 1 5 1 0 5 2 0 3 1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MoxAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	0 1 1 0 1 1 1 2 5 5 500 1 1 5 1 0 5 2 0 3 1 1	0 1 1 1 0 1 1 1 2 5 500 1 5 1 0 5 2 0 3 1 1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldpatSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	0 1 1 0 1 1 1 2 5 5 500 1 1 5 1 0 5 2 0 3 1 1	0 1 1 1 0 1 1 1 2 5 500 1 5 1 0 5 2 0 3 1 1 1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0 1 1 1 0 1 1 1 2 5 500 1 5 1 0 5 2 0 3 1 1 1 0 {170, 172, 173} {1, 1, 1}	0 1 1 1 0 1 1 1 2 5 500 1 5 1 0 5 2 0 3 1 1 1 0 {170, 172, 173}	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 1 1 1 0 1 1 1 2 5 5 500 1 5 1 0 3 1 1 1 0 {170, 172, 173} {1, 1, 1} {1, 1, 1}	0 1 1 1 0 1 1 1 2 5 500 1 5 1 0 5 2 0 3 1 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1}	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 1 1 1 0 1 1 1 2 5 5 500 1 5 1 0 3 1 1 1 0 {170, 172, 173} {1, 1, 1} {1, 1, 1} 500	0 1 1 1 0 1 1 1 2 5 5 500 1 5 1 0 5 2 0 3 1 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LoASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotOurrIdptSigSV_Cnt_M_u08 LoaMgr_MotOurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvaii_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0 1 1 1 0 1 1 1 2 5 5 500 1 5 1 0 3 1 1 1 0 {170, 172, 173} {1, 1, 1} {1, 1, 1} 500 1	0 1 1 1 0 1 1 1 2 5 5 500 1 5 1 0 5 2 0 3 1 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotOurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvaii_Cnt_M_lgc LoaMgr_TloaAvaii_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	0 1 1 1 0 1 1 1 2 5 5 500 1 5 1 0 5 2 0 3 1 1 1 0 {170, 172, 173} {1, 1, 1} {1, 1, 1} 500 1 1 1	0 1 1 1 0 1 1 1 2 5 5 500 1 5 1 0 5 2 0 3 1 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001 1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VtlgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0 1 1 1 0 1 1 1 2 5 5 500 1 5 1 0 5 2 0 3 1 1 1 0 {1,1,1} {1,1,1} 500 1 1 1 1	0 1 1 1 0 1 1 1 2 5 5 500 1 5 1 0 5 2 0 3 1 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 1 1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_lwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotOurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlAgtSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	0 1 1 1 0 1 1 1 2 5 5 500 1 5 1 0 5 2 0 3 1 1 1 0 {1,1,1} {1,1,1} 500 1 1 1 5	0 1 1 1 0 1 1 1 2 5 5 500 1 5 1 0 5 2 0 3 1 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 1 1 5	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VtlgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0 1 1 1 0 1 1 1 2 5 5 500 1 5 1 0 5 2 0 3 1 1 1 0 {1,1,1} {1,1,1} 500 1 1 1 1	0 1 1 1 0 1 1 1 2 5 5 500 1 5 1 0 5 2 0 3 1 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 1 1	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	✓

Test Step 1.77 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr MotAgldptSigSV Cnt M u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1.
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1.
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	-	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u		
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target LoaMgr Per1 SteerMod Cnt enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M Igc	1	1	✓ ×
LoaMgr HwTqldptSigSV Cnt M u08	0	0	_
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	
LoaMgr_LOASt_State_M_enum	5	5	·
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	_
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	-
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 172, 173}	{170, 172, 173}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_endin)	{1, 1, 1}	{0, 0, 0, 0, 0}	W
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	
Rte Write Ap LoaMgr LoaRateLimit UlspS f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	-
	0	0	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Sealing Justification Ju	Test Step 1.78 (Repeat Count = 1)	13
Leading Fart Capability Cor M Loss Leading Fart Capability Cor M Loss 1 Leading Fart Capability Cor M Loss 2 Leading Fart Capability Cor M Loss 1 Leading Fart Capability Cor M Loss 2 Leading Carbon Market Links 2 Leading Links 3 Leading Links 4 Leadin		
Leading - Indiquisation Co. P. Mgs Leading - Indiquisation Use Co. Leading - Indiquisation Use		
Landary Juniferpies Cort M, 100 Landary Juniferpies Cort Landary Cort M, 100 Landary Juniferpies Cort Landary Cort M, 100 Landary Juniferpies Cort Landary Cort M, 100 Landary Juniferpies Cort Cort M, 100 Landary Juni	LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
Landary Juniferson (Co. M. 1, 100	LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
Looking Johnson, Ch. M. Julio Looking Johnson, Ch. Julio Looking Jo	LoaMgr_HwTqRespSV_Cnt_M_u08	1
Landary Mandylapathyperis, Cert. My per Landary Mandylapathyperis, Landary My per Landary My periodic My per Landary My periodic	LoaMgr_lvtrldptSigSV_Cnt_M_u08	-
Loading MonApples (1907, V. O.)	LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	
Loading Manufage in Stage Manufage Loading Loading Manufage in Stage Loading Manufage Loading Manufage Loading Manufage Loading Manufage Loading Loading Manufage Loading Loading Manufage Loading Loading Manufage Loading Loading Loading Manufage Loading Loadi		
Landary, MacCardina Staglistic Cont. Jul. 198 Landary, Landary, Cont. Jul. 198 Landary, La		
Loading McCorridor(Sign) (Loading Control Marge 1 1 1 1 1 1 1 1 1		1
Loading John Company Loading		
Landary Carthyridespread Urings 522 1	LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	
Loading / Contribiting Price Lipid Str.	LoaMgr_MotCurrRespSV_Cnt_M_u08	1
Internation	LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
Landary Controlotignosa: Prof. 10 1	LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Luading / CarrierMingrisca/Lucinia (Lucinia) (Luading / CarrierMingrisca/Lucinia) (Luading / CarrierMingrisca/Lucinia) (Luading / ArrierMingrisca/Lucinia) (Luading / ArrierMi	Rte_Inst_Ap_LoaMgr	V
Loading Failed Shiften Users 1		
Leading / Hirofest Spi Towakowillase, Ort. 108		
Landary Hartfeldrist (Introduction Charles)		
Loading Mr Options The Tourism Loading Loading Loading Mr Options The Tourism The Touris		
Loading Mindigos (2011		
Loading Mr Option (SQ Total Resp. Cmt UB	k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	
Loaking-Hw7qlqptiSig4ffeeg_Cnt_UB	k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	
Loading-HwTqMgginFiltriD_Cri_U0S	k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
Loaking MrVTqMigginSaip LispS .122	k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
Loaking Introduction Loaking	k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
Loaking Indirigisig Reap Crit Loaking Loaking Loaking Indirigisig Reap Crit Loaking Indirigisig Reap Crit Loaking Indirigisig Reap Crit Loaking Indirigisig Reap Reap Crit Loaking Indirigisig Indirigisig Reap Reap Crit Loaking Indirigisig Indirigisi	k_LoaMgr_HwTqMtgtnRate_UlspS_f32	
Loaklgr_IntriptSig2Resp_Crt_u08		
Loaking NrtiopisSigFiTTINd CPI USB 12		
L Loaking / Intrinsipasa (Lips)S		
Loading		
Loading		
LoaMgr_MorLagidpsigarResp_Cnt_u08	k_LoaMgr_lvtrMtgtnSca_Uls_f32	
LoaMgr_MotAgloptSig3Resp_Cnt_u08	k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
LoaMgr_MotAgliqhisigFitThd_Cnt_u08	k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
LoaMgr_MotAgMtghtRate_UlspS_132	k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
C_LoaMgr_MotAgMignScaZeroEn_Cnt_lgc 0 C_LoaMgr_MotAgMignSca_Uls_132 0 C_LoaMgr_MotAgMignSca_Uls_132 0 K_LoaMgr_MotCurlidptSignResp_Cnt_u08 2 K_LoaMgr_MotCurlidptSignResp_Cnt_u08 0 K_LoaMgr_MotCurlidptSignResp_Cnt_u08 0 K_LoaMgr_MotCurlidptSignThd_Cnt_u08 0 K_LoaMgr_MotCurlidptSignResp_Cnt_u08 0 K_LoaMgr_MotCurlidptSignResp_Cnt_u08 1 K_LoaMgr_MotCurlidptSignResp_Cnt_u08 1 K_LoaMgr_MotCurlidptSignResp_Cnt_u08 1 K_LoaMgr_MotCurlidptSignCalls_132 1 K_LoaMgr_RedostSisca_Uls_132 1 K_LoaMgr_RedostSisca_Uls_132 1 K_LoaMgr_RedostSisca_Uls_132 1 K_LoaMgr_Pert_DiagcStsIsvt2linactv_Cnt_lgc_value 0 Larget_LoaMgr_Pert_DiagcStsIsvt2linactv_Cnt_lgc_value 1 Larget_LoaMgr_Pert_MotCurlidptSig_Cnt_u08 value 3 Larget_LoaMgr_Pert_MotCurlidptSig_Cnt_u08 value 0 Larget_LoaMgr_Pert_MitPoslidptSig_Cnt_u08 value 2 Larget_LoaMgr_Pert_TolaQualign_Cnt_lgc_value 0 Larget_LoaMgr_Pert_MitPoslidptSig_Cnt_u08 value <td< td=""><td>k_LoaMgr_MotAgldptSigFltThd_Cnt_u08</td><td></td></td<>	k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	
k_LoaMgr_MotAgSmaAvailResp_Ont_u08 2 k_LoaMgr_MotAgSmaAvailResp_Ont_u08 2 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_Per_DiagcStsivtrilnactv_Cnt_lgc.value 1 larget_LoaMgr_Per_HotQsig1Sig_Cnt_u08 value 3 larget_LoaMgr_Per_Ser_Ser_MotQualve 0 larget_LoaMgr_Per_Ser_MotQualve 1 larget_LoaMgr_Per_Ser_MotQualve 1 <		
LoaMgr_MotOurridptSig0Resp_Cnt_u08 2 k_LoaMgr_MotCurridptSig0Resp_Cnt_u08 2 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 1 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_MotCurridptSig2a_Uls_32 1 k_LoaMgr_RedcdSiRea_Uls_Sp_32 1 k_LoaMgr_RedcdSiRea_Uls_Sp_32 1 k_LoaMgr_Per1_DiagcSistvtr1lnactv_Cnt_lgc.value 0 larget_LoaMgr_Per1_DiagcSistvtr2lnactv_Cnt_lgc.value 1 larget_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value 3 larget_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value 0 larget_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value 0 larget_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value 0 larget_LoaMgr_Per1_ToaAvail_Cnt_lgc.value 0 larget_LoaMgr_Per1_ToaAvail_Cnt_lgc.value 0 larget_LoaMgr_LoaMgr_LoaMgr_Pe		
k_LoaMgr_MotCurridptSig0Resp_Cnt_u08 2 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_Ugc 1 k_LoaMgr_MotCurridptSig2SaZeroEn_Cnt_Ugc 1 k_LoaMgr_MotCurridptSig.SaZeroEn_Cnt_Ugc 1 k_LoaMgr_RedcdSiStade_Uls_732 1 k_LoaMgr_RedcdSiStade_Uls_732 1 k_LoaMgr_Perd_DiagcStstvfr1Inactv_Cnt_Ugc.value 0 target_LoaMgr_Per1_DiagcStstvfr2Inactv_Cnt_Ugc.value 1 target_LoaMgr_Per1_LoaScaD_Cnt_Ugc.value 3 target_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MirPosidptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MirPosidptSig_Cnt_u08.value 2 target_LoaMgr_Per1_ToaDi_Cnt_Ugc.value 1 target_LoaMgr_Per1_ToaDi_Cnt_Ugc.value 1 target_LoaMgr_Per1_ToaDi_Cnt_Ugc.value 1 target_LoaMgr_Per1_ToaDi_Cnt_Ugc.value 1 target_LoaMgr_Per1_ToaDi_Cnt_Ugc.value 1 target_LoaMgr_Per1_DiagcS		
k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2FitThd_Cnt_u08 1 k_LoaMgr_MotCurridptSig2FitThd_Unstrate_Ulsp5_f32 1 k_LoaMgr_MotCurridptSigaCa_Uls_f32 1 k_LoaMgr_RedcdSitRate_Ulsp5_f32 1 k_LoaMgr_RedcdSitRate_Ulsp5_f32 1 k_LoaMgr_Per1_DiagcStstvfrInactv_Cnt_lgc.value 0 target_LoaMgr_Per1_DiagcStstvfrInactv_Cnt_lgc.value 0 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 0 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 0 target_LoaMgr_Per1_MrPosidptSig_Cnt_u08 value 2 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_ToaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_ToaAvail_Cnt_lgc.value 0 target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsivtrInactv_Cnt_lgc target_LoaMgr_Per1_DiagcStsivtrInactv_Cnt_lgc target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsivtrInactv_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per		
k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSigFitThd_Cnt_u08 0 k_LoaMgr_MotCurridptSigFitThd_Cnt_u08 1 k_LoaMgr_MotCurridptInsca_VispS_f32 1 k_LoaMgr_MotCurridptSigSca_Vis_f32 1 k_LoaMgr_RedcdSiKate_UispS_f32 1 k_LoaMgr_RedcdSiKate_UispS_f32 1 karget_LoaMgr_Per1_DiagcSislvfrInactv_Cnt_igc value 0 larget_LoaMgr_Per1_DiagcSislvfr2inactv_Cnt_igc value 1 larget_LoaMgr_Per1_MriqlptSig_Cnt_u08 value 3 larget_LoaMgr_Per1_MriPosidptSig_Cnt_u08 value 0 larget_LoaMgr_Per1_MriPosidptSig_Cnt_u08 value 0 larget_LoaMgr_Per1_MriPosidptSig_Cnt_u08 value 2 larget_LoaMgr_Per1_TioaD_Cnt_igc value 0 larget_LoaMgr_Per1_TioaD_Cnt_igc value 1 larget_LoaMgr_Per1_TioaD_Cnt_igc value 0 larget_Re_Inst_Ap_LoaMgr_Per1_DiagcSistvfzInactv_Cnt_igc larget_LoaMgr_Per1_DiagcSistvir2inactv_Cnt_igc larget_Re_Inst_Ap_LoaMgr_Per1_DiagcSistvfzInactv_Cnt_igc larget_LoaMgr_Per1_DiagcSistvir2inactv_Cnt_igc larget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwrTqLoaMignEn_Cnt_igc larget_LoaMgr_Per1_DiagcSistvir2inactv_Cnt_igc larget_Re_Inst_A		
k_LoaMgr_MotCurrIdptSigFitThd_Cnt_u08 0 k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 1 k_LoaMgr_MotCurrMtgtnSca_Uls f32 1 k_LoaMgr_MotCurrMtgtnSca_Uls f32 1 k_LoaMgr_RedcdStRate_UlspS_f32 1 k_LoaMgr_RedcdStRate_UlspS_f32 1 k_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc.value 0 target_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc.value 1 target_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc.value 3 target_LoaMgr_Perd_MotCurrIdptSig_Cnt_u08.value 0 target_LoaMgr_Perd_MotCurrIdptSig_Cnt_u08.value 0 target_LoaMgr_Perd_MotCurrIdptSig_Cnt_u08.value 2 target_LoaMgr_Perd_SteerMod_Cnt_enum.value 0 target_LoaMgr_Perd_Tioal_Cnt_lgc.value 1 target_Nell_First_Ap_LoaMgr_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc target_LoaMgr_Perd_Tioal_Cnt_lgc.value target_Rell_First_Ap_LoaMgr_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc target_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc target_Rell_First_Ap_LoaMgr_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc target_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc target_Rell_First_Ap_LoaMgr_LoaMgr_Perd_HvTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Perd_HvTqLoaMtgtnEn_Cnt_lgc target_Rell_First_Ap_LoaM	k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	
LoaMgr_MotCurrMigtnScaZercEn_Cnt_lgc k_LoaMgr_MotCurrMigtnSca_Uls_f32 k_LoaMgr_RedcdSiRate_UlspS_f32 k_LoaMgr_RedcdSiRate_UlspS_f32 larget_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value larget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value larget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value larget_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value larget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value larget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value larget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value larget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value larget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value larget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value larget_LoaMgr_Per1_SteerMod_Cnt_enum.value larget_LoaMgr_Per1_ToaDi_Cnt_lgc.value larget_LoaMgr_Per1_ToaDi_Cnt_lgc.value larget_LoaMgr_Per1_ToaDi_Cnt_lgc.value larget_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc larget_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc larget_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc larget_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 larget_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 larget_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc larget_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum larget_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum larget_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc larget_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc larget_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLo	k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
LoaMgr_MotCurrMigtnSca_Uls_f32	k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
K_LoaMgr_RedcdStRate_UispS_f32	k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	k_LoaMgr_MotCurrMtgtnSca_Uls_f32	
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value 1 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 3 target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 0 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnen_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnen_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnen_Cnt_lgc target_LoaMgr_Per1_LoaMgr_Per1_LoaMtgn_Cnt_lgc target_LoaMgr_Per1_LoaMtgn_Cnt_lgc target_LoaMgr_Per1_LoaMtgn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_L	k_LoaMgr_RedcdStRate_UlspS_f32	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 2 target_LoaMgr_Per1_MrtCurrldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value 2 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value 2 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value 2 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc 4 target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc 5 target_Ne_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc 6 target_Ne_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 6 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc 6 target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_Igc 6 target_Ne_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc 6 target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc 6 target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc 6 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc 6 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc 6 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc 6 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc 6 target_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_Igc 6 target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr		
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target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value			
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc			
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	0	0	~	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~	
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	~	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~	
LoaMgr_LOASt_State_M_enum	5	5	•	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	~	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~	
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	•	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•	
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~	
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~	
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~	
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	-	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.79 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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LoaMgr_Per1



Name	Input Value			
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1			
k_LoaMgr_FadeOutStRate_UlspS_f32	1			
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2			
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4			
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2			
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1			
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0			
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0			
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1			
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1			
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1			
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0			
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0			
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1			
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1			
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5			
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1			
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	2			
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k LoaMgr_MotAgMtgtnRate UlspS f32	1			
k_LoaMgr_MotAgMtgtnRate_UispS_t32 k LoaMgr MotAgMtgtnScaZeroEn Cnt Igc	0			
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1			
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2			
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2			
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0			
k LoaMgr MotCurrldptSig2Resp Cnt u08	0			
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1			
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1			
k_LoaMgr_RedcdStRate_UlspS_f32	1			
k_LoaMgr_RedcdStSca_Uls_f32	1			
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0			
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0			
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0			
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1			
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0			
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0			
	U			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1			
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1			
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	1 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C			
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	1 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	nt_lgc		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	nt_igc } t_igc		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cntarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cntarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Iquid	nt_igc } t_igc		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cntarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cntarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cntarget_LoaMgr_Per1_LOASt_State_enum	nt_igc } t_igc		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_uget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_uget_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	nt_lgc s t_lgc gc		
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target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAQLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cntarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cntarget_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crtarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crtarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0target_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cn	nt_lgc st_lgc gc st_lgc gc st_lgc st_	***	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IVtrRespSV_Cnt_M_u08 LoaMgr_LOaSt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	1 1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoASc_Di_Cnt_lgc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crt target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaAvail_Cnt_lgc Actual Value 0 0 0 4 0 0 5 5 5 500	nt_lgc st_lgc st_lgc gc nt_lgc 08 Cnt_lgc 08 Expected Value 0 0 0 4 0 0 5 5 5	***	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqlpdtSigSV_Cnt_M_u08 LoaMgr_HwTqlcaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cntarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cntarget_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotGurrIdptSig_Cnt_u0target_LoaMgr_Per1_MotCurrLoaMgtnEn_Crtarget_LoaMgr_Per1_MotCurrLoaMgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0target_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 0 4 0 0 5 5 5 500 1	nt_lgc st_lgc st_lgc gc nt_lgc os st_lgc st	******	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cntarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cntarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cntarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Crtarget_LoaMgr_Per1_MotCurrIdptSig_Cnt_u0target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0target_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 0 5 5 5 500 1 5	nt_lgc st_lgc gc nt_lgc gc nt_lgc 08 Cnt_lgc 08 Expected Value 0 0 0 0 4 0 0 5 5 5 5000 1 5	***************************************	

LoaMgr_MotAgRespSV_Cnt_M_u08

2018-05-18, 14:04:23+0530

LoaMgr_Per1



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	✓
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	✓
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	1	1 ± 0.001	✓
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	-
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.80 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
Name	1		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08			
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0 2		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	1		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	0		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	0.697300017		
	2		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSig2Resp Cnt u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k LoaMgr MotCurrMtgtnScaZeroEn Cnt Igc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	target LoaMgr Per1 DiagcStslvtr1Inactv C	nt lao	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C		
	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_loaMgr_Per1_IvtrLoaM	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	st Inc.	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_(
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	06	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc			
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	✓ ✓
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	· ·
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	- 4
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	¥
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	¥
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	V
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc			
LoaMgr_MotAgRespSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	4	4	V
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	4 0	4 0	*
	4 0 1	4 0 1	<i>y</i>
LoaMgr_MotCurrRespSV_Cnt_M_u08	4 0 1 2	4 0 1 2	\rightarrow \frac{1}{\rightarrow \frac{1}{\rightarr
LoaMgr_TloaAvail_Cnt_M_lgc	4 0 1 2 1	4 0 1 2 1	· ·
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	4 0 1 2 1	4 0 1 2 1 1	\rightarrow \frac{1}{2}
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	4 0 1 2 1 1 0	4 0 1 2 1 1 0	\rightarrow \right
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	4 0 1 2 1 1 0 {170, 172, 173, 174}	4 0 1 2 1 1 0 {170, 172, 173, 174}	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4 0 1 2 1 1 0 {170, 172, 173, 174} {1, 1, 1, 1}	4 0 1 2 1 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0, 0}	
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	4 0 1 2 1 1 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1}	4 0 1 2 1 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1}	
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	4 0 1 2 1 1 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1} 500	4 0 1 2 1 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01	
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	4 0 1 2 1 1 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1} 500	4 0 1 2 1 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001	
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	4 0 1 2 1 1 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1} 500 1	4 0 1 2 1 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 0	
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	4 0 1 2 1 1 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1} 500 1 0	4 0 1 2 1 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 0	
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	4 0 1 2 1 1 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1} 500 1 0 0 5	4 0 1 2 1 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 0 5	
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	4 0 1 2 1 1 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1} 500 1 0	4 0 1 2 1 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 0	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.81 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrivtrMtgtnScaZeroEn Cnt lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
	2
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
	0
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_UIs_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	0.0099999978
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k LoaMgr RedcdStSca Uls f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C	:nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C	:nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_l	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ci	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	-
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	~
LoaMgr_LOASt_State_M_enum	4	4	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	4	4	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{171, 172, 173}	{171, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	4	4	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	✓





Test Step 1.82 (Repeat Count = 1)	X
Name Input Value	
LoaMgr_AgSV_Cnt_M_lgc 0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc 1	
LoaMgr_HwTqRespSV_Cnt_M_u08 4	
LoaMgr_lvtrldptSigSV_Cnt_M_u08 1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc 0	
LoaMgr_lvtrRespSV_Cnt_M_u08 4	
LoaMgr_MotAgldptSigSV_Cnt_M_u08 3	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc 1 LoaMgr_MotAgRespSV_Cnt_M_u08 4	
LoaMigr_MotCurrIdptSigSV_Cnt_M_u08 1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 0	
LoaMgr_MotCurrRespSV_Cnt_M_u08 3	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc 1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08 0	
Rte_Inst_Ap_LoaMgr target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32 1	
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc 0	
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32 1	
k_LoaMgr_FadeOutStRate_UlspS_f32	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08 2	
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 4 k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 2	
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 2 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 1	
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 0	
k LoaMgr HwTqldptSig4Resp Cnt u08 0	
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08 1	
k_LoaMgr_HwTqMtgtnRate_UlspS_f32 1	
k_LoaMgr_HwTqMtgtnSca_Uls_f32	
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08 0	
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08 0	
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08 0	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32 1	
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc 0	
k_LoaMgr_lvtrMtgtnSca_UIs_f32 1	
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 5 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 1	
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 0	
k LoaMgr MotAgldptSigFltThd Cnt u08 2	
k LoaMgr MotAgMtgtnRate UlspS f32 500	
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc 0	
k_LoaMgr_MotAgMtgtnSca_UIs_f32	
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 2	
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 2	
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08 0	
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08 0	
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08 0	
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 1 k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc 1	
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_igc 1 k_LoaMgr_MotCurrMtgtnSca_Uls_f32 1	
k_LoaMgr_RedcdStRate_UlspS_f32 1	
k_LoaMgr_RedcdStSca_Uls_f32	
target LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value 0	
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value 0	
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 0	
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1	
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 2	
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1	
target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2	
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	
target_Rte_inst_Ap_LoaMgr.LoaMgr_Peri_Diagcstsivtr1inactv_Cnt_igc target_LoaMgr_Peri_Diagcstsivtr1inactv_Cnt_igc target_LoaMgr.LoaMgr.LoaMgr_Peri_Diagcstsivtr2inactv_Cnt_igc target_LoaMgr.Peri_Diagcstsivtr2inactv_Cnt_igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_blagcossvtzInactv_Crit_igc target_LoaMgr.Per1_blagcossvtzInactv_Crit_igc target_LoaMgr.LoaMgr.LoaMgr.Per1_hwTqldptSig_Cnt_u08 target_LoaMgr.Per1_hwTqldptSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	
target_tyte_inst_Ap_coalwign.coalwign.com/geodawign.cn_ont_ige target_coalwign_cn_windy.coalwign.cn_ge	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc	





Name	Input Value			
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc			
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	0	0	~	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	✓	
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	~	
LoaMgr_LOASt_State_M_enum	5	5	✓	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	•	
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	✓	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~	
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~	
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~	
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	•	
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	~	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	✓	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.83 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlytrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08 k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target LoaMgr Per1 HwTqldptSig Cnt u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target LoaMgr Per1 HwTqLoaMtgtnEn Cnt		
target_Rie_Inst_Ap_LoaMgr.LoaMgr_Per1_InvrtLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_nw1qLoaMtgtnEn_Cnt_lg		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	,-	
target Rte Inst Ap LoaMgr.LoaMgr Per1 LoaScaDi Cnt Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cn	ıt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_ut		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	V
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0 1	0	~
LoaMar lytrl oaMtatnEn Cot M Ico		1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc		5	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	5 5	5	~
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	5 5 500	5 500	*
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	5 5	5	~
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	5 5 500 0	5 500 0	, ,
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	5 5 500 0 5	5 500 0 5	\rightarrow \frac{1}{2}

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LoaMgr_Per1



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	✓
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc.value	1	1	✓

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.84 (Repeat Count = 1)	N .
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name Input Value	
Name Input Value k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 1	
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 0	
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 2	
k LoaMgr MotAgMtgtnRate UlspS f32 300.489197	
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc 0	
k_LoaMgr_MotAgMtgtnSca_Uls_f32	
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 2	
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08 2	
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08 0	
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08 0	
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08 0	
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 1	
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc 1 k LoaMgr MotCurrMtgtnSca Uls f32 1	
k_LoaMgr_MotCurrMtgtnSca_Uls_f32 1 k LoaMgr RedcdStRate UlspS f32 1	
k LoaMgr RedcdStSca Uls f32	
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value 0	
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value 1	
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 1	
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 0	
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value 1	
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1	
target_LoaMgr_Per1_SteerMod_Cnt_enum.value 1	
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1	
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_SteerMod_Cnt_enum	
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvaii_Cnt_lgc target_LoaMgr_Per1_TloaAvaii_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Pcr1_TloaDi_Cnt_lgc target_LoaMgr_Pcr1_TloaDi_Cnt_lgc	
Name Actual Value Expected Value	Result
LoaMgr_AgsV_Cnt_M_lgc 1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08 1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc 1 1	V
LoaMgr_HwTqRespSV_Cnt_M_u08 4 4	V
LoaMgr_lvtrldptSigSV_Cnt_M_u08 0 0 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc 1 1	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc 1 1 LoaMgr_ivtrRespSV_Cnt_M_u08 5 5	
LoaMgr LOASt State M enum 5 5	
LoaMgr_LoaRateLimit_UlspS_M_f32 500 500	_
LoaMgr LoaScaleFctr Uls M f32 0 0	✓
LoaMgr_MaxMtgtnResp_Cnt_M_u08 5	_
LoaMgr_MotAgldptSigSV_Cnt_M_u08 1 1	✓
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc 0 0	•
LoaMgr_MotAgRespSV_Cnt_M_u08 5	✓
LoaMgr_MotCurrldptSigSV_Cnt_M_u08 0	✓
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08 2 2	~
LoaMgr_TloaAvail_Cnt_M_lgc 1	✓
LoaMgr_TloaDi_Cnt_M_lgc 1	Y
LoaMgr_VitgMdSrcSV_Cnt_M_u08 1 1	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 172, 173} {170, 172, 173} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) /1 1 1 /0 0 0 0 0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1}	
- 1.0_0ai_, ip_eoaiigi_inti Diagrigi_Ooti 1 Ootatao(Otatao_Ott_1_otatii) [11, 1, 1] [11, 1, 1]	
	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 1 1	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 1 1 target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value 1 1	•



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.85 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr MotAgldptSigSV Cnt M u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrIdptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k LoaMgr MotAgldptSig3Resp Cnt u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k LoaMgr MotAgMtgtnSca Uls f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
	2
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurridptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurridptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
	1
k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1
target_Loanigt_Ferr_Diagooloivti illidetv_Offt_ige.value	





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	18	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	-	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	ent Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u		
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M Igc	0	0	/ toouli
LoaMgr HwTqldptSiqSV Cnt M u08	0	0	_
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_IvtrRespSV_Cnt_M_u08	2	2	
LoaMgr_LOASt_State_M_enum	5	5	·
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	_
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	·
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	-
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	
LoaMgr_TloaDi_Cnt_M_lgc	1	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 172, 173}	{170, 172, 173}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_endin)	{1, 1, 1}	{0, 0, 0, 0, 0}	W
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Farani_Cnt_T_enum)	{1, 1, 1} {1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	-
target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	-
	0	0	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	U	U	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	✓
RqstHwTqResp	1	RqstHwTqResp	1	✓
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 1.86 (Repeat Count = 1)	x
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_UIs_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08 k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k LoaMgr IvtrMtgtnRate UlspS f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	0
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedodStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	0
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3
target LoaMgr Per1 LoaScaDi Cnt Igc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1lnactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_InvTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	✓
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	✓
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.87 (Repeat Count = 1)	12
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgMtataPate_Ulsps_f32	2		
k_LoaMgr_MotAgMtataScaZeroEn_Cnt_los	0		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k LoaMgr MotCurrldptSig1Resp Cnt u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	0		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt	_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enur	n 	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	V
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	✓
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	V
LoaMgr_MotAgloptSigSV_Cnt_M_u08	2	2	V
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr MotAgRespSV Cnt M u08	2	2	✓

LoaMgr_MotAgRespSV_Cnt_M_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	-
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	-
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	-
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.88 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5

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LoaMgr_Per1



Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k LoaMgr MotAgMtgtnSca Uls f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08			
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt	_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_(Ont lac	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_i		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum	n	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr HwTqRespSV Cnt M u08			
	4	4	✓
· - · ·			· ·
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	, , , , , , , , , , , , , , , , , , ,
LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0 0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08	0 0 5	0 0 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	0 0 5 5	0 0 5 5	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	0 0 5 5 5	0 0 5 5 5	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	0 0 5 5 5 500	0 0 5 5 5 500	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	0 0 5 5 5 500 1 5	0 0 5 5 5 500 1 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	0 0 5 5 5 500	0 0 5 5 5 500	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	0 0 5 5 5 500 1 5	0 0 5 5 5 500 1 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	0 0 5 5 5 500 1 5	0 0 5 5 5 500 1 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0 0 5 5 5 500 1 5 2	0 0 5 5 5 500 1 5 2	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08	0 0 5 5 5 500 1 5 2 1	0 0 5 5 5 500 1 5 2 1	
LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIoaMtgtnEn_Cnt_M_lgc	0 0 5 5 5 500 1 5 2 1 4 0	0 0 5 5 5 500 1 5 2 1 4 0	
LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08	0 0 5 5 5 500 1 5 2 1 4 0	0 0 5 5 5 500 1 5 2 1 4 0	
LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc	0 0 5 5 5 500 1 5 2 1 4 0	0 0 5 5 5 500 1 5 2 1 4 0	
LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	0 0 5 5 5 500 1 5 2 1 4 0 1 2 1	0 0 5 5 5 500 1 5 2 1 4 0 1 2	
LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	0 0 5 5 5 500 1 5 2 1 4 0 1 2 1 2	0 0 5 5 5 500 1 5 2 1 4 0 1 2 1 1	
LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0 0 5 5 5 500 1 5 2 1 4 0 1 2 1 1 2 1 1 0 (170, 172, 173, 174}	0 0 5 5 5 500 1 5 2 1 4 0 1 2 1 1 2 1 1 0 {170, 172, 173, 174}	
LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0 0 5 5 5 500 1 5 2 1 4 0 1 2 1 1 1 0 {170, 172, 173, 174} {1, 1, 1, 1}	0 0 5 5 5 500 1 5 2 1 4 0 1 2 1 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0}	
LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgtloptSigSV_Cnt_M_u08 LoaMgr_MotAgtloptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvaii_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 0 5 5 5 500 1 5 2 1 4 0 1 2 1 1 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1}	0 0 5 5 5 500 1 5 2 1 4 0 1 2 1 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1, 1}	
LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0 0 5 5 5 500 1 5 2 1 4 0 1 2 1 1 1 0 {170, 172, 173, 174} {1, 1, 1, 1}	0 0 5 5 5 500 1 5 2 1 4 0 1 2 1 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0}	
LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgtloptSigSV_Cnt_M_u08 LoaMgr_MotAgtloptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvaii_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 0 5 5 5 500 1 5 2 1 4 0 1 2 1 1 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1}	0 0 5 5 5 500 1 5 2 1 4 0 1 2 1 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1, 1}	
LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgtloptSigSV_Cnt_M_u08 LoaMgr_MotAgtloptSigSV_Cnt_M_u08 LoaMgr_MotAgtloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 RoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_StloaMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	0 0 5 5 5 500 1 5 2 1 4 0 1 2 1 1 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1}	0 0 0 5 5 5 5 500 1 1 5 5 2 1 1 4 4 0 0 1 1 2 1 1 1 0 0 {170, 172, 173, 174} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01	
LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_StyltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0 0 5 5 5 5 500 1 5 2 1 4 0 1 2 1 1 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1} 500	0 0 5 5 5 500 1 5 2 1 4 0 1 2 1 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001	
LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0 0 5 5 5 5 500 1 5 2 1 4 0 1 2 1 1 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1} 500 1	0 0 5 5 5 500 1 5 2 1 4 0 1 2 1 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 0	
LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 Roamgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	0 0 5 5 5 5 500 1 1 5 2 1 4 0 1 2 1 1 1 0 {170, 172, 173, 174} {1, 1, 1, 1} 500 1 0 0 5	0 0 5 5 5 500 1 5 2 1 4 0 1 2 1 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 0 0 5	
LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0 0 5 5 5 5 500 1 1 5 2 1 4 0 1 2 1 1 1 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1} 500 1 0 0	0 0 0 5 5 5 5 500 1 1 5 2 1 4 4 0 0 1 1 2 2 1 1 1 0 0 {\{170, 172, 173, 174\} \{\{0, 0, 0, 0, 0\} \} \{\{1, 1, 1, 1\} \} 500 \(\frac{\pmathcal{T}}{2} \) \(\pmathcal	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.89 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr MotAgldptSigSV Cnt M u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_Igc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1.
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	0.671599984
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	ıt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_l	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ci	nt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0		
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	V
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	-
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	-
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	V
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	-
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr MotCurrRespSV Cnt M u08	2	2	
LoaMgr TloaAvail Cnt M lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	
	0	0	-
LoaMgr_VltgMdSrcSV_Cnt_M_u08 Pto_Coll_Ap_LoaMgr_NlytrDiagMgr_SctNTCStatus(NTC_Cnt_T_cnum)	{170, 172, 173, 174}	{170, 172, 173, 174}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	{1, 1, 1, 1} 500	{1, 1, 1, 1} 500 ± 0.01	-
Rte Write Ap LoaMgr LoaScaleFctr Uls f32(data)	1	1 ± 0.001	- J
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_hw1qLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	
	5	5	
target_LoaMgr_Per1_LOASt_State_enum.value target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value		1	
	1	1	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1		

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	✓





Test Step 1.90 (Repeat Count = 1)	
	Input Value
	1
V - V	2
0_ 11 0	0
0_ , 0 0	0
	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
32 3 3 3 3 2 2 2 3	0
0_ 0 1	4
0_	2
0 = 0 = 0 = 0	0
·	3
0 = 0 = 0 = 0	1
32 3 11 12 2 2 11	1 torret Dte leet An LeeMer
	target_Rte_Inst_Ap_LoaMgr
_ 0_ 0 _ 1 _	0
_ 0_ 0 = _0	1
_ 0_ 0	1
	2
_ 0_ 11 0 1	4
	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
	0
_ 0_ 1. 0	1
_ 0_ 10 _ 1	1
_ 0_ , 0	1
_ 0_ 1	0
2 44 3 2 4 4 4 4 3 4 4 2 4 2 4 4	0
	1
	0
_ 0_ 0	1
	5
	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
_ 0_ 0	0
5 5	1
2 11 32 11 31 11 11 11 21 21 11	2
	2
_ 0_ 1 0 1 _	0
	0
	0.0099999978
	1
	1
_ 0_ 0	1
	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0
0 = 0 = = = = = = = = = = = = = = = = =	0
0 _ 0	2
· - · - · - · - ·	1
0 = 0 = = = =	2
0 = 0= = = =0	0
0 - 0	0 target LeaMar Part DiagoStelytr1Inacty Cet Inc.
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
	target_LoaMgr_Per1_DiagcStsivtrzinaciv_Cnt_igc target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc
	target_LoaMgr_Per1_LOASt_State_enum
	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Peri_LoaScaUi_Cnt_lgc target_LoaMgr_Peri_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	✓
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_IvtrRespSV_Cnt_M_u08	4	4	~
LoaMgr_LOASt_State_M_enum	4	4	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	4	4	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{171, 172, 173}	{171, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	4	4	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	*

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•

Test Step 1.91 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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LoaMgr_Per1



Nama	Innut Value		
Name	Input Value		
k_LoaMgr_CurrlytrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32 k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k LoaMgr HwTqldptSigFltThd Cnt u08	1		
k LoaMgr HwTqMtgtnRate UlspS f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k LoaMgr MotAgldptSigFltThd Cnt u08	2		
k LoaMgr MotAgMtgtnRate UlspS f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_Igc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	500		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target LoaMgr Per1 MotCurrldptSig Cnt u08.value	2		
target LoaMgr Per1 MtrPosldptSig Cnt u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C	nt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_le		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	•
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	•
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	•
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
Lookley Mathel collists Fo. Cat. M. Inc.	4	4	

LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	✓
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	✓
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.92 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Nama	Input Value		
Name k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k LoaMgr RedcdStRate UlspS f32	1		
k LoaMgr RedcdStSca Uls f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	,	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_k		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	,,,	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0	98	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	V
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	V
LoaMgr_HwTqRespSV_Cnt_M_u08	•	0	Š
LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	1	-
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	-
LoaMgr LOASt State M enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	-
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	V
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
	0	0	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.93 (Repeat Count = 1)	The state of the s
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1.
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1.
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k LoaMgr HwTqldptSig1TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_Igc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
	5
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	1
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	2
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	1
k_LoaMgr_MotAgMtgtnSea_ZoaSp_Cat. Iga	0
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotAgMtgtnSca_Uls_f32	2
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	350.459595
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1

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LoaMgr_Per1

Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C	:nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_l	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	1	1	_
LoaMgr HwTgldptSigSV Cnt M u08	0	0	~
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	-
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 172, 173}	{170, 172, 173}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	-
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	✓



Test Step 1.94 (Repeat Count = 1)	x
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_UIs_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08 k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k LoaMgr IvtrMtgtnRate UlspS f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	0
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	0
target LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3
target LoaMgr Per1 LoaScaDi Cnt Igc.value	1
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_InvTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	2	2	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.95 (Repeat Count = 1)	12
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_Igc	1		
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	2		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	1		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_eroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	1 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C	nt_lgc	
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08	nt_lgc 3	
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	nt_lgc 3 t_lgc	
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target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotQurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrlcoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_loatget_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Crt_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crt_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cttarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cttarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0ttarget_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1 4 5 500	nt_lgc 3 t_lgc gc nt_lgc nt_lgc 08 Cnt_lgc 08 Expected Value 1 0 1 4 1 0 4 5 5 500	***
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotQurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrlcoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqloaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqlespSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1 4 1 0 4 5	nt_lgc 3 t_lgc gc nt_lgc nt_lgc 08 Cnt_lgc 08 Expected Value 1 0 1 4 1 0 4 5	***
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaRtgtnEn_Cnt_M_lgc LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_ligtarget_LoaMgr_Per1_Index_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_ligc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crt_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_target_LoaMgr_Per1_MotCurrLoaMgtnEn_target_LoaMgr_Per1_TloaAvail_Cnt_ligc target_LoaMgr_Per1_TloaAvail_Cnt_ligc target_LoaMgr_Per1_TloaDi_Cnt_ligc **Actual Value** 1 0 1 4 5 500 0 5	nt_lgc 3 t_lgc gc nt_lgc gc nt_lgc 08 Cnt_lgc 08 Expected Value 1 0 1 4 1 0 4 5 5 500 0	***
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_ligtarget_LoaMgr_Per1_Index_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_ligc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crt_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crt_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_ligc target_LoaMgr_Per1_TloaDi_Cnt_ligc Actual Value 1 0 1 4 5 500 0	nt_lgc 3 t_lgc gc nt_lgc gc nt_lgc 08 Cnt_lgc 08 Expected Value 1 0 1 4 1 0 4 5 5 500 0 5	***

LoaMgr_MotAgRespSV_Cnt_M_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.96 (Repeat Count = 1)	13
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	0
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k LoaMgr MotAgMtgtnSca UIs f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSig2Resp Cnt u08	0		
	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08			
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value)	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqIdptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_	u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc		1	I
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	_	
LoaMgr_LOASt_State_M_enum		5	_
LoaMgr_LoaRateLimit_UlspS_M_f32	5	5	V
		5	· ·
	500	5 500	· ·
LoaMgr_LoaScaleFctr_Uls_M_f32	500	5 500 0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	500 0 5	5 500 0 5	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	500 0 5 2	5 500 0 5 2	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	500 0 5 2	5 500 0 5 2	-
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	500 0 5 2 1 2	5 500 0 5 2 1	· · · · · · · · · · · · · · · · · · ·
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	500 0 5 2 1 2 0	5 500 0 5 2 1 2	-
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	500 0 5 2 1 2 0	5 500 0 5 2 1	· · · · · · · · · · · · · · · · · · ·
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	500 0 5 2 1 2 0 1 2	5 500 0 5 2 1 2	· · · · · · · · · · · · · · · · · · ·
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	500 0 5 2 1 2 0	5 500 0 5 2 1 2 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	500 0 5 2 1 2 0 1 2	5 500 0 5 2 1 2 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc	500 0 5 2 1 2 0 1 2	5 500 0 5 2 1 2 0 1 2	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	500 0 5 2 1 2 0 1 2 1 0	5 500 0 5 2 1 2 0 1 2 1 2	· · · · · · · · · · · · · · · · · · ·
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	500 0 5 2 1 2 0 1 2 1 0 0 (170, 172, 173, 174)	5 500 0 5 2 1 2 0 1 2 1 0 0 {170, 172, 173, 174}	· · · · · · · · · · · · · · · · · · ·
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_igc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_igc LoaMgr_TloaDi_Cnt_M_igc LoaMgr_TloaDi_Cnt_M_igc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	500 0 5 2 1 2 0 1 2 1 0 0 (170, 172, 173, 174) (1, 1, 1, 1)	5 500 0 5 2 1 2 0 1 2 1 0 0 {170, 172, 173, 174} {0, 0, 0, 0, 0}	
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	500 0 5 2 1 2 0 1 2 1 0 0 (170, 172, 173, 174) (1, 1, 1, 1) (1, 1, 1, 1)	5 500 0 5 2 1 2 0 1 2 1 0 0 {170, 172, 173, 174} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1}	
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500 0 5 2 1 2 0 1 2 1 0 0 (170, 172, 173, 174) (1, 1, 1, 1) (1, 1, 1, 1) 500	5 500 0 5 2 1 2 0 1 2 0 (170, 172, 173, 174) (0, 0, 0, 0, 0) {1, 1, 1, 1, 1} 500 ± 0.01	
LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_igc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_igc LoaMgr_TloaDi_Cnt_M_igc LoaMgr_TloaDi_Cnt_M_igc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UispS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uis_f32(data)	500 0 5 2 1 2 0 1 2 0 1 2 0 1 2 1 0 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1} 500 0	5 500 0 5 2 1 2 0 1 2 1 0 0 {170, 172, 173, 174} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 0 ± 0.001	
LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	500 0 5 2 1 2 0 1 2 0 1 2 0 1 2 1 0 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1} 500 0 1	5 500 0 5 2 1 2 0 1 2 1 0 0 {170, 172, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 0 ± 0.001 1	
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	500 0 5 2 1 2 0 1 2 0 1 2 1 0 0 {170, 172, 173, 174} {1, 1, 1, 1, 1} {1, 1, 1, 1, 1} 500 0 1 1 1	5 500 0 5 2 1 2 0 1 2 0 (170, 172, 173, 174) {0, 0, 0, 0, 0, 0} {1, 1, 1, 1, 1} 500 ± 0.01 0 ± 0.001 1 1	
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	500 0 5 2 1 2 0 1 2 1 0 0 (170, 172, 173, 174) (1, 1, 1, 1) (1, 1, 1, 1, 1) 500 0 1 1	5 500 0 5 2 1 2 0 1 2 0 (170, 172, 173, 174) {0, 0, 0, 0, 0, 0} {1, 1, 1, 1, 1} 500 ± 0.01 0 ± 0.001 1 1 5	
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	500 0 5 2 1 2 0 1 2 0 1 2 1 0 0 {170, 172, 173, 174} {1, 1, 1, 1, 1} {1, 1, 1, 1, 1} 500 0 1 1 1	5 500 0 5 2 1 2 0 1 2 0 (170, 172, 173, 174) {0, 0, 0, 0, 0, 0} {1, 1, 1, 1, 1} 500 ± 0.01 0 ± 0.001 1 1	· · · · · · · · · · · · · · · · · · ·



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	✓
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	-
AssignScale	1	AssignScale	1	✓
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.97 (Repeat Count = 1)	Input Value
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0 4
LoaMgr_HwTqRespSV_Cnt_M_u08	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	4 2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	4
LoaMgr_MotAgRespSV_Cnt_M_u08	
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1 0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0 torrect Pto Inst An LooMar
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlytrMtgtnRate_UlspS_f32	
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target LoaMgr Per1 MtrPosldptSig Cnt u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Ina	ctv Cnt lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Ina		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target LoaMgr Per1 HwTqldptSig Cn		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqLoaMtgtnEn Cnt Igc	target_LoaMgr_Per1_HwTqLoaMtgtnE	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_en		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_loa		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnE		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_t		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtr	_	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_C		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_e	_	
target_Rte_Inst_Ap_LoaMgr_Der1_TloaAvail_Cnt_lgc	target LoaMgr Per1 TloaAvail Cnt Ig		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target LoaMgr Per1 TloaDi Cnt Igc		
		Expected Value	Result
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	· ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	<u> </u>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	<u> </u>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	✓
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	✓



Test Step 1.98 (Repeat Count = 1) Name	⊠
·	
LoaMgr_AgSV_Cnt_M_lgc 0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc 0	
LoaMgr_HwTqRespSV_Cnt_M_u08 4	
LoaMgr_lvtrldptSigSV_Cnt_M_u08 0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc 0	
LoaMgr_lvtrRespSV_Cnt_M_u08 4	
LoaMgr_MotAgldptSigSV_Cnt_M_u08 2 LoaMgr_MotAgLoaMtdtnEn_Cnt_M_loc 1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc 1 LoaMgr_MotAgRespSV_Cnt_M_u08 4	
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 0	
LoaMgr_MotCurrRespSV_Cnt_M_u08 0	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc 1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	
Rte_Inst_Ap_LoaMgr target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc 0	
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	
k_LoaMgr_FadeOutStRate_UlspS_f32	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08 2	
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 4 k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 2	
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 0	
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 0	
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	
k_LoaMgr_HwTqMtgtnSca_Uls_f32	
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08 0	
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08 0	
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32 1	
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc 0	
k_LoaMgr_lvtrMtgtnSca_Uls_f32	
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 1	
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 0	
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 2	
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc 0	
k_LoaMgr_MotAgMtgtnSca_Uls_f32	
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 2	
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 2	
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08 0	
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08 0	
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08 0 k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 1	
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 1 k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc 1	
k LoaMgr MotCurrMtgtnSca Uls f32	
k_LoaMgr_RedcdStRate_UlspS_f32	
k_LoaMgr_RedcdStSca_Uls_f32 1	
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value 0	
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value 0	
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 0	
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1	
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 0	
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 2	
target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0	
target LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target LoaMgr Per1 TloaDi Cnt lgc.value 1	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	
target_Rte_inst_Ap_LoaMgr.LoaMgr.Per1_DiagcStslvtr1lnactv_Cnt_igc target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_igc target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_igc target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_igc	
target_Rte_Inst_Ap_LoaMgr.Per1_Bhagcsistvtziniactv_Crit_igc target_LoaMgr_Per1_Bhagcsistvtziniactv_Crit_igc target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	
target_Rte_inst_Ap_LoaMgr.LoaMgr.Per1_HwTqLoaMtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.Per1_ivtrLoaMtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum	
target_Rte_inst_Ap_LoaMgr_Peri_LOASt_State_enum target_LoaMgr_Peri_LOASt_State_enum target_LoaMgr_Peri_LoASt_State_enum target_LoaMgr_Peri_LoaScaDi_Cnt_lgc target_LoaMgr_Peri_LoaScaDi_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc	





Name	Input Value			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u	08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc			
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	0	0	~	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	✓	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	✓	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	✓	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~	
LoaMgr_LOASt_State_M_enum	5	5	✓	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	•	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~	
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	~	
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~	
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~	
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~	
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	~	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	×	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~	
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	~	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.99 (Repeat Count = 1)	2
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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LoaMgr_Per1



Name	Innut Value		
Name	Input Value		
k_LoaMgr_CurrlytrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32 k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k LoaMgr HwTqMtgtnRate UlspS f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k LoaMgr lvtrldptSig1Resp Cnt u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	0.0099999978		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value		at lan	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lot target_LoaMgr_Per1_LOASt_State_enum	gC	
target Rte Inst Ap LoaMgr.LoaMgr Perl LoaScaDi Cnt Igc	target LoaMgr Peri_LOASt_State_enum target LoaMgr Peri LoaScaDi Cnt lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_MotAgLoaMtgtnEn_Cnt_lgc		nt lac	
target Rte Inst Ap LoaMgr.LoaMgr Perl MotCurrldptSig Cnt u08	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_(
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_Steenwou_Crit_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	- Coount
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	
LoaMgr_LOASt_State_M_enum	4	4	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	4	4	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~

LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08

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Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	✓
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	✓
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{171, 172, 173}	{171, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	✓
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_LOASt_State_enum.value	4	4	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc.value	0	0	✓

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.100 (Repeat Count = 1)	N .
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	500
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5

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Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inac	ctv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inac	ctv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cn	t_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEi	n_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_en	um	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lg	IC .	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnE	n_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_0	Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtr	En_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_C	nt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_e	num	
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lg	С	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
	target_Loawigi_Ferr_Tloabi_Crit_igc		
Name	Actual Value	Expected Value	Result
Name LoaMgr_AgSV_Cnt_M_lgc		Expected Value	Result 🗸
	Actual Value		Result
LoaMgr_AgSV_Cnt_M_lgc	Actual Value	0	~
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08	Actual Value 0 0	0	,
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc	Actual Value 0 0 1	0 0 1	•
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08	Actual Value 0 0 1 0	0 0 1 0 0	•
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08	Actual Value 0 0 1 0 1 0 4	0 0 1 0 1 0 1 0	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	Actual Value 0 0 1 0 1 0 1 0	0 0 1 0 1 0 4 5	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08	Actual Value 0 0 1 0 1 0 4	0 0 1 0 1 0 1 0	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	Actual Value 0 0 1 0 1 0 4 5	0 0 1 0 1 0 4 5	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	Actual Value 0 0 1 0 1 0 4 5 500	0 0 1 0 1 0 4 5 500	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	Actual Value 0 0 1 0 1 0 4 5 500 1	0 0 1 0 1 0 4 5 500	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc	Actual Value 0 0 1 0 1 0 4 5 500 1 1 1 1	0 0 1 0 1 0 4 5 5 500 1 5	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_ivtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	Actual Value 0 0 1 0 1 0 4 5 500 1 5 1 4	0 0 1 0 1 0 4 5 5 500 1 5 1	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_ivtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	Actual Value 0 0 1 0 1 0 4 5 500 1 1 1 1 1 1 1	0 0 1 0 1 0 4 5 5 500 1 5 1 1 1	
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	Actual Value 0 0 1 0 1 0 4 5 500 1 1 1 4 1 0	0 0 1 0 1 0 4 5 5 500 1 5 1 1 1 4	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	Actual Value 0 0 1 0 1 0 4 5 500 1 1 1 4 1 0 3	0 0 1 0 1 0 4 5 5 500 1 5 1 1 4 1 0 3	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc	Actual Value 0 0 1 0 1 0 4 5 500 1 1 4 1 0 3 1	0 0 1 0 1 0 1 0 4 5 5 500 1 5 1 1 1 4 1	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	Actual Value 0 0 1 0 1 0 4 5 500 1 1 4 1 0 3 1 1	0 0 1 0 1 0 4 5 5 500 1 5 1 1 1 4 1	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VtgMdSrcSV_Cnt_M_u08	Actual Value 0 0 1 0 1 0 4 5 500 1 1 4 1 0 3 1 1 0	0 0 1 0 1 0 4 5 5 500 1 5 1 1 4 1 0 3 1	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_NotCurrl_M_lgc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	Actual Value 0 0 1 0 1 0 4 5 500 1 1 4 1 0 3 1 1 0 {170, 172, 173}	0 0 1 0 1 0 4 5 500 1 5 1 1 1 1 0 3 1 1 0 4 1	
LoaMgr_AgsV_Cnt_M_lgc LoaMgr_HwTqldptSigsV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrtCadMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrtCadMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrtCadMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrtCadMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	Actual Value 0 0 1 0 1 0 4 5 500 1 1 4 1 0 3 1 1 0 (170, 172, 173) (1, 1, 1)	0 0 1 0 1 0 1 0 4 5 500 1 1 5 1 1 1 0 3 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0}	
LoaMgr_AgsV_Cnt_M_lgc LoaMgr_HwTqldptSigsV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigsV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigsV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotOcurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	Actual Value 0 0 1 0 1 0 1 0 4 5 500 1 1 1 0 3 1 1 0 (170, 172, 173) {1, 1, 1} {1, 1, 1}	0 0 1 0 1 0 1 0 4 5 500 1 1 5 1 1 1 0 3 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1}	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaRcaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_StypMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	Actual Value 0 0 1 0 1 0 1 0 4 5 500 1 1 1 0 3 1 1 0 (170, 172, 173) {1, 1, 1} {1, 1, 1} 500	0 0 1 0 1 0 1 0 4 5 500 1 1 5 1 1 1 0 4 1 0 3 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01	
LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_StyDespt_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	Actual Value 0 0 1 0 1 0 1 0 4 5 5 500 1 1 1 0 3 1 1 0 (170, 172, 173) {1, 1, 1} {1, 1, 1} 500 1	0 0 1 0 1 0 1 0 4 5 500 1 5 1 1 1 0 4 1 0 (170, 172, 173) {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001	
LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VttgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	Actual Value 0 0 1 0 1 0 1 0 4 5 5 500 1 1 1 0 3 1 1 0 {170, 172, 173} {1, 1, 1} {1, 1, 1} 500 1	0 0 1 0 1 0 1 0 4 5 500 1 5 1 1 1 0 4 1 0 3 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001 1	
LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	Actual Value 0 0 1 0 1 0 1 0 4 5 5 500 1 1 1 0 3 1 1 0 {170, 172, 173} {1, 1, 1} {1, 1, 1} 500 1	0 0 1 0 1 0 1 0 4 5 500 1 5 1 1 1 0 3 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001 1 0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VtgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_loASt_State_enum.value	Actual Value 0 0 1 0 1 0 1 0 4 5 5 500 1 1 1 0 3 1 1 0 {170, 172, 173} {1, 1, 1} {1, 1, 1} 500 1 1 0 5	0 0 1 0 1 0 1 0 4 5 500 1 5 1 1 1 0 3 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001 1 0 5	
LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	Actual Value 0 0 1 0 1 0 1 0 4 5 5 500 1 1 1 0 3 1 1 0 {170, 172, 173} {1, 1, 1} {1, 1, 1} 500 1	0 0 1 0 1 0 1 0 4 5 500 1 5 1 1 1 0 3 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001 1 0	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.101 (Repeat Count = 1)	<u> </u>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_IvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1.
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0
LoaMgr MotCurrLoaMtgtnEn Cnt M lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k LoaMgr HwTqldptSig1TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C	:nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_l	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ci	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr HwTqldptSigSV Cnt M u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	V
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr MotCurrLoaMtgtnEn Cnt M lgc	1	1	V
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	-
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 173}	{170, 173}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

「est Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 1.102 (Repeat Count = 1)	8
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	367.258606
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k LoaMgr MotCurrldptSig2Resp Cnt u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target LoaMgr_Per1 SteerMod Cnt enum.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_Der1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace	est Step Call Trace			✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.103 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k LoaMgr CurrivtrMtgtnSca Uls f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08 k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnSco-ZooEn_Cot_loa	1		
k_LoaMgr_MotAgMtstaCoa IIIa f33	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k LoaMgr MotCurrldptSig1Resp Cnt u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target LoaMgr Per1 TloaDi Cnt Igc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	_,	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrloaMtgtpEp		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target Rte Inst Ap LoaMgr.LoaMgr Peri SteerMod Cnt enum	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc	•	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	1	X
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_IvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	2	2	~
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	V
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	Y
LoaMgr_MotAgldptSigSV_Cnt_M_u08			•

1

0 0

LoaMgr_MotAgldptSigSV_Cnt_M_u08

LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc

LoaMgr_MotAgRespSV_Cnt_M_u08

1

0

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Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	✓
LoaMgr_VltgMdSrcSV_Cnt_M_u08	3	3	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	✓
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1	1 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc.value	1	1	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	✓
SwBasMtgtn	1	SwBasMtgtn	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	✓
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.104 (Repeat Count = 1)		*
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_lvtrRespSV_Cnt_M_u08	0	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	0	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	1	
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1	
k_LoaMgr_FadeOutStRate_UlspS_f32	1	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2	
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1	
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2	
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1	
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0	
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0	
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1	
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1	
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1	
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0	
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0	
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0	
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1	
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0	
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1	





Name	Input Value		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target LoaMgr Per1 MotCurrldptSig Cnt u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target LoaMgr Per1 SteerMod Cnt enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_Igc.value	0		
target LoaMgr Per1 TloaDi Cnt Igc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Ci	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_	u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	1	*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	V
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	V
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	•
LoaMgr_LOASt_State_M_enum	2	2	V
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	-
LoaMgr_LoaScaleFctr_Uls_M_f32	2	2	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	1	1	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr MotAgRespSV Cnt M u08	0	0	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr MotCurrRespSV Cnt M u08	2	2	
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	V
LoaMgr_TloaDi_Cnt_M_lgc	1	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	3	3	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)		{173, 174}	~
	{173, 174}		
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{173, 174} {1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)		Time in the second seco	×
	{1, 1}	{0, 0, 0, 0, 0}	_
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1} {1, 1}	{0, 0, 0, 0, 0} {1, 1}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	{1, 1} {1, 1} 500	{0, 0, 0, 0, 0} {1, 1} 500 ± 0.01	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	(1, 1) {1, 1} 500 1	{0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	(1, 1) {1, 1} 500 1 0	{0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0	***
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	(1, 1) {1, 1} 500 1 0	{0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0	***



est Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
SwBasMtgtn	1	SwBasMtgtn	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.105 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_IvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrlvtrMtgtnScaZeroEn Cnt lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	0
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k LoaMgr MotAgldptSigFltThd Cnt u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k LoaMgr MotAgMtgtnScaZeroEn Cnt lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k LoaMgr MotAgSmaAvailResp Cnt u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	18	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_	u08	
target Rte Inst Ap LoaMgr.LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target LoaMgr Per1 MtrPosldptSig Cnt u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target LoaMgr Per1 SteerMod Cnt enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	rtoodit
LoaMgr HwTqldptSigSV Cnt M u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	2	-
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	-
LoaMgr_LOASt_State_M_enum	4	4	V
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	-
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	4	4	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	~
LoaMgr MotCurrldptSigSV Cnt M u08	2	2	-
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	
	0	0	-
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	
LoaMgr_TloaDi_Cnt_M_lgc	1	1	-
LoaMgr_VitgMdSrcSV_Cnt_M_u08	· ·		
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{171, 172, 173}	{171, 172, 173}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{0, 0, 0, 0, 0}	~
	{1, 1, 1}	{1, 1, 1}	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	0	500 ± 0.01	*
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)		0 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	0	0	V
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	V
target_LoaMgr_Per1_LOASt_State_enum.value	4	4	V
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 1.106 (Repeat Count = 1)	13
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k LoaMgr HwTqldptSigFltThd Cnt u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_UIs_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgNtratsPate_UlanS_f33	1
k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k LoaMgr MotAgMtgtnSca Uls f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1
target LoaMgr Per1 LoaScaDi Cnt Igc.value	1
target_LoaMgr_Per1_LoaScabl_Crit_igc.value	2
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc
5	2. 72.7



Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	✓
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace ✓				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	✓

Test Step 1.107 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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LoaMgr_Per1



Name	Input Value			
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	0.987299979			
k_LoaMgr_FadeOutStRate_UlspS_f32	1			
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2			
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4			
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1			
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0			
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0			
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1			
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1			
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0			
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08 k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0			
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1			
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1			
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5			
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1			
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0			
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2			
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1			
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2			
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2			
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0			
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0			
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1			
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1			
k_LoaMgr_RedcdStRate_UlspS_f32	1			
k_LoaMgr_RedcdStSca_Uls_f32				
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0 1			
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value		1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1			
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2			
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1			
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_			
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u			
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum			
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	Esmanta d Value	Desc !!	
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	0	0		
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	V	
	1	1	~	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	~	
LoaMgr_HwTqRespSV_Cnt_M_u08	1 1 0	1 1 0	v	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08	1 1 0 1	1 1 0 1	*	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1 1 0 1 0	1 1 0 1 0	v	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08	1 1 0 1	1 1 0 1	· · · · · · · · · · · · · · · · · · ·	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	1 1 0 1 0 4	1 1 0 1 0 4	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08	1 1 0 1 0 4 5	1 1 0 1 0 4 5	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	1 1 0 1 0 4 5 500	1 1 0 1 0 4 5 500	***************************************	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	1 1 0 1 0 4 5 500	1 1 0 1 0 4 5 5 500	***	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_vtrldptSigSV_Cnt_M_u08 LoaMgr_vtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_vtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 1 0 1 0 4 5 5 500 1 5	1 1 0 1 0 4 5 5 500 1 5	***	

LoaMgr_MotAgRespSV_Cnt_M_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	✓
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	✓
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	✓
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc.value	0	0	✓

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.108 (Repeat Count = 1)	XI
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	0.0099999978
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Landary Developation Computer	Name	Input Value		
Landary Monigensignation Company	Name			
Landary Mandagolishing Lings 12				
Landay Morighterise Use 12				
Licably_Moderates Use 1		1		
Looking MoCardistigNResp. CH LIGS 2	k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
Loading / McCorrispisign Resp. Crit L08 0	k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
Landary_MonComprisor(1978) PRIOR_COLLUBS	k_LoaMgr_MotAgSmaAvailResp_Cnt_u08			
Landing John-Carristop/Prince Cort Linds	k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08			
Landary McCardingSepTime Cort Lands Landary McCardingSepTime Cort Lands Landary McCardingSea Lands Cort Lands Landary McCardingSea Lands Cort Lands Landary Reductificate Usps 102 Landary Reductificate Usps 102 Landary Reductificate Usps 102 Landary Pert Disposition Front Cort Lipt continue Landary Pert Landary Pert Landary Cort Landary Cort Landary Pert				
Lacety McCurringhreSazzerien Leg 1				
Looking / Redough Re				
Loading-Red States Use D2		· ·		
Landary Rescolations Landary				
Image_Loady_Der_Loagestein/Tenetry_Cort_govalue 1				
Image Loady Per Mortargosis, Cet USA value		0		
Langet_Loady_ Perf_LosSeaD_Cnt_go:auto Langet_Loady_ Perf_Micropicing_Cnt_u08 value Langet_Loady_ Perf_Micropicing_Cnt_u08 value Langet_Loady_ Perf_Nocorruphicing_Cnt_u08 value Langet_Loady_ Perf_Loady_Cnt_go:auto Langet_Loady_ Perf_Loady_Cnt_go:auto Langet_Loady_ Perf_Loady_Cnt_go:auto Langet_Loady_ Perf_Loady_Cnt_go:auto Langet_Roady_ Perf_Loady_Cnt_go:autopet_Roady_ Perf_Loady_Cnt_go:autopet_Roady_ Perf_Loady_Cnt_go:autopet_Roady_ Perf_Loady_Cnt_go:autopet_Roady_ Perf_Loady_Cnt_go:autopet_Roady_Perf_Loady_Cnt_go:autopet_Roady_Perf_Loady_Cnt_go:autopet_Roady_Perf_Loady_Cnt_go:autopet_Roady_Perf_Loady_Cnt_go:autopet_Roady_Perf_Loady_Cnt_go:autopet_Roady_Perf_Loady_Cnt_go:autopet_Roady_Perf_Doady_Cnt_goady_Perf_Doady_Cnt_go:autopet_Roady_Perf_Doady_Cnt_goady_Perf_Doady_Cnt_goady_Cnt_goady_Perf_D		1		
Image Landing Pert Medicineriplisis Cert Landing Landing Pert Medicineriplisis Landing Pert Landing Landing Pert Medicineriplisis Landing Pert Landing Landing	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
Integred_Loading_PerT_TORDI_COL_EQUE value	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
Image Canadar Canada	target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
	V - V	1		
target, Rein, Rad, Loadlyr, Loadlyr, Perf. DisposSisht/Planck, Cnt. Igo target, Rein, Rad, Loadlyr, Loadlyr, Perf. Hw7qt, Coadlyr, Perf. Lync, Cnt. Igo target, Rein, Rad, Loadlyr, Loadlyr, Perf. Lync, Loadlyr, Perf. Lync, Coadlyr, Coadlyr, Coadlyr, Coadlyr, Coadlyr, Coadlyr, Coadlyr, Coadlyr, Lync, Coadlyr, Coadlyr, Lync, Coadlyr, Lync, Coadl	0 = 0 = = = = = = = = = = = = = = = = =			
single_Rinet_Ap_LoaMy_LoaMy_Per1_Hr7qipoRisig_Cnt_U8				
target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1 Nri*ToLoadkginEn_Cnt_lgc target_Rel_inst_Ap_Loadkgr_Per1 Nri*ToLoadkginEn_Cnt_lgc target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1 LOASS_State_enum target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1 LOASS_State_enum target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1 LOASS_Dtate_enum target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1 LOASS_Dtate_enum target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1 MoRourdostigs_Cnt_uo8 target_Loadkgr_Per1 MoRourdostigs_Cnt_uo8 target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1 MoRourdostigs_Cnt_uo8 target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1 MoRourdostigs_Cnt_uo8 target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1 MoRourdostigs_Cnt_uo8 target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1 MoRourdostigs_Cnt_uo8 target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1 MoRourdostigs_Cnt_uo8 target_Loadkgr_Per1 MoRourdostigs_Cnt_uo8 target_Loadkgr_Per1 MoRourdostigs_Cnt_uo8 target_Loadkgr_Doadkgr_Doadkgr_Per1 MoRourdostigs_Cnt_uo8 target_Loadkgr_Doadkgr_Doadkgr_Per1 MoRourdostigs_Cnt_uo8 target_Loadkgr_Doadk				
large_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_unit_caltytinEn_Cnt_Upc			_	
target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_LOASL_State_enum arget_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_LoaScaDi_Cnt_lgc target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_MotOuridpiSig_Cnt_U08 target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_MotOuridpiSig_Cnt_U08 target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_MotOuridpiSig_Cnt_U08 target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_MotOuridpiSig_Cnt_U08 target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_MotOuridpiSig_Cnt_U08 target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_MotOuridpiSig_Cnt_U08 target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_MotOuridpiSig_Cnt_U08 target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_MotOuridpiSig_Cnt_U08 target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_MotOuridpiSig_Cnt_U08 target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaDi_Cnt_lgc target_Rite_Inst_Ap_LoaMgr				
target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_LoaScaDi_Cnt_lgc target_LoaMgr_Pert_MotOgLoaMignen_Cnt_lgc target_Rite_Inst_Ap_LoaMigr_LoaMgr_Pert_SteemMod_Cnt_enum target_LoaMgr_Pert_Nort-MotOgN_Pert_SteemMod_Cnt_enum target_LoaMgr_Pert_ToaAvail_Cnt_lgc target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_lgc target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_lgc target_LoaMgr_Pert_ToaAvail_Cnt_lgc target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_lgc target_LoaMgr_Pert_ToaAvail_Cnt_lgc ta				
target_Rie_Inst_Ap_Loaklyr_Loaklyr_Pert_MicNQLoaklytinEn_Cnt_Igo target_Rie_Inst_Ap_Loaklyr_Loaklyr_Pert_MicNQLoaklytinEn_Cnt_Igo target_Rie_Inst_Ap_Loaklyr_Loaklyr_Pert_MicNQLoaklyr_MicNQ				
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target_Rie_Inst_Ap_Loak/gr_Loak/gr_Pert_TioaAvail_Cnt_lgc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtr	nEn_Cnt_lgc	
target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_TioaNc_Cnt_Igc target_LoaMgr_Pert_TioaDi_Cnt_Igc target_LoaMgr_Pert_TioaDi_Cnt_Igc target_LoaMgr_Pert_TioaDi_Cnt_Igc target_LoaMgr_Pert_TioaDi_Cnt_Igc target_LoaMgr_Pert_TioaDi_Cnt_Igc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_C	nt_u08	
target_Rie_Inst_Ap_LoaMgr_LoaMgr_Pert_TloaDi_Cnt_lgc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_e	num	
Name Actual Value Expected Value Result LoaMgr_AgSV_Cnt_M_Igc 1 1 1 LoaMgr_HwTqLoaMignEn_Cnt_M_Igc 1 1 1 LoaMgr_HwTqLoaMignEn_Cnt_M_Igc 1 1 1 LoaMgr_HwTqLoaMignEn_Cnt_M_u08 4 4 4 LoaMgr_LwtrLoaMignEn_Cnt_M_Igc 1 1 1 LoaMgr_LwtrRespSV_Cnt_M_u08 5 5 5 LoaMgr_LwtrRespSV_Cnt_M_u08 5 5 5 LoaMgr_LwtrRespSV_Cnt_M_u08 5 5 5 LoaMgr_LoaRscale-furr_UIS_M_f32 500 500 500 LoaMgr_LoaRscale-furr_UIS_M_f32 0 0 0 LoaMgr_MotAglotSigSV_Cnt_M_u08 1 1 1 LoaMgr_MotAglotSigSV_Cnt_M_u08 1 1 1 LoaMgr_MotAglotSigSV_Cnt_M_u08 0 0 0 LoaMgr_MotCurrLoaMignEn_Cnt_M_lgc 0 0 0 LoaMgr_MotCurrLoaMignEn_Cnt_M_lgc 1 1 1 LoaMgr_TioaAvai_Cnt_M_lgc <			С	
LoaMgr_HvTqlptSigSV_Cnt_M_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Loaklyr_HwTqlcpKisjqSV_Cnt_M_u08			·	Result
LoaMgr_HwTqLoaMtgInEn_Cnt_M_lgc				
LoaMgr_HwTqRespSV_Cnt_M_u08				
LoaMgr_IvitridptSigSV_Cnt_M_u08		· ·		
LoaMgr_IvtrLoaMtghEn_Cnt_M_log				
LoaMgr_IvtRespSV_Cnt_M_u08				
LoaMgr_LoaSt_State_Menum				
LoaMgr_LoaRateLimit_UispS_M_f32 500 500 LoaMgr_LoaScaleFctr_Uis_M_f32 0 0 LoaMgr_MaxMighrResp_Cnt_M_u08 5 5 LoaMgr_MotAgLoaMtighten_Cnt_M_lgc 0 0 LoaMgr_MotAgRespSV_Cnt_M_u08 5 5 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 0 0 LoaMgr_MotCurrloaMtighten_Cnt_M_lgc 1 1 LoaMgr_MotCurrloaMtighten_Cnt_M_lgc 1 1 LoaMgr_MotQurrRespSV_Cnt_M_u08 2 2 LoaMgr_TloaAvail_Cnt_M_lgc 1 1 LoaMgr_TloaDi_Cnt_M_lgc 1 1 LoaMgr_VitgMSrcSV_Cnt_M_u08 1 1 LoaMgr_VitgMSrcSV_Cnt_M_u08 1 1 LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 172, 173} {170, 172, 173} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0, 0, 0, 0} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1, 1} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_Se				·
LoaMgr_LoaScaleFct_Uls_M_f32 0 0 LoaMgr_MaxMtgtnResp_Cnt_M_u08 5 5 LoaMgr_MotAgldptSigSV_Cnt_M_u08 1 1 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc 0 0 LoaMgr_MotQrudtptSigSV_Cnt_M_u08 5 5 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 1 1 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 1 1 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 1 1 LoaMgr_MotOut_M_lgc 1 1 LoaMgr_TloaDi_Cnt_M_lgc 1 1 LoaMgr_TloaDi_Cnt_M_lgc 1 1 LoaMgr_VltgMSrcSV_Cnt_M_u08 1 1 Rte_Call_Ap_LoaMgr_NttrDiagMgr_SetNTCStatus(NTC_Cnt_Tenum) (170, 172, 173) (170, 172, 173) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_Tu08) (1, 1, 1) (1, 1, 1) Rte_Vite_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum) (1, 1, 1) (1, 1, 1) Rte_Vite_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum) (1, 1, 1) (1, 1, 1) Rte_Vite_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum) (1, 1, 1) (1, 1, 1) Rte_Vite_A	·			_
LoaMgr_MotAgldptSigSV_Cnt_M_u08 1 1 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc 0 0 LoaMgr_MotAgRespSV_Cnt_M_u08 5 5 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 0 0 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 1 1 LoaMgr_MotCurrRespSV_Cnt_M_u08 2 2 LoaMgr_TloaAvail_Cnt_M_lgc 1 1 LoaMgr_TloaDi_Cnt_M_lgc 1 1 LoaMgr_VitgMdSrcsV_Cnt_M_u08 1 1 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 172, 173} {170, 172, 173} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0} ** Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} ** Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} ** Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1, 1} {1, 1, 1, 1} ** Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 500 500 ± 0.01 ** At arget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value				✓
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc 0 0 LoaMgr_MotAgRespSV_Cnt_M_u08 5 5 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 0 0 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 1 1 LoaMgr_MotCurrRespSV_Cnt_M_u08 2 2 LoaMgr_TloaAvail_Cnt_M_lgc 1 1 LoaMgr_TloaDi_Cnt_M_lgc 1 1 LoaMgr_TloaDi_Cnt_M_lgc 1 1 LoaMgr_VttgMotScSV_Cnt_M_u08 1 1 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 172, 173} {170, 172, 173} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Cont_T_enum) {1, 1, 1} {1, 1, 1} Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 1 1 target_LoaMgr_Per1_LoASt_State_enum.value 5 5	LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	✓
LoaMgr_MotAgRespSV_Cnt_M_u08 5 5 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 0 0 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 1 1 LoaMgr_MotCurrRespSV_Cnt_M_u08 2 2 LoaMgr_TloaAvail_Cnt_M_lgc 1 1 LoaMgr_NtloaMgr_Sct_Cnt_M_u08 1 1 LoaMgr_VltgMdSrcSV_Cnt_M_u08 1 1 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 172, 173} {170, 172, 173} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0} ** Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} ** Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} ** Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 ** Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 ** target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 1 1 1 target_LoaMgr_Per1_LoASt_State_enum.value 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.v	LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08 0 0 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 1 1 LoaMgr_MotCurrRespSV_Cnt_M_u08 2 2 LoaMgr_TloaAvail_Cnt_M_lgc 1 1 LoaMgr_TloaDi_Cnt_M_lgc 1 1 LoaMgr_VltgMdSrcSV_Cnt_M_u08 1 1 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 172, 173} {170, 172, 173} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0} 3 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} {1, 1, 1} Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 4 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 4 rarget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 1 1 4 rarget_LoaMgr_Per1_LOASt_State_enum.value 5 5 4 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0 0	LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 1 1 2 LoaMgr_MotCurrRespSV_Cnt_M_u08 2 2 2 LoaMgr_TloaAvail_Cnt_M_lgc 1 1 1 LoaMgr_VltgMdSrcSV_Cnt_M_u08 1 1 1 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 172, 173} {170, 172, 173} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0} X Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} {1, 1, 1} Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 X Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 X Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 X target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 1 1 1 target_LoaMgr_Per1_LOASt_State_enum.value 5 5 X target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0 X	LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrRespSV_Cnt_M_u08 2 2 LoaMgr_TloaAvail_Cnt_M_lgc 1 1 LoaMgr_VltgMdSrcSV_Cnt_M_u08 1 1 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 172, 173} {170, 172, 173} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 1 1 target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value 1 1 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0	LoaMgr_MotCurrldptSigSV_Cnt_M_u08			~
LoaMgr_TloaAvail_Cnt_M_lgc 1 1 1 LoaMgr_TloaDi_Cnt_M_lgc 1 1 1 LoaMgr_VltgMdSrcSV_Cnt_M_u08 1 1 1 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 172, 173} {170, 172, 173} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 1 1 target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value 1 1 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0				~
LoaMgr_TloaDi_Cnt_M_lgc 1 1 LoaMgr_VltgMdSrcSV_Cnt_M_u08 1 1 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 172, 173} {170, 172, 173} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0} X Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} {1, 1, 1} X Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 X Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 X Rte_Write_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 1 1 X target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value 1 1 X target_LoaMgr_Per1_LOASt_State_enum.value 5 5 X target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0 X				~
LoaMgr_VltgMdSrcSV_Cnt_M_u08 1 1 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 172, 173} {170, 172, 173} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0} X Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} {1, 1, 1} X Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 X Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 X target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 1 1 X target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value 1 1 X target_LoaMgr_Per1_LOASt_State_enum.value 5 5 X target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0 X				
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 172, 173} {170, 172, 173} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 1 1 target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value 1 1 target_LoaMgr_Per1_LOASt_State_enum.value 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0				V
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 1 1 target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value 1 1 target_LoaMgr_Per1_LOASt_State_enum.value 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0				· ·
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_renum) {1, 1, 1} {1, 1, 1} Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 1 1 target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value 1 1 target_LoaMgr_Per1_LOASt_State_enum.value 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0		Ti i		
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 1 1 target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value 1 1 target_LoaMgr_Per1_LOASt_State_enum.value 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0				
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data) 0 0 ± 0.001 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value 1 1 target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value 1 1 target_LoaMgr_Per1_LOASt_State_enum.value 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc.value 0 0				
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1				
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value				~
target_LoaMgr_Per1_LOASt_State_enum.value 5 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0				~
0 = 0 = 0 = 0 = 0		5	5	✓
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value 1	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.109 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	500
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	18	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	-	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	ent Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u		
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	0	0	/ toouli
LoaMgr HwTqldptSiqSV Cnt M u08	0	0	_
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_IvtrRespSV_Cnt_M_u08	2	2	
LoaMgr_LOASt_State_M_enum	5	5	_
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	_
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	·
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	-
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	
LoaMgr_TloaDi_Cnt_M_lgc	1	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 172, 173}	{170, 172, 173}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_endin)	{1, 1, 1}	{0, 0, 0, 0, 0}	W
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Farani_Cnt_T_enum)	{1, 1, 1} {1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	-
target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	
	0	0	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	U	U	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 1.110 (Repeat Count = 1)	3
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08 k_LoaMgr_lvtrMtgtnRate_UlspS_f32	0
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k LoaMgr RedcdStRate UlspS f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Perl_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Perl_LoaScaDi_Cnt_Igc	target_LoaMgr_Perl_LOASt_State_enum target_LoaMgr_Perl_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaD1_Cht_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
1	, = = = = = = = = = = = = = = = = = = =





Name	Input Value			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc			
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	1	1	~	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	✓	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	✓	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	✓	
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	~	
LoaMgr_LOASt_State_M_enum	5	5	✓	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	✓	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	✓	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	✓	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓	
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~	
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓	
LoaMgr_TloaDi_Cnt_M_lgc	1	1	✓	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	•	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓	
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	~	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 1.111 (Repeat Count = 1)	12
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	477.259705

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LoaMgr_Per1



Nama	Inmut Value		
Name	Input Value		
k_LoaMgr_CurrlytrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32 k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k LoaMgr HwTqMtgtnRate UlspS f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_le	gu	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LoaScaDi_Cot_loa		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	at lac	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u target_LoaMgr_Per1_MotCurrLoaMtgtnEn_(
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_MtrPosidptSig_Cnt_ut target_LoaMgr_Per1_SteerMod_Cnt_enum	,,,	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_Steet Mod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_SteetWod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
		·	Result
LoaMgr_AgSV_Cnt_M_lgc	0	1 0	~
LoaMgr_HwTqlqaMtatpEp_Cot_M_lgc	1	1	-
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	1 4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	9
Ecamar_mothquaptorgov_oni_m_uoo	•	•	

LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc.value	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~



```
Test Case 2: Path Test
 Specification
                                                                                                         Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:
                                                                                                     CPU Cycles:

TS 5.1 5012.00 Cycles
TS 5.2 5012.00 Cycles
TS 5.3 4974.00 Cycles
TS 5.4 5006.00 Cycles
TS 5.4 5006.00 Cycles
TS 5.6 4926.00 Cycles
TS 5.6 4926.00 Cycles
TS 5.7 4920.00 Cycles
TS 5.7 4920.00 Cycles
TS 5.9 4551.00 Cycles
TS 5.9 4551.00 Cycles
TS 5.11 4930.00 Cycles
TS 5.11 4930.00 Cycles
TS 5.12 4930.00 Cycles
TS 5.15 4994.00 Cycles
TS 5.16 4969.00 Cycles
TS 5.17 4934.00 Cycles
TS 5.18 4934.00 Cycles
TS 5.19 4987.00 Cycles
TS 5.19 4987.00 Cycles
TS 5.19 4987.00 Cycles
TS 5.20 4941.00 Cycles
TS 5.21 4925.00 Cycles
TS 5.21 4925.00 Cycles
TS 5.22 4908.00 Cycles
TS 5.23 4951.00 Cycles
TS 5.24 4961.00 Cycles
TS 5.24 4961.00 Cycles
                                                                                                         TS 5.24 4961.00 Cycles
                                                                                                      Vector Description:
Description
                                                                                                     TS5.1(HwTqldptSig_Cnt_T_u08 > D_MAXHWTQIDPTSIGALLWD_CNT_U08)==>True
TS5.2(MtrPosldptSig_Cnt_T_u08 > D_MAXMOTPOSNIDPTSIGALLWD_CNT_U08)==>True
TS5.3(MotCurrldptSig_Cnt_T_u08 > D_MAXMOTCURRIDPTSIGALLWD_CNT_U08)==>True
TS5.4(LoaMgr_LOASt_State_M_enum == LOAST_FADEOUT)==>True
TS5.5(LoaMgr_LOASt_State_M_enum == LOAST_SWBASDMTGTN)==>True
TS5.6(LoaMgr_LOASt_State_M_enum == LOAST_REDCD)==>True
TS5.7(LoaMgr_LOASt_State_M_enum == LOAST_NORM)==>True
                                                                                                       TS5.8(SteerMod_Cnt_T_enum == STEERMOD_FULLYATNMS)==>True
                                                                                                  TS5.9/default:

LoaMgr_LOASt_State_M_enum = LOAST_FADEOUT==>True"

TS5.10(SteerMod_Cnt_T_enum == STEERMOD_FULLYATNMS)==>True
TS5.11((NewValFlag_Cnt_T_lgc == TRUE) ==>True && (k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc == TRUE))==>True
TS5.12((NewValFlag_Cnt_T_lgc == TRUE) ==>False && (k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc == TRUE))
TS5.13((NewValFlag_Cnt_T_lgc == TRUE) ==>F && (k_LoaMgr_MotCurrlvtgtnScaZeroEn_Cnt_lgc == TRUE))
TS5.14((NewValFlag_Cnt_T_lgc == TRUE) ==>T && (k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc == TRUE))
TS5.15((NewValFlag_Cnt_T_lgc == TRUE) ==>T && (k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc == TRUE))==>T
TS5.16((NewValFlag_Cnt_T_lgc == TRUE)==>T && (k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc == TRUE)) ==>F
TS5.17((NewValFlag_Cnt_T_lgc == TRUE)==>T && (k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc == TRUE)) ==>F
TS5.18((NewValFlag_Cnt_T_lgc == TRUE)==>T && (k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc == TRUE)) ==>T
TS5.18((NewValFlag_Cnt_T_lgc == TRUE)==>F && (k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc == TRUE))
TS5.19(LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc == TRUE)==>T
TS5.20(LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc == TRUE)==>T
TS5.21((k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc == TRUE)==>T
TS5.22((k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc == TRUE)==>T
TS5.23 (LoaMgr_LoaRateLimit_UlspS_M_f32<=0.01)=>T
TS5.24(LoaMgr_LoaScaleFctr_Uls_M_f32<=0.01)=>T
                                                                                                       TS5.9"default:
```

Test Step 2.1 (Repeat Count = 1)	18
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	0.0099999978
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	0
k_LoaMgr_FadeOutStRate_UlspS_f32	0.0099999978
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	0
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	0.0099999978

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TS5.24(LoaMgr_LoaScaleFctr_Uls_M_f32<=0)=>F





Name	Input Value		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0		
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	0.0099999978		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	0		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	0		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	0		
k LoaMgr MotAgMtgtnRate UlspS f32	0.00999999978		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	0		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k LoaMgr MotCurrldptSig0Resp Cnt u08	0		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k LoaMgr MotCurrMtgtnRate UlspS f32	0.0099999978		
	0		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32			
k_LoaMgr_RedcdStRate_UlspS_f32	0.0099999978		
k_LoaMgr_RedcdStSca_Uls_f32	0		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	5		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqIdptSig_Cnt_u(08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0	Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target LoaMgr Per1 MotCurrLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaAvail Cnt Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
		l=v.	n u
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	Y
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	•
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	✓
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	✓
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	-
LoaMgr_TloaDi_Cnt_M_lgc	0	0	
	0	0	-
LoaMgr_VltgMdSrcSV_Cnt_M_u08 Pto_Call_Ap_LoaMgr_NytrDiagMgr_SetNTCStatus(NTC_Cpt_T_epum)		1	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	Y
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	





Name	Actual Value	Expected Value	Result
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	✓

Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Name	Input Value
_oaMgr_AgSV_Cnt_M_lgc	0
_oaMgr_HwTqldptSigSV_Cnt_M_u08	0
.oaMgr HwTqLoaMtgtnEn Cnt M Igc	0
	0
.oaMgr_HwTqRespSV_Cnt_M_u08	
.oaMgr_lvtrldptSigSV_Cnt_M_u08	0
.oaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
.oaMgr_lvtrRespSV_Cnt_M_u08	0
.oaMgr_MotAgldptSigSV_Cnt_M_u08	0
.oaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
.oaMgr_MotAgRespSV_Cnt_M_u08	0
.oaMgr_MotCurrldptSigSV_Cnt_M_u08	0
oaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
.oaMgr_MotCurrRespSV_Cnt_M_u08	0
oaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
oaMgr_VltgMdSrcSV_Cnt_M_u08	0
tte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	0.0099999978
_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	0
_LoaMgr_FadeOutStRate_UlspS_f32	0.0099999978
_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	0
_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	0
_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	0
_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	0
_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
_LoaMgr_HwTqldptSigFltThd_Cnt_u08	0
LoaMgr HwTqMtgtnRate UlspS f32	0.0099999978
LoaMgr_HwTqMtgtnSca_Uls_f32	0
_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
LoaMgr lvtrldptSig2Resp Cnt u08	0
LoaMgr lvtrldptSigFltThd Cnt u08	0
_LoaMgr_IvtrMtgtnRate_UlspS_f32	0.0099999978
LoaMgr IvtrMtgtnScaZeroEn Cnt Igc	0
_coalingr_ivtrintgtriscazerocit_crit_igc _coalingr_ivtrMtgtnSca_Uls_f32	0
LoaMgr MotAgldptSig1NoSmaResp Cnt u08	0
_coalligr_wooxglaptoig?resp_Cnt_u08	0
LoaMgr MotAgldptSig3Resp Cnt u08	0
_coamgr_motAgidptSigSresp_crit_uo6 _LoaMgr_MotAgidptSigFltThd_Cnt_u08	0
	0.0099999978
_LoaMgr_MotAgMtgtnRate_UlspS_f32	0.0099999978
_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	
_LoaMgr_MotAgMtgtnSca_Uls_f32	0
_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0
_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0
_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
c_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0
_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0





Name	Input Value		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	0.00999999978		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	0		
k_LoaMgr_RedcdStRate_UlspS_f32	0.00999999978		
k_LoaMgr_RedcdStSca_Uls_f32	0		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target LoaMgr Per1 DiagcStslvtr1Inact	v Cnt Igc	
target Rte Inst Ap LoaMgr.LoaMgr Per1 DiagcStslvtr2Inactv Cnt Igc	target_LoaMgr_Per1_DiagcStslvtr2Inact	v Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_C		
target Rte Inst Ap LoaMgr.LoaMgr Per1 LOASt State enum	target_LoaMgr_Per1_LOASt_State_enu		
target Rte Inst Ap LoaMgr.LoaMgr Per1 LoaScaDi Cnt Igc	target LoaMgr Per1 LoaScaDi Cnt Igo		
target Rte Inst Ap LoaMgr.LoaMgr Per1 MotAgLoaMtgtnEn Cnt Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnl	_	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cn		
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target_LoaMgr_Per1_SteerMod_Cnt_en	_	
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
	Actual Value	Expected Value	Popul
Name		Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	•
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32			
	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	0 5	0 5	
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	0 5 0 0	0 5 0 0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	0 5 0	0 5 0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0 5 0 0	0 5 0 0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	0 5 0 0 5	0 5 0 0 5	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0 5 0 0 5 0	0 5 0 0 5 0	
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0 5 0 0 5 0	0 5 0 0 5 0	
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	0 5 0 0 5 0 0	0 5 0 0 5 0 0	
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc	0 5 0 0 5 0 0 0	0 5 0 0 5 0 0 0	
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaOvail_Cnt_M_lgc LoaMgr_TloaOvail_Cnt_M_lgc	0 5 0 0 5 0 0 0 0	0 5 0 0 5 0 0 0 0	
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	0 5 0 0 5 0 0 0 0 0	0 5 0 0 5 0 0 0 0 0	
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0 5 0 0 5 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0 5 0 0 5 0 0 0 0 0 0 0 0 0 0 170, 172, 174}	
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0 5 0 0 5 0 0 0 0 0 0 0 0 0 0 (170, 172, 174)	0 5 0 0 5 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 5 0 0 0 5 0 0 0 0 0 0 0 0 0 {170, 172, 174} {1, 1, 1}	0 5 0 0 0 5 0 0 0 0 0 0 0 0 0 0 0 0 0 0	
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 5 0 0 5 0 0 0 0 0 0 0 0 (170, 172, 174) (1, 1, 1) (1, 1, 1) 500	0 5 0 0 5 0 0 0 0 0 0 0 0 (170, 172, 174) {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01	
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	0 5 0 0 0 5 0 0 0 0 0 0 0 0 (170, 172, 174) {1, 1, 1} {1, 1, 1} 500 0	0 5 0 0 0 5 0 0 0 0 0 0 0 0 (170, 172, 174) {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001	
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0 5 0 0 0 5 0 0 0 0 0 0 0 0 0 (170, 172, 174) (1, 1, 1) (1, 1, 1) 500 0 0 0 0	0 5 0 0 0 5 0 0 0 0 0 0 0 0 0 0 (170, 172, 174) (0, 0, 0, 0, 0) (1, 1, 1) 500 ± 0.01 0 0 0 0	
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	0 5 0 0 0 5 0 0 0 0 0 0 0 0 (170, 172, 174) (1, 1, 1) {1, 1, 1} 500 0 0	0 5 0 0 0 5 0 0 0 0 0 0 0 0 (170, 172, 174) {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 0	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.3 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	0.0099999978
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	0
k LoaMgr FadeOutStRate UlspS f32	0.0099999978
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	0
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	0
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	0
	0.0099999978
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	0
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	0.0099999978
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	0
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	0
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	0.0099999978
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	0
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	0.00999999978
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	0
k_LoaMgr_RedcdStRate_UlspS_f32	0.0099999978
k_LoaMgr_RedcdStSca_Uls_f32	0
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C	:nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	:nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_l	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	-	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ci	nt Igc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0	Ont Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr HwTgLoaMtgtnEn Cnt M Igc	0	0	-
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr lvtrldptSigSV Cnt M u08	0	0	-
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	_
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	-
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	✓
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	-
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	-
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	-
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(Param Cnt T u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	V
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	
target LoaMgr Per1 HwTqLoaMtgtnEn Cnt Igc.value	0	0	•
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target LoaMgr Per1 MotAgLoaMtgtnEn Cnt Igc.value	0	0	-
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~
2			

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 2.4 (Repeat Count = 1)	3
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08 k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k LoaMgr MotCurrMtgtnScaZeroEn Cnt Igc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1 0
target_LoaMgr_Per1_SteerMod_Crit_enum.value target_LoaMgr_Per1_TloaAvail_Crit_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc



Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	3	3	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	3	3	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotAgRespSV_Cnt_M_u08	3	3	✓
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	•
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1	1 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	3	3	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	✓

Test Step 2.5 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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		•	10.10
Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32 k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_UIs_f32	1 0		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target LoaMgr Per1 MotCurrldptSig Cnt u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Ont Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	1=	
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	1	2
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	•
LoaMgr_LOASt_State_M_enum	2	2	•
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	•
LOAMOR LOASCAIRECTE UIS M 132		11	

1

2

1

0

1

2

1

0

LoaMgr_LoaScaleFctr_Uls_M_f32

LoaMgr_MaxMtgtnResp_Cnt_M_u08

LoaMgr_MotAgldptSigSV_Cnt_M_u08

LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc

LoaMgr_MotAgRespSV_Cnt_M_u08

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Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	✓
LoaMgr_VltgMdSrcSV_Cnt_M_u08	3	3	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1	1 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	1	1 ± 0.001	✓
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	•
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	✓
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	•
AssignScale	1	AssignScale	1	✓
SwBasMtgtn	1	SwBasMtgtn	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	-
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	✓
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.6 (Repeat Count = 1)		E
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_lvtrRespSV_Cnt_M_u08	0	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	0	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0	
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1	
k_LoaMgr_FadeOutStRate_UlspS_f32	1	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2	
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1	
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2	
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1	
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0	
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0	
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1	
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1	
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1	
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0	
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0	
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0	
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1	
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0	
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1	





Name	Input Value		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	1		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Ina	actv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Ina	,	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_C	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnB		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LOASt_State_eltarget_LoaMgr_Per1_LoaScaDi_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Crit_igc target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtn		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgr	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_(
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_	enum	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_l	gc	
		gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_l	gc Expected Value	Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_l target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	Expected Value	~
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_l target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1	Expected Value 1	· ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_l target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	Expected Value	~
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_letarget_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1	Expected Value 1 1 0 1	~
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_ltarget_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1 1	Expected Value 1 1 0 1	~
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIoaMtgtnEn_Cnt_M_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_ltarget_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1 1	Expected Value 1 1 0 1 1	~
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_ltarget_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1 1	Expected Value 1 1 0 1 1 0 1 0 0 0 0 0 0 0 0 0 0 0 0	· ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIoaMtgtnEn_Cnt_M_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_ltarget_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1 1 0 1 0	Expected Value 1 1 0 1 1	**************************************
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IOASt_State_M_enum	target_LoaMgr_Per1_TloaAvail_Cnt_ltarget_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1 1 1 1 1 1 1 1 1 1 1	Expected Value 1 1 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	**************************************
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	target_LoaMgr_Per1_TloaAvail_Cnt_l target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1 1 1 1 1 1 1 1 1 1 1	Expected Value 1 1 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	**************************************
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_UIspS_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32	target_LoaMgr_Per1_TloaAvail_Cnt_l target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1 1 1 1 1 1 1 1 1 1 1	Expected Value 1 1 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	**************************************
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_vtrIdptSigSV_Cnt_M_u08 LoaMgr_vtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaSt_atate_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_igc	target_LoaMgr_Per1_TloaAvail_Cnt_l target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1 1 1 1 1 0 1 1 1 0 1 1	Expected Value 1 1 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 0	**************************************
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_vtrIdptSigSV_Cnt_M_u08 LoaMgr_vtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_vtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaSt_atate_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_l target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1 1 1 1 0 1 1 1 0 0 1 1	Expected Value 1 1 0 1 1 1 1 1 1 1 1 0 1 1 1 1 1 0 0 0 0	**************************************
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_VtrIdptSigSV_Cnt_M_u08 LoaMgr_VtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_VtrRespSV_Cnt_M_u08 LoaMgr_VtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_l target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 0 1 1 1 1 0 1 1 0 0 1 1	Expected Value 1 1 0 1 1 1 1 1 1 1 0 1 1 1 1 0 0 0 0	**************************************
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IthrIqptSigSV_Cnt_M_u08 LoaMgr_VtrIdptSigSV_Cnt_M_u08 LoaMgr_VtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_VtrRespSV_Cnt_M_u08 LoaMgr_VtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIoaMtgtnEn_Cnt_M_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_l target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 0 1 1 1 1 0 1 1 0 0 1 1	Expected Value 1 1 0 1 1 1 1 0 1 1 1 1 0 1 1 1 0 0 0 0 0 0 0 0	**************************************
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgltoaMtgtnEn_Cnt_M_u08 LoaMgr_MotAgltoaMtgtnEn_Cnt_M_u08 LoaMgr_MotAgltoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_letarget_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 0 1 1 1 0 1 1 0 0 1 1	Expected Value 1 1 0 1 1 1 1 0 1 1 1 1 0 0 1 1 1 1 0 0 0 0 0 0 1 1	**************************************
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LoASt_State_M_enum LoaMgr_LoASt_State_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_letarget_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 0 1 1 1 0 1 1 0 0 1 1	Expected Value 1 1 0 1 1 1 1 0 1 1 1 1 0 0 1 1 1 1 0 0 0 0 0 0 1 1 0	**************************************
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrlcoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_igc LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_letarget_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 0 1 1 1 0 1 1 0 0 1 1	Expected Value 1 1 0 1 1 1 1 0 1 1 1 1 0 0 1 1 1 1 0 0 0 0 0 0 1 1	**************************************
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LoASt_State_M_enum LoaMgr_LoASt_State_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lstarget_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 0 1 1 1 0 1 1 1 0 1 1	Expected Value 1 1 0 1 1 0 1 1 1 1 0 1 1 1 0 0 1 1 1 1 0 0 0 0 0 1	**************************************
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_igc LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lstarget_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 0 1 1 1 0 1 1 1 0 1 1	Expected Value 1 1 0 1 1 0 1 1 1 1 0 1 1 1 0 0 1 1 1 1 0 0 0 0 0 1	**************************************
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgCoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_NtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	target_LoaMgr_Per1_TloaAvail_Cnt_lstarget_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 0 1 1 1 0 1 1 1 0 1 1	Expected Value 1 1 0 1 1 0 1 1 1 1 1 0 1 1 1 0 0 0 0	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFct_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgHotSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_NotCurrLoaMtgtnEn_Status(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_LoaMgr_Per1_TloaAvail_Cnt_ltarget_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 0 1 1 1 0 1 1 1 1 1 1	Expected Value 1 1 0 1 1 0 1 1 1 1 1 0 1 1 1 1 1 0 0 0 0 0 0 0 1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFct_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgHotSigSV_Cnt_M_u08 LoaMgr_MotCurrIcptSigSV_Cnt_M_u08 LoaMgr_MotCurrIcptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_StetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	target_LoaMgr_Per1_TloaAvail_Cnt_l target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 0 1 1 1 1 0 1 1 1 1 1 1 1 1 1 1 1	Expected Value 1 1 1 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaSt_State_M_enum LoaMgr_LoaScaleFct_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrIcaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrIcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrIcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrIcaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_StentCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	target_LoaMgr_Per1_TloaAvail_Cnt_l target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 0 1 1 1 1 0 1 1 1 1 1 1 1 1 1 1 1	Expected Value 1 1 1 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 0 0 0 0 0 0 0 1 1 1 {173, 174} {0, 0, 0, 0, 0} {1, 1} 1 ± 0.01 1 ± 0.001 0	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	target_LoaMgr_Per1_TloaAvail_Cnt_l target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 0 1 1 1 1 0 1 1 1 1 1 1 1 1 1 1 1	Expected Value 1 1 1 0 1 1 1 1 1 1 1 0 1 1 1 1 1 1 0 0 0 0 0 0 1 1 1 1 {173, 174} {0, 0, 0, 0, 0} {1, 1} 1 ± 0.01 1 ± 0.001 0 1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LoASt_State_M_enum LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_LOASt_State_enum.value	target_LoaMgr_Per1_TloaAvail_Cnt_l target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 0 1 1 1 1 0 1 1 1 1 1 1 1 1 1 1 1	Expected Value 1 1 0 1 1 1 1 0 1 1 1 1 1 1 1 1 1 1 1	V V V V V V V V V V V V V V V V V V V
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	target_LoaMgr_Per1_TloaAvail_Cnt_l target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 0 1 1 1 1 0 1 1 1 1 1 1 1 1 1 1 1	Expected Value 1 1 1 0 1 1 1 1 1 1 1 0 1 1 1 1 1 1 0 0 0 0 0 0 1 1 1 1 {173, 174} {0, 0, 0, 0, 0} {1, 1} 1 ± 0.01 1 ± 0.001 0 1	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.7 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr MotAgldptSigSV Cnt M u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr MotCurrLoaMtgtnEn Cnt M Igc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1.
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target LoaMgr Per1 HwTqldptSig Cnt u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1l	nactv Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2li		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqldptSig Cnt u08	target_LoaMgr_Per1_HwTqldptSig_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgt	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnE		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtg		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cni		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaAvail Cnt Igc	target_LoaMgr_Per1_TloaAvail_Cnt		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lg		
Name	Actual Value	Expected Value	Result
	1	1	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	
LoaMgr_lytrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc		0	
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	Ž
LoaMgr_LOASt_State_M_enum		i i	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	0	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	0		
LoaMgr_MotAgldptSigSV_Cnt_M_u08		1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	Ž
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	V
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	V
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	•
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	*
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_LOASt_State_enum.value	0	0	*
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Name	Test Step 2.8 (Repeat Count = 1)	3
Looking, Levingonisty, Cort, Aug. 1999 Looking, Heritgonisty, Cort, Aug. 1999 Looking, Lookin		
Loosing Profit Opinion Co. (Co.)		·
Loading Justification Cort M. go Loadin		
Landery - Minispressy Cort M. 2008 Landery - Minispressy Minispre		
Loading Methodipside Cort M. 106		
Landary Environgency Crut Murolis Landary Conformation Crut Murolis Landary Conformation Crut Murolis Landary Conformation Landary Landary Landary Conformation Landary L	LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
Loading / MASQ-place(SSV_COT_M_USB)	LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
Coading - Manage analogo Pico - Pic	LoaMgr_lvtrRespSV_Cnt_M_u08	0
Coading	LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
Loading / McCurcia/deptitor Crit / Mile	LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
Loading MoChartecky Cort M. 198 Loading Seriothkonique Cort M. 198 Loading Contributing Seriothkonique Loading Cort M. 198 Loading Contributing Seriothkonique Loading Cort M. 198 Loading Contributing Seriothkonique Loading Cort M. 198 Loading Loading Loading Tourishkonique Cort M. 198 Loading Loading Loading Loading Cort M. 198 Loading Loading Loading Loading Loading Cort M. 198 Loading Loading Loading Loading Loading Cort M. 198 Loading Load	LoaMgr_MotAgRespSV_Cnt_M_u08	
Loading Senderfallorges Loading Loadin	LoaMgr_MotCurrldptSigSV_Cnt_M_u08	
Lookings		
Loading / Jiny Margines Lings 129		
Institute Inst	· - · · ·	
L_analog_Currientingsroad_relian_Curriege L_analog_Currientingsroad_Curriege L_analog_Currientingsroad_Currieded_Currieded_		
L. Landarg. Juristy Mingriss Care Description City		V
Loading-Info@displane-lusins-Income Loading-Info@displane-lusins-I		
L. Loadby_InVolagingStyTeachwallResp_Cott_U08	k LoaMgr FadeOutStRate UlspS f32	
Loading Mindignis Transport Transp	k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	
Landley_MindigsSig1TranDARsig_Cort_U08	k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	
L. Loading / Hardydosis/Garlian/Resp. Cnt. U08	k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	
Loaking-Hw7qtqlSig4Rep_Cnt_U8	k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
Loaking-HwTqNtgRispRTMC ort, UGB	k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
L. Loadeg HwTqMtghrRate Lights 132	k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
L. Loading _ Invitrightispic Resp_ Cnt_ u08	k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
Loaking_NritighSigResp_Cnt_u08	k_LoaMgr_HwTqMtgtnRate_UlspS_f32	
Loading		
Loaking		
L_LoaMgr_InvirMignRate_UlspS_132		•
LoaMgr_MoNAglophSig1NoSmaResp_Cnt_U08		
LoaMgr_MotAgidptSig1NsSmaResp_Cnt_u08		
LoaMgr_MotAgldpiSig2Resp_Cnt_u08		
k_LoaMgr_MotAgidptSigSResp_Cnt_u08 0 k_LoaMgr_MotAgidptSigFirthd_Cnt_u08 2 k_LoaMgr_MotAgMignRate_UlbspS_f02 1 k_LoaMgr_MotAgMignRate_UlbspS_f02 0 k_LoaMgr_MotAgMignSca_Uls_f32 1 k_LoaMgr_MotAgMignSca_Uls_f32 1 k_LoaMgr_MotAgSmaAvaliResp_Cnt_u08 0 k_LoaMgr_MotCurridptSigTResp_Cnt_u08 1 k_LoaMgr_MotCurridptSigTesp_Cnt_u08 0 k_LoaMgr_MotCurridptSigTesp_Cnt_u08 0 k_LoaMgr_MotCurridptSigTesp_Cnt_u08 0 k_LoaMgr_MotCurridptSigTesp_Cnt_u08 0 k_LoaMgr_MotCurridptSigTesp_Cnt_u08 0 k_LoaMgr_MotCurridptSigTesp_Cnt_u08 1 k_LoaMgr_MotCurridptSigTesp_Uls_S2 1 k_LoaMgr_MotCurridptSigTesp_Uls_S2 1 k_LoaMgr_RedodSitea_Uls_S2 1 k_LoaMgr_Red		
LoaMgr_MotAgMtghrRate_UlspS_f32	k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgMtginSca_ZeroEn_Cnt_lgc 0 k_LoaMgr_MotAgMtginSca_Uis_132 1 k_LoaMgr_MotAgMtginSca_Uis_132 1 k_LoaMgr_MotCurridptSigGResp_Cnt_u08 0 k_LoaMgr_MotCurridptSigGResp_Cnt_u08 0 k_LoaMgr_MotCurridptSigFilThd_Cnt_u08 0 k_LoaMgr_MotCurridptSigFilThd_Cnt_u08 0 k_LoaMgr_MotCurridptSigFilThd_Cnt_u08 0 k_LoaMgr_MotCurrMginSac_UispS_132 1 k_LoaMgr_MotCurridptSigFilThd_Cnt_u08 1 k_LoaMgr_MotCurridptSigCareEn_Cnt_lgc 1 k_LoaMgr_MotCurridptSig_Creen_Cnt_lgc 1 k_LoaMgr_MotCurridptSig_Creen_Cnt_lgc 1 k_LoaMgr_MotCurridptSig_Creen_Cnt_lgc 1 k_LoaMgr_ReddSiSca_Uis_132 1 k_LoaMgr_Perl_DiagcStsivtr1inact_Cnt_lgc_value 0 target_LoaMgr_Perl_DiagcStsivtr2inact_Cnt_lgc_value 0 target_LoaMgr_Perl_MotCurridptSig_Cnt_u08 value 1 target_LoaMgr_Perl_MotCurridptSig_Cnt_u08 value 1 target_LoaMgr_Perl_MotCurridptSig_Cnt_u08 value 0 target_LoaMgr_Perl_MotCurridptSig_Cnt_u08 value 1 target_LoaMgr_Perl_TolaQcvalic_cnt_lgc	k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
LoaMgr_MotAgMtginSca_Uls_f32	k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
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target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMgr_LoaMgr_Per1_HwTqLoaMgr_Der1_HwTqLoaMgr_Der1_HwTqLoaMgr_Der1_HwTqLoaMgr_Der1_HwTqLoaMgr_Der1_HwTqLoaMgr_Der1_HwTqLoaMgr_Der1_LoaMgr_Der1_VrLoaMgr_Der1_VrLoaMgr_Der1_VrLoaMgr_Der1_VrLoaMgr_Der1_VrLoaMgr_Der1_VrLoaMgr_Der1_VrLoaMgr_Der1_VrLoaMgr_Der1_VrLoaMgr_Der1_VrLoaMgr_Der1_VrLoaMgr_Der1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_NoaMgr_Der1_LoaMgr_Der1_MotAgLoaMgr_Der1_MotAgLoaMgr_Der1_MotAgLoaMgr_Der1_MotAgLoaMgr_Der1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Der1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	
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target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	
	target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_MotCurrldptSig_Cnt_u08	
target_kte_inst_Ap_Loamgr.LoamgrPer1_MotUurrLoamtgtn⊑n_Unt_igc target_LoaMgrPer1_MotUurrLoaMtgtnEn_Cnt_igc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_IvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	1	1	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	1	1	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	1	1	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.9 (Repeat Count = 1)	12
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	2		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	1		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target LoaMgr Per1 SteerMod Cnt enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	nt lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Citarget_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_uo8 target_Rte_inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_t		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_IvtrRespSV_Cnt_M_u08	0	0	V
LoaMgr_LOASt_State_M_enum	5	5	V
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	•
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	

LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	✓
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.10 (Repeat Count = 1)	3
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Inmus Value		
Name k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	Input Value		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 k LoaMgr MotCurrMtgtnScaZeroEn Cnt lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_	Cat las	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_		
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt	_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enur	n	
	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	Francisco d Volum	Danulé
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	Expected Value	Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0	1	*
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1	1	×
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0	1	*
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0	1 1 0	×
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 0	1 1 0 0	×
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 1	1 1 0 0 1	×
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target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrlOaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 1 1 0 2 500 1	1 1 0 0 1 1 1 0 2 500	×
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 1 1 0 2 500 1 2	1 1 0 0 1 1 1 0 2 500 1 2	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 1 1 0 2 500 1 2 1	1 1 0 0 0 1 1 1 0 2 500 1 2	×
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target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaState_Limit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 0 1 1 1 0 2 500 1 2 1 0 0 0	1 1 0 0 0 1 1 1 0 2 500 1 2 1 0 0	
target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_ItvrldptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaState_Limit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 0 1 1 1 0 2 500 1 2 1 0 0 0 0 0 0 0 0 0 0 0 0 0	1 1 0 0 0 1 1 1 0 2 500 1 2 1 0 0	
target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LVtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 0 1 1 1 0 2 500 1 2 1 0 0 0 1 1 1 1 0 1 1 1 1 0 1 1 1 1	1 1 0 0 0 1 1 1 0 2 500 1 2 1 0 0	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgldoaMtgtnEn_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 0 1 1 1 0 2 500 1 2 1 0 0 0 0 0 0 0 0 0 0 0 0 0	1 1 0 0 0 1 1 1 0 2 500 1 2 1 0 0	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrlcoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 0 1 1 0 2 500 1 2 1 0 0 0 1 2 1 2 1 0 0 0 1 2 1 2 1	1 1 0 0 0 1 1 1 0 2 500 1 2 1 0 0 0	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgldoaMtgtnEn_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 0 1 1 0 2 500 1 2 1 0 0 0 1 2 1 2 1 0 0 0 0 1	1 1 0 0 0 1 1 1 0 2 500 1 1 2 1 0 0 0	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 0 1 1 0 2 500 1 2 1 0 0 0 1 2 1 2 1 0 0 0 1 1 2 1 0 0 0 1 1 1 1	1 1 0 0 0 1 1 1 0 2 500 1 1 2 1 0 0 0 0 1 2	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrlOaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MoxAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgnEn_Cnt_M_lgc LoaMgr_MotCurrloptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 0 1 1 0 2 500 1 2 1 0 0 0 1 2 1 0 0 1 2 1 3	1 1 0 0 0 1 1 1 0 2 500 1 1 2 1 0 0 0 0 1 2 1 2	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 0 1 1 0 2 500 1 2 1 0 0 1 2 1 1 1 1 1 1 1 1 1 1 1	1 1 0 0 0 1 1 1 0 2 500 1 2 1 0 0 0 0 1 2 1 2 1 0 0 1 2 1 1 2 0 1 1 2 0 1 1 1 2 0 0 1 1 1 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LoASt_State_M_enum LoaMgr_LoaState_Imit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgCoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 0 1 1 0 2 500 1 2 1 0 0 1 2 1 3 {173, 174} {1, 1}	1 1 0 0 0 1 1 1 1 0 2 500 1 2 1 0 0 0 1 2 1 2 1 1 0 0 0 1 2 1 2	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrlOaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 0 1 1 0 2 500 1 2 1 0 0 1 2 1 1 0 1 1 1 1 1 1 1 1 1 1	1 1 0 0 0 1 1 1 1 0 2 500 1 2 1 0 0 0 1 2 1 3 {173, 174} {0, 0, 0, 0, 0} {1, 1}	
target_Rte_Inst_Ap_LoaMgr_Ner1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgCont_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvaii_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 0 1 1 0 2 500 1 2 1 0 0 1 2 1 3 {173, 174} {1, 1} {1, 1} 500	1 1 0 0 0 1 1 1 1 0 2 500 1 2 1 0 0 0 1 2 1 2 1 0 0 1 2 0 1 3 {173, 174} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0	
target_Rte_Inst_Ap_LoaMgr_Ner1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgCont_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRsteLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 0 1 1 0 2 500 1 2 1 0 0 1 2 1 1 0 0 1 2 1 0 1 1 0 0 1 2 1 0 1 1 1 0 0 1 2 1 0 1 1 2 1 0 1 2 1 1 0 1 1 2 0 1 1 2 0 1 1 3 {173, 174} {1, 1} {1, 1} 500 1 0 1	1 1 0 0 0 1 1 1 1 0 2 500 1 2 500 1 2 1 0 0 0 1 2 1 3 {173,174} {0,0,0,0,0} {1,1} 500±0.01 1±0.001 0 1	
target_Rte_Inst_Ap_LoaMgr_Ner1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrlQaSigSV_Cnt_M_u08 LoaMgr_IvtrlQaSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrlQptSigSV_Cnt_M_u08 LoaMgr_MotCurrlQptSigSV_Cnt_M_u08 LoaMgr_MotCurrlCoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 0 1 1 0 2 500 1 2 1 0 0 1 2 1 1 0 0 1 2 1 0 1 1 0 1 2 1 0 1 2 1 0 1 2 1 0 1 2 1 0 1 2 1 1 2 0 1 1 2 0 1 1 2 0 1 1 2 0 1 1 2 0 1 1 2 0 1 1 2 1 1 1 1 1 1 1 1 1 1	1 1 0 0 0 1 1 1 1 0 2 500 1 2 500 1 2 1 0 0 0 1 2 1 3 {173, 174} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0 1 2	
target_Rte_Inst_Ap_LoaMgr_Ner1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgCont_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_StyltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 0 1 1 0 2 500 1 2 1 0 0 1 2 1 1 0 0 1 2 1 0 1 1 0 0 1 2 1 0 1 1 1 0 0 1 2 1 0 1 1 2 1 0 1 2 1 1 0 1 1 2 0 1 1 2 0 1 1 3 {173, 174} {1, 1} {1, 1} 500 1 0 1	1 1 0 0 0 1 1 1 1 0 2 500 1 2 500 1 2 1 0 0 0 1 2 1 3 {173,174} {0,0,0,0,0} {1,1} 500±0.01 1±0.001 0 1	



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
CalclvtrState	2	CalcivtrState	2	•	
LatchInputs	1	LatchInputs	1	~	
RqstRespConds	1	RqstRespConds	1	~	
RqstHwTqResp	1	RqstHwTqResp	1	~	
ArbResp	1	ArbResp	1	~	
ChooseFinalResp	1	ChooseFinalResp	1	•	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~	
AssignScale	1	AssignScale	1	•	
SwBasMtgtn	1	SwBasMtgtn	1	•	
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~	
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•	

Test Step 2.11 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrivtrMtgtnScaZeroEn Cnt Igc	1
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	1
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnRate_Olsp3_132	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
	1
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	0
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_lvtrMtgtnSca_Uls_f32	5
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	1
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
	1
k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
	0
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	·
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	2 0
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrNdptSigFltThd_Cnt_u08	1
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	





Loawigi_Feri		•	OIL CITORIO
Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIv	rtr1Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIv	rtr2Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqIdptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldpt	Sig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoal	MtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtg	tnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_St	ate_enum	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoa	MtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrld	ptSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLo	paMtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdp	tSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod	_Cnt_enum	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cr	nt_lgc	
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc			2
LoaMgr HwTqldptSigSV Cnt M u08	1	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	•
LoaMgr_LOASt_State_M_enum	2	2	
LoaMgr LoaRateLimit UlspS M f32	500	500	

Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0		×
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	2	2	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	3	3	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	•
SwBasMtgtn	1	SwBasMtgtn	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte Exit An LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMor EaLoaScaleAndRate	1	_





Test Step 2.12 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	3
LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Inst_Ap_LoaMgr	target Rte Inst Ap LoaMgr
k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08 k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k LoaMgr RedcdStRate UlspS f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	



Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc			×
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	2	2	~
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	•
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	3	3	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1	1 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	✓
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
SwBasMtgtn	1	SwBasMtgtn	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	-
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	✓
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	~

Test Step 2.13 (Repeat Count = 1)	13
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr





Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIv	tr1Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIv		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptS	Sig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaM		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtg	··	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_Sta		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoal		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldp		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLo	·	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdp		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cn		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	1	F
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	-
	The second secon	· ·	

target_Rte_inst_Ap_Loalwigi_Peri_noabi_Cnt_igc	target_Loawigt_Peri_floabl_Cht_igc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc			**
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	✓
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	✓
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	✓
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	✓
LoaMgr_LOASt_State_M_enum	2	2	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	✓
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	✓
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	✓
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	✓
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	✓

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Name	Actual Value	Expected Value	Result
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1	1 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	•
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
SwBasMtgtn	1	SwBasMtgtn	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.14 (Repeat Count = 1)	· ·
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	2
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0

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Name	Input Value		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
<_LoaMgr_RedcdStRate_UlspS_f32	1		
LoaMgr_RedcdStSca_Uls_f32	1		
arget_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
arget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
arget_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
arget_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
arget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
arget_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
arget_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
arget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1In		
arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2In		
arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_C	Cnt_u08	
arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtn	En_Cnt_lgc	
arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn	_Cnt_lgc	
arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_e	enum	
arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_	_lgc	
arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtr	nEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtg		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_		
arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	:	
Name	Actual Value	Expected Value	Res
_oaMgr_AgSV_Cnt_M_lgc	0	1	
_oaMgr_HwTqIdptSigSV_Cnt_M_u08	1	1	
	0	0	
.oaMgr_HwTqRespSV_Cnt_M_u08	1	1	
.oaMgr_HwTqRespSV_Cnt_M_u08 .oaMgr_lvtrldptSigSV_Cnt_M_u08	1 1	1	
_oaMgr_HwTqRespSV_Cnt_M_u08 _oaMgr_lvtrldptSigSV_Cnt_M_u08 _oaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1 1 0	1 1 0	
.oaMgr_HwTqRespSV_Cnt_M_u08 .oaMgr_IvtrldptSigSV_Cnt_M_u08 .oaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc .oaMgr_IvtrRespSV_Cnt_M_u08	1 1 0 0	1 1 0 0	
.oaMgr_HwTqRespSV_Cnt_M_u08 .oaMgr_IvtrldptSigSV_Cnt_M_u08 .oaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc .oaMgr_IvtrRespSV_Cnt_M_u08 .oaMgr_IvtrRespSV_Cnt_M_u08	1 1 0 0 2	1 1 0 0 2	
.oaMgr_HwTqRespSV_Cnt_M_u08 .oaMgr_IvtrldptSigSV_Cnt_M_u08 .oaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc .oaMgr_IvtrRespSV_Cnt_M_u08 .oaMgr_IvtrRespSV_Cnt_M_u08 .oaMgr_LOASt_State_M_enum .oaMgr_LoaRateLimit_UlspS_M_f32	1 1 0 0 2 500	1 1 0 0 2 500	
.oaMgr_HwTqRespSV_Cnt_M_u08 .oaMgr_lvtrldptSigSV_Cnt_M_u08 .oaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc .oaMgr_lvtrRespSV_Cnt_M_u08 .oaMgr_LOASt_State_M_enum .oaMgr_LoaRateLimit_UlspS_M_f32 .oaMgr_LoaScaleFctr_Uls_M_f32	1 1 0 0 2 500	1 1 0 0 2 500	
.oaMgr_HwTqRespSV_Cnt_M_u08 .oaMgr_lvtrldptSigSV_Cnt_M_u08 .oaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc .oaMgr_lvtrRespSV_Cnt_M_u08 .oaMgr_LOASt_State_M_enum .oaMgr_LoaRateLimit_UlspS_M_f32 .oaMgr_LoaScaleFctr_Uls_M_f32 .oaMgr_MaxMtgtnResp_Cnt_M_u08	1 1 0 0 2 500 1 2	1 1 0 0 2 500 1 2	
.oaMgr_HwTqRespSV_Cnt_M_u08 .oaMgr_lvtrldptSigSV_Cnt_M_u08 .oaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc .oaMgr_lvtrRespSV_Cnt_M_u08 .oaMgr_LOASt_State_M_enum .oaMgr_LoaRateLimit_UlspS_M_f32 .oaMgr_LoaScaleFctr_Uls_M_f32 .oaMgr_MaxMtgtnResp_Cnt_M_u08 .oaMgr_MotAgldptSigSV_Cnt_M_u08	1 1 0 0 2 500 1 2	1 1 0 0 2 500 1 2	
_oaMgr_HwTqRespSV_Cnt_M_u08 _oaMgr_lvtrldptSigSV_Cnt_M_u08 _oaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc _oaMgr_lvtrRespSV_Cnt_M_u08 _oaMgr_LOASt_State_M_enum _oaMgr_LoaRateLimit_UlspS_M_f32 _oaMgr_LoaScaleFctr_Uls_M_f32 _oaMgr_MaxMtgtnResp_Cnt_M_u08 _oaMgr_MotAgldptSigSV_Cnt_M_u08 _oaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1 1 0 0 2 500 1 2 1	1 1 0 0 2 500 1 2 1	
.oaMgr_HwTqRespSV_Cnt_M_u08 .oaMgr_IvtrldptSigSV_Cnt_M_u08 .oaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc .oaMgr_IvtrRespSV_Cnt_M_u08 .oaMgr_LOASt_State_M_enum .oaMgr_LoaRateLimit_UlspS_M_f32 .oaMgr_LoaScaleFctr_Uls_M_f32 .oaMgr_MaxMtgtnResp_Cnt_M_u08 .oaMgr_MotAgldptSigSV_Cnt_M_u08 .oaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc .oaMgr_MotAgRespSV_Cnt_M_u08	1 1 0 0 2 500 1 2 1 0	1 1 0 0 2 500 1 2 1 0	
.oaMgr_HwTqRespSV_Cnt_M_u08 .oaMgr_IvtrldptSigSV_Cnt_M_u08 .oaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc .oaMgr_IvtrRespSV_Cnt_M_u08 .oaMgr_LOASt_State_M_enum .oaMgr_LoaRateLimit_UlspS_M_f32 .oaMgr_LoaScaleFctr_Uls_M_f32 .oaMgr_MaxMtgtnResp_Cnt_M_u08 .oaMgr_MotAgldptSigSV_Cnt_M_u08 .oaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc .oaMgr_MotAgRespSV_Cnt_M_u08 .oaMgr_MotAgRespSV_Cnt_M_u08	1 1 0 0 2 500 1 2 1 0 0	1 1 0 0 2 500 1 2 1 0	
.oaMgr_HwTqRespSV_Cnt_M_u08 .oaMgr_IvtrldptSigSV_Cnt_M_u08 .oaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc .oaMgr_IvtrRespSV_Cnt_M_u08 .oaMgr_LOASt_State_M_enum .oaMgr_LoaRateLimit_UlspS_M_f32 .oaMgr_LoaScaleFctr_Uls_M_f32 .oaMgr_MaxMtgtnResp_Cnt_M_u08 .oaMgr_MotAgldptSigSV_Cnt_M_u08 .oaMgr_MotAglcaMtgtnEn_Cnt_M_lgc .oaMgr_MotAgRespSV_Cnt_M_u08 .oaMgr_MotAgRespSV_Cnt_M_u08	1 1 0 0 2 500 1 2 1 0 0	1 1 0 0 2 500 1 2 1 0 0	
.oaMgr_HwTqRespSV_Cnt_M_u08 .oaMgr_IvtrldptSigSV_Cnt_M_u08 .oaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc .oaMgr_IvtrRespSV_Cnt_M_u08 .oaMgr_LOASt_State_M_enum .oaMgr_LoaRateLimit_UlspS_M_f32 .oaMgr_LoaScaleFctr_Uls_M_f32 .oaMgr_MaxMtgtnResp_Cnt_M_u08 .oaMgr_MotAgldptSigSV_Cnt_M_u08 .oaMgr_MotAglcaMtgtnEn_Cnt_M_lgc .oaMgr_MotAgRespSV_Cnt_M_u08 .oaMgr_MotCurrldptSigSV_Cnt_M_u08 .oaMgr_MotCurrldptSigSV_Cnt_M_u08	1 1 0 0 2 500 1 2 1 0 0 0	1 1 0 0 2 500 1 2 1 0 0	
.oaMgr_HwTqRespSV_Cnt_M_u08 .oaMgr_IvtrldptSigSV_Cnt_M_u08 .oaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc .oaMgr_IvtrRespSV_Cnt_M_u08 .oaMgr_LOASt_State_M_enum .oaMgr_LoaRateLimit_UlspS_M_f32 .oaMgr_LoaScaleFctr_Uls_M_f32 .oaMgr_MaxMtgtnResp_Cnt_M_u08 .oaMgr_MotAgldptSigSV_Cnt_M_u08 .oaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc .oaMgr_MotAgRespSV_Cnt_M_u08 .oaMgr_MotCurrldptSigSV_Cnt_M_u08 .oaMgr_MotCurrldptSigSV_Cnt_M_u08 .oaMgr_MotCurrldptSigSV_Cnt_M_u08	1 1 0 0 2 500 1 2 1 0 0 0	1 1 0 0 2 500 1 2 1 0 0 0	
.oaMgr_HwTqRespSV_Cnt_M_u08 .oaMgr_IvtrldptSigSV_Cnt_M_u08 .oaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc .oaMgr_IvtrRespSV_Cnt_M_u08 .oaMgr_LOASt_State_M_enum .oaMgr_LoaRateLimit_UlspS_M_f32 .oaMgr_LoaScaleFctr_Uls_M_f32 .oaMgr_MaxMtgtnResp_Cnt_M_u08 .oaMgr_MotAgldptSigSV_Cnt_M_u08 .oaMgr_MotAgloaMtgtnEn_Cnt_M_lgc .oaMgr_MotAgRespSV_Cnt_M_u08 .oaMgr_MotCurrldptSigSV_Cnt_M_u08 .oaMgr_MotCurrldptSigSV_Cnt_M_u08 .oaMgr_MotCurrldptSigSV_Cnt_M_u08 .oaMgr_MotCurrlaptSigSV_Cnt_M_u08 .oaMgr_MotCurrlaptSigSV_Cnt_M_u08 .oaMgr_MotCurrlaptSigSV_Cnt_M_u08 .oaMgr_MotCurrlaptSigSV_Cnt_M_u08 .oaMgr_MotCurrRespSV_Cnt_M_u08 .oaMgr_TloaAvail_Cnt_M_lgc .oaMgr_TloaDi_Cnt_M_lgc	1 1 0 0 2 500 1 2 1 0 0 0 0 1 2	1 1 0 0 2 500 1 2 1 0 0 0 1 2	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VttgMdSrcSV_Cnt_M_u08	1 1 0 0 2 500 1 2 1 0 0 0 0 1 2 1 1 0	1 1 0 0 2 500 1 2 1 0 0 0 0 1 2 1 1 0	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	1 1 0 0 2 500 1 2 1 0 0 0 0 1 2 1 1 2 0 1 1 2 0 1 1 2 1 1 1 1 1 1 1 1 1 1 1 1 1	1 1 0 0 0 2 500 1 2 1 0 0 0 0 1 2 2 1 1 0 0	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrloptSigSV_Cnt_M_u08 LoaMgr_MotCurrloptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1 1 0 0 2 500 1 2 1 0 0 0 0 1 2 0 1 2 0 1 1 2 0 1 1 2 1 1 1 2 1 1 1 1 1 1 1 1 1 1 1 1 1	1 1 0 0 0 2 500 1 2 1 0 0 0 0 1 2 0 1 2 0 1 1 2 0 1 1 2 0 1 1 2 0 1 1 2 0 0 1 1 2 0 1 1 1 1 1 1 1 1 1 1 1 1 1	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIcaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1 1 0 0 0 2 500 1 2 1 0 0 0 0 1 2 0 1 1 2 0 1 1 2 0 1 1 2 1 1 1 1 1 1 1 1 1 1 1 1 1	1 1 0 0 0 2 500 1 2 1 0 0 0 0 1 2 0 1 1 2 0 1 1 2 0 1 1 2 0 1 1 2 0 1 1 2 0 1 1 1 1 1 1 1 1 1 1 1 1 1	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1 1 0 0 0 2 500 1 2 1 0 0 0 1 2 1 1 0 0 0 1 1 2 0 1 1 1 {173, 174} {1, 1} {1, 1} 500	1 1 1 0 0 0 2 500 1 2 1 0 0 0 0 1 2 1 1 0 1 1 2 0 1 1 1 {173, 174} {0, 0, 0, 0, 0, 0} {1, 1} 500 ± 0.01	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VttgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1 1 0 0 0 2 500 1 2 1 0 0 0 1 2 1 1 0 0 1 1 2 1 1 1 {173, 174} {1, 1} 500 1	1 1 1 0 0 0 2 500 1 2 500 1 2 1 0 0 0 1 1 2 0 1 1 1 {173, 174} {0, 0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1 1 0 0 0 2 500 1 2 1 0 0 0 1 2 1 1 2 0 1 1 1 {173, 174} {1, 1} 500 1 0	1 1 0 0 0 2 500 1 2 1 0 0 0 1 2 1 1 0 0 1 1 2 0 1 1 {173, 174} {0, 0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaState_M_enum LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1 1 0 0 0 2 500 1 2 1 0 0 0 1 2 1 1 1 1 {173, 174} {1, 1} 500 1 0 0	1 1 0 0 0 2 500 1 2 1 0 0 0 0 1 2 1 1 0 0 1 1 2 0 1 1 {173, 174} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0 0	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgltptSigSV_Cnt_M_u08 LoaMgr_MotAgltptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1 1 0 0 0 2 500 1 2 1 0 0 0 1 2 1 1 2 0 1 1 1 {173, 174} {1, 1} 500 1 0	1 1 0 0 0 2 500 1 2 1 0 0 0 1 2 1 1 0 0 1 1 2 0 1 1 {173, 174} {0, 0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	•
AssignScale	1	AssignScale	1	•
SwBasMtgtn	1	SwBasMtgtn	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.15 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr MotAgLoaMtgtnEn Cnt M lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	2
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	target_kte_inst_Ap_coawigi
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnRate_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	1
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1 0
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1





Loamgi_i ci i			1000
Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1	Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2	Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_	_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgr	tnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnE	n_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_	_enum	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtg	tnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSi	ig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaM	tgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig	_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cn	t_enum	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lo	gc	
Name	Actual Value	Expected Value	Resul
LoaMgr_AgSV_Cnt_M_lgc			
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	
LoaMgr_LOASt_State_M_enum	2	2	•
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	•
LoaMgr MotAgldptSigSV Cnt M u08	1	1	

Loawigi_TiwTqldptSig3V_Citi_W_doo		!	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_IvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	2	2	•
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1	1 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace ✓				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	✓
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
SwBasMtgtn	1	SwBasMtgtn	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit An LoaMor FalloaScaleAndRate	1	Rte Exit An LoaMor FalloaScaleAndRate	1	_



Test Step 2.16 (Repeat Count = 1)	
Name LoaMgr_AgSV_Cnt_M_lgc	Input Value
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08 k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k LoaMgr MotCurrMtgtnRate UlspS f32	1
k LoaMgr MotCurrMtgtnScaZeroEn Cnt Igc	1
k_LoaMgr_MotCurrMtgtnSca_UIs_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdpt	Sig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_0	Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_0	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt	_lgc		
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc			×	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	✓	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	✓	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	✓	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	✓	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	✓	
LoaMgr_lvtrRespSV_Cnt_M_u08	2	2	✓	
LoaMgr_LOASt_State_M_enum	2	2	✓	
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	✓	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	✓	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	✓	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	✓	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	✓	
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	✓	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	✓	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	✓	
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	✓	
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓	
LoaMgr_TloaDi_Cnt_M_lgc	1	1	✓	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	2	2	✓	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	-	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1	1 ± 0.01	✓	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	-	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓	
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	-	
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	✓	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	-	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	✓	

Test Step Call Trace			✓	
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
SwBasMtgtn	1	SwBasMtgtn	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	✓
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	-

Test Step 2.17 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr





Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k LoaMgr MotAgldptSig1NoSmaResp Cnt u08	5		
k LoaMgr MotAgldptSig2Resp Cnt u08	1		
k LoaMgr MotAgldptSig3Resp Cnt u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
	1		
k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08			
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcSts	.vtr1Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcSts	vtr2Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldp	Sig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoa	MtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMt	gtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_S	tate_enum	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaD	i_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLo	aMtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrle	lptSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrL	oaMtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosId	ptSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod	I_Cnt_enum	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_C		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	1	F
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	·
· · · · · · · · · · · · · · · · · · ·			

target_Rte_inst_Ap_Loawigr_Loawigr_Perr_noabi_Cnt_igc	target_Loawigt_Perit_floabi_Cht_igc			
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc			×	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	~	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~	
LoaMgr_lvtrRespSV_Cnt_M_u08	2	2	~	
LoaMgr_LOASt_State_M_enum	2	2	~	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	~	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~	

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Name	Actual Value	Expected Value	Result
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	2	2	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	•
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
SwBasMtgtn	1	SwBasMtgtn	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.18 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	2
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0

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Name	Input Value		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0 2		
k_LoaMgr_MotAgIdptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32 k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1 target LeaMar Port DiagoStelytr1Inact	y Cat las	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv target_LoaMgr_Per1_DiagcStslvtr2Inactv		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enur		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cr	nt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnE		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_end		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Resu
LoaMgr_AgSV_Cnt_M_lgc	0	1	Resu
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr HwTgRespSV Cnt M u08	1	1	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_lvtrRespSV_Cnt_M_u08	2	2	
LoaMgr_LOASt_State_M_enum	2	2	
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	
LoaMgr_MotAgloaMtotsEp_Cot_M_u08	0	1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	
Loamgr_motAgRespSv_Cnt_m_uu8 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	
LoaMgr_TloaDi_Cnt_M_lgc	1	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	2	2	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	
	1	1 ± 0.01	
		1 ± 0.001	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1		
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	0	0	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	0	0	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	•
AssignScale	1	AssignScale	1	•
SwBasMtgtn	1	SwBasMtgtn	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.19 (Repeat Count = 1) Name	Input Value
LoaMgr AgSV Cnt M Igc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrlytrMtgtnScaZeroEn Cnt lgc	0
k_LoaMgr_CurrlytrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	2
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_UIs_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k LoaMgr MotAgldptSig3Resp Cnt u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k LoaMgr MotAgMtgtnScaZeroEn Cnt Igc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k LoaMgr MotAgSmaAvailResp Cnt u08	0
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIv	rtr1Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIv	rtr2Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldpts	Sig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoal	MtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtg	tnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_St	ate_enum	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoa	MtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrld	ptSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLo	aMtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdp	tSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod	_Cnt_enum	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cr	ıt_lgc	
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc			×
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	✓
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_IvtrRespSV_Cnt_M_u08	0	0	•
LoaMgr_LOASt_State_M_enum	2	2	-
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	-
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	-
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	-
LoaMar TloaAvail Cat M lac			

LoaMgr_MotCurridptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	•
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1	1 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	✓
SwBasMtgtn	1	SwBasMtgtn	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Injust Injus	Test Step 2.20 (Repeat Count = 1)	8
Looking Hardingstop Cort M		
Looking Information Dr. III Jpc		·
Loading Juniferpoint Complete Comple		1
Landing - Mendenghage Cort M. 108 Looking - Menden	LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
Landing Medical May 100 M 100	LoaMgr_HwTqRespSV_Cnt_M_u08	1
Loading Jankingstreet Ja	LoaMgr_lvtrldptSigSV_Cnt_M_u08	
Looking Markelpring Corp. M. pts 0		
Coadags Manage Manage Coadags Manage Manage Coadags Manage		·
Loading MacGardings Cort M, USB Loading MacGardings Cort M, USB Loading MacGardings Cort M, Mg Rise Intel A Loading Linding Cardinds Application (Cort Mg Linding Cardinds Application (Cort Mg Linding Cardinds Application (Cort Mg Linding Cardinds Cort Mg Linding Cardinds Cort Mg Linding Cardinds Cort Mg Linding Cardinds Cort Mg Linding MacGardings Line CO Linding MacGardings Line Cort Line Linding MacGardings Line Line Cort Line Linding MacGardings Line Line Cort Line Linding MacGardings Line Line Linding MacGardings Line Line Linding MacGardings Line Line Linding MacGardings Line Line Lindings MacGardings Line Lindings MacGardin		
Loading / McContradigners Cott / M. 196 Loading / McContradigners / M. 196 Loading / McContradig		
Leading Mochanis Analysis Cont. Mulge Leading Analysis Cont. Mulge Leading Analysis Cont. Mulge Leading Analysis Cont. Mulge 1 Leading Analysis Cont. Mulge 2 Leading Analysis Cont. Mulge 1 Leading Analysis Cont. Mulge 2 Leading Analysis Cont. Mulge 3 Leading Analysis Cont. Mulge 4 Leading Analysis Cont. Mulge 4 Leading Analysis Cont. Mulge 4 Leading Analysis Cont. Mulge 5 Leading Analysis Cont. Mulge 5 Leading Analysis Cont. Mulge 6 Leading Analysis Cont. Mulge 6 Leading Analysis Cont. Mulge 7 Leading Analysis Cont. Mulge 8 Leading Analysis Cont. Mulge 9 Leading Analysis Cont. Mulge 1 Leading Analysis Cont. Mulg		
Loadleg. Control Margines Mar	LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	
Loadley_CaylorGeneral_Uses_122	LoaMgr_MotCurrRespSV_Cnt_M_u08	0
Restands Lander	LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
	LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Linable_Driftonthalpriss_Driftonthalpr	Rte_Inst_Ap_LoaMgr	· · - ·
Loading-InvitoglisignTrans-Availance_Cnt_u08		
L. Loading-In-Minigrosity-Transcription, Crit. (Job. 1)		
Loading-InterdispSSI2TeaDResig Cht_UBS		
	k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	
	k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	
		0
	k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
K_LosMgr_MridghignSea_Dis_18_22	k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
Loading_NitrightSignResp_Crd_u08	k_LoaMgr_HwTqMtgtnRate_UlspS_f32	
K_LosMgr_Intringtsig2Resp_Cnt_U08		
L. Loukings / Intriducising Fittind C. Cnt. U08		
k_Looklogr_IvrinfignRate_UispS_132 1 k_Looklogr_IvrinfignScac_UispS_2 0 k_Looklogr_IvrinfignScac_UispS_2 1 k_Looklogr_MoldgidpSig2Resp_Cnt_u08 5 k_Looklogr_MoldgidpSig2Resp_Cnt_u08 0 k_Looklogr_MoldgidpSig2Resp_Cnt_u08 0 k_Looklogr_MoldgidpSig2Resp_Cnt_u08 0 k_Looklogr_MoldgidpSig2Resp_Cnt_u08 2 k_Looklogr_MoldgidpSig2Resp_Cnt_u08 0 k_Looklogr_MoldgidpSig2Resp_Cnt_u08 0 k_Looklogr_MoldgidpSig2Resp_Cnt_u08 0 k_Looklogr_MoldgidpSig2Resp_Cnt_u08 0 k_Looklogr_MoldcurridpSig1Resp_Cnt_u08 0 <t< td=""><td></td><td></td></t<>		
K, LouMgr_InvintignSca_Uis_IS2 1 K, LouMgr_InvintignSca_Uis_IS2 1 K, LouMgr_MoNajdipSig1NSignSresp_Cnt_U08 5 K, LouMgr_MoNajdipSig1NSignSresp_Cnt_U08 1 K, LouMgr_MoNajdipSig1NSignSresp_Cnt_U08 0 K, LouMgr_MoNajdipSigNSignSresp_Cnt_U08 2 K, LouMgr_MoNajdipSignSresp_Cnt_U08 2 K, LouMgr_MoNajdipSignSresp_Cnt_U08 0 K, LouMgr_MoNajdipSison_Uis_I32 1 K, LouMgr_MoNajdipSison_Uis_I32 1 K, LouMgr_MoNajdipSison_Uis_I32 1 K, LouMgr_MoldupSison_Uis_I32 1 K, LouMgr_MoldupSison_Uis_I32 1 K, LouMgr_MoldupSison_Uis_I32 0 K, LouMgr_MoldupSison_Uis_I32 0 K, LouMgr_MoldupSison_Uis_I32 0 K, LouMgr_MoldupSison_Uis_I32 0 K, LouMgr_MoldupSison_Uis_I32 1 K, Lo		
LoaMgr MorAgidptSig3Resp_Cnt_U08		
k_LoaMgr_MorAgidptSig3Resp_Cnt_u08 0 k_LoaMgr_MorAgidptSig1FiThd_Cnt_u08 2 k_LoaMgr_MorAgidptSig1FiThd_Cnt_u08 1 k_LoaMgr_MorAgMignRate_UBsp5_f22 1 k_LoaMgr_MorAgMignSca_Uis_f32 1 k_LoaMgr_MorAgMignSca_Uis_f32 1 k_LoaMgr_MorAgSmaAvailResp_Cnt_u08 0 k_LoaMgr_MorCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MorCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MorCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MorCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MorCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MorCurridptSig1Resp_Sf122 1 k_LoaMgr_MorCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_MorCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_MorCurridptSig2Resp_Sf122 1 k_LoaMgr_MorCurridptSig2Resp_Sf22 1 k_LoaMgr_RedodSf3Res_Ulsp_Sf22 1 k_LoaMgr_RedodSf3Res_Ulsp_Sf22 1 k_LoaMgr_RedodSf3Res_Uls_Sf22 1 k_LoaMgr_Pert_DiagcStstvr1inactv_Cnt_igc value 0 target_LoaMgr_Pert_LoaScaD_Loa 0 target_LoaMgr_Pert_I_MorCoal <td< td=""><td>k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08</td><td>5</td></td<>	k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
K_LoaMgr_MotAgldplSigFitTnd_Cnt_u08 2 K_LoaMgr_MotAghtghtRate_UlspS_132 1 K_LoaMgr_MotAghtghtScace_Cnt_u08 0 K_LoaMgr_MotAghtghtScace_Dct_u08 0 K_LoaMgr_MotCuridplSigGResp_Cnt_u08 1 K_LoaMgr_MotCuridplSigGResp_Cnt_u08 1 K_LoaMgr_RedcdSiRate_UlspS_132 1 K_LoaMgr_RedcdSiRate_UlspS_132 1 K_LoaMgr_Pert_DiagcSistivtInactv_Cnt_lgc value 0 target_LoaMgr_Pert_DiagcSistivtInactv_Cnt_lgc value 0 target_LoaMgr_Pert_HwiTqGtSig_Cnt_u08 value 1 target_LoaMgr_Pert_MirrostolSig_Cnt_u08 value 1 target_LoaMgr_Pert_MirrostolSig_Cnt_u08 value 1 <	k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
K_LoaMgr_MotAyMightRate_UlspS_f32 1 K_LoaMgr_MotAyMightSacZencEn_Cnt_igc 0 K_LoaMgr_MotAyMightSacZencEn_Cnt_igc 1 K_LoaMgr_MotAyMightSac_Ulsp_f32 1 K_LoaMgr_MotAyMightSac_Ulsp_f32 0 K_LoaMgr_MotCurridpsIsigPasp_Cnt_u08 0 K_LoaMgr_MotCurridpsIsigPasp_Cnt_u08 0 K_LoaMgr_MotCurridpsIsigPasp_Cnt_u08 0 K_LoaMgr_MotCurridpsisigPitTnd_Cnt_u08 0 K_LoaMgr_MotCurridpsisigPitTnd_Cnt_u08 0 K_LoaMgr_MotCurridpsispac_Ulsp_f32 1 K_LoaMgr_MotCurridpsispac_Ulsp_f32 1 K_LoaMgr_MotCurridpsispac_Ulsp_f32 1 K_LoaMgr_Port_Disp_f35 1 K_LoaMgr_Port_Disp_f35 1 K_LoaMgr_Port_Disp_f35 1 K_LoaMgr_Port_Disp_f35 1 Larget_LoaMgr_Port_Disp_f35 1 Larget_LoaMgr_Port_Disp_f35 1 Larget_LoaMgr_Port_MotCurridpsig_Cnt_u08 value 1 Larget_LoaMgr_Port_MotCurridpsig_Cnt_u08 value 1 Larget_LoaMgr_Port_Disp_f35 1 LoaMgr_Port_NotCurridpsis_Cnt_u08 value 0	k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
K_LoaMgr_MotAgMtgthSca_ZeroEn_Cn_Igc 0 K_LoaMgr_MotAgMtgthSca_Uls_132 1 K_LoaMgr_MotAgMtgthSca_Uls_132 0 K_LoaMgr_MotCurridptSigGResp_Cnt_u08 0 K_LoaMgr_MotCurridptSigGresp_Cnt_u08 0 K_LoaMgr_MotCurridptSigFiltThd_Cnt_u08 0 K_LoaMgr_MotCurridptSigFiltThd_Cnt_u08 0 K_LoaMgr_MotCurridptSigFiltThd_Cnt_u08 0 K_LoaMgr_MotCurridptSigFiltThd_Cnt_u08 1 K_LoaMgr_MotCurridptSigFiltThd_Cnt_u08 1 K_LoaMgr_MotCurridptSigScreen_Cnt_u0g 1 K_LoaMgr_MotCurridptSig_Cnt_u08 1 K_LoaMgr_ReddSiSca_Uls_5122 1 K_LoaMgr_Pert_DiagcStsivtrInack_Cnt_lgc_value 1 LoaMgr_Pert_DiagcStsivtrInack_Cnt_lgc_value 0 Larget_LoaMgr_Pert_DiagcStsivtrInack_Cnt_lgc_value 1 Larget_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value 1 Larget_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value 1 Larget_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value 1 Larget_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value 1 Larget_LoaMgr_Pert_Toalo_Cnt_lgc_value 1 Larget_LoaMgr_Pert_MotCurridpt	k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	
k_LoaMgr_MotAgMiginSca_Ulis_f32 1 k_LoaMgr_MotAgSmaAvaiiResp_Cnt_u08 0 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_MotCurridptSig2.Cnt_u08 1 k_LoaMgr_MotCurridptSig2.Cnt_u08 1 k_LoaMgr_ReddSiSca_Uis_132 1 k_LoaMgr_ReddSiSca_Uis_132 1 k_LoaMgr_Pert_DiagcStsivfTinactv_Cnt_gc value 0 target_LoaMgr_Pert_UascStsivfTinactv_Cnt_gc value 1 target_LoaMgr_Pert_MotSig_Cnt_u08 value 1 target_LoaMgr_Pert_Tinactilent_Cnt_gc value 1 target_LoaMgr_Pert_Tinactilent_Cnt_gc value 1	k_LoaMgr_MotAgMtgtnRate_UlspS_f32	
k_LoaMgr_MotOurdiptSig0Resp_Cnt_u08 0 k_LoaMgr_MotCurrdiptSig0Resp_Cnt_u08 0 k_LoaMgr_MotCurrdiptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurrdiptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurrdiptSig1FIThd_Cnt_u08 0 k_LoaMgr_MotCurrMigInSca_Usp_S_132 1 k_LoaMgr_MotCurrMigInSca_Usp_S_128 1 k_LoaMgr_MotCurrMigInSca_Usp_S_128 1 k_LoaMgr_MotCurrMigInSca_Usp_S_128 1 k_LoaMgr_RedGISSca_Usp_S_128 1 k_LoaMgr_RedGISSca_Usp_S_128 1 k_LoaMgr_Pert_DiagcSisivIrtInactv_Cnt_igc_value 0 target_LoaMgr_Pert_DiagcSisivIrtInactv_Cnt_igc_value 0 target_LoaMgr_Pert_HwTqidptSig_Cnt_u08_value 2 target_LoaMgr_Pert_MotCurridptSig_Cnt_u08_value 1 target_LoaMgr_Pert_MotCurridptSig_Cnt_u08_value 1 target_LoaMgr_Pert_MotCurridptSig_Cnt_u08_value 1 target_LoaMgr_Pert_MotCurridptSig_Cnt_u08_value 1 target_LoaMgr_Pert_TioaOl_Cnt_igc_value 1 target_LoaMgr_Pert_TioaOl_Cnt_igc_value 1 target_LoaMgr_Pert_TioaOl_Cnt_igc_value 1 target_LoaMgr_Pert_TioaOl_C		
k_LoaMgr_MotCurridptSig0Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptRate_UispS_132 1 k_LoaMgr_RedcdSiRate_UispS_32 1 k_LoaMgr_RedcdSiRate_UispS_132 1 k_LoaMgr_Perd_DiagcSistvir1nactv_Cnt_lgc_value 0 target_LoaMgr_Per1_DiagcSistvir2nactv_Cnt_lgc_value 0 target_LoaMgr_Per1_DiagcSistvir2nactv_Cnt_lgc_value 0 target_LoaMgr_Per1_Mr_LoaMgr_Der1_U08 value 2 target_LoaMgr_Per1_Mr_LoaMgr_Der1_U08 value 1 target_LoaMgr_Per1_MrPosliptSig_Cnt_u08 value 1 target_LoaMgr_Per1_ToaDr_Lot_lgc_value 1 target_LoaMgr_Per1_ToaDr_Lot_lgc_value 1 target_LoaMgr_Per1_ToaDr_Lot_lgc_value 1 target_Re_Inst_Ap_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_	_ 0_ 0	
k_LoaMgr_MolCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MolCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MolCurridptSigFilThd_Cnt_u08 0 k_LoaMgr_MolCurrMtgtnRset_UlspS_132 1 k_LoaMgr_MolCurrMtgtnSea_UlspS_132 1 k_LoaMgr_RedcdStRate_UlspS_132 1 k_LoaMgr_RedcdStRate_UlspS_132 1 k_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc_value 0 target_LoaMgr_Perf_DiagcStsIvtrInactv_Cnt_lgc_value 0 target_LoaMgr_Perf_IndeptSig_Cnt_u08_value 2 target_LoaMgr_Perf_MolCurridptSig_Cnt_u08_value 1 target_LoaMgr_Perf_MolCurridptSig_Cnt_u08_value 1 target_LoaMgr_Perf_MolCurridptSig_Cnt_u08_value 1 target_LoaMgr_Perf_SteerMod_Cnt_enum_value 2 target_LoaMgr_Perf_ToaDi_Cnt_lgc_value 1 target_LoaMgr_Perf_ToaDi_Cnt_lgc_value 1 target_LoaMgr_Perf_ToaDi_Cnt_lgc_value 1 target_Re_Inst_Ap_LoaMgr_LoaMgr_Perf_DiagcStsIvtrInactv_Cnt_lgc target_LoaMgr_Perf_DiagcStsIvtrInactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Perf_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Perf_HwTqLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Perf_HvTqLoaMtgtnEn_Cnt_lgc ta		
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target_LoaMgr_Per1_DiagcStstvtr2Inactv_Cnt_lgc.value 2 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc 1 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_LoaMgr_Per1_HwTqLoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_LoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_Inactv_Dert_Inactv_		
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target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_InvTLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_TloaAvai_Cnt_lgc.value 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_NotAgLoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_NotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_ivtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u(08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	✓
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	✓
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	2	2	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	✓
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	✓
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	✓
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
SwBasMtgtn	1	SwBasMtgtn	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	✓
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	-

Test Step 2.21 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr

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Input Value		
1		
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1		
1		
1		
1		
1 1 1		
1 1 1 0		
1 1 1 0 0		
1 1 1 0 0 2		
1 1 1 0 0 2 1		
1 1 1 0 0 2 1 0		
1 1 1 0 0 2 1		
1 1 1 0 0 2 1 0 1 2 0		
1 1 1 0 0 0 2 1 0 1 2 0 1 target_LoaMgr_Per1_DiagcStslvtr1lnactv_Cr		
1 1 1 0 0 0 2 1 0 1 2 0 1 target_LoaMgr_Per1_DiagcStslvtr1lnactv_Cr target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cr		
1 1 1 0 0 0 2 1 1 2 0 1 target_LoaMgr_Per1_DiagcStslvtr1lnactv_Cr target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cr target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	tt_lgc	
1 1 1 0 0 2 1 1 0 1 target_LoaMgr_Per1_DiagcStslvtr1lnactv_Cr target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cr target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt	tt_lgc _lgc	
1 1 1 0 0 2 1 1 1 1 1 1 1 1 1 1 1 1 1 1	tt_lgc _lgc	
1 1 1 0 0 2 1 1 1 1 1 1 1 1 1 1 1 1 1 1	tt_lgc _lgc	
1 1 1 0 0 0 2 1 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	tt_lgc _lgc c	
1 1 1 0 0 0 2 1 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgtarget_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	it_lgc c t_lgc	
1 1 1 0 0 0 2 1 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	tt_lgc c t_lgc 8	
1 1 1 1 0 0 0 2 1 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_HwTqloptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_Incompleted to the complete of the complete	it_lgc c t_lgc b8 nt_lgc	
1 1 1 1 0 0 0 2 1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cr target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u0target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u0target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	it_lgc c t_lgc b8 nt_lgc	
1 1 1 1 0 0 0 2 1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cr target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cr target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0t_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0t_t	tt_lgc c t_lgc b8 nt_lgc	
1 1 1 1 0 0 0 2 1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cr target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cr target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IdoASt_State_enum target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrloptSig_Cnt_u0t_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0t_target_LoaMgr_Per1_SteerMod_Cnt_enum	tt_lgc c t_lgc b8 nt_lgc	
1 1 1 1 0 0 0 2 1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cr target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cr target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LOASc_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrloptSig_Cnt_u08target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc	tt_lgc c t_lgc b8 nt_lgc	Result
1 1 1 1 0 0 0 2 1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cr target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cr target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0t_target_LoaMgr_Per1_MotCurrloaMtgtnEn_C target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0t_target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0t_target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc	it_lgc c t_lgc 3	
1 1 1 1 0 0 0 2 1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cr target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cr target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1	tt_lgc lgc c t_lgc 08 nt_lgc 3 Expected Value 1	Result
1 1 1 1 0 0 0 2 1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cr target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cr target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct_target_LoaMgr_Per1_TloaAvail_Cnt_lgc_target_LoaMgr_Per1_TloaDi_Cnt_lgc_target_LoaMgr_Per1_TloaDi_Cnt_lgc_Actual_Value_1 1 0	t_lgc c t_lgc 08 nt_lgc 3 Expected Value 1 1 0	<i>y</i>
1 1 1 1 0 0 0 2 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cr target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cr target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct_target_LoaMgr_Per1_TloaAvail_Cnt_lgc_target_LoaMgr_Per1_TloaDi_Cnt_lgc_target_LoaMg	t_lgc c t_lgc 08 nt_lgc 3 Expected Value 1 1 0 0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
1 1 1 1 0 0 0 2 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cr target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt, target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0t target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0t target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 0 1	Ligc c t_igc c t_igc 88 nt_igc 3 Expected Value 1 1 0 0 1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
1 1 1 1 0 0 0 2 1 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 0 1	Ligc c c t_igc 8 nt_igc 3 Expected Value 1 0 0 1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
1 1 1 1 1 0 0 0 2 1 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 0 1	Ligc c c Ligc 88 nt_igc 8 Expected Value 1 0 0 1	· · · · · · · · · · · · · · · · · · ·
1 1 1 1 1 0 0 0 2 1 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_gtarget_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrloptdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrloptdptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 0 1 0 1	Ligc c c t_igc 88 nt_igc 3 Expected Value 1 0 0 1 0 0 2	· · · · · · · · · · · · · · · · · · ·
1 1 1 1 1 0 0 0 2 1 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0t_target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0t_target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0t_target_LoaMgr_Per1_SteerMod_Cnt_enum_target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 0 1 0 2 500	L Igc	· · · · · · · · · · · · · · · · · · ·
1 1 1 1 1 0 0 0 2 1 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_gtarget_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrloptdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrloptdptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 0 1 0 1	Ligc c c t_igc 88 nt_igc 3 Expected Value 1 0 0 1 0 0 2	· · · · · · · · · · · · · · · · · · ·
	1 0 1 1 1 2 2 2 2 2 1 1 0 0 0 1 1 1 1 1	1

LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc





Name	Actual Value	Expected Value	Result
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	•

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
SwBasMtgtn	1	SwBasMtgtn	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.22 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_Igc	0

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Nome	Innut Value		
Name	Input Value		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	5		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08			
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target LoaMgr Per1 MotCurrldptSig Cnt u08.value	0		
	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt	_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt	_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn	Cnt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enur		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaAvail Cnt Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
			Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	V
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_IvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	2	2	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	•
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	•
LoaMgr MotCurrldptSigSV Cnt M u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	
	0	0	-
LoaMgr_TloaAvail_Cnt_M_lgc			
LoaMgr_TloaDi_Cnt_M_lgc	1	1	-
LoaMgr_VitgMdSrcSV_Cnt_M_u08	1 (472, 474)	1 (472, 474)	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	(1, 1)	{0, 0, 0, 0, 0}	×
	111 11	{1, 1}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}		✓
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	
	500	1 ± 0.001	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500		·
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	500	1 ± 0.001	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	500 1 0	1 ± 0.001 0	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	500 1 0 0	1 ± 0.001 0 0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	•
AssignScale	1	AssignScale	1	•
SwBasMtgtn	1	SwBasMtgtn	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.23 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrivtrMtgtnScaZeroEn Cnt Igc	0
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	0
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	2
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
g (0000010000_010_102	





0		
2 1 0 1 2 0		
1 0 1 2 0		
0 1 2 0		
1 2 0		
2 0 1		
0		
1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv		
	_Cnt_lgc	
target_LoaMgr_Per1_DiagcStslvtr2Inactv	_Cnt_lgc	
target_LoaMgr_Per1_HwTqldptSig_Cnt_u	108	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_	Cnt_lgc	
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cn	t_lgc	
target_LoaMgr_Per1_LOASt_State_enum	 	
target LoaMgr Per1 LoaScaDi Cnt Igc		
target LoaMgr Per1 MotAgLoaMtgtnEn	Cnt Igc	
target LoaMgr Per1 MtrPosldptSig Cnt	u08	
Actual Value	Expected Value	Result
0		
		✓
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	target_LoaMgr_Per1_HwTqLoaMtgtnEn_target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnttarget_LoaMgr_Per1_LOASt_State_enumtarget_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotAgLoaMtgtnEn_target_LoaMgr_Per1_MotCurrIdptSig_Cnttarget_LoaMgr_Per1_MotCurrLoaMtgtnEntarget_LoaMgr_Per1_MtrPosIdptSig_Cnttarget_LoaMgr_Per1_SteerMod_Cnt_enutarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_MotQLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_MtPosIdptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaDi_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0

Test Step Call Trace ✓						
Actual Function	Count	Expected Function	Count	Result		
CalclvtrState	2	CalclvtrState	2	~		
LatchInputs	1	LatchInputs	1	✓		
RqstRespConds	1	RqstRespConds	1	•		
RqstHwTqResp	1	RqstHwTqResp	1	~		
ArbResp	1	ArbResp	1	~		
ChooseFinalResp	1	ChooseFinalResp	1	~		
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~		
AssignScale	1	AssignScale	1	~		
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~		
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•		
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~		
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~		



Test Step 2.24 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnNcaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	0
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32 k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0 1
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k LoaMgr RedcdStRate UlspS f32	1
k_LoaMgr_RedcdStSca_Uls_f32	0.123400003
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Perl_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Perl_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaD1_Cht_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
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Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	✓
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	✓
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	1	1	~
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0.123400003	0.123400003	✓
LoaMgr_MaxMtgtnResp_Cnt_M_u08	1	1	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	✓
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	1	1	✓
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1	1 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0.123400003	0.123400003 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	1	1	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
CalclvtrState	2	CalclvtrState	2	~		
LatchInputs	1	LatchInputs	1	✓		
RqstRespConds	1	RqstRespConds	1	•		
RqstHwTqResp	1	RqstHwTqResp	1	✓		
ArbResp	1	ArbResp	1	✓		
ChooseFinalResp	1	ChooseFinalResp	1	✓		
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~		
AssignScale	1	AssignScale	1	•		
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~		
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓		
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•		
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	✓		