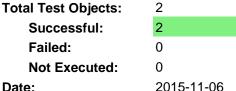
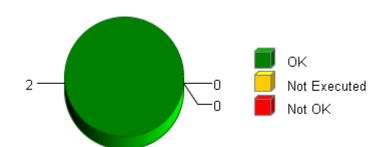


Summary

Overall Test Object Results (including Coverage)



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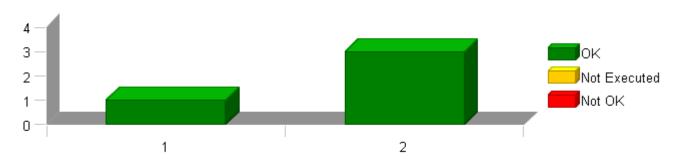
Selected Project Items

Test Collection "CBD_UnitTest"

Used Test Environments

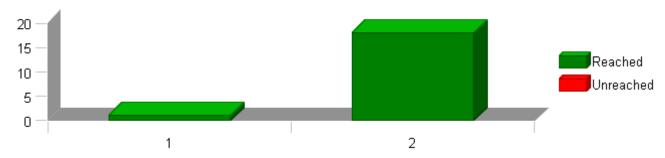
TI TMS 570 PLS UDE (Default)

Test Case Results for Each Test Object (without Coverage)



The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

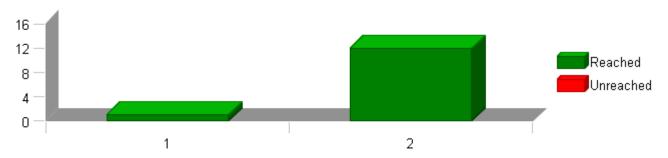
Statement (C0) Coverage: Total Statements for Each Test Object



The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

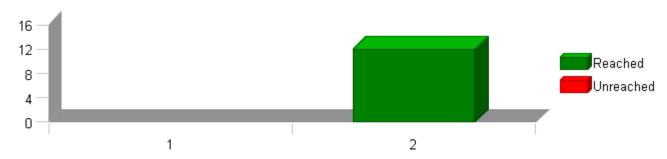


Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

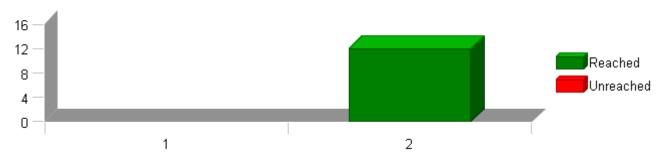
Decision Coverage: Total Decision Outcomes for Each Test Object



The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.

MC/DC Coverage: Total Condition Combinations for Each Test Object

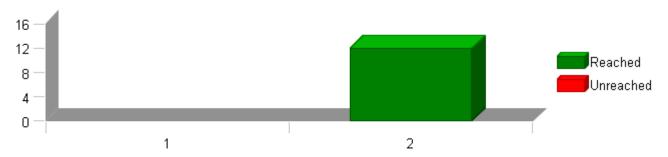


The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.



MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

TEST OVERVIEW REPORT

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Test Object List

Project Ap_ePWM_1

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	DC	MC/DC	MCC	Test Cases Resu	ılt
	Ap_ePWM_1	100 %	100 %	100 %	100 %	100 %	4 of 4 passed	•
	CBD_UnitTest	100 %	100 %	100 %	100 %	100 %	4 of 4 passed	•
	ePWM_1	100 %	100 %	100 %	100 %	100 %	4 of 4 passed	•
1	ePWM_Init1	100 %	100 %	-	-	-	1 of 1 passed	•
2	ePWM_Per1	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	•

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Project Ap_ePWM_1 Module ePWM_1 **Test Object** ePWM_Init1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

ePWM_Init1

Total Testcases	1	
Successful	1	✓
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\9BXX_ePWM_Up
Configuration File	D:\Synergy_Work_Area\9BXX_ePWM_Up\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\ePWM_Up\src\ePWM.c
Compiler Options	-DSTATIC= -D_DATA_ACCESS= -Dinline= -Dconst= -I\$(PROJECTROOT)\ePWM_Up\utp\contract\Ap_ePWM2 -I\$(PROJECTROOT)\ePWM_Up\utp\contract\Ap_ePWM2 -I\$(PROJECTROOT)\extrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(PROJECTROOT)\ePWM_Up\include -I\$(Compiler Install Path)\include

Name	Text
Module 'ePWM_1'	UNIT TEST DESCRIPTION Name of Tester:Jayesh Jahagirdar Code File(s) Under Test:ePWM.c Code File(s) Version:2 Module Design Document:ePWM MDD.docx Module Design Document Version:2 Data Dictionary Version:2 Unit Test Plan Version:1 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):564 Total RAM Used (Bytes):28 Total CALS Used (Bytes):6 Special Test Requirements: Test Date:11-06-2015 Comments:"NOTE1: Inline function defined in ""GlobalMacro.h"" are not unit tested. NOTE2: ""CBD_Sandbox_dbg.map"" map file is embedded for reference."

Attributes			
Name	Value		
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5		
Float Precision	9		
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj		
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src		
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd		
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl		
Target Install Path	\$(ProgramFiles)\pls\UDE 4.0		
Timer Enabled	false		
Timer Prescale	0		
Timer Resolution	1		
Timer Unit	Cycles		
UDE Config File	<pre>\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg</pre>		

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Workenace File

ePWM_Init1

D:\Synergy_Work_Area\9BXX_ePWM_Up\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



Test Case 1: Boundary Test

Specification

Performance metrics (With "None" Instrumentation and "WithPS" environment)

309.00 Cycles TS 1.1 TS 1.2 TS 1.3 TS 1.4 TS 1.5 TS 1.6 TS 1.7 TS 1.8

Vector Description: Description

TS1.1All min
TS1.2All max
TS1.3k_PwmDeadBand_Cnt_u16=min
TS1.4k_PwmDeadBand_Cnt_u16=max
TS1.5k_PwmDeadBand_Cnt_u16=pos/Default
TS1.6k_PwmRelay_Cnt_u16=min
TS1.7k_PwmRelay_Cnt_u16=max
TS1.8k_PwmRelay_Cnt_u16=pos/default

TS1.8k_PwmRelay_Cnt_u16=pos/defa	uit		
Test Step 1.1 (Repeat Count = 1)			V
Name	Input Value		
ePWM1_temp	target_ePWM1_temp		
ePWM2_temp	target_ePWM2_temp		
ePWM3_temp	target_ePWM3_temp		
ePWM4_temp	target_ePWM4_temp		
ePWM7_temp	target_ePWM7_temp		
k_PwmDeadBand_Cnt_u16	0		
k_PwmRelay_Cnt_u16	0		
target_ePWM1_temp.DBCTL	11		
target_ePWM2_temp.DBCTL	11		
target_ePWM3_temp.DBCTL	11		
Name	Actual Value	Expected Value	Result
target_ePWM1_temp.TBCTL	8196	8196	, 100 di
target ePWM1 temp.TBPHS	0	0	-
target_ePWM1_temp.TBPRD	65535	65535	
target_ePWM1_temp.CMPCTL	0	0	
target_ePWM1_temp.CMPA	2499	2499	
target ePWM1 temp.AQCTLA	289	289	
target_ePWM1_temp.CMPB	2499	2499	
target ePWM1 temp.DBCTL	8	8	
target_ePWM1_temp.AQCSFRC	5	5	
	0	0	
target_ePWM1_temp.DBFED	0	0	
target_ePWM1_temp.DBRED	4095	4095	
target_ePWM1_temp.TZCTL	0	0	
target_ePWM1_temp.ETSEL		0	
target_ePWM1_temp.PCCTL	0		
target_ePWM2_temp.TBCTL	8196	8196	
target_ePWM2_temp.TBPHS	0	0	
target_ePWM2_temp.TBPRD	65535	65535	
target_ePWM2_temp.CMPCTL	0	0	_
target_ePWM2_temp.CMPA	2499	2499	
target_ePWM2_temp.AQCTLA	288	288	~
target_ePWM2_temp.CMPB	2499	2499	
target_ePWM2_temp.DBCTL	8	8	✓
target_ePWM2_temp.AQCSFRC	5	5	~
target_ePWM2_temp.DBFED	0	0	✓
target_ePWM2_temp.DBRED	0	0	~
target_ePWM2_temp.TZCTL	4095	4095	~
target_ePWM2_temp.ETSEL	0	0	~
target_ePWM2_temp.PCCTL	0	0	~
target_ePWM3_temp.TBCTL	8196	8196	~
target_ePWM3_temp.TBPHS	0	0	•
target_ePWM3_temp.TBPRD	65535	65535	~
target_ePWM3_temp.CMPCTL	0	0	✓
target_ePWM3_temp.CMPA	2499	2499	~
target_ePWM3_temp.AQCTLA	288	288	~
target_ePWM3_temp.CMPB	2499	2499	~
target_ePWM3_temp.DBCTL	8	8	~
target_ePWM3_temp.AQCSFRC	5	5	~
target_ePWM3_temp.DBFED	0	0	✓
target_ePWM3_temp.DBRED	0	0	✓
target_ePWM3_temp.TZCTL	4095	4095	✓

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Name	Actual Value	Expected Value	Result
target_ePWM3_temp.ETSEL	0	0	~
target_ePWM3_temp.PCCTL	0	0	✓
target_ePWM4_temp.TBCTL	4	4	✓
target_ePWM4_temp.TBPHS	0	0	✓
target_ePWM4_temp.TBPRD	65535	65535	✓
target_ePWM4_temp.CMPCTL	0	0	✓
target_ePWM4_temp.CMPA	2499	2499	✓
target_ePWM4_temp.AQCTLA	0	0	✓
target_ePWM4_temp.CMPB	65535	65535	✓
target_ePWM4_temp.DBCTL	0	0	✓
target_ePWM4_temp.TZCTL	4095	4095	✓
target_ePWM4_temp.ETSEL	60416	60416	✓
target_ePWM4_temp.ETPS	4352	4352	✓
target_ePWM4_temp.PCCTL	0	0	✓
target_ePWM7_temp.TBCTL	4	4	✓
target_ePWM7_temp.TBPHS	0	0	✓
target_ePWM7_temp.TBPRD	65535	65535	✓
target_ePWM7_temp.CMPCTL	0	0	✓
target_ePWM7_temp.CMPA	0	0	✓
target_ePWM7_temp.AQCTLB	33	33	✓
target_ePWM7_temp.DBCTL	0	0	✓
target_ePWM7_temp.TZCTL	4095	4095	✓
target_ePWM7_temp.ETSEL	0	0	✓
target_ePWM7_temp.PCCTL	0	0	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.2 (Repeat Count = 1)			
Name	Input Value		
ePWM1_temp	target_ePWM1_temp		
ePWM2_temp	target_ePWM2_temp		
ePWM3_temp	target_ePWM3_temp		
ePWM4_temp	target_ePWM4_temp		
ePWM7_temp	target_ePWM7_temp		
k_PwmDeadBand_Cnt_u16	1024		
k_PwmRelay_Cnt_u16	65535		
target_ePWM1_temp.DBCTL	11		
target_ePWM2_temp.DBCTL	11		
target_ePWM3_temp.DBCTL	11		
Name	Actual Value	Expected Value	Result
target_ePWM1_temp.TBCTL	8196	8196	•
target_ePWM1_temp.TBPHS	0	0	•
target_ePWM1_temp.TBPRD	65535	65535	•
target_ePWM1_temp.CMPCTL	0	0	•
target_ePWM1_temp.CMPA	2499	2499	•
target_ePWM1_temp.AQCTLA	289	289	•
target_ePWM1_temp.CMPB	2499	2499	•
target_ePWM1_temp.DBCTL	8	8	•
target_ePWM1_temp.AQCSFRC	5	5	•
target_ePWM1_temp.DBFED	1024	1024	•
target_ePWM1_temp.DBRED	1024	1024	•
target_ePWM1_temp.TZCTL	4095	4095	•
target_ePWM1_temp.ETSEL	0	0	•
target_ePWM1_temp.PCCTL	0	0	•
target_ePWM2_temp.TBCTL	8196	8196	•
target_ePWM2_temp.TBPHS	0	0	•
target_ePWM2_temp.TBPRD	65535	65535	•
target_ePWM2_temp.CMPCTL	0	0	•
target_ePWM2_temp.CMPA	2499	2499	•
target_ePWM2_temp.AQCTLA	288	288	•
target_ePWM2_temp.CMPB	2499	2499	•
target_ePWM2_temp.DBCTL	8	8	•
target_ePWM2_temp.AQCSFRC	5	5	•
target_ePWM2_temp.DBFED	1024	1024	•
target_ePWM2_temp.DBRED	1024	1024	•
target_ePWM2_temp.TZCTL	4095	4095	•
target_ePWM2_temp.ETSEL	0	0	•
target_ePWM2_temp.PCCTL	0	0	•

ePWM_Init1

target_ePWM7_temp.PCCTL

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Actual Value **Expected Value** target_ePWM3_temp.TBCTL target_ePWM3_temp.TBPHS target_ePWM3_temp.TBPRD target_ePWM3_temp.CMPCTL target_ePWM3_temp.CMPA target_ePWM3_temp.AQCTLA target_ePWM3_temp.CMPB $target_ePWM3_temp.DBCTL$ target_ePWM3_temp.AQCSFRC target_ePWM3_temp.DBFED target_ePWM3_temp.DBRED target_ePWM3_temp.TZCTL target_ePWM3_temp.ETSEL target_ePWM3_temp.PCCTL target_ePWM4_temp.TBCTL target_ePWM4_temp.TBPHS target_ePWM4_temp.TBPRD target_ePWM4_temp.CMPCTL target_ePWM4_temp.CMPA target_ePWM4_temp.AQCTLA target_ePWM4_temp.CMPB target_ePWM4_temp.DBCTL target_ePWM4_temp.TZCTL target_ePWM4_temp.ETSEL target_ePWM4_temp.ETPS target_ePWM4_temp.PCCTL target_ePWM7_temp.TBCTL target_ePWM7_temp.TBPHS target_ePWM7_temp.TBPRD target_ePWM7_temp.CMPCTL target_ePWM7_temp.CMPA target_ePWM7_temp.AQCTLB target ePWM7 temp.DBCTL target_ePWM7_temp.TZCTL target_ePWM7_temp.ETSEL

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.3 (Repeat Count = 1)			✓
Name	Input Value		
ePWM1_temp	target_ePWM1_temp		
ePWM2_temp	target_ePWM2_temp		
ePWM3_temp	target_ePWM3_temp		
ePWM4_temp	target_ePWM4_temp		
ePWM7_temp	target_ePWM7_temp		
k_PwmDeadBand_Cnt_u16	0		
k_PwmRelay_Cnt_u16	1025		
target_ePWM1_temp.DBCTL	11		
target_ePWM2_temp.DBCTL	11		
target_ePWM3_temp.DBCTL	11		
Name	Actual Value	Expected Value	Result
target_ePWM1_temp.TBCTL	8196	8196	~
target_ePWM1_temp.TBPHS	0	0	•
target_ePWM1_temp.TBPRD	65535	65535	~
target_ePWM1_temp.CMPCTL	0	0	•
target_ePWM1_temp.CMPA	2499	2499	~
target_ePWM1_temp.AQCTLA	289	289	•
target_ePWM1_temp.CMPB	2499	2499	~
target_ePWM1_temp.DBCTL	8	8	•
target_ePWM1_temp.AQCSFRC	5	5	~
target_ePWM1_temp.DBFED	0	0	•
target_ePWM1_temp.DBRED	0	0	~
target_ePWM1_temp.TZCTL	4095	4095	•
target_ePWM1_temp.ETSEL	0	0	~
target_ePWM1_temp.PCCTL	0	0	~
target_ePWM2_temp.TBCTL	8196	8196	~
target_ePWM2_temp.TBPHS	0	0	~

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target_EMVAZ_tempc (MFCPL 100 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	Name	Actual Value	Expected Value	Result
target_ePMAZ_temp.ADCTLA 2499 2499	target_ePWM2_temp.TBPRD	65535	65535	✓
Ingregit_ePMAZ_lemp DACTIA 288 288 2499 2	target_ePWM2_temp.CMPCTL	0	0	✓
Insignal_PPMIX_temp_DBCTL	target_ePWM2_temp.CMPA	2499	2499	✓
Integrie_PMWAZ_temp_DBCTL	target_ePWM2_temp.AQCTLA	288	288	✓
target_ePMMZ_temp_DBFED 5 target_ePMMZ_temp_DBFED 0 target_ePMMZ_temp_DBFED 0 target_ePMMZ_temp_DBFED 0 target_ePMMZ_temp_ETSEL 0 0 0 target_ePMMZ_temp_ETSEL 0 0 0 target_ePMMZ_temp_DBFED 0 target_ePMMZ_temp_DBFCTL 8196 target_ePMMS_temp_TBFNS 0 target_ePMMS_temp_DBFCTL 0 0 0 target_ePMMS_temp_CMPCTL 0 1 0 target_ePMMS_temp_CMPCTL 0 target_ePMMS_temp_CMPCTL 0 target_ePMMS_temp_CMPCTL 2499 target_ePMMS_temp_ACCTLA 288 target_ePMMS_temp_ACCSFRC 5 target_ePMMS_temp_ACCTL 0 target_ePMMS_temp_DBECD 0 target_ePMMS_temp_DBECD 0 target_ePMMS_temp_DECTL 0 target_ePMMS_temp_DECTL 0 target_ePMMS_temp_DECTL 0 target_ePMMS_temp_DECTL <td< td=""><td>target_ePWM2_temp.CMPB</td><td>2499</td><td>2499</td><td>✓</td></td<>	target_ePWM2_temp.CMPB	2499	2499	✓
larget_PPMMZ_temp_DBRED	target_ePWM2_temp.DBCTL	8	8	✓
larget_ePMMZ_temp_DBRED	target_ePWM2_temp.AQCSFRC	5	5	✓
larget_ePWMZ_temp.TZCTL	target_ePWM2_temp.DBFED	0	0	✓
target_ePWM2_temp ETSEL 0 0 0 target_ePWM2_temp ETCTL 8196 8196 V target_ePWM3_temp TBCTL 8196 8196 V target_ePWM3_temp TBFHS 0 0 0 target_ePWM3_temp TBFHD 65535 65535 65535 target_ePWM3_temp CMPCTL 0 0 0 target_ePWM3_temp CMPCTL 2499 2499 2491 target_ePWM3_temp DBCTL 8 8 8 target_ePWM3_temp DBCTL 8 8 8 target_ePWM3_temp DBCTL 9 0 0 target_ePWM3_temp DBCTL 8 8 8 target_ePWM3_temp DBCTL 0 0 0 target_ePWM3_temp DBCTD 0 0 0 target_ePWM3_temp DBCTD 0 0 0 target_ePWM3_temp TSTEL 0 0 0 target_ePWM3_temp TSTEL 0 0 0 target_ePWM4_temp TSCTL 4 4 0 <td< td=""><td>target_ePWM2_temp.DBRED</td><td>0</td><td>0</td><td>✓</td></td<>	target_ePWM2_temp.DBRED	0	0	✓
Barget_ePWM2_temp.PCCTL S196 S196 S196 Valarget_ePWM3_temp.TBCTL S196 S196 Valarget_ePWM3_temp.TBCTL S196 S196 Valarget_ePWM3_temp.TBFNB O O O O O O O O O O O O O O O O O O	target_ePWM2_temp.TZCTL	4095	4095	✓
target_ePWM3_temp.TBCTL 8196 8196 target_ePWM3_temp.TBPHS 0 0 target_ePWM3_temp.TBPTD 65535 65535 target_ePWM3_temp.CMPCTL 0 0 target_ePWM3_temp.ACCTLA 288 288 288 target_ePWM3_temp.ACCTLA 8 8 8 target_ePWM3_temp.DBCTL 8 8 8 target_ePWM3_temp.ACCSFRC 5 5 1 target_ePWM3_temp.DBCED 0 0 target_ePWM3_temp.DERED 0 0 target_ePWM3_temp.ETSEL 0 0 target_ePWM3_temp.ETSEL 0 0 target_ePWM4_temp.TBCTL 4 4 target_ePWM4_temp.TBCTL 4 4 target_ePWM4_temp.EMCTL 0 0 target_ePWM4_temp.EMCTL 0 0 target_ePWM4_temp.EMCTL 0 0	target_ePWM2_temp.ETSEL	0	0	✓
larget_ePWM3_temp.TBPHS	target_ePWM2_temp.PCCTL	0	0	✓
target_ePWM3_temp.TBPRD 65535 65535 1 target_ePWM3_temp.CMPCTL 0 0 0 target_ePWM3_temp.CMPA 2499 2499 249 target_ePWM3_temp.ACTLA 288 288 288 target_ePWM3_temp.DBCTL 8 8 8 target_ePWM3_temp.DBCTL 8 8 8 target_ePWM3_temp.DBRED 0 0 0 target_ePWM3_temp.DBRED 0 0 0 target_ePWM3_temp.DBRED 0 0 0 target_ePWM3_temp.TBCTL 4095 4095 0 target_ePWM3_temp.TBCTL 0 0 0 target_ePWM4_temp.TBCTL 4 4 4 target_ePWM4_temp.TBCTL 4 4 4 target_ePWM4_temp.TBCTL 0 0 0 target_ePWM4_temp.CMCTL 0 0 0 target_ePWM4_temp.CMCTL 0 0 0 target_ePWM4_temp.CMCTL 0 0 0 target	target_ePWM3_temp.TBCTL	8196	8196	~
larget_ePWM3_temp.CMPCTL	target_ePWM3_temp.TBPHS	0	0	✓
target_ePWM3_temp.CMPA 2499 2499 1 target_ePWM3_temp.ACCTLA 288 288 2499	target_ePWM3_temp.TBPRD	65535	65535	✓
larget_ePWM3_temp.AQCTLA	target_ePWM3_temp.CMPCTL	0	0	✓
target_ePWM3_temp_DBCTL 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8	target_ePWM3_temp.CMPA	2499	2499	~
target_ePWM3_temp_DBCTL 8 8 target_ePWM3_temp_AQCSFRC 5 5 target_ePWM3_temp_DBFED 0 0 target_ePWM3_temp_DBRED 0 0 target_ePWM3_temp_TZCTL 4095 4095 target_ePWM3_temp_TETSEL 0 0 target_ePWM4_temp_TBCTL 4 4 target_ePWM4_temp_TBPHS 0 0 target_ePWM4_temp_TBPBD 65535 65535 target_ePWM4_temp_CMPCTL 0 0 target_ePWM4_temp_CMPA 2499 2499 target_ePWM4_temp_CMPA 0 0 target_ePWM4_temp_CMPB 65535 65535 target_ePWM4_temp_DBCTL 0 0 target_ePWM4_temp_DBCTL 0 0 target_ePWM4_temp_ETSEL 60416 60416 target_ePWM4_temp_ETSEL 4352 4352 target_ePWM7_temp_TBPHS 0 0 target_ePWM7_temp_TBPHS 0 0 target_ePWM7_temp_TBPCD 65535 65535 target_ePWM7	target_ePWM3_temp.AQCTLA	288	288	✓
target_PPWMS_temp_AQCSFRC 5 5 target_ePPWMS_temp_DBFED 0 0 target_ePWMS_temp_DBFED 0 0 target_ePWMS_temp_DERCDL 4095 4095 target_ePWMS_temp_ETSEL 0 0 target_ePWMS_temp_DCCTL 0 0 target_ePWM4_temp_TBCTL 4 4 target_ePWM4_temp_TBPRD 65535 65535 target_ePWM4_temp_CMPCTL 0 0 target_ePWM4_temp_CMPA 2499 2499 target_ePWM4_temp_AQCTLA 0 0 target_ePWM4_temp_DBCTL 0 0 target_ePWM4_temp_DBCTL 0 0 target_ePWM4_temp_DBCTL 0 0 target_ePWM4_temp_ETSEL 60416 60416 target_ePWM4_temp_ETSEL 60416 60416 target_ePWM7_temp_TBCTL 0 0 target_ePWM7_temp_TBCTL 0 0 target_ePWM7_temp_TBCND 65535 65535 target_ePWM7_temp_TBCND 0 0 target_ePWM7_t	target_ePWM3_temp.CMPB	2499	2499	✓
target_ePWM3_temp_DBFED 0 0	target_ePWM3_temp.DBCTL	8	8	✓
target_ePWM3_temp_DBRED 0 0 washing 4095 washing <	target_ePWM3_temp.AQCSFRC	5	5	✓
target_ePWM3_temp_TZCTL 4095 4095 target_ePWM3_temp_ETSEL 0 0 target_ePWM3_temp_PCCTL 0 0 target_ePWM4_temp_TBCTL 4 4 target_ePWM4_temp_TBPRD 65535 65535 target_ePWM4_temp_CMPCTL 0 0 target_ePWM4_temp_CMPCTL 0 0 target_ePWM4_temp_CMPCA 2499 2499 target_ePWM4_temp_CMPB 65535 65535 target_ePWM4_temp_DBCTL 0 0 target_ePWM4_temp_DBCTL 0 0 target_ePWM4_temp_ETSEL 60416 60416 target_ePWM4_temp_ETSEL 60416 60416 target_ePWM7_temp_ETBCTL 0 0 target_ePWM7_temp_ETBCTL 4 4 target_ePWM7_temp_TBCTL 0 0 target_ePWM7_temp_TBCTL 0 0 target_ePWM7_temp_TBCTL 4 4 target_ePWM7_temp_TBCD 65535 65535 target_ePWM7_temp_CMPCTL 0 0 target_e	target_ePWM3_temp.DBFED	0	0	✓
target_ePWM3_temp_ECTSEL 0 0 varget_ePWM4_temp_ECTL 0 0 varget_ePWM4_temp_TBCTL 4 4 varget_ePWM4_temp_TBCTL 4 4 varget_ePWM4_temp_TBCTL 0 0 0 varget_ePWM4_temp_TBPRD 0 0 0 varget_ePWM4_temp_CMPCTL 0 0 0 0 varget_ePWM4_temp_CMPCTL 0 0 0 0 varget_ePWM4_temp_CMPC 0 0 0 0 varget_ePWM4_temp_CMPA 0 <t< td=""><td>target_ePWM3_temp.DBRED</td><td>0</td><td>0</td><td>✓</td></t<>	target_ePWM3_temp.DBRED	0	0	✓
target_ePWM3_temp.PCCTL 0 0 varget_ePWM4_temp.TBCTL 4 4 4 varget_ePWM4_temp.TBCTL 4 4 4 varget_ePWM4_temp.TBCTL 0 0 varget_ePWM4_temp.TBCTL 0 0 varget_ePWM4_temp.CMPCTL 0 0 varget_ePWM4_temp.CMPA 2499 2499 varget_ePWM4_temp.CMPA 2499 varget_ePWM4_temp.CMPB 0 0 varget_ePWM4_temp.CMPB 0 0 varget_ePWM4_temp.DBCTL 0 0 varget_ePWM4_temp.DBCTL 0 0 varget_ePWM4_temp.DBCTL 0 0 varget_ePWM4_temp.ETSEL 60416 60416 0 varget_ePWM4_temp.ETSEL 60416 60416 varget_ePWM4_temp.PCCTL 0 0 varget_ePWM4_temp.ETSEL 4 4 varget_ePWM7_temp.TBCTL 4 4 varget_ePWM7_temp.TBCTL 4 4 varget_ePWM7_temp.TBCTL 0 0 varget_ePWM7_temp.CMCTL 0 0 varget_ePWM7_temp.CMCCTL 0 0 varget_ePWM7_temp.CMCCTL varget_ePWM7_temp.CMCCTL 0 0 varget_ePWM7_temp.AQCTLB 33 33 33	target ePWM3 temp.TZCTL	4095	4095	✓
target_ePWM4_temp.TBCTL 4 4 V target_ePWM4_temp.TBPHS 0 0 V target_ePWM4_temp.TBPRD 65535 65535 V target_ePWM4_temp.CMPCTL 0 0 V target_ePWM4_temp.CMPA 2499 2499 V target_ePWM4_temp.AQCTLA 0 0 V target_ePWM4_temp.CMPB 65535 65535 65535 target_ePWM4_temp.DBCTL 0 0 V target_ePWM4_temp.DBCTL 4095 4095 V target_ePWM4_temp.ETSEL 60416 60416 V target_ePWM4_temp.ETPS 4352 4352 V target_ePWM7_temp.TBCTL 4 4 V target_ePWM7_temp.TBCTL 4 4 V target_ePWM7_temp.TBPRD 65535 65535 65535 V target_ePWM7_temp.CMPCTL 0 0 V V target_ePWM7_temp.CMPCTL 0 0 V V V V V <	target ePWM3 temp.ETSEL	0	0	✓
target_ePWM4_temp.TBPHS 0 0 varget_ePWM4_temp.TBPRD target_ePWM4_temp.CMPCTL 0 0 varget_ePWM4_temp.CMPCTL target_ePWM4_temp.CMPA 2499 2499 2499 target_ePWM4_temp.AQCTLA 0 0 varget_ePWM4_temp.AQCTLA target_ePWM4_temp.DBCTL 0 0 varget_ePWM4_temp.DBCTL target_ePWM4_temp.TZCTL 4095 4095 varget_ePWM4_temp.ETSEL target_ePWM4_temp.ETPS 4352 4352 varget_ePWM7_temp.PCCTL varget_ePWM7_temp.PCCTL 0 0 varget_ePWM7_temp.TBCTL 4 4 varget_ePWM7_temp.TBPHS 0 0 varget_ePWM7_temp.TBPRD 65535 65535 varget_ePWM7_temp.CMPCTL 0 0 varget_ePWM7_temp.CMPCTL 0 0 varget_ePWM7_temp.CMPCTL 0 0 varget_ePWM7_temp.CMPCTL 0 0 varget_ePWM7_temp.AQCTLB 33 33 33 averaget_ePWM7_temp.AQCTLB 33 33 33 averaget_ePWM7_temp.AQCTLB 4095 4095 4095 4095 4095 4095 4095 4095 <t< td=""><td>target_ePWM3_temp.PCCTL</td><td>0</td><td>0</td><td>✓</td></t<>	target_ePWM3_temp.PCCTL	0	0	✓
target_ePWM4_temp.TBPRD 65535 65535 varget_ePWM4_temp.CMPCTL target_ePWM4_temp.CMPA 2499 2499 2499 target_ePWM4_temp.AQCTLA 0 0 0 target_ePWM4_temp.CMPB 65535 65535 varget_ePWM4_temp.DBCTL target_ePWM4_temp.TZCTL 4095 4095 varget_ePWM4_temp.ETSEL target_ePWM4_temp.ETPS 4352 4352 varget_ePWM4_temp.TBCTL varget_ePWM7_temp.TBCTL varget_ePWM7_temp.TBCTL varget_ePWM7_temp.TBCTL varget_ePWM7_temp.TBCTL varget_ePWM7_temp.TBCTL varget_ePWM7_temp.TBCTL varget_ePWM7_temp.TBCTL varget_ePWM7_temp.TBCTL varget_ePWM7_temp.TBCTL varget_ePWM7_temp.CMCCTL varget_ePWM7_temp.DBCCTL varget_ePWM7_temp.DBCCTL varget_ePWM7_temp.DBCCTL varget_ePWM7_temp.DBCCTL varget_ePWM7_temp.DBCCTL varget_ePWM7_temp.DBCCTL varget_ePWM7_temp.DBCCTL varget_ePWM7_temp.ETSEL varget_ePWM7_temp.ETSEL varget_eP	target_ePWM4_temp.TBCTL	4	4	✓
target_ePWM4_temp.CMPCTL 0 0 2499 2498 2499 2499 2499 2498 2495 2496 2495 2495 2495 2495 2495 2495 2495 2495 2495 2495 2495 2495 2495 2495 2495 2495 2495 <td>target_ePWM4_temp.TBPHS</td> <td>0</td> <td>0</td> <td>✓</td>	target_ePWM4_temp.TBPHS	0	0	✓
target_ePWM4_temp.CMPA 2499 2499 target_ePWM4_temp.AQCTLA 0 0 target_ePWM4_temp.CMPB 65535 65535 target_ePWM4_temp.DBCTL 0 0 target_ePWM4_temp.TZCTL 4095 4095 target_ePWM4_temp.ETSEL 60416 60416 target_ePWM4_temp.ETPS 4352 4352 target_ePWM4_temp.PCCTL 0 0 target_ePWM7_temp.TBCTL 4 4 target_ePWM7_temp.TBPHS 0 0 target_ePWM7_temp.TBPRD 65535 65535 target_ePWM7_temp.CMPCTL 0 0 target_ePWM7_temp.CMPCTL 0 0 target_ePWM7_temp.CMPA 1025 1025 target_ePWM7_temp.AQCTLB 33 33 target_ePWM7_temp.DBCTL 0 0 target_ePWM7_temp.DBCTL 0 0 target_ePWM7_temp.TZCTL 4095 4095 target_ePWM7_temp.ETSEL 0 0	target ePWM4 temp.TBPRD	65535	65535	✓
target_ePWM4_temp.AQCTLA 0 0 target_ePWM4_temp.CMPB 65535 65535 target_ePWM4_temp.DBCTL 0 0 target_ePWM4_temp.DBCTL 4095 4095 target_ePWM4_temp.ETSEL 60416 60416 target_ePWM4_temp.ETPS 4352 4352 target_ePWM7_temp.DCTL 0 0 target_ePWM7_temp.TBCTL 4 4 target_ePWM7_temp.TBPHS 0 0 target_ePWM7_temp.TBPRD 65535 65535 target_ePWM7_temp.CMPCTL 0 0 target_ePWM7_temp.CMPA 1025 1025 target_ePWM7_temp.AQCTLB 33 33 target_ePWM7_temp.DBCTL 0 0 target_ePWM7_temp.DBCTL 0 0 target_ePWM7_temp.TZCTL 4095 4095 target_ePWM7_temp.ETSEL 0 0	target ePWM4 temp.CMPCTL	0	0	✓
target_ePWM4_temp.CMPB 65535 65535 v target_ePWM4_temp.DBCTL 0 0 v target_ePWM4_temp.TZCTL 4095 4095 v target_ePWM4_temp.ETSEL 60416 60416 60416 v target_ePWM4_temp.ETPS 4352 4352 v v target_ePWM7_temp.PCCTL 0 0 0 v v target_ePWM7_temp.TBCTL 4 4 4 v v target_ePWM7_temp.TBPRD 65535 65535 65535 v target_ePWM7_temp.CMPCTL 0 0 v v target_ePWM7_temp.CMPA 1025 1025 v v target_ePWM7_temp.AQCTLB 33 33 33 v v target_ePWM7_temp.DBCTL 0 0 0 v v target_ePWM7_temp.TZCTL 4095 4095 v v target_ePWM7_temp.ETSEL 0 0 v v target_ePWM7_temp.ETSEL 0 0 v v target_ePWM7_temp.ETSEL 0 0 v v	target ePWM4 temp.CMPA	2499	2499	✓
target_ePWM4_temp.DBCTL 0 0 4095 40	target ePWM4 temp.AQCTLA	0	0	✓
target_ePWM_temp.TZCTL 4095 4095 target_ePWM_temp.ETSEL 60416 60416 target_ePWM_temp.ETPS 4352 4352 target_ePWMM_temp.PCCTL 0 0 target_ePWM7_temp.TBCTL 4 4 target_ePWM7_temp.TBPHS 0 0 target_ePWM7_temp.TBPRD 65535 65535 target_ePWM7_temp.CMPCTL 0 0 target_ePWM7_temp.CMPA 1025 1025 target_ePWM7_temp.AQCTLB 33 33 target_ePWM7_temp.DBCTL 0 0 target_ePWM7_temp.DBCTL 0 0 target_ePWM7_temp.TZCTL 4095 4095 target_ePWM7_temp.ETSEL 0 0	target ePWM4 temp.CMPB	65535	65535	✓
target_ePWM4_temp.TZCTL 4095 4095 target_ePWM4_temp.ETSEL 60416 60416 target_ePWM4_temp.ETPS 4352 4352 target_ePWM4_temp.PCCTL 0 0 target_ePWM7_temp.TBCTL 4 4 target_ePWM7_temp.TBPHS 0 0 target_ePWM7_temp.TBPRD 65535 65535 target_ePWM7_temp.CMPCTL 0 0 target_ePWM7_temp.CMPA 1025 1025 target_ePWM7_temp.AQCTLB 33 33 target_ePWM7_temp.DBCTL 0 0 target_ePWM7_temp.DBCTL 0 0 target_ePWM7_temp.TZCTL 4095 4095 target_ePWM7_temp.ETSEL 0 0	target ePWM4 temp.DBCTL	0	0	✓
target_ePWM_temp.ETSEL 60416 60416 4352	· ·	4095	4095	✓
target_ePWM_temp.ETPS 4352 4352 4352 target_ePWM_temp.PCCTL 0 0 0 target_ePWM7_temp.TBCTL 4 4 4 target_ePWM7_temp.TBPHS 0 0 0 target_ePWM7_temp.TBPRD 65535 65535 5 target_ePWM7_temp.CMPCTL 0 0 0 target_ePWM7_temp.CMPA 1025 1025 4 target_ePWM7_temp.AQCTLB 33 33 33 4 target_ePWM7_temp.DBCTL 0 0 0 4 target_ePWM7_temp.TZCTL 4095 4095 4 target_ePWM7_temp.ETSEL 0 0 0 0		60416	60416	✓
target_ePWM_temp.PCCTL 0 0 v target_ePWM7_temp.TBCTL 4 4 4 target_ePWM7_temp.TBPHS 0 0 0 target_ePWM7_temp.TBPRD 65535 65535 5 target_ePWM7_temp.CMPCTL 0 0 0 target_ePWM7_temp.CMPA 1025 1025 3 target_ePWM7_temp.AQCTLB 33 33 3 target_ePWM7_temp.DBCTL 0 0 0 target_ePWM7_temp.TZCTL 4095 4095 4095 target_ePWM7_temp.ETSEL 0 0 0	target ePWM4 temp.ETPS	4352	4352	✓
target_ePWM7_temp.TBCTL 4 4 4 target_ePWM7_temp.TBPHS 0 0 0 target_ePWM7_temp.TBPRD 65535 65535 ✓ target_ePWM7_temp.CMPCTL 0 0 ✓ target_ePWM7_temp.CMPA 1025 1025 ✓ target_ePWM7_temp.AQCTLB 33 33 ✓ target_ePWM7_temp.DBCTL 0 0 ✓ target_ePWM7_temp.TZCTL 4095 4095 ✓ target_ePWM7_temp.ETSEL 0 0 ✓	· ·	0	0	✓
target_ePWM7_temp.TBPHS 0 0 v target_ePWM7_temp.TBPRD 65535 65535 v target_ePWM7_temp.CMPCTL 0 0 v target_ePWM7_temp.CMPA 1025 1025 v target_ePWM7_temp.AQCTLB 33 33 v target_ePWM7_temp.DBCTL 0 0 v target_ePWM7_temp.TZCTL 4095 4095 v target_ePWM7_temp.ETSEL 0 0 v	· ·	4	4	
target_ePWM7_temp.TBPRD 65535 65535 target_ePWM7_temp.CMPCTL 0 0 target_ePWM7_temp.CMPA 1025 1025 target_ePWM7_temp.AQCTLB 33 33 target_ePWM7_temp.DBCTL 0 0 target_ePWM7_temp.TZCTL 4095 4095 target_ePWM7_temp.ETSEL 0 0			0	✓
target_ePWM7_temp.CMPCTL 0 0 v target_ePWM7_temp.CMPA 1025 1025 v target_ePWM7_temp.AQCTLB 33 33 v target_ePWM7_temp.DBCTL 0 0 v target_ePWM7_temp.TZCTL 4095 4095 v target_ePWM7_temp.ETSEL 0 0 v				
target_ePWM7_temp.CMPA 1025 1025 target_ePWM7_temp.AQCTLB 33 33 target_ePWM7_temp.DBCTL 0 0 target_ePWM7_temp.TZCTL 4095 4095 target_ePWM7_temp.ETSEL 0 0				→
target_ePWM7_temp.AQCTLB 33 33 target_ePWM7_temp.DBCTL 0 0 target_ePWM7_temp.TZCTL 4095 4095 target_ePWM7_temp.ETSEL 0 0	· ·		i i	
target_ePWM7_temp.DBCTL 0 0 v target_ePWM7_temp.TZCTL 4095 4095 v target_ePWM7_temp.ETSEL 0 0 v				
target_ePWM7_temp.TZCTL 4095 4095 target_ePWM7_temp.ETSEL 0 0				
target_ePWM7_temp.ETSEL 0 0				
g*	· ·	1111		

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.4 (Repeat Count = 1)			~
Name	Input Value		
ePWM1_temp	target_ePWM1_temp		
ePWM2_temp	target_ePWM2_temp		
ePWM3_temp	target_ePWM3_temp		
ePWM4_temp	target_ePWM4_temp		
ePWM7_temp	target_ePWM7_temp		
k_PwmDeadBand_Cnt_u16	1024		
k_PwmRelay_Cnt_u16	625		
target_ePWM1_temp.DBCTL	11		
target_ePWM2_temp.DBCTL	11		
target_ePWM3_temp.DBCTL	11		
Name	Actual Value	Expected Value	Result
target_ePWM1_temp.TBCTL	8196	8196	✓
target_ePWM1_temp.TBPHS	0	0	✓
target_ePWM1_temp.TBPRD	65535	65535	✓
target ePWM1 temp.CMPCTL	0	0	✓

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Name	Actual Value	Expected Value	Result
target_ePWM1_temp.CMPA	2499	2499	~
target_ePWM1_temp.AQCTLA	289	289	✓
target_ePWM1_temp.CMPB	2499	2499	✓
target_ePWM1_temp.DBCTL	8	8	✓
target_ePWM1_temp.AQCSFRC	5	5	~
target_ePWM1_temp.DBFED	1024	1024	•
target_ePWM1_temp.DBRED	1024	1024	✓
target_ePWM1_temp.TZCTL	4095	4095	✓
target ePWM1 temp.ETSEL	0	0	✓
target_ePWM1_temp.PCCTL	0	0	✓
target_ePWM2_temp.TBCTL	8196	8196	✓
target_ePWM2_temp.TBPHS	0	0	✓
target_ePWM2_temp.TBPRD	65535	65535	•
target_ePWM2_temp.CMPCTL	0	0	•
target_ePWM2_temp.CMPA	2499	2499	_
target_ePWM2_temp.AQCTLA	288	288	·
target_ePWM2_temp.CMPB	2499	2499	
target_ePWM2_temp.DBCTL	8	8	·
target_ePWM2_temp.AQCSFRC	5	5	
	1024	1024	
target_ePWM2_temp.DBFED			
target_ePWM2_temp.DBRED	1024	1024	
target_ePWM2_temp.TZCTL	4095	4095	
target_ePWM2_temp.ETSEL	0	0	~
target_ePWM2_temp.PCCTL	0	0	V
target_ePWM3_temp.TBCTL	8196	8196	~
target_ePWM3_temp.TBPHS	0	0	~
target_ePWM3_temp.TBPRD	65535	65535	~
target_ePWM3_temp.CMPCTL	0	0	~
target_ePWM3_temp.CMPA	2499	2499	~
target_ePWM3_temp.AQCTLA	288	288	~
target_ePWM3_temp.CMPB	2499	2499	~
target_ePWM3_temp.DBCTL	8	8	~
target_ePWM3_temp.AQCSFRC	5	5	~
target_ePWM3_temp.DBFED	1024	1024	•
target_ePWM3_temp.DBRED	1024	1024	~
target_ePWM3_temp.TZCTL	4095	4095	~
target_ePWM3_temp.ETSEL	0	0	~
target_ePWM3_temp.PCCTL	0	0	✓
target_ePWM4_temp.TBCTL	4	4	✓
target_ePWM4_temp.TBPHS	0	0	✓
target_ePWM4_temp.TBPRD	65535	65535	✓
target_ePWM4_temp.CMPCTL	0	0	✓
target_ePWM4_temp.CMPA	2499	2499	~
target_ePWM4_temp.AQCTLA	0	0	•
target_ePWM4_temp.CMPB	65535	65535	✓
target_ePWM4_temp.DBCTL	0	0	✓
target_ePWM4_temp.TZCTL	4095	4095	✓
target_ePWM4_temp.ETSEL	60416	60416	✓
target_ePWM4_temp.ETPS	4352	4352	_
target_ePWM4_temp.PCCTL	0	0	✓
target_ePWM7_temp.TBCTL	4	4	_
target_ePWM7_temp.TBPHS	0	0	·
target_ePWM7_temp.TBPRD	65535	65535	
target ePWM7 temp.CMPCTL	0	0	~
target_ePWM7_temp.CMPA	625	625	
target ePWM7 temp.AQCTLB	33	33	-
	0	0	
target_ePWM7_temp.DBCTL			
target_ePWM7_temp.TZCTL	4095	4095	· ·
target_ePWM7_temp.ETSEL	0	0	V
target_ePWM7_temp.PCCTL	0	0	✓

Test Step Call Trace					✓	
	Actual Function	Count	Expected Function	Count	Result	
	none	0	*** No Call Expected ***	0	~	

Test Step 1.5 (Repeat Count = 1)		✓
Name	Input Value	
ePWM1_temp	target_ePWM1_temp	
ePWM2_temp	target_ePWM2_temp	
ePWM3 temp	target ePWM3 temp	

ePWM Init1

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Name Input Value ePWM4 temp target_ePWM4_temp ePWM7_temp target_ePWM7_temp k PwmDeadBand Cnt u16 k_PwmRelay_Cnt_u16 3214 target ePWM1 temp.DBCTL 11 target_ePWM2_temp.DBCTL 11 target_ePWM3_temp.DBCTL 11 Actual Value **Expected Value** Result target_ePWM1_temp.TBCTL 8196 8196 target_ePWM1_temp.TBPHS 0 target ePWM1 temp.TBPRD 65535 65535 target_ePWM1_temp.CMPCTL 0 2499 target_ePWM1_temp.CMPA 2499 289 289 target_ePWM1_temp.AQCTLA target ePWM1 temp.CMPB 2499 2499 target_ePWM1_temp.DBCTL 8 8 target_ePWM1_temp.AQCSFRC 5 5 target_ePWM1_temp.DBFED 15 15 target_ePWM1_temp.DBRED 15 15 $target_ePWM1_temp.TZCTL$ 4095 4095 target_ePWM1_temp.ETSEL 0 0 target_ePWM1_temp.PCCTL 0 0 target_ePWM2_temp.TBCTL 8196 8196 target_ePWM2_temp.TBPHS 0 0 target_ePWM2_temp.TBPRD 65535 65535 target_ePWM2_temp.CMPCTL 0 0 2499 target_ePWM2_temp.CMPA 2499 288 288 target ePWM2 temp.AQCTLA target_ePWM2_temp.CMPB 2499 2499 target_ePWM2_temp.DBCTL 8 8 target_ePWM2_temp.AQCSFRC 5 5 15 15 target ePWM2 temp.DBFED target_ePWM2_temp.DBRED 15 15 target ePWM2 temp.TZCTL 4095 4095 target_ePWM2_temp.ETSEL 0 n target_ePWM2_temp.PCCTL 0 0 • target ePWM3 temp.TBCTL 8196 8196 target_ePWM3_temp.TBPHS target_ePWM3_temp.TBPRD 65535 65535 target_ePWM3_temp.CMPCTL 0 target ePWM3 temp.CMPA 2499 2499 target_ePWM3_temp.AQCTLA 288 288 2499 2499 target ePWM3 temp.CMPB target_ePWM3_temp.DBCTL 8 8 target_ePWM3_temp.AQCSFRC 5 5 15 $target_ePWM3_temp.DBFED$ 15 target_ePWM3_temp.DBRED 15 15 4095 4095 target ePWM3 temp.TZCTL target_ePWM3_temp.ETSEL 0 0 $target_ePWM3_temp.PCCTL$ 0 n target_ePWM4_temp.TBCTL 4 4 target ePWM4 temp.TBPHS 0 0 target_ePWM4_temp.TBPRD 65535 65535 target_ePWM4_temp.CMPCTL 0 0 target_ePWM4_temp.CMPA 2499 2499 target_ePWM4_temp.AQCTLA 0 0 target_ePWM4_temp.CMPB 65535 65535 target_ePWM4_temp.DBCTL 0 0 target_ePWM4_temp.TZCTL 4095 4095 target_ePWM4_temp.ETSEL 60416 60416 target_ePWM4_temp.ETPS 4352 4352 0 0 target_ePWM4_temp.PCCTL target ePWM7 temp.TBCTL 4 4 target_ePWM7_temp.TBPHS 0 0 65535 65535 target ePWM7 temp.TBPRD target_ePWM7_temp.CMPCTL 0 0 target ePWM7 temp.CMPA 3214 3214 target_ePWM7_temp.AQCTLB 33 33 target ePWM7 temp.DBCTL 0 0 target_ePWM7_temp.TZCTL 4095 4095 target_ePWM7_temp.ETSEL 0 0 target_ePWM7_temp.PCCTL 0 0



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	

Test Step 1.6 (Repeat Count = 1)			
Name	Input Value		
ePWM1_temp	target_ePWM1_temp		
ePWM2 temp	target ePWM2 temp		
ePWM3_temp	target_ePWM3_temp		
ePWM4 temp	target_ePWM4_temp		
ePWM7 temp	target_ePWM7_temp		
k_PwmDeadBand_Cnt_u16	120		
k_PwmRelay_Cnt_u16	0		
target ePWM1 temp.DBCTL	11		
target ePWM2 temp.DBCTL	11		
target_ePWM3_temp.DBCTL	11		
Name	Actual Value	Expected Value	Result
target_ePWM1_temp.TBCTL	8196	8196	Kesuit
target_ePWM1_temp.TBPHS	0	0	·
target_ePWM1_temp.TBPRD	65535	65535	
target_ePWM1_temp.CMPCTL	0	0	·
target ePWM1 temp.CMPA	2499	2499	
target_ePWM1_temp.AQCTLA	289	289	-
target ePWM1 temp.CMPB	2499	2499	
target_ePWM1_temp.DBCTL	8	8	-
target_ePWM1_temp.AQCSFRC	5	5	
target_ePWM1_temp.DBFED	120	120	-
target_ePWM1_temp.DBRED	120	120	
target ePWM1 temp.TZCTL	4095	4095	-
target_ePWM1_temp.ETSEL	0	0	
	0	0	-
target_ePWM1_temp.PCCTL target_ePWM2_temp.TBCTL	8196	8196	
target_ePWM2_temp.TBPHS	0	0	-
target_ePWM2_temp.TBPRD	65535	65535	
target_ePWM2_temp.CMPCTL	0	0	_
	2499	2499	
target_ePWM2_temp.CMPA target_ePWM2_temp.AQCTLA	288	288	
target_ePWM2_temp.CMPB	2499	2499	
target ePWM2 temp.DBCTL	8	8	·
target_ePWM2_temp.AQCSFRC	5	5	
target_ePWM2_temp.DBFED	120	120	<u> </u>
target_ePWM2_temp.DBRED	120	120	
target_ePWM2_temp.TZCTL	4095	4095	✓
target_ePWM2_temp.ETSEL	0	0	
target_ePWM2_temp.PCCTL	0	0	·
target ePWM3 temp.TBCTL	8196	8196	
target_ePWM3_temp.TBPHS	0	0	<u> </u>
target_ePWM3_temp.TBPRD	65535	65535	
target_ePWM3_temp.CMPCTL	0	0	·
target_ePWM3_temp.CMPA	2499	2499	
target_ePWM3_temp.AQCTLA	288	288	~
target_ePWM3_temp.CMPB	2499	2499	
target_ePWM3_temp.DBCTL	8	8	~
target_ePWM3_temp.AQCSFRC	5	5	
target_ePWM3_temp.DBFED	120	120	~
target ePWM3 temp.DBRED	120	120	
target_ePWM3_temp.TZCTL	4095	4095	~
target ePWM3 temp.ETSEL	0	0	
target_ePWM3_temp.PCCTL	0	0	~
target_ePWM4_temp.TBCTL	4	4	
target ePWM4 temp.TBPHS	0	0	-
target ePWM4 temp.TBPRD	65535	65535	
target_ePWM4_temp.CMPCTL	0	0	~
target_ePWM4_temp.CMPA	2499	2499	
target_ePWM4_temp.AQCTLA	0	0	~
target_ePWM4_temp.CMPB	65535	65535	
target_ePWM4_temp.DBCTL	0	0	~
target_ePWM4_temp.TZCTL	4095	4095	
target_ePWM4_temp.ETSEL	60416	60416	-
target_ePWM4_temp.ETPS	4352	4352	
tangot_or vvivit_tomp.ETFO	7004	7004	

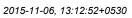
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Name	Actual Value	Expected Value	Result
target_ePWM4_temp.PCCTL	0	0	✓
target_ePWM7_temp.TBCTL	4	4	✓
target_ePWM7_temp.TBPHS	0	0	✓
target_ePWM7_temp.TBPRD	65535	65535	✓
target_ePWM7_temp.CMPCTL	0	0	✓
target_ePWM7_temp.CMPA	0	0	✓
target_ePWM7_temp.AQCTLB	33	33	✓
target_ePWM7_temp.DBCTL	0	0	✓
target_ePWM7_temp.TZCTL	4095	4095	✓
target_ePWM7_temp.ETSEL	0	0	✓
target_ePWM7_temp.PCCTL	0	0	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	

Test Step 1.7 (Repeat Count = 1)			V
Name	Input Value		
ePWM1_temp	target_ePWM1_temp		
ePWM2 temp	target ePWM2 temp		
ePWM3 temp	target_ePWM3_temp		
ePWM4_temp	target_ePWM4_temp		
ePWM7_temp	target_ePWM7_temp		
k_PwmDeadBand_Cnt_u16	66		
k_PwmRelay_Cnt_u16	65535		
target_ePWM1_temp.DBCTL	11		
target ePWM2 temp.DBCTL	11		
target_ePWM3_temp.DBCTL	11		
Name	Actual Value	Expected Value	Result
target_ePWM1_temp.TBCTL	8196	8196	~
target_ePWM1_temp.TBPHS	0	0	~
target_ePWM1_temp.TBPRD	65535	65535	-
target_ePWM1_temp.CMPCTL	0	0	✓
target_ePWM1_temp.CMPA	2499	2499	~
target_ePWM1_temp.AQCTLA	289	289	✓
target_ePWM1_temp.CMPB	2499	2499	_
target_ePWM1_temp.DBCTL	8	8	✓
target_ePWM1_temp.AQCSFRC	5	5	_
target_ePWM1_temp.DBFED	66	66	✓
target_ePWM1_temp.DBRED	66	66	_
target_ePWM1_temp.TZCTL	4095	4095	•
target_ePWM1_temp.ETSEL	0	0	
target_ePWM1_temp.PCCTL	0	0	•
target_ePWM2_temp.TBCTL	8196	8196	_
target_ePWM2_temp.TBPHS	0	0	•
target_ePWM2_temp.TBPRD	65535	65535	_
target_ePWM2_temp.CMPCTL	0	0	•
target_ePWM2_temp.CMPA	2499	2499	_
target_ePWM2_temp.AQCTLA	288	288	✓
target_ePWM2_temp.CMPB	2499	2499	_
target_ePWM2_temp.DBCTL	8	8	✓
target_ePWM2_temp.AQCSFRC	5	5	_
target_ePWM2_temp.DBFED	66	66	•
target_ePWM2_temp.DBRED	66	66	_
target ePWM2 temp.TZCTL	4095	4095	•
target_ePWM2_temp.ETSEL	0	0	_
target_ePWM2_temp.PCCTL	0	0	✓
target_ePWM3_temp.TBCTL	8196	8196	
target_ePWM3_temp.TBPHS	0	0	•
target_ePWM3_temp.TBPRD	65535	65535	
target_ePWM3_temp.CMPCTL	0	0	•
target_ePWM3_temp.CMPA	2499	2499	
target_ePWM3_temp.AQCTLA	288	288	~
target_ePWM3_temp.CMPB	2499	2499	-
target_ePWM3_temp.DBCTL	8	8	•
target_ePWM3_temp.AQCSFRC	5	5	
target_ePWM3_temp.DBFED	66	66	~
target_ePWM3_temp.DBRED	66	66	
target_ePWM3_temp.TZCTL	4095	4095	•
target_ePWM3_temp.ETSEL	0	0	
targot_or VVIVIO_tottle	U	0	





Name	Actual Value	Expected Value	Result
target_ePWM3_temp.PCCTL	0	0	~
target_ePWM4_temp.TBCTL	4	4	~
target_ePWM4_temp.TBPHS	0	0	~
target_ePWM4_temp.TBPRD	65535	65535	~
target_ePWM4_temp.CMPCTL	0	0	~
target_ePWM4_temp.CMPA	2499	2499	~
target_ePWM4_temp.AQCTLA	0	0	~
target_ePWM4_temp.CMPB	65535	65535	~
target_ePWM4_temp.DBCTL	0	0	~
target_ePWM4_temp.TZCTL	4095	4095	~
target_ePWM4_temp.ETSEL	60416	60416	~
target_ePWM4_temp.ETPS	4352	4352	~
target_ePWM4_temp.PCCTL	0	0	~
target_ePWM7_temp.TBCTL	4	4	~
target_ePWM7_temp.TBPHS	0	0	~
target_ePWM7_temp.TBPRD	65535	65535	~
target_ePWM7_temp.CMPCTL	0	0	~
target_ePWM7_temp.CMPA	65535	65535	~
target_ePWM7_temp.AQCTLB	33	33	~
target_ePWM7_temp.DBCTL	0	0	~
target_ePWM7_temp.TZCTL	4095	4095	~
target_ePWM7_temp.ETSEL	0	0	~
target_ePWM7_temp.PCCTL	0	0	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.8 (Repeat Count = 1)			→	
Name	Input Value			
ePWM1_temp	target_ePWM1_temp			
ePWM2_temp	target_ePWM2_temp			
ePWM3_temp	target_ePWM3_temp			
ePWM4_temp	target_ePWM4_temp			
ePWM7_temp	target_ePWM7_temp			
k_PwmDeadBand_Cnt_u16	485			
k_PwmRelay_Cnt_u16	2500	2500		
target_ePWM1_temp.DBCTL	11			
target_ePWM2_temp.DBCTL	11			
target_ePWM3_temp.DBCTL	11			
Name	Actual Value	Expected Value	Result	
target_ePWM1_temp.TBCTL	8196	8196	•	
target_ePWM1_temp.TBPHS	0	0	•	
target_ePWM1_temp.TBPRD	65535	65535	•	
target_ePWM1_temp.CMPCTL	0	0	•	
target_ePWM1_temp.CMPA	2499	2499	•	
target_ePWM1_temp.AQCTLA	289	289	•	
target_ePWM1_temp.CMPB	2499	2499	•	
target_ePWM1_temp.DBCTL	8	8	•	
target_ePWM1_temp.AQCSFRC	5	5	•	
target_ePWM1_temp.DBFED	485	485	•	
target_ePWM1_temp.DBRED	485	485	•	
target_ePWM1_temp.TZCTL	4095	4095	•	
target_ePWM1_temp.ETSEL	0	0	•	
target_ePWM1_temp.PCCTL	0	0	•	
target_ePWM2_temp.TBCTL	8196	8196	•	
target_ePWM2_temp.TBPHS	0	0	•	
target_ePWM2_temp.TBPRD	65535	65535	•	
target_ePWM2_temp.CMPCTL	0	0	•	
target_ePWM2_temp.CMPA	2499	2499	•	
target_ePWM2_temp.AQCTLA	288	288	•	
target_ePWM2_temp.CMPB	2499	2499	•	
target_ePWM2_temp.DBCTL	8	8	•	
target_ePWM2_temp.AQCSFRC	5	5	•	
target_ePWM2_temp.DBFED	485	485	•	
target_ePWM2_temp.DBRED	485	485	•	
target_ePWM2_temp.TZCTL	4095	4095	•	
target_ePWM2_temp.ETSEL	0	0	•	
target_ePWM2_temp.PCCTL	0	0	•	
target_ePWM3_temp.TBCTL	8196	8196	•	

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Name	Actual Value	Expected Value	Result
target_ePWM3_temp.TBPHS	0	0	~
target_ePWM3_temp.TBPRD	65535	65535	✓
target_ePWM3_temp.CMPCTL	0	0	✓
target_ePWM3_temp.CMPA	2499	2499	✓
target_ePWM3_temp.AQCTLA	288	288	✓
target_ePWM3_temp.CMPB	2499	2499	✓
target_ePWM3_temp.DBCTL	8	8	✓
target_ePWM3_temp.AQCSFRC	5	5	~
target_ePWM3_temp.DBFED	485	485	~
target_ePWM3_temp.DBRED	485	485	~
target_ePWM3_temp.TZCTL	4095	4095	~
target_ePWM3_temp.ETSEL	0	0	~
target_ePWM3_temp.PCCTL	0	0	~
target_ePWM4_temp.TBCTL	4	4	~
target_ePWM4_temp.TBPHS	0	0	✓
target_ePWM4_temp.TBPRD	65535	65535	✓
target_ePWM4_temp.CMPCTL	0	0	~
target_ePWM4_temp.CMPA	2499	2499	~
target_ePWM4_temp.AQCTLA	0	0	~
target_ePWM4_temp.CMPB	65535	65535	~
target_ePWM4_temp.DBCTL	0	0	~
target_ePWM4_temp.TZCTL	4095	4095	~
target_ePWM4_temp.ETSEL	60416	60416	~
target_ePWM4_temp.ETPS	4352	4352	~
target_ePWM4_temp.PCCTL	0	0	✓
target_ePWM7_temp.TBCTL	4	4	~
target_ePWM7_temp.TBPHS	0	0	✓
target_ePWM7_temp.TBPRD	65535	65535	~
target_ePWM7_temp.CMPCTL	0	0	~
target_ePWM7_temp.CMPA	2500	2500	~
target_ePWM7_temp.AQCTLB	33	33	✓
target_ePWM7_temp.DBCTL	0	0	~
target_ePWM7_temp.TZCTL	4095	4095	✓
target_ePWM7_temp.ETSEL	0	0	~
target_ePWM7_temp.PCCTL	0	0	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

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ePWM_Per1

Project	Ap_ePWM_1
Module	ePWM_1
Test Object	ePWM_Per1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3	
Successful	3	~
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\9BXX_ePWM_Up
Configuration File	D:\Synergy_Work_Area\9BXX_ePWM_Up\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\ePWM_Up\src\ePWM.c
Compiler Options	-DSTATIC= -D_DATA_ACCESS= -Dinline= -Dconst= -I\$(PROJECTROOT)\ePWM_Up\utp\contract\Ap_ePWM2 -I\$(PROJECTROOT)\ePWM_Up\utp\contract -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(PROJECTROOT)\ePWM_Up\include -I\$(Compiler Install Path)\include

Name	Text
/lodule 'ePWM_1'	Name of Tester:Jayesh Jahagirdar Code File(s) Under Test:ePWM.c Code File(s) Version:2 Module Design Document:ePWM MDD.docx Module Design Document Version:2 Data Dictionary Version:2 Unit Test Plan Version:1 Optimization Level:Level 2 Compiler (CodeGen) Version:TMS470_4.9.5 Model Type:Excel Macro Model Version:Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):564 Total RAM Used (Bytes):565 Total CALS Used (Bytes):6 Special Test Requirements: Test Date:11-06-2015 Comments:"NOTE1: Inline function defined in ""GlobalMacro.h"" are not unit tested. NOTE2: ""CBD Sandbox dbg.map"" map file is embedded for reference."

Attributes			
Name	Value		
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5		
Float Precision	9		
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj		
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src		
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd		
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl		
Target Install Path	\$(ProgramFiles)\pls\UDE 4.0		
Timer Enabled	false		
Timer Prescale	0		

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Attributes		
Name	Value	
Timer Resolution		
Timer Unit	Cycles	
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg	
Workspace File	D:\Synergy_Work_Area\9BXX_ePWM_Up\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP	



Test Case 1: Metrices Test

Specification

Performance metrics (With "None" Instrumentation and "WithPS" environment)

TS 1.1 107.00 Cycles TS 1.2 107.00 Cycles

Description Vector Description:

TS1.1"Shortest Execution Path:

TS1.1"Shortest Execution Path:

(CmpAPhaseA_Cnt_T_u16 > 535U)=True

(CmpBPhaseA_Cnt_T_u16 > (PWMPeriod_Cnt_T_u16 - 1U))=False

(CmpAPhaseB_Cnt_T_u16 > 535U)=True

(CmpAPhaseB_Cnt_T_u16 > (PWMPeriod_Cnt_T_u16 - 1U))=False

(CmpAPhaseC_Cnt_T_u16 > 535U)=True

(CmpAPhaseC_Cnt_T_u16 > (PWMPeriod_Cnt_T_u16 - 1U))=False

TS1.2"Longest Execution Path:

(CmpAPhaseA_Cnt_T_u16 > 535U)=False

(CmpAPhaseA_Cnt_T_u16 > 535U)=False

IS1.2"Longest Execution Path:
(CmpAPhaseA_Cnt_T_u16 > 535U)=False
(CmpBPhaseA_Cnt_T_u16 > (PWMPeriod_Cnt_T_u16 - 1U))=True
(CmpAPhaseB_Cnt_T_u16 > 535U)=False
(CmpBPhaseB_Cnt_T_u16 > (PWMPeriod_Cnt_T_u16 - 1U))=True
(CmpAPhaseC_Cnt_T_u16 > 535U)=True
(CmpBPhaseC_Cnt_T_u16 > (PWMPeriod_Cnt_T_u16 - 1U))=True"

Test Step 1.1 (Repeat Count = 1)			✓
Name	Input Value		
DummyVar4BIn	0		
DummyVarDCA	0		
DummyVarDCB	0		
DummyVarDCC	0		
DummyVarPeriodIn	3632		
k_ADCTrig1Offset_Cnt_s16	0		
Name	Actual Value	Expected Value	Result
DummyVar1A	1281	1281	~
DummyVar1B	1281	1281	✓
DummyVar2A	1281	1281	✓
DummyVar2B	1281	1281	✓
DummyVar3A	1281	1281	✓
DummyVar3B	1281	1281	✓
Dullinyvarsb			
DummyVar4A	1281	1281	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 1.2 (Repeat Count = 1)			
Name	Input Value		
DummyVar4Bln	6000		
DummyVarDCA	6000		
DummyVarDCB	6000		
DummyVarDCC	6000		
DummyVarPeriodIn	6000		
k_ADCTrig1Offset_Cnt_s16	1000		
Name	Actual Value	Expected Value	Result
DummyVar1A	1	1	✓
DummyVar1B	5999	5999	✓
DummyVar2A	1	1	~
DummyVar2B	5999	5999	✓
DummyVar3A	1	1	✓
DummyVar3B	5999	5999	✓
DummyVar4A	1465	1465	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~



Test Case 2: Boundary Test

Specification

Performance metrics (With "None" Instrumentation and "WithPS" environment)

"WithPS" environment)

TS 2.1 107.00 Cycles
TS 2.2 107.00 Cycles
TS 2.3 107.00 Cycles
TS 2.4 107.00 Cycles
TS 2.4 107.00 Cycles
TS 2.5 107.00 Cycles
TS 2.6 107.00 Cycles
TS 2.7 107.00 Cycles
TS 2.8 107.00 Cycles
TS 2.9 107.00 Cycles
TS 2.1 107.00 Cycles
TS 2.1 107.00 Cycles
TS 2.11 107.00 Cycles
TS 2.12 107.00 Cycles
TS 2.13 107.00 Cycles
TS 2.14 107.00 Cycles
TS 2.15 107.00 Cycles
TS 2.16 107.00 Cycles
TS 2.17 107.00 Cycles
TS 2.18 107.00 Cycles
TS 2.19 107.00 Cycles
TS 2.19 107.00 Cycles
TS 2.19 107.00 Cycles
TS 2.19 107.00 Cycles
TS 2.20 107.00 Cycles
TS 2.21 107.00 Cycles
TS 2.21 107.00 Cycles

Description Vector Description:

TS2.1All min

TS2.2All max TS2.3PWMPeriod_u16==>Min TS2.4PWMPeriod_u16==>Max TS2.5PWMPeriod_u16==>Pos TS2.6DCPhsAComp_u16==>Min TS2.7DCPhsAComp_u16==>Max TS2.8DCPhsAComp_u16==>Pos TS2.9DCPhsBComp_u16==>Min TS2.10DCPhsBComp_u16==>Max TS2.11DCPhsBComp_u16==>Pos TS2.11DCPhsBComp_u16==>Pos
TS2.12DCPhsCComp_u16==>Min
TS2.13DCPhsCComp_u16==>Max
TS2.14DCPhsCComp_u16==>Pos
TS2.15ePWM4CMPB_Cnt_u16==>Min
TS2.16ePWM4CMPB_Cnt_u16==>Max
TS2.17ePWM4CMPB_Cnt_u16==>Pos
TS2.18k_ADCTrig1Offset_Cnt_s16==>Max
TS2.20k_ADCTrig1Offset_Cnt_s16==>Pos
TS2.21k_ADCTrig1Offset_Cnt_s16==>Pos
TS2.21k_ADCTrig1Offset_Cnt_s16==>Pos

Test Step 2.1 (Repeat Count = 1)			✓
Name	Input Value		
DummyVar4BIn	0		
DummyVarDCA	0		
DummyVarDCB	0		
DummyVarDCC	0		
DummyVarPeriodIn	3632		
k_ADCTrig1Offset_Cnt_s16	0		
Name	Actual Value	Expected Value	Result
DummyVar1A	1281	1281	~
DummyVar1B	1281	1281	✓
DummyVar2A	1281	1281	✓
DummyVar2B	1281	1281	✓
DummyVar3A	1281	1281	✓
DummyVar3B	1281	1281	✓
DummyVar4A	1281	1281	✓
DummyVar4B	0	0	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	

Test Step 2.2 (Repeat Count = 1)			✓
Name	Input Value		
DummyVar4Bln	6000		
DummyVarDCA	6000		
DummyVarDCB	6000		
DummyVarDCC	6000		
DummyVarPeriodIn	6000		
k_ADCTrig1Offset_Cnt_s16	1000		
Name	Actual Value	Expected Value	Result
DummyVar1A	1	1	✓

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Name	Actual Value	Expected Value	Result
DummyVar1B	5999	5999	~
DummyVar2A	1	1	~
DummyVar2B	5999	5999	~
DummyVar3A	1	1	~
DummyVar3B	5999	5999	✓
DummyVar4A	1465	1465	•
DummyVar4B	6000	6000	✓

Test Step Call Trace					✓
	Actual Function	Count	Expected Function	Count	Result
	none	0	*** No Call Expected ***	0	~

Test Step 2.3 (Repeat Count = 1)			✓
Name	Input Value		
DummyVar4BIn	4948		
DummyVarDCA	2663		
DummyVarDCB	4707		
DummyVarDCC	5777		
DummyVarPeriodIn	3632		
k_ADCTrig1Offset_Cnt_s16	645		
Name	Actual Value	Expected Value	Result
DummyVar1A	1	1	~
DummyVar1B	2664	2664	✓
DummyVar2A	64463	64463	✓
DummyVar2B	3631	3631	✓
DummyVar3A	63928	63928	✓
DummyVar3B	3631	3631	✓
DummyVar4A	636	636	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	•

Test Step 2.4 (Repeat Count = 1)			✓
Name	Input Value		
DummyVar4BIn	4313		
DummyVarDCA	1637		
DummyVarDCB	301		
DummyVarDCC	5599		
DummyVarPeriodIn	6000		
k_ADCTrig1Offset_Cnt_s16	982		
Name	Actual Value	Expected Value	Result
DummyVar1A	1646	1646	✓
DummyVar1B	3283	3283	✓
DummyVar2A	2314	2314	~
DummyVar2B	2615	2615	✓
DummyVar3A	1	1	✓
DummyVar3B	5600	5600	✓
DummyVar4A	1483	1483	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	_

Test Step 2.5 (Repeat Count = 1)	✓
Name	Input Value
DummyVar4BIn	5376
DummyVarDCA	5236
DummyVarDCB	3108
DummyVarDCC	5956
DummyVarPeriodIn	3632

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Name	Input Value		
k_ADCTrig1Offset_Cnt_s16	312		
Name	Actual Value	Expected Value	Result
DummyVar1A	64199	64199	~
DummyVar1B	3631	3631	~
DummyVar2A	1	1	~
DummyVar2B	3109	3109	~
DummyVar3A	63839	63839	~
DummyVar3B	3631	3631	~
DummyVar4A	969	969	~
DummyVar4B	5376	5376	•

Test Step Call Trace					✓
	Actual Function	Count	Expected Function	Count	Result
	none	0	*** No Call Expected ***	0	~

Test Step 2.6 (Repeat Count = 1)			✓
Name	Input Value		
DummyVar4BIn	1335		
DummyVarDCA	0		
DummyVarDCB	4542		
DummyVarDCC	5038		
DummyVarPeriodIn	4317		
k_ADCTrig1Offset_Cnt_s16	240		
Name	Actual Value	Expected Value	Result
DummyVar1A	1623	1623	~
DummyVar1B	1623	1623	✓
DummyVar2A	64888	64888	✓
DummyVar2B	3894	3894	✓
DummyVar3A	64640	64640	✓
DummyVar3B	4142	4142	✓
DummyVar4A	1383	1383	~
DummyVar4B	1335	1335	✓

Test Step Call Trace					✓
Actual Function	on	Count	Expected Function	Count	Result
none		0	*** No Call Expected ***	0	~

Test Step 2.7 (Repeat Count = 1)			✓
Name	Input Value		
DummyVar4BIn	2136		
DummyVarDCA	6000		
DummyVarDCB	4804		
DummyVarDCC	2456		
DummyVarPeriodIn	4347		
k_ADCTrig1Offset_Cnt_s16	879		
Name	Actual Value	Expected Value	Result
DummyVar1A	64174	64174	~
DummyVar1B	4346	4346	✓
DummyVar2A	64772	64772	✓
DummyVar2B	4040	4040	✓
DummyVar3A	410	410	✓
DummyVar3B	2866	2866	✓
·	2866 759	2866 759	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0		

Test Step 2.8 (Repeat Count = 1)	✓
Name	Input Value
DummyVar4Bln	3320
DummyVarDCA	5878

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Name	Input Value		
DummyVarDCB	1951		
DummyVarDCC	4832		
DummyVarPeriodIn	5467		
k_ADCTrig1Offset_Cnt_s16	129		
Name	Actual Value	Expected Value	Result
DummyVar1A	64795	64795	~
DummyVar1B	5137	5137	•
DummyVar2A	1223	1223	•
DummyVar2B	3174	3174	✓
DummyVar3A	1	1	✓
DummyVar3B	4833	4833	•
DummyVar4A	2069	2069	~
DummyVar4B	3320	3320	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	_

Test Step 2.9 (Repeat Count = 1)			✓
Name	Input Value		
DummyVar4BIn	2133		
DummyVarDCA	1540		
DummyVarDCB	0		
DummyVarDCC	5937		
DummyVarPeriodIn	3665		
k_ADCTrig1Offset_Cnt_s16	570		
Name	Actual Value	Expected Value	Result
DummyVar1A	527	527	~
DummyVar1B	2067	2067	~
DummyVar2A	1297	1297	✓
DummyVar2B	1297	1297	✓
DummyVar3A	63865	63865	~
DummyVar3B	3664	3664	✓
DummyVar4A	727	727	~
DummyVar4B	2133	2133	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.10 (Repeat Count = 1)			✓
Name	Input Value		
DummyVar4BIn	1226		
DummyVarDCA	539		
DummyVarDCB	6000		
DummyVarDCC	1550		
DummyVarPeriodIn	5434		
k_ADCTrig1Offset_Cnt_s16	607		
Name	Actual Value	Expected Value	Result
DummyVar1A	1912	1912	~
DummyVar1B	2451	2451	✓
DummyVar2A	64718	64718	✓
DummyVar2B	5182	5182	✓
DummyVar3A	1407	1407	✓
DummyVar3B	2957	2957	✓
DummyVar4A	1575	1575	✓
DummvVar4B	1226	1226	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~



Test Step 2.11 (Repeat Count = 1)			✓
Name	Input Value		
DummyVar4BIn	3474		
DummyVarDCA	185		
DummyVarDCB	3474		
DummyVarDCC	1047		
DummyVarPeriodIn	5561		
k_ADCTrig1Offset_Cnt_s16	610		
Name	Actual Value	Expected Value	Result
DummyVar1A	2153	2153	✓
DummyVar1A DummyVar1B	2153 2338	2153 2338	· ·
·			•
DummyVar1B	2338	2338	•
DummyVar1B DummyVar2A	2338 508	2338 508	~
DummyVar1B DummyVar2A DummyVar2B	2338 508 3982	2338 508 3982	~
DummyVar1B DummyVar2A DummyVar2B DummyVar3A	2338 508 3982 1722	2338 508 3982 1722	·

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.12 (Repeat Count = 1)			✓
Name	Input Value		
DummyVar4Bln	3780		
DummyVarDCA	1936		
DummyVarDCB	5431		
DummyVarDCC	0		
DummyVarPeriodIn	5311		
k_ADCTrig1Offset_Cnt_s16	694		
Name	Actual Value	Expected Value	Result
DummyVar1A	1152	1152	~
DummyVar1B	3088	3088	~
DummyVar2A	64941	64941	✓
	0.0	04341	
DummyVar2B	4836	4836	~
			· ·
DummyVar2B	4836	4836	
DummyVar2B DummyVar3A	4836 2120	4836 2120	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.13 (Repeat Count = 1)			✓
Name	Input Value		
DummyVar4Bln	836		
DummyVarDCA	4741		
DummyVarDCB	5255		
DummyVarDCC	6000		
DummyVarPeriodIn	3739		
k_ADCTrig1Offset_Cnt_s16	850		
Name	Actual Value	Expected Value	Result
DummyVar1A	64500	64500	~
DummyVar1B	3705	3705	✓
DummyVar2A	64243	64243	~
DummyVar2B	3738	3738	✓
DummyVar3A	63870	63870	~
	3738	3738	✓
DummyVar3B	3130	0.00	
DummyVar3B DummyVar4A	484	484	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	•



Test Step 2.14 (Repeat Count = 1)			✓
Name	Input Value		
DummyVar4BIn	365		
DummyVarDCA	1783		
DummyVarDCB	2500		
DummyVarDCC	3160		
DummyVarPeriodIn	4612		
k_ADCTrig1Offset_Cnt_s16	97		
Name	Actual Value	Expected Value	Result
DummyVar1A	879	879	~
DummyVar1B	2662	2662	✓
DummyVar2A	521	521	~
DummyVar2B	3021	3021	~
DummyVar3A	191	191	~
DummyVar3B	3351	3351	~
DummyVar4A	1674	1674	~
DummyVar4B	365	365	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.15 (Repeat Count = 1)			✓
Name	Input Value		
DummyVar4Bln	0		
DummyVarDCA	1411		
DummyVarDCB	738		
DummyVarDCC	4155		
DummyVarPeriodIn	5675		
k_ADCTrig1Offset_Cnt_s16	394		
Name	Actual Value	Expected Value	Result
DummyVar1A	1597	1597	~
DummyVar1B	3008	3008	✓
DummyVar2A	1933	1933	✓
DummyVar2B	2671	2671	✓
DummyVar3A	225	225	✓
		1000	
DummyVar3B	4380	4380	•
DummyVar3B DummyVar4A	4380 1908	1908	-

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.16 (Repeat Count = 1)			✓
Name	Input Value		
DummyVar4BIn	6000		
DummyVarDCA	2883		
DummyVarDCB	5725		
DummyVarDCC	662		
DummyVarPeriodIn	5504		
k_ADCTrig1Offset_Cnt_s16	823		
Name	Actual Value	Expected Value	Result
DummyVar1A	775	775	✓
DummyVar1B	3658	3658	✓
DummyVar2A	64890	64890	✓
DummyVar2B	5079	5079	✓
DummyVar3A	1886	1886	✓
DummyVar3B	2548	2548	✓
DummyVar4A	1394	1394	✓
DummyVar4B	6000	6000	✓





Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.17 (Repeat Count = 1)			✓
Name	Input Value		
DummyVar4BIn	804		
DummyVarDCA	1527		
DummyVarDCB	4784		
DummyVarDCC	658		
DummyVarPeriodIn	4516		
k_ADCTrig1Offset_Cnt_s16	268		
Name	Actual Value	Expected Value	Result
DummyVar1A	959	959	✓
DummyVar1B	2486	2486	✓
DummyVar2A	64867	64867	✓
DummyVar2B	4115	4115	✓
DummyVar3A	1394	1394	✓
DummyVar3B	2052	2052	~
DummyVar4A	1455	1455	~
DummyVar4B	804	804	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.18 (Repeat Count = 1)			✓
Name	Input Value		
DummyVar4BIn	2549		
DummyVarDCA	2043		
DummyVarDCB	4178		
DummyVarDCC	1743		
DummyVarPeriodIn	5910		
k_ADCTrig1Offset_Cnt_s16	0		
Name	Actual Value	Expected Value	Result
DummyVar1A	1398	1398	✓
DummyVar1B	3441	3441	✓
DummyVar2A	331	331	✓
DummyVar2B	4509	4509	✓
DummyVar3A	1548	1548	✓
DummyVar3B	3291	3291	✓
DummyVar4A	2420	2420	✓
DummyVar4B	2549	2549	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.19 (Repeat Count = 1)			✓
Name	Input Value		
DummyVar4BIn	1122		
DummyVarDCA	269		
DummyVarDCB	2410		
DummyVarDCC	2393		
DummyVarPeriodIn	5696		
k_ADCTrig1Offset_Cnt_s16	1000		
Name	Actual Value	Expected Value	Result
DummyVar1A	2178	2178	~
DummyVar1B	2447	2447	~
DummyVar2A	1108	1108	~
DummyVar2B	3518	3518	✓
DummyVar3A	1116	1116	~
DummyVar3B	3509	3509	~

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Name	Actual Value	Expected Value	Result
DummyVar4A	1313	1313	~
DummyVar4B	1122	1122	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.20 (Repeat Count = 1)			✓
Name	Input Value		
DummyVar4Bln	2973		
DummyVarDCA	2895		
DummyVarDCB	97		
DummyVarDCC	3354		
DummyVarPeriodIn	3765		
k_ADCTrig1Offset_Cnt_s16	899		
Name	Actual Value	Expected Value	Result
DummyVar1A	1	1	~
DummyVar1B	2896	2896	~
DummyVar2A	1299	1299	~
DummyVar2B	1396	1396	~
DummyVar3A	1	1	~
DummyVar3B	3355	3355	✓
DummyVar4A	448	448	~
DummyVar4B	2973	2973	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~

Test Step 2.21 (Repeat Count = 1)			✓
Name	Input Value		
DummyVar4BIn	2973		
DummyVarDCA	2895		
DummyVarDCB	97		
DummyVarDCC	3354		
DummyVarPeriodIn	3765		
k_ADCTrig1Offset_Cnt_s16	15		
Name	Actual Value	Expected Value	Result
DummyVar1A	1	1	~
DummyVar1B	2896	2896	✓
DummyVar2A	1299	1299	✓
DummyVar2A DummyVar2B	1299 1396	1299 1396	~
·			-
DummyVar2B			~
DummyVar2B DummyVar3A	1396 1	1396 1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
none	0	*** No Call Expected ***	0	~



Test Case 3: Path Test

Specification

Performance metrics (With "None" Instrumentation and "WithPS" environment)

TS 3.1 107.00 Cycles TS 3.2 107.00 Cycles

Description

Vector Description:

TS3.1"

TS3.1"
(CmpAPhaseA_Cnt_T_u16 > 535U)=True,
(CmpBPhaseA_Cnt_T_u16 > (PWMPeriod_Cnt_T_u16 - 1U))=False,
(CmpAPhaseB_Cnt_T_u16 > 535U)=True,
(CmpBPhaseB_Cnt_T_u16 > (PWMPeriod_Cnt_T_u16 - 1U))=False,
(CmpAPhaseC_Cnt_T_u16 > (535U)=True,
(CmpAPhaseC_Cnt_T_u16 > (PWMPeriod_Cnt_T_u16 - 1U))=False,
(CmpAPhaseC_Cnt_T_u16 > (PWMPeriod_Cnt_T_u16 - 1U))=False,
"TS3.2"

TS3.2"
(CmpAPhaseA_Cnt_T_u16 > 535U)=False,
(CmpBPhaseA_Cnt_T_u16 > (PWMPeriod_Cnt_T_u16 - 1U))=True,
(CmpAPhaseB_Cnt_T_u16 > 535U)=False,
(CmpBPhaseB_Cnt_T_u16 > (PWMPeriod_Cnt_T_u16 - 1U))=True,
(CmpAPhaseC_Cnt_T_u16 > 535U)=False,
(CmpBPhaseC_Cnt_T_u16 > (PWMPeriod_Cnt_T_u16 - 1U))=True,"

Test Step 3.1 (Repeat Count = 1)			✓
Name	Input Value		
DummyVar4BIn	0		
DummyVarDCA	0		
DummyVarDCB	0		
DummyVarDCC	0		
DummyVarPeriodIn	3632		
k_ADCTrig1Offset_Cnt_s16	0		
Name	Actual Value	Expected Value	Result
DummyVar1A	1281	1281	~
DummyVar1B	1281	1281	✓
DummyVar2A	1281	1281	~
DummyVar2B	1281	1281	✓
DummyVar3A	1281	1281	~
DummyVar3B	1281	1281	✓
DummyVar4A	1281	1281	~
DummyVar4B	0	0	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	_	

Test Step 3.2 (Repeat Count = 1)			✓
Name	Input Value		
DummyVar4BIn	6000		
DummyVarDCA	6000		
DummyVarDCB	6000		
DummyVarDCC	6000		
DummyVarPeriodIn	6000		
k_ADCTrig1Offset_Cnt_s16	1000		
Name	Actual Value	Expected Value	Result
DummyVar1A	1	1	~
DummyVar1B	5999	5999	✓
DummyVar2A	1	1	✓
DummyVar2B	5999	5999	✓
DummyVar3A	1	1	✓
DummyVar3B	5999	5999	✓
DummyVar4A	1465	1465	✓
DummyVar4B	6000	6000	✓

Test Step Call Trace ✓					
Actual Function	Count	Expected Function	Count	Result	
none	0	*** No Call Expected ***	0	~	