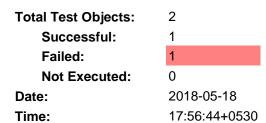
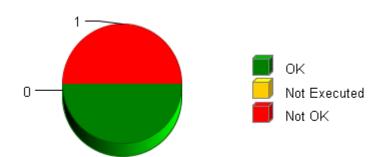


### **Summary**

### **Overall Test Object Results (including Coverage)**





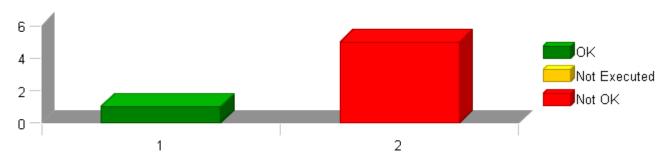
### **Selected Project Items**

Test Object "UnitTest/LoaMgr/LoaMgr\_Init1" Test Object "UnitTest/LoaMgr/LoaMgr\_Per1"

#### **Used Test Environments**

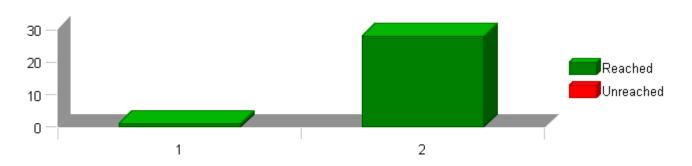
GNU GCC Eclipse CDT (Default)

### **Test Case Results for Each Test Object (without Coverage)**



The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

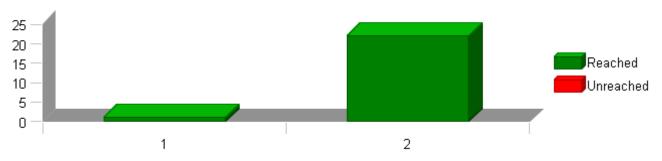
### Statement (C0) Coverage: Total Statements for Each Test Object





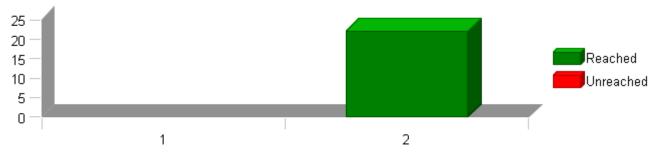
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

### Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

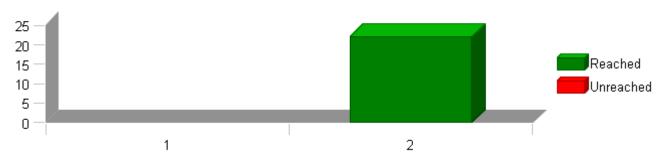
### **Decision Coverage: Total Decision Outcomes for Each Test Object**



The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.

#### MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

### **TEST OVERVIEW REPORT**

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### **Test Object List**

Project SF049A\_LoaMg

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	CO	C1	DC	MC/DC	Test Cases	Result
	SF049A_LoaMg	100 %	100 %	100 %	100 %	5 of 6 failed	×
	UnitTest	100 %	100 %	100 %	100 %	5 of 6 failed	*
	LoaMgr	100 %	100 %	100 %	100 %	5 of 6 failed	×
1	LoaMgr_Init1	100 %	100 %	-	-	1 of 1 passed	•
2	LoaMgr Per1	100 %	100 %	100 %	100 %	5 of 5 failed	×

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LoaMgr\_Per1

 Project
 SF049A\_LoaMg

 Module
 LoaMgr

 Test Object
 LoaMgr\_Per1

### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MC/DC Coverage	100 %

#### **Statistics**

Total Testcases	5
Successful	0
Failed	5 🗷
Not Executed	0



### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\LoaMgr_SF049A_4.3.0_0_NoUTP		
Configuration File	D:\Synergy_Work_Area\LoaMgr_SF049A_4.3.0_0_NoUTP\tessy\config\configuration.xml		
Target Environment	GNU GCC Eclipse CDT (Default)		
Kind of Test	Unit Test		
Linker Options			
Source File(s)			
File	\$(PROJECTROOT)\LoaMgr\src\Ap_LoaMgr.c		
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\LoaMgr\tools\contract -I\$(PROJECTROOT)\LoaMgr\tools\contract\Ap_LoaMgr -I\$ (PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramW6432)\Texas Instruments\ccsv4\tools\compiler \tms470_4.9.5\include		

Comments/Description	Text
Module 'LoaMgr'	Name of Tester-Pradnya Mhetre Code File(s) Under Test-Ap_LoaMigr.c Code File(s) Under Test-Ap_LoaMigr.c Code File(s) Version:11 Module Design Version:19 Requirement Document Version:NA Data Dictionary Version:03 Unit Test Plan Version:3 Unit Test Plan Version:3 Unit Test Plan Version:5 Optimization Level/Ogeneral Compiler (Code-Gen) Version:TMS470_4.9.5 Model Type:None Model Version:Nexteer EPS Unit Test Tool 1.0 Total FLASH Used (Bytes):224 Total CALS Used (Bytes):27 Total CALS Used (Bytes):27 Total CALS Used (Bytes):27 Special Test Requirements:NA Test Date:5/18/2018 Comments: Note1:Inline functions defined in 'globalmacro.h' are not unit tested. Note2:The Expected output calculated from MATLAB model. Note3:Local functions:""".atchinguist(", "", "RagtRespConds(", ""ArbResp()"", ""AssignScale()"", ""RqstHwTqResp()", ""ChooseFinalResp()" and ""SwBasMtgtn()" are not tested individually as they are tested in "LoaMgr_Per1" unction. Please refer snapshot "LoaMgr_Per1" coverage" for the same. Note4:In CSV 9 and CSV_10 value for boolean variable ""DiagcStsMrt1nactv" given in "Simdata" is in float. Hence input values for "DiagcStsIvtr1 Inactv" are captured from Matlab design and given in Tessy for those test steps. Note5: Out of range value is given to the variable ""MtPosldptSig_Cnt_u08=5"" over the DD range {0,4}, to cover the path at src line no.908 in TS5.1 Note6: Out of range value is given to the variable ""MtPosldptSig_Cnt_u08=4"" over the DD range {0,2}, to cover the path at src line no.928 in TS5.3 Note8: Out of range value is given to the variable ""MtPosldptSig_Cnt_u08=3"" over the DD range {0,2}, to cover the path at src line no.928 in TS5.3 Note8: Out of Tange value is given to the variable ""MtPosldptSig_Cnt_u08=3"" over the DD range {0,1}, to cover the path at src line no.928 in TS5.3 Note8: Out of Tange value is given to the variable ""MtPosldptSig_Cnt_u08=3"" over the

Attributes	
Name	Value
Compiler Install Path	\$(ProgramW6432)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include
Float Precision	9



```
Test Case 1: Metric Test
Specification
                                                                               Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:
                                                                               TS1.1 2849.00 Cycles
TS1.2 5486.00 Cycles
Description
                                                                              Vector Description:
                                                                        Vector Description:

TS1.1Shortest Execution Path

if(LoaMgr_LOASt_State_M_enum == LOAST_RPDSHTDWNREQD)=>T

if(LoaMgr_LOASt_State_M_enum == LOAST_RPDSHTDWNREQD)=>T

if(LoaMgr_LOASt_State_M_enum == LOAST_SPDSHTDWNREQD)=>T

(LoaMgr_LOASt_State_M_enum == LOAST_SWMTGTN_CNT_U08)=>F||(LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc == TRUE)=>F)

if((MtvTqResp_Cnt_T_u08 == D_LOASTSWMTGTN_CNT_U08)=>F||(LoaMgr_MotAqLoaMtgtnEn_Cnt_M_lgc == TRUE)=>F)

if((MtvTqResp_Cnt_T_u08 == D_LOASTSWMTGTN_CNT_U08)=>F||(LoaMgr_MotAqLoaMtgtnEn_Cnt_M_lgc == TRUE)=>F)

if((NtvTqSp_Cnt_T_u08 == D_LOASTSWMTGTN_CNT_U08)=>F||(LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc == TRUE)=>F)

if((NtpTqSp_Cnt_T_u08 == D_LOASTSWMTGTN_CNT_U08)=>F||(LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc == TRUE)=>F)

if((LoaMgr_MotAqLoaMtgtnEn_Cnt_M_lgc == TRUE)=>F)

if((MtgtnCount_Cnt_T_u08 == D_LOASTSWMTGTN_CNT_U08)=>F||(LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc == TRUE)=>F)

if((MtgtnCount_Cnt_T_u08 == D_LOASTSWMTGTN_CNT_U08)=>F

else if(LoaMgr_LOASt_State_M_enum == LOAST_RORM)=>F

else if(LoaMgr_LOASt_State_M_enum == LOAST_REDCD)=>F

else if(LoaMgr_LOASt_State_M_enum == LOAST_REDCD)=>F

else if(LoaMgr_LOASt_State_M_enum == LOAST_CTRLDSHTDWNREQD)=>F

if(Ivtrlnactv_Cnt_T_logl == TRUE)=>F

Max_m(MultiMtgtnResp_Cnt_T_u08_Max_m(MtvTqResp_Cnt_T_u08, Max_m(MtvTqSp_Cnt_T_u08, MtvTqSp_Cnt_T_u08, Max_m(MtvTqSp_Cnt_T_u08, MtvTqSp_Cnt_T_u08, MtvTqSp_Cnt_T
                                                                              TS1.1Shortest Execution Path
                                                                              TS1.2 Largest Execution Path
                                                                         if(LoaMgr_LOASt_State_M_enum == LOAST_RPDSHTDWNREQD)>F
```

Test Step 1.1 (Repeat Count = 1)		
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_lvtrRespSV_Cnt_M_u08	1	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	

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Name	Input Value		
LoaMgr_MotAgRespSV_Cnt_M_u08	5		
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1		
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	3		
LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0		
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0		
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr		
k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	1 0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k LoaMgr MotAgldptSig2Resp Cnt u08	5		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08 k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3 2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target LoaMgr Per1 TloaAvail Cnt lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	nt les	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc		Expected Value	Result
target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_lgc  Name	Actual Value		
	Actual Value	0	•
Name	0	0	•
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0 0 0	0	•
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08	0 0 0 0	0 0 0	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0 0 0	0	•

target\_LoaMgr\_Per1\_MotCurrLoaMtgtnEn\_Cnt\_lgc.value

LoaMgr\_Per1

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**Actual Value Expected Value** LoaMgr\_lvtrRespSV\_Cnt\_M\_u08 LoaMgr\_LOASt\_State\_M\_enum 5 5 LoaMgr\_LoaRateLimit\_UlspS\_M\_f32 500 500 LoaMgr\_LoaScaleFctr\_Uls\_M\_f32 1 LoaMgr\_MaxMtgtnResp\_Cnt\_M\_u08 5 5 LoaMgr\_MotAgldptSigSV\_Cnt\_M\_u08 0 0 LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc 1 1 LoaMgr\_MotAgRespSV\_Cnt\_M\_u08 5 5 LoaMgr\_MotCurrldptSigSV\_Cnt\_M\_u08  $LoaMgr\_MotCurrLoaMtgtnEn\_Cnt\_M\_lgc$ 0 0 LoaMgr\_MotCurrRespSV\_Cnt\_M\_u08 3 3 LoaMgr\_TloaAvail\_Cnt\_M\_lgc 0 0 LoaMgr\_TloaDi\_Cnt\_M\_lgc LoaMgr\_VltgMdSrcSV\_Cnt\_M\_u08 0 0 ✓ × ✓ Rte\_Call\_Ap\_LoaMgr\_NxtrDiagMgr\_SetNTCStatus(NTC\_Cnt\_T\_enum) {170} {170} Rte\_Call\_Ap\_LoaMgr\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08)  $Rte\_Call\_Ap\_LoaMgr\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum)$ {1} {1} Rte\_Write\_Ap\_LoaMgr\_LoaRateLimit\_UlspS\_f32(data) 500 ± 0.01 500  $Rte\_Write\_Ap\_LoaMgr\_LoaScaleFctr\_Uls\_f32(data)$ 1 1 ± 0.001  $target\_LoaMgr\_Per1\_HwTqLoaMtgtnEn\_Cnt\_lgc.value$ 0 0  $target\_LoaMgr\_Per1\_lvtrLoaMtgtnEn\_Cnt\_lgc.value$ 0 0 target\_LoaMgr\_Per1\_LOASt\_State\_enum.value 5 5  $target\_LoaMgr\_Per1\_MotAgLoaMtgtnEn\_Cnt\_lgc.value$ 

Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
CalclvtrState	2	CalclvtrState	2	~		
LatchInputs	1	LatchInputs	1	•		
RqstRespConds	1	RqstRespConds	1	-		
RqstHwTqResp	1	RqstHwTqResp	1	~		
ArbResp	1	ArbResp	1	~		
ChooseFinalResp	1	ChooseFinalResp	1	•		
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	~		
AssignScale	1	AssignScale	1	~		
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~		
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•		
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~		
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	<b>~</b>		

0

Test Step 1.2 (Repeat Count = 1)	<b>13</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	0
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1





	Input Value		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k LoaMgr MotAgldptSig3Resp Cnt u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	1		
k LoaMgr MotAgMtgtnSca Uls f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
	0		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08			
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	0.123400003		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIv	tr1Inacty Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslv		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptS		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaM		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtg		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_Sta	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoal		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldp		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoa		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdpt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_	· -	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_0	Cnt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc			
	target_LoaMgr_Per1_TloaDi_Cn	t_lgc	
Name	Actual Value	t_lgc Expected Value	Result
	<u> </u>		
Name LoaMgr_AgSV_Cnt_M_lgc	Actual Value	Expected Value	~
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08	Actual Value 0 1	Expected Value 0 1	
Name  LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc	Actual Value 0 1 0	Expected Value 0 1 0	•
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08	Actual Value 0 1 0 1	Expected Value  0 1 0 1	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08	Actual Value  0 1 0 1 1	Expected Value  0 1 0 1 1 1	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	Actual Value 0 1 0 1 0 1 0 1	Expected Value  0 1 0 1 1 0 1 0 1	9
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08	Actual Value  0 1 0 1 0 1 0 0 0 0 0	Expected Value  0 1 0 1 1 0 1 0 0 0	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_lvtrRespSV_Cnt_M_u08	Actual Value  0 1 0 1 0 1 1 0 0 1 1	Expected Value  0 1 0 1 0 1 0 1 1 1 1 0 0 1	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32	Actual Value  0 1 0 1 0 1 1 0 0 1 1 1 1 0 1 1 1 1	Expected Value  0 1 0 1 0 1 1 0 0 1 1 1 1 0 1 1 1 1 1	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32	Actual Value  0  1  0  1  1  0  1  1  1  0  0  1  1	Expected Value  0 1 0 1 0 1 1 0 0 1 1 1 0 0 0 0 1 1 1 0.123400003	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08	Actual Value  0 1 0 1 0 1 1 0 0 1 1 0 0 0 1 1 1 0.123400003	Expected Value  0 1 0 1 0 1 1 0 0 1 1 1 0 0 1 1 1 0 1 1 1 1 1 1 0.123400003	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32	Actual Value  0  1  0  1  1  0  1  1  0  0  1  1  0  0	Expected Value  0 1 0 1 0 1 1 0 0 1 1 1 0 0 0 1 1 1 1	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08	Actual Value  0 1 0 1 0 1 1 0 0 1 1 0 0 0 1 1 1 0.123400003	Expected Value  0 1 0 1 0 1 1 0 0 1 1 1 0 0 0 1 1 1 0.123400003 1 1 0	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08	Actual Value  0  1  0  1  1  0  1  1  0  0  1  1  0  0	Expected Value  0 1 0 1 0 1 1 0 0 1 1 1 0 0 0 1 1 1 1	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IwtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	Actual Value  0 1 0 1 0 1 1 0 0 1 1 1 0 0 1 1 1 0.123400003 1 1 0 1 0	Expected Value  0 1 0 1 0 1 1 0 0 1 1 1 0 0 0 1 1 1 0.123400003 1 1 0 1 0 1	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IwtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08	Actual Value  0 1 0 1 0 1 1 0 0 1 1 1 0 0 1 1 1 0.123400003 1 1 0 1	Expected Value  0 1 0 1 0 1 1 0 0 1 1 1 0 0 0 1 1 1 0.123400003 1 1 0 1	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IwtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08	Actual Value  0 1 0 1 0 1 1 0 0 1 1 1 0 0 1 1 1 0.123400003 1 1 0 1 0	Expected Value  0 1 0 1 0 1 1 0 0 1 1 1 0 0 0 1 1 1 0.123400003 1 1 0 1 0 1	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc	Actual Value  0 1 0 1 0 1 1 0 0 1 1 1 0 0 1 1 1 0.123400003 1 1 0 0 0 0	Expected Value  0 1 0 1 0 1 1 0 0 1 1 1 0 0 0 1 1 1 0.123400003 1 1 0 1 0 0 0	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08	Actual Value  0 1 0 1 1 0 1 1 0 0 1 1 1 0 0 1 1 1 0.123400003 1 1 0 1 0 1 1 0 1	Expected Value  0 1 0 1 0 1 1 0 0 1 1 1 0 0 0 1 1 1 0.123400003 1 1 0 1 0 1 0 1	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrlaptSigSV_Cnt_M_u08  LoaMgr_MotCurrlaptSigSV_Cnt_M_u08  LoaMgr_MotCurrlaptSigSV_Cnt_M_u08  LoaMgr_MotCurrlaptSigSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc	Actual Value  0 1 0 1 0 1 1 0 0 1 1 1 0 0 1 1 1 0.123400003 1 1 0 0 1 0 0 1	Expected Value  0 1 0 1 0 1 1 0 0 1 1 1 0 0 0 1 1 1 0.123400003 1 1 0 1 0 1 0 0 1	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_loASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAglAgespSV_Cnt_M_u08  LoaMgr_MotAglCaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_VtlgMdSrcSV_Cnt_M_u08	Actual Value  0 1 0 1 0 1 1 1 0 0 1 1 1 0 0 1 1 1 0.123400003 1 1 0 0 1 0 1 1 0 1 1 1 1 1 1 1 1	Expected Value  0 1 0 1 0 1 1 0 1 1 0 0 1 1 1 0 0 1 1 0 1 0 1 0 1 0 1 1 0 1	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_loaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	Actual Value  0 1 0 1 1 0 1 1 1 0 0 0 1 1 1 0.123400003 1 1 0 0 1 1 0 1 1 1 1 1 1 1 1 1 1	Expected Value  0 1 0 1 0 1 1 1 0 0 1 1 1 0 0 0 1 1 1 0.123400003 1 1 0 0 1 0 1 1 1 1 1 1 1 1 1 1 1	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_loASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFct_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	Actual Value  0 1 0 1 0 1 1 1 0 0 1 1 1 0 0 1 1 0 0 1 1 0 0 1 1 0 0 1 1 1 1 (173, 174) {1, 1}	Expected Value  0 1 0 1 0 1 1 1 0 0 1 1 1 0 0 0 1 1 1 0.123400003 1 1 0 0 1 1 0 1 1 1 0 1 1 1 0 0 1 1 0 0 1 1 0 0 1 1 0 0 1 1 0 0 1 1 0 0 1 1 0 0 0 1 1 0 0 0 1 1 0 0 0 1 1 0 0 0 1 1 0 0 0 0 1 0 0 0 1 0 0 0 0 1 0	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_loaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	Actual Value  0 1 0 1 1 0 1 1 1 0 0 0 1 1 1 0.123400003 1 1 0 0 1 1 0 1 1 1 1 1 1 1 1 1 1	Expected Value  0 1 0 1 0 1 1 1 0 0 1 1 1 0 0 0 1 1 1 0.123400003 1 1 0 0 1 0 1 1 1 1 1 1 1 1 1 1 1	Result

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Name	Actual Value	Expected Value	Result
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	✓
target_LoaMgr_Per1_LOASt_State_enum.value	1	1	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc.value	0	0	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

#### Test Case 2: Range Test

#### Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:

TS2.1 5287.00 Cycles
TS2.2 5486.00 Cycles
TS2.2 5486.00 Cycles
TS2.3 5054.00 Cycles
TS2.3 5054.00 Cycles
TS2.4 5021.00 Cycles
TS2.5 5005.00 Cycles
TS2.6 5005.00 Cycles
TS2.7 5005.00 Cycles
TS2.9 5005.00 Cycles
TS2.9 5005.00 Cycles
TS2.10 5005.00 Cycles
TS2.11 5005.00 Cycles
TS2.12 5005.00 Cycles
TS2.13 5005.00 Cycles
TS2.14 5005.00 Cycles
TS2.15 5005.00 Cycles
TS2.15 5005.00 Cycles
TS2.16 5005.00 Cycles
TS2.17 5005.00 Cycles
TS2.18 5005.00 Cycles
TS2.19 5005.00 Cycles
TS2.19 5005.00 Cycles
TS2.19 5005.00 Cycles
TS2.20 5005.00 Cycles
TS2.22 5005.00 Cycles
TS2.22 5005.00 Cycles
TS2.23 5005.00 Cycles TS2.23 5005.00 Cycles TS2.24 5005.00 Cycles

### Description

Vector Description:

TS2.1All Min

TS2.2All Max

TS2.2All Mix
TS2.2Bl Max
TS2.3DiagcStslvtr1Inactv\_Cnt\_lgc==>Min
TS2.4DiagcStslvtr1Inactv\_Cnt\_lgc==>Mix
TS2.5DiagcStslvtr2Inactv\_Cnt\_lgc==>Mix
TS2.5DiagcStslvtr2Inactv\_Cnt\_lgc==>Mix
TS2.6DiagcStslvtr2Inactv\_Cnt\_lgc==>Max
TS2.7HwTqldptSig\_Cnt\_u08==>Max
TS2.7HwTqldptSig\_Cnt\_u08==>Pos
TS2.9HwTqldptSig\_Cnt\_u08==>Max
TS2.91MotCurrldptSig\_Cnt\_u08==>Max
TS2.11MotCurrldptSig\_Cnt\_u08==>Max
TS2.12MotCurrldptSig\_Cnt\_u08==>Max
TS2.13MtrPosldptSig\_Cnt\_u08==>Max
TS2.12MotCurrldptSig\_Cnt\_u08==>Max
TS2.14MtrPosldptSig\_Cnt\_u08==>Max
TS2.16TloaAvail\_Cnt\_lgc==>Min
TS2.16TloaAvail\_Cnt\_lgc==>Max

TS2.17TloaAvail\_Cnt\_lgc==>Max

TS2.18TloaDi\_Cnt\_lgc=>Max
TS2.18TloaDi\_Cnt\_lgc=>Min
TS2.19TloaDi\_Cnt\_lgc=>Max
TS2.20LoaScaDi\_Cnt\_lgc=>Min
TS2.21LoaScaDi\_Cnt\_lgc=>Min
TS2.21LoaScaDi\_Cnt\_lgc=>Max
TS2.22SteerMod\_Cnt\_enum=>STEERMOD\_BASEPS
TS2.23SteerMod\_Cnt\_enum=>STEERMOD\_SEMIATNMS
TS2.24SteerMod\_Cnt\_enum=>STEERMOD\_SEMIATNMS TS2.24SteerMod\_Cnt\_enum==>STEERMOD\_FULLYATNMS

Test Step 2.1 (Repeat Count = 1)		×
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	

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Name	Input Value		
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0		
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0		
LoaMgr_IvtrRespSV_Cnt_M_u08	0		
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0		
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0		
LoaMgr_MotAgRespSV_Cnt_M_u08	0		
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0		
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0		
LoaMgr_MotCurrRespSV_Cnt_M_u08	0		
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0		
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0		
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr		
k_LoaMgr_CurrlytrMtgtnRate_UlspS_f32	0.00999999978		
k_LoaMgr_CurrlytrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32 k_LoaMgr_FadeOutStRate_UlspS_f32	0.00999999978		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	0.00999999978		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	0		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	0.00999999978		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	0.00999999978		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	0		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	0		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	0.0099999978		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	0		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurridptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0.00999999978		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	0.00999999978		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	0		
k LoaMgr RedcdStRate UlspS f32	0.0099999978		
k_LoaMgr_RedcdStSca_Uls_f32	0.0033333370		
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value	0		
target LoaMgr Per1 HwTqldptSig Cnt u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcSts	lvtr1Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcSts	lvtr2Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldp	stSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLo	aMtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaM		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_S	State_enum	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaE		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLo		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrl		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrl		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPoslo		
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMo		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvai		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_0	int_igc	
Name	Actual Value	Expected Value	Result

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Name	Actual Value	Expected Value	Result
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	•
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte Exit An LoaMor FalloaScaleAndRate	1	Rte Exit An LoaMor FalloaScaleAndRate	1	<b>V</b>

Test Step 2.2 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	4
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	5
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	5
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	500
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	500
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	5
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	5
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	5		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	5		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	5		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	4		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	500		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	5		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	5		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	2		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	500		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	5		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	3		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	500		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	5		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	5		
k LoaMgr MotCurrldptSig1Resp Cnt u08	5		
k LoaMgr MotCurrldptSig2Resp Cnt u08	5		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	500		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	500		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt lac	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_DiagcStslvtr2Inactv_Cnt_lgc	target LoaMgr Per1 DiagcStslvtr1mactv_C		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_Intr_LoaMtgtnEn_Cnt_lgc	target LoaMgr Per1 IvtrLoaMtgtnEn Cnt Ig		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_LOASt_State_enum	target LoaMgr Per1_IVITLOaMtgtnEn_Cnt_iq	JO	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 MotAgLoaMtgtnEn Cnt Igc	target LoaMgr Per1 MotAgLoaMtgtnEn Cr	at lac	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_MotCurrIdptSig_Cnt_u08	target LoaMgr Per1 MotCurrldptSig Cnt u		
target Rte Inst Ap LoaMgr.LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc	target LoaMgr Per1 MotCurrLoaMtgtnEn (		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	0	
	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc		
		Fynastad Value	Decult
Name	Actual Value	Expected Value	Result

target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	4	4	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	•
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3	3	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	•
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	5	5	•
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>





Name	Actual Value	Expected Value	Result
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174, 175}	{170, 172, 173, 174, 175}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1, 1}	{1, 1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	•
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.3 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1

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Name	Input Value		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target LoaMgr Per1 DiagcStslvtr1Inactv_Crit_igc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_l	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	at les	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Citarget LoaMgr Per1_MotCurrIdptSig_Cnt u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_t		
target_rte_inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMig_Per1_MtrPosIdptSig_Cnt_u(		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum	50	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	-
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	•
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	0 0 5	0 0 5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0 0 5 0	0 0 5 0	
LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0 0 5 0	0 0 5 0 1	
LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08	0 0 5 0 1 2	0 0 5 0 1 2	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc	0 0 5 0	0 0 5 0 1	
LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc	0 0 5 0 1 2	0 0 5 0 1 2	
LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08	0 0 5 0 1 2 1	0 0 5 0 1 2 1	
LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VitgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0 0 5 0 1 2 1 1	0 0 5 0 1 2 1 1	
LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VttgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0 0 5 0 1 2 1 1 0 {170, 172}	0 0 5 0 1 2 1 1 0 {170, 172}	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VitgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 0 5 0 1 2 1 1 0 {170, 172}	0 0 5 0 1 2 1 1 0 {170, 172}	\ \ \ \
LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VitgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	0 0 5 0 1 2 1 1 0 {170, 172}	0 0 5 0 1 2 1 1 0 {170, 172} {0, 0, 0, 0, 0} {1, 1}	\ \ \ \
LoaMgr_MotAgloptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrloptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VttgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0 0 5 0 1 2 1 1 1 0 {170, 172} {1, 1} {1, 1}	0 0 5 0 1 2 1 1 0 {170, 172} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01	×
LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VitgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0 0 5 0 1 2 1 1 1 0 {170, 172} {1, 1} {1, 1} 500 1	0 0 5 0 1 2 1 1 0 {170, 172} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0	×
LoaMgr_MotAgloptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VttgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value  target_LoaMgr_Per1_LOASt_State_enum.value	0 0 5 0 1 2 1 1 1 0 {170, 172} {1, 1} {1, 1} 500 1	0 0 5 0 1 2 1 1 0 {170, 172} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0 5	×
LoaMgr_MotAgloptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrloptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_VttgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value  target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0 0 5 0 1 2 1 1 1 0 {170, 172} {1, 1} {1, 1} 500 1	0 0 5 0 1 2 1 1 0 {170, 172} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0	11



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.4 (Repeat Count = 1)	
	<b>3</b>
Name Input Value	
LoaMgr_AgSV_Cnt_M_lgc *none*	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 *none*	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc *none*	
LoaMgr_HwTqRespSV_Cnt_M_u08 *none*	
LoaMgr_lvtrldptSigSV_Cnt_M_u08 *none*	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc *none*	
LoaMgr_lvtrRespSV_Cnt_M_u08 *none*	
LoaMgr_MotAgldptSigSV_Cnt_M_u08 *none*	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc *none*	
LoaMgr_MotAgRespSV_Cnt_M_u08 *none*	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08 *none*	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc *none*	
LoaMgr_MotCurrRespSV_Cnt_M_u08 *none*	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc *none*	
LoaMgr_VltgMdSrcSV_Cnt_M_u08 *none*	
Rte_Inst_Ap_LoaMgr target_Rte_Inst	_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32 1	
k LoaMgr CurrlytrMtgtnScaZeroEn Cnt lgc 0	
k LoaMgr CurrlvtrMtgtnSca Uls f32	
k_LoaMgr_FadeOutStRate_UlspS_f32	
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08 2	
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 4	
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 2	
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 0	
k LoaMgr HwTgldptSig4Resp Cnt u08 0	
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
k_LoaMgr_HwTqMtgtnRate_UlspS_f32 1	
k_LoaMgr_HwTqMtgtnSca_Uls_f32	
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08 0	
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08 0	
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08 0	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc 0	
k_LoaMgr_lvtrMtgtnSca_Uls_f32	
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 5	
1	
_ 0_ 0 1 0 1	
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 0 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 2	
_ 0_ 0 0 _ 1 1 _	
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_Igc 0 k_LoaMgr_MotAgMtgtnSca_Uls_f32 1	
2 ** 32 ** 3 ** 5 ** 5 ** 2 ** 2 **	
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 2	
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 2	
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08 0	
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08 0	
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08 0	
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc 1	
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	
k_LoaMgr_RedcdStRate_UlspS_f32 1	
k_LoaMgr_RedcdStSca_Uls_f32	
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value 1	





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_load	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M Igc	0	0	
LoaMgr HwTqldptSigSV Cnt M u08	0	0	<b>~</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>~</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>V</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174, 175}	{170, 172, 173, 174, 175}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1, 1}	{1, 1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	Ō	0	-
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	~
AssignScale	1	AssignScale	1	<b>✓</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~



Test Step 2.5 (Repeat Count = 1)	<b>3</b>
Name LoaMgr AgSV Cnt M Igc	Input Value *none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32 k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	1
k LoaMgr CurrivtrMtgtnSca Uls f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k LoaMgr RedcdStRate UlspS f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
1	, 5 = 5 = 5 = 5 = 5

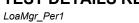


Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	0	0	•
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172}	{170, 172}	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.6 (Repeat Count = 1)		×
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	*none*	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*	
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*	
LoaMgr_IvtrRespSV_Cnt_M_u08	*none*	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*	
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*	
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

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Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08 k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtaSeaZgraEa_Cat_laa	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	0		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target LoaMgr Per1 TloaDi Cnt lgc.value	0		
target Rte Inst Ap LoaMgr.LoaMgr Per1 DiagcStslvtr1Inactv Cnt Igc	target LoaMgr Per1 DiagcStslvtr1Inactv	Cnt lac	
target Rte Inst Ap LoaMgr.LoaMgr Per1 DiagcStslvtr2Inactv Cnt Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt	_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	0.1.1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_i		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_ target_LoaMgr_Per1_MotCurrLoaMtgtnEn	_	
target Rte Inst Ap LoaMgr.LoaMgr Per1 MtrPosldptSig Cnt u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enur		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	•
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	· ·
LoaMgr_HwTqRespSV_Cnt_M_u08	0	4	
LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
	1	1	
LoaMgr_LoaScaleFctr_Uls_M_f32	1		
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•
	5	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5		





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>~</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc.value	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.7 (Repeat Count = 1)	13
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_IvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqIdptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lo	gc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	- 100 U.I.
LoaMgr HwTqldptSigSV Cnt M u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr HwTqRespSV Cnt M u08	4	4	-
· · · ·		0	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0		
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc		0	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	_
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>~</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	-
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	
target_LoaMgr_Per1_LOASt_State_enum.value target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	
	14	10	· •
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

- 101 00 /B 10 11 11	
Test Step 2.8 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k LoaMgr MotAgMtgtnRate UlspS f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k LoaMgr MotCurrldptSig2Resp Cnt u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	ļ'





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u	08	
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr HwTqldptSigSV Cnt M u08	0	0	_
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	_
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	_
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	_
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	-
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 173}	{170, 173}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	-
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	-
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~
v = =v			

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	<b>✓</b>
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 2.9 (Repeat Count = 1)	
Name	Input Value
LoaMgr AgSV Cnt M Igc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32 k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	1 0
k LoaMgr CurrivtrMtgtnSca Uls f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0 1
k_LoaMgr_lvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k LoaMgr MotAgMtgtnRate UlspS f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1.
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedodStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Ont_Igc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMigtnEn_Cnt_igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
goopcosmgcosmgi_r or i_motouricosmginicii_oni_igo	



Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 174}	{170, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.10 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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LoaMgr\_Per1



Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08 k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_InwTqLoaMtgtnEn_Cnt_lgc			
	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	ıt_lgc	
target Rte Inst Ap LoaMgr.LoaMgr Per1 LOASt State enum	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I	ıt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_loc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I target_LoaMgr_Per1_LOASt_State_enum	ıt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	ıt_lgc gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_l target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ci	t_lgc gc nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	t_lgc gc nt_lgc i08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ci target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	nt_lgc gc nt_lgc i08 Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ci target_LoaMgr_Per1_MotCurrIdptSig_Cnt_t target_LoaMgr_Per1_MotCurrLoaMtgtnEn_i	nt_lgc gc nt_lgc i08 Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ci target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u target_LoaMgr_Per1_MotCurrLoaMtgtnEn_target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u(	nt_lgc gc nt_lgc i08 Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ci target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u target_LoaMgr_Per1_MotCurrLoaMtgtnEn_i target_LoaMgr_Per1_MtrPosIdptSig_Cnt_ui target_LoaMgr_Per1_SteerMod_Cnt_enum	nt_lgc gc nt_lgc i08 Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ci target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u target_LoaMgr_Per1_MotCurrLoaMtgtnEn_i target_LoaMgr_Per1_MtrPosIdptSig_Cnt_ui target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc	nt_lgc gc nt_lgc i08 Cnt_lgc	Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ci target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u target_LoaMgr_Per1_MotCurrLoaMtgtnEn_i target_LoaMgr_Per1_MtrPosIdptSig_Cnt_ui target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc	t_lgc gc nt_lgc n08 Cnt_lgc 08	Result
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target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LoaScaleFctr_UIs_M_1632 LoaMgr_LoaScaleFctr_UIs_M_1632	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ci target_LoaMgr_Per1_MotCurrIdptSig_Cnt_ut target_LoaMgr_Per1_MtrPosIdptSig_Cnt_ut target_LoaMgr_Per1_MtrPosIdptSig_Cnt_ut target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_Igc target_LoaMgr_Per1_TloaDi_Cnt_Igc Actual Value 0 0 0 0 5 5 5 500 0	t_lgc gc  nt_lgc 108  Cnt_lgc 108  Expected Value 0 0 0 0 4 0 5 5 5 5 500 0	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_108 LoaMgr_IvtrRespSV_Cnt_M_108 LoaMgr_LoaSt_state_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ci target_LoaMgr_Per1_MotCurrIdptSig_Cnt_ut target_LoaMgr_Per1_MtrPosIdptSig_Cnt_ut target_LoaMgr_Per1_MtrPosIdptSig_Cnt_ut target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_Igc target_LoaMgr_Per1_TloaDi_Cnt_Igc Actual Value 0 0 0 5 5 5 500 0 5	t_lgc gc  nt_lgc 108  Cnt_lgc 108  Expected Value 0 0 0 0 4 0 0 5 5 5 5000 0 5	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LoaScaleFctr_UIS_M_Ig2	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ci target_LoaMgr_Per1_MotCurrIdptSig_Cnt_ut target_LoaMgr_Per1_MtrPosIdptSig_Cnt_ut target_LoaMgr_Per1_MtrPosIdptSig_Cnt_ut target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_Igc target_LoaMgr_Per1_TloaDi_Cnt_Igc Actual Value 0 0 0 0 5 5 5 500 0	t_lgc gc  nt_lgc 108  Cnt_lgc 108  Expected Value 0 0 0 0 4 0 5 5 5 5 500 0	· · · · · · · · · · · · · · · · · · ·

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

2018-05-18, 17:54:37+0530



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>~</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 174}	{170, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>~</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt lgc.value	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.11 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





N.	1		
Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2		
	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_loaning	дс	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr HwTqldptSigSV Cnt M u08	0	0	·
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr HwTqRespSV Cnt M u08	0	0	~
· · · ·			
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc		0	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	-
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	
LAIGUE LUAINUI FEIT LUAGE GIAITE CHUIT.VAIUE	5	J	
	0	0	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>V</b>



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	<b>~</b>
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	-
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>~</b>

Test Step 2.12 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt		
target Rte Inst Ap LoaMgr.LoaMgr Per1 lvtrLoaMtgtnEn Cnt Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lg		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cn	t lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaAvail Cnt Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	Result
	0	0	
LoaMgr_HwTqlgotNtgtpEp_Cot_M_loa	0	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	4	4	-
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
	0	0	-
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	5	5	
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LoaRstol imit_Ulass_M_f22	500	500	
LoaMgr_LoaRateLimit_UlspS_M_f32	0	0	-
LoaMgr_LoaScaleFctr_Uls_M_f32	5	5	
LoaMgr_MaxMtgtnResp_Cnt_M_u08		0	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0		
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0 1	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc		2	
LoaMgr_MotCurrRespSV_Cnt_M_u08	2		-
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	
LoaMgr_TloaDi_Cnt_M_lgc	1	1	•
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	<b>{1, 1, 1}</b>	{0, 0, 0, 0, 0}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	¥ .
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	•
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	-
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	_
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>~</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 2.13 (Repeat Count = 1)	12
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*  *none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k LoaMgr lvtrldptSig2Resp Cnt u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k LoaMgr MotCurrldptSig2Resp Cnt u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target LoaMgr_Per1 SteerMod Cnt enum.value	0
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc



Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•

Test Step 2.14 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Input Value





k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value			
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1 1 0		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1 1 0 3		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1 1 0 3 0		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1 1 0 3 0	nt_lgc	
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target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	1 1 0 3 0 1 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
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target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASc_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HvTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	1 1 1 0 3 0 1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LoASc_State_enum target_LoaMgr_Per1_LoASc_Di_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 0 0 4 0 0 5 5 5	nt_lgc t_lgc gc  at_lgc gc  at_lgc gs  Expected Value 0 0 0 4 0 0 5 5	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASC_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaRtgtnEn_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	1 1 1 0 3 0 1 1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LoASc_State_enum target_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 0 0 5 5 5 5000	Expected Value	***
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASc_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaNtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaRcleImit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	1 1 1 0 3 0 1 1 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_LoaMgr_Per1_MotQurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 0 0 5 5 5 500 1	Expected Value	***
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqlptSigSV_Cnt_M_u08 LoaMgr_HwTqlptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaNtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaRsetLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 1 1 0 3 0 1 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_log target_LoaMgr_Per1_loaScaDi_Cnt_log target_LoaMgr_Per1_LoaScaDi_Cnt_log target_LoaMgr_Per1_MotQurnIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrloaMtgtnEn_Crt_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Crt_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Ct_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Ct_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Ct_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Ct_target_LoaMgr_Per1_ToaAvail_Cnt_logc_target_LoaMgr_Per1_TloaDi_Cnt_logc_target_LoaMgr_Per1_LoaMgr_Per1_Lo	Expected Value	***
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqltptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaNtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaRtelimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	1 1 1 0 3 0 1 1 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_LoaMgr_Per1_MotQurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 0 0 5 5 5 500 1	Expected Value	***

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

2018-05-18, 17:54:37+0530



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 174}	{170, 172, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.15 (Repeat Count = 1)		
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	*none*	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*	
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*	
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*	
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*	
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0	
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1	
k_LoaMgr_FadeOutStRate_UlspS_f32	1	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2	
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4	
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2	
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1	
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0	
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0	
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1	
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1	
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1	
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0	
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0	
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1	
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0	
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1	
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5	

LoaMgr Per1

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Input Value k\_LoaMgr\_MotAgldptSig2Resp\_Cnt\_u08 k\_LoaMgr\_MotAgldptSig3Resp\_Cnt\_u08 0 k LoaMar MotAaldptSigFltThd Cnt u08 2 k\_LoaMgr\_MotAgMtgtnRate\_UlspS\_f32 k LoaMar MotAaMtatnScaZeroEn Cnt lac 0 k\_LoaMgr\_MotAgMtgtnSca\_Uls\_f32 1 k\_LoaMgr\_MotAgSmaAvailResp\_Cnt\_u08 2  $k\_LoaMgr\_MotCurrldptSig0Resp\_Cnt\_u08$ 2 k\_LoaMgr\_MotCurrldptSig1Resp\_Cnt\_u08 0 k\_LoaMgr\_MotCurrldptSig2Resp\_Cnt\_u08 n k\_LoaMgr\_MotCurrldptSigFltThd\_Cnt\_u08 0 k\_LoaMgr\_MotCurrMtgtnRate\_UlspS\_f32 k LoaMgr MotCurrMtgtnScaZeroEn Cnt lgc k LoaMgr MotCurrMtgtnSca Uls f32 1 k\_LoaMgr\_RedcdStRate\_UlspS\_f32 k\_LoaMgr\_RedcdStSca\_Uls\_f32 target\_LoaMgr\_Per1\_DiagcStsIvtr1Inactv\_Cnt\_lgc.value target\_LoaMgr\_Per1\_DiagcStslvtr2Inactv\_Cnt\_lgc.value 1 target\_LoaMgr\_Per1\_HwTqldptSig\_Cnt\_u08.value 2 target\_LoaMgr\_Per1\_LoaScaDi\_Cnt\_lgc.value 0  $target\_LoaMgr\_Per1\_MotCurrIdptSig\_Cnt\_u08.value$ n target\_LoaMgr\_Per1\_MtrPosIdptSig\_Cnt\_u08.value 1  $target\_LoaMgr\_Per1\_SteerMod\_Cnt\_enum.value$ n target\_LoaMgr\_Per1\_TloaAvail\_Cnt\_lgc.value 1 target LoaMgr Per1 TloaDi Cnt lgc.value  $target\_Rte\_Inst\_Ap\_LoaMgr\_Per1\_DiagcStsIvtr1Inactv\_Cnt\_Igc$ target\_LoaMgr\_Per1\_DiagcStslvtr1Inactv\_Cnt\_lgc target\_Rte\_Inst\_Ap\_LoaMgr.LoaMgr\_Per1\_DiagcStsIvtr2Inactv\_Cnt\_Igc target LoaMgr Per1 DiagcStslvtr2Inactv Cnt Igc target\_Rte\_Inst\_Ap\_LoaMgr.LoaMgr\_Per1\_HwTqldptSig\_Cnt\_u08 target\_LoaMgr\_Per1\_HwTqIdptSig\_Cnt\_u08 target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqLoaMtgtnEn Cnt Igc target LoaMgr Per1 HwTqLoaMtgtnEn Cnt lgc  $target\_Rte\_Inst\_Ap\_LoaMgr.LoaMgr\_Per1\_IvtrLoaMtgtnEn\_Cnt\_Igc$ target\_LoaMgr\_Per1\_lvtrLoaMtgtnEn\_Cnt\_lgc target Rte Inst Ap LoaMgr.LoaMgr Per1 LOASt State enum target LoaMgr Per1 LOASt State enum target\_Rte\_Inst\_Ap\_LoaMgr.LoaMgr\_Per1\_LoaScaDi\_Cnt\_lgc target\_LoaMgr\_Per1\_LoaScaDi\_Cnt\_lgc target Rte Inst Ap LoaMgr.LoaMgr Per1 MotAgLoaMtgtnEn Cnt Igc target LoaMgr Per1 MotAgLoaMtgtnEn Cnt lgc target\_Rte\_Inst\_Ap\_LoaMgr.LoaMgr\_Per1\_MotCurrIdptSig\_Cnt\_u08 target\_LoaMgr\_Per1\_MotCurrldptSig\_Cnt\_u08 target\_LoaMgr\_Per1\_MotCurrLoaMtgtnEn\_Cnt\_lgc  $target\_Rte\_Inst\_Ap\_LoaMgr.LoaMgr\_Per1\_MotCurrLoaMtgtnEn\_Cnt\_lgc$  $target\_Rte\_Inst\_Ap\_LoaMgr.LoaMgr\_Per1\_MtrPosIdptSig\_Cnt\_u08$ target\_LoaMgr\_Per1\_MtrPosIdptSig\_Cnt\_u08 target\_Rte\_Inst\_Ap\_LoaMgr.LoaMgr\_Per1\_SteerMod\_Cnt\_enum target\_LoaMgr\_Per1\_SteerMod\_Cnt\_enum  $target\_Rte\_Inst\_Ap\_LoaMgr\_LoaMgr\_Per1\_TloaAvail\_Cnt\_lgc$ target\_LoaMgr\_Per1\_TloaAvail\_Cnt\_lgc target\_Rte\_Inst\_Ap\_LoaMgr.LoaMgr\_Per1\_TloaDi\_Cnt\_lgc target\_LoaMgr\_Per1\_TloaDi\_Cnt\_lgc Name **Actual Value Expected Value** LoaMgr AgSV Cnt M lgc 0 0 LoaMgr\_HwTqIdptSigSV\_Cnt\_M\_u08 0 0 LoaMgr HwTgLoaMtgtnEn Cnt M lgc 0 0 LoaMgr\_HwTqRespSV\_Cnt\_M\_u08 4 4 LoaMgr\_lvtrldptSigSV\_Cnt\_M\_u08 0 0 LoaMgr\_lvtrLoaMtgtnEn\_Cnt\_M\_lgc 0 n LoaMgr\_lvtrRespSV\_Cnt\_M\_u08 5 LoaMgr\_LOASt\_State\_M\_enum 5 5 LoaMgr\_LoaRateLimit\_UlspS\_M\_f32 500 500 LoaMgr\_LoaScaleFctr\_Uls\_M\_f32 0 n LoaMgr MaxMtgtnResp Cnt M u08 5 5 • LoaMar MotAaldptSiaSV Cnt M u08 0 0 LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc 0 0 **~** LoaMgr\_MotAgRespSV\_Cnt\_M\_u08 5 5 LoaMgr\_MotCurrIdptSigSV\_Cnt\_M\_u08 0 0 **~** LoaMgr\_MotCurrLoaMtgtnEn\_Cnt\_M\_lgc 1 1 LoaMgr\_MotCurrRespSV\_Cnt\_M\_u08 2 2 LoaMgr\_TloaAvail\_Cnt\_M\_lgc 1 ~ LoaMgr TloaDi Cnt M lgc n Λ LoaMgr\_VltgMdSrcSV\_Cnt\_M\_u08 ~ Rte\_Call\_Ap\_LoaMgr\_NxtrDiagMgr\_SetNTCStatus(NTC\_Cnt\_T\_enum) {170, 173, 174, 175} {170, 173, 174, 175} **X** Call\_Ap\_LoaMgr\_NxtrDiagMgr\_SetNTCStatus(Param\_Cnt\_T\_u08 Rte\_Call\_Ap\_LoaMgr\_NxtrDiagMgr\_SetNTCStatus(Status\_Cnt\_T\_enum) {1, 1, 1, 1} {1, 1, 1, 1} Rte\_Write\_Ap\_LoaMgr\_LoaRateLimit\_UlspS\_f32(data) 500 500 ± 0.01 Rte Write Ap LoaMgr LoaScaleFctr Uls f32(data) 0 ± 0.001 0 target\_LoaMgr\_Per1\_HwTqLoaMtgtnEn\_Cnt\_lgc.value 0 0 target LoaMgr Per1 lvtrLoaMtgtnEn Cnt lgc.value 0 0 target\_LoaMgr\_Per1\_LOASt\_State\_enum.value 5 5 target LoaMgr Per1 MotAgLoaMtgtnEn Cnt lgc.value 0 0 target\_LoaMgr\_Per1\_MotCurrLoaMtgtnEn\_Cnt\_lgc.value 1



Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	-
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.16 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_IvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target Rte Inst Ap LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrlvtrMtgtnScaZeroEn Cnt lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1
<u> </u>	'





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u	.08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	-
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr HwTqRespSV Cnt M u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr TloaDi Cnt M lgc	0	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 174}	{170, 172, 174}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	•
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	•
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	-
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	•

Test Step Call Trace			<b>✓</b>	
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	<b>✓</b>
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 2.17 (Repeat Count = 1)	<b>□</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV Cnt M u08	*none*  *none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2 4
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4 2
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k LoaMgr HwTqldptSigFltThd Cnt u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k LoaMgr MotAgMtgtnScaZeroEn Cnt lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32	1  1
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
1 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	



Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	0	0	•
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	<b>{1, 1}</b>	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace			<b>✓</b>	
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•

Test Step 2.18 (Repeat Count = 1)	<b>13</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_IvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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LoaMgr\_Per1



Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	1		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k LoaMgr MotAgMtgtnScaZeroEn Cnt lgc	0		
	1		
k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSig2Resp Cnt u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
toward Landley Days Diseaschalates Impacts Cont. Inc. under	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	U		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	4 1 0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	4 1 0 1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	4 1 0 1 0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	4 1 0 1 0 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	4 1 0 1 0 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C	nt_lgc	
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	4 1 0 1 0 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	nt_lgc	
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	4 1 0 1 0 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt	nt_lgc lgc	
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	4 1 0 1 0 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IntrLoaMtgtnEn_IntrLoaMtgtnEn_IntrLoaMtgtnEn_IntrLoaMtgtnEn_IntrLoaMtgtnEn_IntrLoaMtgtnEn_IntrLoaMtgtnEn_I	nt_lgc lgc	
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	4 1 0 1 0 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_vtrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_vtrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_loASt_State_enum	nt_lgc lgc	
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target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08	4 1 0 1 0 1 0 target_LoaMgr_Per1_DiagcStsivtr1Inactv_C target_LoaMgr_Per1_DiagcStsivtr2Inactv_C target_LoaMgr_Per1_DiagcStsivtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgt target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoASt_Dicnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrloaMtgtnEn_Ct target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc Actual Value 0 0 0 0	nt_lgc t_lgc gc  t_lgc 08 Cnt_lgc 8  Expected Value 0 0 0 4	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
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target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc	4 1 0 1 0 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_uclarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cr target_LoaMgr_Per1_MtrPosIdptSig_Cnt_uclarget_LoaMgr_Per1_MtrPosIdptSig_Cnt_uclarget_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 0 4 0 0	et_lgc et_lgc os  et_l	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
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target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqlaptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	4 1 0 1 0 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lg target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LoASc_State_enum target_LoaMgr_Per1_LoASc_Di_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cr_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cr_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cr_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cr_target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 0 5 5 5	nt_lgc t_lgc gc  t_lgc gc  t_lgc gc  Expected Value 0 0 0 4 0 0 5 5	***
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target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASc_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASc_Di_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrlogMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrlogMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrlogMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrloaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	4 1 0 1 0 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_LoaMgr_Per1_MotQLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrloptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaDi_Cnt_lgc active LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 0 5 5 5 500 1	Expected Value	***************************************
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqloaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqloaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaRsepSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08	4 1 0 1 0 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lq target_LoaMgr_Per1_IoASt_State_enum target_LoaMgr_Per1_MotAgLoaMtgtnEn_Crt_uget_LoaMgr_Per1_MotAgLoaMtgtnEn_Crt_uget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct_uget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct_uget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct_uget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct_uget_LoaMgr_Per1_SteerMod_Cnt_enum_target_LoaMgr_Per1_SteerMod_Cnt_enum_target_LoaMgr_Per1_TloaDi_Cnt_lgc_target_LoaMgr_Per1_TloaDi_Cnt_lgc_Actual_Value_0 0 0 5 5 5 500 1 5	Expected Value	***
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASc_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASc_Di_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrlogMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrlogMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrlogMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrloaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	4 1 0 1 0 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_LoaMgr_Per1_MotQLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrloptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaDi_Cnt_lgc active LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 0 5 5 5 500 1	Expected Value	***************************************

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

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Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.19 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5

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Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_	u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc			
		0	
	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0 0 0	0	· ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	0 0 0 0	0 0 0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08	0 0 0 0 0	0 0 0 0	· ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0 0 0 0 0 0	0 0 0 0 0	· ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08	0 0 0 0 0 0 0 5	0 0 0 0 0 0 5	· ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	0 0 0 0 0 0 0 5	0 0 0 0 0 0 5 5	· ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	0 0 0 0 0 0 0 5 5 5	0 0 0 0 0 5 5 5 5	· ·
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LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaStateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglCoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlAgtnEn_Cnt_M_lgc LoaMgr_MotCurrlAgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	0 0 0 0 0 0 0 5 5 5 500 0 0 5 0 0	0 0 0 0 0 5 5 5 5 500 0 0 5 0 0 5	
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LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_LOASt_State_M_enum  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VtgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0 0 0 0 0 0 0 5 5 5 500 0 0 5 0 0 1 2 1 1 1 0 {170, 173, 175}	0 0 0 0 0 5 5 5 500 0 5 0 0 1 2 1 1 0 {170, 173, 175} {0, 0, 0, 0, 0, 0}	
LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_u08  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 0 0 0 0 0 5 5 5 500 0 5 0 0 1 2 1 1 0 {170, 173, 175} {1, 1, 1} {1, 1, 1}	0 0 0 0 0 5 5 5 500 0 5 0 0 1 2 1 1 0 {170, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1}	
LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 0 0 0 0 0 0 5 5 5 500 0 0 5 0 0 1 2 1 1 0 {170, 173, 175} {1, 1, 1} {1, 1, 1} 500	0 0 0 0 0 5 5 5 500 0 5 0 0 1 2 1 1 0 {170, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01	
LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrlcoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvaii_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0 0 0 0 0 0 0 0 0 5 5 5 5 0 0 0 1 1 2 1 1 0 {1,1,1} {1,1,1} 500	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IwTqRespSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0 0 0 0 0 0 0 0 0 5 5 5 5 0 0 0 1 1 2 1 1 0 {1,1,1} {1,1,1} 500 0 0	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VtltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value  target_LoaMgr_Per1_LOASt_State_enum.value	0 0 0 0 0 0 0 0 0 5 5 5 5 0 0 0 1 1 2 1 1 0 {1, 1, 1} {1, 1, 1} 500 0 0 0 0 0	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IwTqRespSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0 0 0 0 0 0 0 0 0 5 5 5 5 0 0 0 1 1 2 1 1 0 {1, 1, 1} {1, 1, 1} 500 0 0 5 1 1 5 0 6 1 1 1 1	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.20 (Repeat Count = 1)	i de la companya de
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k LoaMgr HwTqldptSigFltThd Cnt u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
	1
k_LoaMgr_MotAgMtgtnSca_Uls_f32	2
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08 k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_ui		
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	-
LoaMgr HwTqldptSigSV Cnt M u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr HwTqRespSV Cnt M u08	0	0	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	_
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	_
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	_
LoaMgr TloaAvail Cnt M lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	-
LoaMgr VltgMdSrcSV Cnt M u08	0	0	•
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170}	{170}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1}	{1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	•
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	•
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	-
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	•
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Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	<b>✓</b>
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	~
AssignScale	1	AssignScale	1	<b>✓</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	<b>✓</b>





Test Step 2.21 (Repeat Count = 1)	The state of the s
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV Cnt M u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2 4
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08 k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0 0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1 0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_real_noabl_cnt_gc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Nte_inst_Ap_Loaivigi.Loaivigi_Feri_iviotAgLoaivitgtiiLin_Crit_igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>~</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.22 (Repeat Count = 1)	12
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_IvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Name	Inmut Value		
Name	Input Value		
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc	0 1		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32 k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k LoaMgr HwTqldptSig1TloaAvailResp Cnt u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	0		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedodStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target LoaMgr Per1 MotCurrldptSig Cnt u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	at lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cn target_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	<b>V</b>
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>V</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	<b>*</b>
LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
	-	· ·	

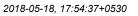




Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.23 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08 k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target LoaMgr Per1 TloaDi Cnt Igc.value	0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inac	etv Cnt Iqc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inac		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt	t_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEr	n_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_en		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lg		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnE		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_C	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrLoaMtgtn target_LoaMgr_Per1_MtrPosIdptSig_Ci		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_el	_	
anger_ne_mer_, p_reaming.reami	targot_zoamgr or i_otoomioa_ont_or		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaAvail Cnt Igc	target LoaMgr Per1 TloaAvail Cnt Ige	C	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgotarget_LoaMgr_Per1_TloaDi_Cnt_lgc	C	
		Expected Value	Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		Result 🗸
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	Expected Value	Result
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0	Expected Value	~
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 0	Expected Value 0 0	~
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  0  4  0	Expected Value 0 0 0 4 0	•
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  0  4  0  0	Expected Value 0 0 0 4 0 0	~
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  0  4  0  5	Expected Value 0 0 0 4 0 5	•
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  0  4  0  5  5	Expected Value  0 0 0 4 0 5 5	• • • • • • • • • • • • • • • • • • •
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_vtrldptSigSV_Cnt_M_u08 LoaMgr_vtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  0  4  0  5  5  500	Expected Value  0 0 0 4 0 5 5 5 500	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_vtrldptSigSV_Cnt_M_u08 LoaMgr_vtrloaMtgtnEn_Cnt_M_lgc LoaMgr_vtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  0  4  0  5  5  500  0	Expected Value  0 0 0 4 0 5 5 5 000 0	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_vtrldptSigSV_Cnt_M_u08 LoaMgr_vtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  0  4  0  5  5  500	Expected Value  0 0 0 4 0 5 5 5 500	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_vtrldptSigSV_Cnt_M_u08 LoaMgr_vtrloaMtgtnEn_Cnt_M_lgc LoaMgr_vtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  0  4  0  5  5  500  0  5	Expected Value  0 0 0 4 0 0 5 5 5 500 0 5	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaSt_atate_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  0  4  0  5  5  500  0  5  0	Expected Value  0 0 0 4 0 0 5 5 5 0 0 0	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_ItwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LOaSt_State_M_enum  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  0  4  0  5  5  500  0  5  0  0  0  0  0  0	Expected Value  0 0 0 4 0 0 5 5 5 0 0 0	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_vtrldptSigSV_Cnt_M_u08 LoaMgr_vtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_vtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  0  0  4  0  5  5  0  0  0  5  0  1	Expected Value  0 0 0 4 0 0 5 5 5 0 0 0 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_VtrldptSigSV_Cnt_M_u08  LoaMgr_VtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_VtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_VtrRespSV_Cnt_M_u08  LoaMgr_VtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgCoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  0  0  4  0  5  5  0  0  5  0  1  2	Expected Value  0 0 0 4 0 0 5 5 5 0 0 1 2	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrlcaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_igc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  0  0  4  0  5  5  500  0  5  0  1  2  0	Expected Value  0 0 0 4 0 0 5 5 5 0 0 1 2 0	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  0  0  4  0  5  5  5  0  0  1  2  0  0  1	Expected Value  0 0 0 0 4 0 0 5 5 5 0 0 1 2 0 0	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  0  0  4  0  5  5  5  0  0  1  2  0  0  1  2  0  0  0  0	Expected Value  0 0 0 0 4 0 0 5 5 5 0 0 1 2 0 0 0 0	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaAttgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_NttDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  0  0  4  0  5  5  0  0  1  2  0  0  1  2  0  0  (170, 173, 174}	Expected Value  0 0 0 4 0 0 5 5 5 0 0 1 2 0 0 {170, 173, 174}	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgMgr_SepSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  0  0  4  0  5  5  5  0  0  1  2  0  0  1  2  0  0  (170, 173, 174)  {1, 1, 1}	Expected Value  0 0 0 0 4 0 0 5 5 5 0 0 0 1 2 0 0 (170, 173, 174) {0, 0, 0, 0, 0}	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaAttgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_NttDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  0  0  4  0  5  5  0  0  1  2  0  0  1  2  0  0  (170, 173, 174}	Expected Value  0 0 0 4 0 0 5 5 5 0 0 1 2 0 0 {170, 173, 174}	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFct_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgdLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgMgr_Soy_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  0  0  4  0  0  5  5  0  0  1  2  0  0  (170, 173, 174)  {1, 1, 1}  {1, 1, 1}	Expected Value  0 0 0 4 0 0 5 5 5 0 0 0 1 2 0 0 (170, 173, 174) {0, 0, 0, 0, 0, 0} {1, 1, 1}	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgdLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgCoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlCaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlCaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlCaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlCaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  0  0  4  0  0  5  5  5  0  0  1  2  0  0  (170, 173, 174)  {1, 1, 1}  {1, 1, 1}  500	Expected Value  0 0 0 4 0 0 5 5 5 500 0 1 2 0 0 11 2 0 0 170,173,174} {0,0,0,0,0} {1,1,1} 500±0.01	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LoaSt_State_M_enum LoaMgr_LoaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgCoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrIcdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIcdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIcdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIcdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIcdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIcdptSigSV_Cnt_M_u08 LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_NotCurrIcdptSigMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  0  0  4  0  0  5  5  5  0  0  1  2  0  0  (170, 173, 174)  {1, 1, 1}  {1, 1, 1}  500  0	Expected Value  0 0 0 4 0 0 5 5 5 500 0 5 0 0 1 2 0 0 11 2 0 0 170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcsV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_loASt_State_enum.value	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  0  0  4  0  0  5  5  5  0  0  1  2  0  0  (170, 173, 174)  (1, 1, 1)  {1, 1, 1}  500  0  0  5  0  0  5  1  1  2  1  1  1  1  1  1  1  1  1  1	Expected Value  0 0 0 4 0 0 5 5 5 5 0 0 1 1 2 0 0 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 0 5	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 0 0 4 0 0 5 5 5 5 0 0 0 1 1 2 0 0 0 {170, 173, 174} {1, 1, 1} {1, 1, 1} 500 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	Expected Value  0 0 0 4 0 0 5 5 5 5 0 0 1 1 2 0 0 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 0 0 0	



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.24 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_IvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0_
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target LoaMgr Per1 HwTqldptSig Cnt u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1I	nacty Cnt loc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target LoaMgr Per1 DiagcStslvtr2l		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target LoaMgr Per1 HwTqldptSig		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqLoaMtgtnEn Cnt Igc	target_LoaMgr_Per1_HwTqLoaMtgt	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnE		
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_		
target_Rte_Inst_Ap_LoaMgr_Deathgr_Fer1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtg		
target_Rte_Inst_Ap_LoaMgr_Der1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSi		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMi		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig		
target_Rte_Inst_Ap_LoaMgr_Der1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target LoaMgr Per1 TloaAvail Cnt		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target LoaMgr Per1 TloaDi Cnt Ig		
			Daguit
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	<b>~</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	<u> </u>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	<b>✓</b>



### Test Case 3: PIM\_R Test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:

TS3.1 5058.00 Cycles
TS3.2 4977.00 Cycles
TS3.3 4970.00 Cycles
TS3.3 4970.00 Cycles
TS3.4 5001.00 Cycles
TS3.5 5063.00 Cycles
TS3.6 5014.00 Cycles
TS3.7 4991.00 Cycles
TS3.7 4991.00 Cycles
TS3.10 5004.00 Cycles
TS3.10 4960.00 Cycles
TS3.11 5102.00 Cycles
TS3.12 5027.00 Cycles
TS3.13 5004.00 Cycles
TS3.14 4980.00 Cycles
TS3.15 5041.00 Cycles
TS3.16 4980.00 Cycles
TS3.17 4980.00 Cycles
TS3.18 5068.00 Cycles
TS3.19 5068.00 Cycles
TS3.19 5068.00 Cycles
TS3.20 5026.00 Cycles
TS3.21 4981.00 Cycles
TS3.22 5041.00 Cycles
TS3.23 5041.00 Cycles
TS3.24 4984.00 Cycles
TS3.25 5041.00 Cycles
TS3.26 4982.00 Cycles
TS3.27 5011.00 Cycles
TS3.28 4960.00 Cycles
TS3.29 5024.00 Cycles
TS3.30 5076.00 Cycles
TS3.31 5038.00 Cycles
TS3.33 5056.00 Cycles
TS3.33 5076.00 Cycles
TS3.33 5056.00 Cycles
TS3.33 5056.00 Cycles
TS3.33 4975.00 Cycles
TS3.34 5023.00 Cycles
TS3.35 4968.00 Cycles
TS3.35 4968.00 Cycles
TS3.36 5057.00 Cycles
TS3.37 5025.00 Cycles
TS3.37 5025.00 Cycles
TS3.33 5025.00 Cycles
TS3.33 5025.00 Cycles
TS3.33 5025.00 Cycles
TS3.33 50550.00 Cycles

### Description

Vector Description:

TS3.1LoaMgr\_HwTqldptSigSV\_Cnt\_M\_u08==>Min
TS3.2LoaMgr\_HwTqldptSigSV\_Cnt\_M\_u08==>Max
TS3.3LoaMgr\_HwTqldptSigSV\_Cnt\_M\_u08==>Pos
TS3.4LoaMgr\_HwTqldptSigSV\_Cnt\_M\_u08==>Min
TS3.5LoaMgr\_HwTqldptSigSV\_Cnt\_M\_u08==>Max
TS3.6LoaMgr\_HwTqldptSigSV\_Cnt\_M\_u08==>Max
TS3.6LoaMgr\_HwTqldptSigSV\_Cnt\_M\_u08==>Pos
TS3.7LoaMgr\_MotCurrldptSigSV\_Cnt\_M\_u08==>Min
TS3.8LoaMgr\_MotCurrldptSigSV\_Cnt\_M\_u08==>Max
TS3.9LoaMgr\_MotCurrldptSigSV\_Cnt\_M\_u08==>Max
TS3.9LoaMgr\_MotCurrldptSigSV\_Cnt\_M\_u08==>Max
TS3.10LoaMgr\_IvtrldptSigSV\_Cnt\_M\_u08==>Max
TS3.11LoaMgr\_IvtrldptSigSV\_Cnt\_M\_u08==>Max
TS3.12LoaMgr\_IvtrldptSigSV\_Cnt\_M\_u08==>Min
TS3.13LoaMgr\_SnsrlsMotAgAvail\_Cnt\_M\_lgc==>Min
TS3.14LoaMgr\_SnsrlsMotAgAvail\_Cnt\_M\_lgc==>Min
TS3.14LoaMgr\_SnsrlsMotAgAvail\_Cnt\_M\_u08==>Min
TS3.16LoaMgr\_MotAgRespSV\_Cnt\_M\_u08==>Max
TS3.17LoaMgr\_MotAgRespSV\_Cnt\_M\_u08==>Max
TS3.17LoaMgr\_MotCurrRespSV\_Cnt\_M\_u08==>Min
TS3.19LoaMgr\_MotCurrRespSV\_Cnt\_M\_u08==>Max
TS3.20LoaMgr\_MotCurrRespSV\_Cnt\_M\_u08==>Max
TS3.21LoaMgr\_MotCurrRespSV\_Cnt\_M\_u08==>Max
TS3.22LoaMgr\_IvtrRespSV\_Cnt\_M\_u08==>Pos
TS3.22LoaMgr\_IvtrRespSV\_Cnt\_M\_u08==>Max
TS3.23LoaMgr\_IvtrRespSV\_Cnt\_M\_u08==>Max
TS3.25LoaMgr\_IvtrRespSV\_Cnt\_M\_u08==>Max
TS3.25LoaMgr\_HwTqRespSV\_Cnt\_M\_u08==>Max
TS3.25LoaMgr\_HwTqRespSV\_Cnt\_M\_u08==>Max
TS3.25LoaMgr\_HwTqRespSV\_Cnt\_M\_u08==>Max
TS3.25LoaMgr\_HwTqRespSV\_Cnt\_M\_u08==>Max
TS3.29LoaMgr\_HwTqLoaMtgtnEn\_Cnt\_M\_lgc==>Min
TS3.22LoaMgr\_HwTqLoaMtgtnEn\_Cnt\_M\_lgc==>Min
TS3.32LoaMgr\_HwTQLoaMtgtnEn\_Cnt\_M\_lgc==>Min
TS3.32LoaMgr\_HwTQLoaMtgtnEn\_Cnt\_M\_lgc==>Min
TS3.32LoaMgr\_HotCurrLoaMtgtnEn\_Cnt\_M\_lgc==>Max
TS3.33LoaMgr\_NotCurrLoaMtgtnEn\_Cnt\_M\_lgc==>Max
TS3.33LoaMgr\_IvtrLoaMtgtnEn\_Cnt\_M\_lgc==>Max
TS3.33LoaMgr\_IvtrLoaMtgtnEn\_Cnt\_M\_lgc==>Max
TS3.36LoaMgr\_IvtrLoaMtgtnEn\_Cnt\_M\_lgc==>Max
TS3.36LoaMgr\_IvtrLoaMtgtnEn\_Cnt\_M\_lgc==>Max
TS3.36LoaMgr\_IvtrLoaMtgtnEn\_Cnt\_M\_lgc==>Max
TS3.36LoaMgr\_IvtrLoaMtgtnEn\_Cnt\_M\_lgc==>Max
TS3.36LoaMgr\_IvtrLoaMtgtnEn\_Cnt\_M\_lgc==>Max
TS3.36LoaMgr\_IvtrLoaMtgtnEn\_Cnt\_M\_lgc==>Max
TS3.36LoaMgr\_IvtrLoaMtgtnEn\_Cnt\_M\_lgc==>Max
TS3.38LoaMgr\_IvtrLoaMtgtnEn\_Cnt\_M\_lgc==>Max
TS3.38LoaMgr\_IvtrLoaM

Test Step 3.1 (Repeat Count = 1)		🔀
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_lvtrRespSV_Cnt_M_u08	4	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	

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LoaMgr_AgSV_Cnt_M_lgc         0         0         0           LoaMgr_HwTqldptSigSV_Cnt_M_u08         0         0         0           LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc         1         1         1           LoaMgr_HwTqRespSV_Cnt_M_u08         2         2         2           LoaMgr_IvtrldptSigSV_Cnt_M_u08         0         0         0	Name	Input Value		
Lisabley Device Report Cort No. 100   1	LoaMgr_MotAgRespSV_Cnt_M_u08	5		
Leading_Sentinstanglesis_Core_Mu_Disc	LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0		
Looking / Looking Commentage   Looking   Loo				
Landay   L				
Rig.   mart   Apr.   Loading				
Lacety Controllaginal Carrier Cort Up				
Landing_Christophigosica_Us_02				
Landary   Land				
Loading-Inviriging(Interlaidings) (Pt 100)		1		
Landary_Invirginging Treatmanseq_Cort_units	k_LoaMgr_FadeOutStRate_UlspS_f32	1		
Landary_Markphage TransPrince_Cru_108	k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08			
L.Confey_HVT-0pt0592Tent=niven_Cnt_U08				
C.LOADIG_INFORESIDENTS_CRT_USS_RES_				
Looking Introduction Resp. Cent. 408   0				
Loaking/Indexigned Lipes   12		0		
Loakgry_Intringinsac_Prints_Chulge	k_LoaMgr_lvtrldptSig2Resp_Cnt_u08			
L.Coahlgr_MoNdquiptSig1NoSmaResp_Cnt_u08				
L. Loaking_ ModigletSigRitene_Cit_USB				
Loakigr Morkg\https://decides.com/spc.com/sp				
		1		
Loaking_MotCurridptSigResp_Cnt_U08   2	k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
	k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_Loaklgr_MotCurridptSigFiTThd_Cn1_u08         0           k_Loaklgr_MotCurridptSigFiTThd_Cn1_u08         1           k_Loaklgr_MotCurridptSica_Uis_S2         1           target_Loaklgr_Per1_DispcSistra_Uis_S2         1           target_Loaklgr_Per1_DispcSistra_Uis_S2         1           target_Loaklgr_Per1_DispcSistra_Uis_S2         1           target_Loaklgr_Per1_LoaScaDl_Cnt_Uis_value         1           target_Loaklgr_Per1_MotCurridptSig_Cnt_Uis_value         1           target_Loaklgr_Per1_MotCurridptSig_Cnt_Uis_value         1           target_Loaklgr_Per1_Sizechod_Cin_temun_value         1           target_Loaklgr_Per1_Sizechod_Cin_temun_value         1           target_Loaklgr_Per1_Diacy_Cistra_Uis_Value         1           target_Loaklgr_Per1_Diacy_Cistra_Uis_Value         1           target_Loaklgr_Per1_Diacy_Cistra_Uis_Value         1           target_Loaklgr_Per1_Diacy_Cistra_Uis_Value         1           target_Loaklgr_Per1_Diacy_Cistra_Uis_Va				
k_Loaklgr_MotCurnMighrRate_UlspS_f32         1           k_Loaklgr_MotCurnMighrRate_UlspS_f32         1           k_Loaklgr_MotCurnMighrSac_PeroEn_Cnt_lgc         1           k_Loaklgr_RedodSiRate_UlspS_f32         1           k_Loaklgr_RedodSiRate_UlspS_f32         1           k_Loaklgr_Pert_DiagcSishtvflinactv_Cnt_lgc value         1           target_Loaklgr_Pert_DiagcSishtvflinactv_Cnt_lgc value         1           target_Loaklgr_Pert_MotCurldptSig_Cnt_u08 value         1           target_Loaklgr_Pert_LoaScaD_Cnt_lgc-value         1           target_Loaklgr_Pert_MotCurldptSig_Cnt_u08 value         1           target_Loaklgr_Pert_MotCurldptSig_Cnt_u08 value         3           target_Loaklgr_Pert_ToaD_Cnt_lgc-value         1           target_Loaklgr_Pert_ToaD_Cnt_lgc-value         1           target_Loaklgr_Pert_ToaD_Cnt_lgc-value         1           target_Loaklgr_Pert_ToaD_Cnt_lgc-value         1           target_Rel_Inst_Ap_Loaklgr_Loaklgr_Pert_HirqfqbtSig_Cnt_u08         1           target_Rel_Inst_Ap_Loaklgr_Loaklgr_Pert_HirqfqbtSig_Cnt_u08         target_Loaklgr_Pert_DiagcStsivtr2inactv_Cnt_lgc           target_Rel_Inst_Ap_Loaklgr_Loaklgr_Pert_HirdfqbtSig_Cnt_u08         target_Loaklgr_Pert_HirdfqbtSig_Cnt_u08           target_Rel_Inst_Ap_Loaklgr_Loaklgr_Pert_HirdfqbtSig_Cnt_u08         target_Loaklgr_Pert_HirdfqbtSig_Cnt_u08				
k_LoaMgr_MotCurrMightsCazPeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMightsCaz Ust_52         1           k_LoaMgr_RedcdSitScaz_Uls_52         1           k_LoaMgr_RedcdSitScaz_Uls_52         1           k_LoaMgr_RedcdSitScaz_Uls_52         1           k_LoaMgr_RedcdSitScaz_Uls_52         1           target_LoaMgr_Pert_DiagcSitsWr2Inactv_Cnt_lgc value         1           target_LoaMgr_Pert_MotCurrDisg_Cnt_Uo8 value         1           target_LoaMgr_Pert_MotCurrDisg_Cnt_Uo8 value         1           target_LoaMgr_Pert_MotCurrDisg_Cnt_Uo8 value         1           target_LoaMgr_Pert_MotCurrDisg_Cnt_Uo8 value         1           target_LoaMgr_Pert_ToaAval_Cnt_lgc_value         1           target_LoaMgr_Pert_ToaAval_Cnt_lgc_value         1           target_LoaMgr_Pert_ToaAval_Cnt_lgc_value         1           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Pert_DiagcStstvtr2Inactv_Cnt_lgc         target_LoaMgr_Pert_DiagcStstvt2Inactv_Cnt_lgc           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Pert_HwTqtdptSig_Cnt_Uo8         target_LoaMgr_Pert_DiagcStstvt2Inactv_Cnt_lgc           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Pert_HwTqtdptSig_Cnt_Uo8         target_LoaMgr_Pert_HwTqtdptSig_Cnt_Uo8           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Pert_MotCatAMgrEn_Cnt_lgc         target_LoaMgr_Pert_HwTqtdptSig_Cnt_Uo8           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Pert_MotCatAMgrEn_Cnt_lgc         target_Loa				
k_Loaklgr_MotCurnktgnRSca_Uis_F32         1           k_Loaklgr_RedodStSca_Uis_F32         1           k_Loaklgr_RedodStSca_Uis_F32         1           target_Loaklgr_Pert_DiagcStshvtrInactv_Cnt_lgc_value         1           target_Loaklgr_Pert_DiagcStshvtrInactv_Cnt_lgc_value         1           target_Loaklgr_Pert_HwTqlqbtSig_Cnt_u08_value         1           target_Loaklgr_Pert_McCurrldptSig_Cnt_u08_value         1           target_Loaklgr_Pert_McCurrldptSig_Cnt_u08_value         1           target_Loaklgr_Pert_McCurrldptSig_Cnt_u08_value         1           target_Loaklgr_Pert_Timoatv_Cnt_lgc_value         1           target_Loaklgr_Pert_Timoatv_Cnt_lgc_value         1           target_Loaklgr_Pert_Timoatv_Cnt_lgc_value         1           target_Loaklgr_Pert_Timoatv_Cnt_lgc_value         1           target_Loaklgr_Pert_Timoatv_Cnt_lgc_value         0           target_Re_inst_Ap_Loaklgr_Pert_DiagcStshvtzlnactv_Cnt_lgc         target_Loaklgr_Pert_DiagcStshvtzlnactv_Cnt_lgc           target_Re_inst_Ap_Loaklgr_Pert_HvTqldptSig_Cnt_u08         target_Loaklgr_Pert_HvTqldptSig_Cnt_u08           target_Re_inst_Ap_Loaklgr_Pert_HvTqloakltginEn_Cnt_lgc         target_Loaklgr_Pert_HvTqloakltginEn_Cnt_lgc           target_Re_inst_Ap_Loaklgr_Pert_LoakltginEn_Cnt_lgc         target_Loaklgr_Pert_HvTqloakltginEn_Cnt_lgc           target_Re_inst_Ap_Loaklgr_Pert_Mort_Noaklgr_Pert_Noak				
LoaMgr   RedodStSca   Uls   F32				
target_LoaMgr_Perl_DiagoStstvtr1Inactv_Cnt_Igc value 1 target_LoaMgr_Perl_DiagoStstvtr2Inactv_Cnt_Igc value 1 target_LoaMgr_Perl_MortQridptSig_Cnt_U08 value 1 target_LoaMgr_Perl_MortQridptSig_Cnt_U08 value 1 target_LoaMgr_Perl_MortQridptSig_Cnt_U08 value 1 target_LoaMgr_Perl_MortQridptSig_Cnt_U08 value 1 target_LoaMgr_Perl_SteerMod_Cnt_enum.value 1 target_LoaMgr_Perl_ToaAvail_Cnt_Igc.value 1 target_LoaMgr_Perl_ToaAvail_Cnt_Igc.value 1 target_LoaMgr_Perl_ToaAvail_Cnt_Igc.value 1 target_LoaMgr_Perl_ToaDol_Cnt_Igc.value 1 target_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_DiagoStstvtr1Inactv_Cnt_Igc 1 target_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_DiagoStstvtr1Inactv_Cnt_Igc 1 target_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_HviTqtoaMgrhen_Cnt_Igc 1 target_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_HviTqtoaMgrhen_Cnt_Igc 1 target_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_HviTqtoaMgrhen_Cnt_Igc 1 target_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_LoASc_State_enum 1 target_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_LoaSc_State_enum 1 target_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_LoaSc_State_enum 1 target_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_MortQatdStg_Cnt_U08 1 target_Re_Inst_Ap_LoaMgr_Perl_MortQatdStg_Cnt_U08 1 target_Re_Inst_Ap_LoaMgr_Perl_MortQatdStg_Cnt_U08 1 target_Re_Inst_Ap_LoaMgr_Perl_MortQatdStg_Cnt_U08 1 target_Re_Inst_Ap_LoaMgr_Perl_ToaDi_Cnt_Igc 1 target_Re_Inst_Ap_LoaMgr_Perl_ToaDi_C	k_LoaMgr_RedcdStRate_UlspS_f32	1		
target_LoaMgr_Perr_IhvTqlqptSig_Cnt_u08 value 1 target_LoaMgr_Perr_IhvTqlqptSig_Cnt_u08 value 1 target_LoaMgr_Perr_IhvTqlqptSig_Cnt_u08 value 1 target_LoaMgr_Perr_IhvTqlqptSig_Cnt_u08 value 1 target_LoaMgr_Perr_McCurridptSig_Cnt_u08 value 1 target_LoaMgr_Perr_McCurridptSig_Cnt_u08 value 1 target_LoaMgr_Perr_SiteerMod_Cnt_enum_value 1 target_LoaMgr_Perr_TicaMgr_Perr_T	k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_McDcurridptSig_Cnt_u08.value 1 target_LoaMgr_Per1_McDsidptSig_Cnt_u08.value 3 target_LoaMgr_Per1_McPosidptSig_Cnt_u08.value 3 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 1 target_LoaMgr_Per1_TioaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TioaDci_Cnt_lgc.value 0 target_RoaMgr_Der1_TioaDci_Cnt_lgc.value 1 target_RoaMgr_Der1_TioaDci_Cnt_lgc.value 1 target_RoaMgr_Der1_DoaMgr_Der1_DiagcStstvtr1inactv_Cnt_lgc 1 target_RoaMgr_Der1_DoaMgr_Der1_HwTqldptSig_Cnt_u08 1 target_RoaMgr_Der1_McDaMgr_Per1_HwTqldptSig_Cnt_u08 1 target_RoaMgr_Der1_McDaMgr_Per1_HwTqloaMlgnEn_Cnt_lgc 1 target_RoaMgr_DoaMgr_Der1_HwTqloaMlgnEn_Cnt_lgc 1 target_RoaMgr_DoaMgr_Der1_HwTqloaMlgnEn_Cnt_lgc 1 target_RoaMgr_DoaMgr_Per1_McDaMgr_Der				
target_LoaMgr_Perr_MotCurridptSig_Cnt_u08.value target_LoaMgr_Perr_MirPosidptSig_Cnt_u08.value target_LoaMgr_Perr_MirPosidptSig_Cnt_u08.value target_LoaMgr_Perr_TioaAvail_Cnt_lgc_value target_LoaMgr_Perr_TioaAvail_Cnt_lgc_value target_LoaMgr_Perr_TioaAvail_Cnt_lgc_value target_LoaMgr_Perr_TioaAvail_Cnt_lgc_value target_LoaMgr_Perr_TioaAvail_Cnt_lgc_value target_RoaMgr_Perr_DiagCStsIvtr2Inactv_Cnt_lgc target_RoaMgr_Perr_DiagCStsIvtr2Inactv_Cnt_lgc target_RoaMgr_Perr_DiagCStsIvtr2Inactv_Cnt_lgc target_RoaMgr_DaMgr_Perr_DiagCStsIvtr2Inactv_Cnt_lgc target_RoaMgr_DaMgr_Perr_DiagCStsIvtr2Inactv_Cnt_lgc target_RoaMgr_DaMgr_Perr_DiagCStsIvtr2Inactv_Cnt_lgc target_RoaMgr_DaMgr_Perr_DiagCStsIvtr2Inactv_Cnt_lgc target_RoaMgr_DaMgr_Perr_DiagCStsIvtr2Inactv_Cnt_lgc target_RoaMgr_DaMgr_Perr_DaMgr_DaMgr_Perr_DaMgr_DaMgr_DaMgr_DaMgr_Perr_DaMgr_				
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqloaMtghEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_IntrloaMtghEn_Cnt_lgc target_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_MortAgLoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MortAgLoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MortCurrLoaMtghEn_Cnt_lgc target_LoaMgr_Per1_MortCurrLoaMtghEn_Cnt_lgc target_LoaMgr_Per1_MortCurrLoaMtghEn_Cnt_lgc target_LoaMgr_Per1_MortCurrLoaMtghEn_Cnt_lgc target_LoaMgr_Per1_MortCurrLoaMtghEn_Cnt_lgc target_LoaMgr_Per1_MortCurrLoaMtghEn_Cnt_lgc target_LoaMgr_Per1_MortCurrLoaMtghEn_Cnt_lgc target_LoaMgr_Per1_MortCurrLoaMtghEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MortCurrLoaMtghEn_Cnt_lgc target_LoaMgr_Per1_MortCurrLoaMtghEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_ToaAvail_Cnt_lgc target_LoaMgr_Per1_ToaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_ToaAvail_Cnt_lgc target_LoaMgr_Per1_ToaAvail_Cnt_lgc target_LoaMgr_Per1_ToaAvail_Cnt_lgc target_LoaMgr_Per1_ToaAvail_Cnt_lgc target_LoaMgr_Per1_ToaAvail_Cnt_lgc target_LoaMgr_Per1_ToaAvail_Cnt_lgc target_LoaMgr_Per1_ToaAvail_Cnt_lgc target_LoaMgr_Per1_ToaAvail_Cnt_lgc target_LoaMgr_Per1_ToaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_ToaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMg				
target_LoaMgr_Perl_SteerMod_Cnt_enum.value 1 target_LoaMgr_Perl_TloaAvaii_Cnt_lgc_value 1 target_LoaMgr_Perl_TloaDi_Cnt_lgc_value 1 target_LoaMgr_Perl_TloaDi_Cnt_lgc_value 0 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_DiagcStsivtrInactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_DiagcStsivtrInactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_DiagcStsivtrInactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_DiagcStsivtrInactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Perl_HwTqldptsig_Cnt_u08 target_LoaMgr_Perl_HwTqldptsig_Cnt_u08 target_LoaMgr_Perl_HwTqldptsig_Cnt_u08 target_LoaMgr_Perl_HwTqldptsig_Cnt_u08 target_LoaMgr_Perl_HwTqldptsig_Cnt_u08 target_LoaMgr_Perl_HwTqldptsig_Cnt_u08 target_LoaMgr_Perl_HwTqldptsig_Cnt_u08 target_LoaMgr_Perl_HwTqldptsig_Cnt_u08 target_LoaMgr_Perl_HwTqldptsig_Cnt_u08 target_LoaMgr_Perl_HwTqldptsig_Cnt_u08 target_LoaMgr_Perl_IoaMgr_Perl_LoaMgr_Perl_LoaMgr_Perl_Worldptsig_Cnt_u08 target_LoaMgr_Perl_LoaMgr_Perl_LoaMgr_Perl_LoaMgr_Perl_Worldptsig_Cnt_u08 target_LoaMgr_Perl_MolAg_LoaMgr_Perl_MolAg_LoaMgr_Perl_Worldptsig_Cnt_u08 target_LoaMgr_Perl_MolAg_LoaMgr_Perl_MolAg_LoaMgr_Perl_Worldptsig_Cnt_u08 target_LoaMgr_Perl_MolAg_LoaMgr_Perl_MolAg_LoaMgr_Perl_MolAg_LoaMgr_Perl_Worldptsig_Cnt_u08 target_LoaMgr_Perl_MolAg_LoaMg				
target_LoaMgr_Per1_TloaAvail_Cnt_lgc_value 1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc_value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc_value 1 target_LoaMgr_Per1_DiagcStsivtr1lnactv_Cnt_lgc 1 target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc 1 target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc 1 target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldoaMtginEn_Cnt_lgc 1 target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldoaMtginEn_Cnt_lgc 1 target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqloaMtginEn_Cnt_lgc 1 target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum 1 target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum 1 target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_Most_Coatligc 1 target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_Most_Coatligc 1 target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_Most_Coatligc 1 target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_Most_Coatligc 1 target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_Most_Coatligc 1 target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_Most_Coatligc 1 target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_Most_Coatligts_Cnt_u08 1 target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_Most_Coatligts_Cnt_u08 1 target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_Most_Coatligts_Cnt_u08 1 target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_Most_Coatligts_Cnt_u08 1 target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_Most_Coatligts_Cnt_u08 1 target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum 1 target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc 1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc 1 target_Lo				
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value				
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_VtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MortQatidptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MortQatidptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MortQatidptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MortQurrloptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MortQurrloptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MortQurrloptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MortQurrloptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum  target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc  target_LoaMgr_Per1_TloaAvail_Cnt_lgc  target_LoaMgr_Per1_TloaAvail_Cnt_lgc  target_LoaMgr_Per1_TloaAvail_Cnt_lgc  target_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  Actual Value  Expected Value  Resu  LoaMgr_HwTqloaMtgtnEn_Cnt_M_u08  0  0  0  0  LoaMgr_HwTqloaMtgtnEn_Cnt_M_u08  0  0  0  0  0  0  0  0  0  0  0  0				
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoAScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc target_LoaMgr_Per1_TloaAvail_Cnt_Igc target_LoaMgr_Per1_TloaAvail_Cnt_Igc target_LoaMgr_Per1_TloaAvail_Cnt_Igc target_LoaMgr_Per1_TloaAvail_Cnt_Igc target_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  Actual Value  Expected Value  Resu  LoaMgr_HwTqltpfSigSV_Cnt_M_u08  LoaMgr_HwTqltpfSigSV_Cnt_M_u08  LoaMgr_HwTqltpfSigSV_Cnt_M_u08  LoaMgr_HwTqltpfSigSV_Cnt_M_u08  LoaMgr_IvtrloptSigSV_Cnt_M_u08  LoaMgr_IvtrloptSigSV_Cnt_M_u08				
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  Actual Value  Expected Value  Resu  LoaMgr_HwTqldptSigSV_Cnt_M_u08  0  0  0  10  10  10  10  10  10  10	target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrlopRig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrlopRig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_TioaDi_Cnt_Igc target_LoaMgr_Per1_TioaDi_Cnt_Igc  Name  Actual Value  Expected Value Resu  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_vtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_Igc target_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc target_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  Actual Value  LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  0  0  0  LoaMgr_HwTqldptSigSV_Cnt_M_u08  2  2  LoaMgr_IvtrldptSigSV_Cnt_M_u08  0  0  0  0  0  0  0  0  0  0  0  0				
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_Igc target_LoaMgr_Per1_TloaDi_Cnt_Igc target_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  Actual Value Expected Value Resu  LoaMgr_AgSV_Cnt_M_Igc  0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0				
target_Rte_Inst_Ap_LoaMgr_Per1_LoaSaaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_Igc target_LoaMgr_Per1_TloaAvail_Cnt_Igc target_LoaMgr_Per1_TloaDi_Cnt_Igc target_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  Actual Value Expected Value Resu LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc 1 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc 1 LoaMgr_HwTqRespSV_Cnt_M_u08  2 2 2 LoaMgr_IvtrIdptSigSV_Cnt_M_u08  0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0			gc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MtrPosldptsigSv_Cnt_MotOs 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0				
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  Actual Value Expected Value Resu LoaMgr_AgSV_Cnt_M_lgc 0 0 0 LoaMgr_HwTqldptSigSV_Cnt_M_u08 0 0 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc 1 LoaMgr_HwTqRespSV_Cnt_M_u08 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0			nt lac	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_Igc target_LoaMgr_Per1_TloaAvail_Cnt_Igc target_LoaMgr_Per1_TloaDi_Cnt_Igc target_LoaMgr_Per1_TloaDi_Cnt_Igc target_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  Actual Value Expected Value Resu LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08				
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  Actual Value Expected Value Resu LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IndptSigSV_Cnt_M_u08				
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc         target_LoaMgr_Per1_TloaAvail_Cnt_lgc           Name         Actual Value         Expected Value         Resu           LoaMgr_AgSV_Cnt_M_lgc         0         0         0           LoaMgr_HwTqldptSigSV_Cnt_M_u08         0         0         0           LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc         1         1         1           LoaMgr_HwTqRespSV_Cnt_M_u08         2         2         2           LoaMgr_IvtrldptSigSV_Cnt_M_u08         0         0         0				
target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_lgc         target_LoaMgr_Per1_TloaDi_Cnt_lgc           Name         Actual Value         Expected Value         Result           LoaMgr_AgSV_Cnt_M_lgc         0         0         0           LoaMgr_HwTqldptSigSV_Cnt_M_u08         0         0         0           LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc         1         1         1           LoaMgr_HwTqRespSV_Cnt_M_u08         2         2         2           LoaMgr_IvtrldptSigSV_Cnt_M_u08         0         0         0	target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
Name         Actual Value         Expected Value         Result           LoaMgr_AgSV_Cnt_M_lgc         0         0         0         0           LoaMgr_HwTqldptSigSV_Cnt_M_u08         0         0         0         0           LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc         1         1         1         0           LoaMgr_HwTqRespSV_Cnt_M_u08         2         2         2         0           LoaMgr_IvtrldptSigSV_Cnt_M_u08         0         0         0         0	target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc			
LoaMgr_AgSV_Cnt_M_lgc         0         0         0           LoaMgr_HwTqldptSigSV_Cnt_M_u08         0         0         0           LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc         1         1         1           LoaMgr_HwTqRespSV_Cnt_M_u08         2         2         2           LoaMgr_lvtrldptSigSV_Cnt_M_u08         0         0         0				
LoaMgr_HwTqldptSigSV_Cnt_M_u08         0         0           LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_HwTqRespSV_Cnt_M_u08         2         2           LoaMgr_lvtrldptSigSV_Cnt_M_u08         0         0				Result
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc         1         1         6           LoaMgr_HwTqRespSV_Cnt_M_u08         2         2         2           LoaMgr_IvtrldptSigSV_Cnt_M_u08         0         0         6				~
LoaMgr_HwTqRespSV_Cnt_M_u08         2         2           LoaMgr_IvtrldptSigSV_Cnt_M_u08         0         0				
LoaMgr_lvtrldptSigSV_Cnt_M_u08 0 0				<b>*</b>
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc 0 0		0	0	<b>~</b>





Name	Actual Value	Expected Value	Result
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 175}	{170, 172, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	•

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>~</b>

Test Step 3.2 (Repeat Count = 1)	<b>13</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	4
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1





Name	Input Value		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	2		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	:nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C	:nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ci		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_ut target_LoaMgr_Per1_SteerMod_Cnt_enum	JO	
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_Igc	target LoaMgr Per1 TloaAvail Cnt lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_Igc	target LoaMgr Per1 TloaDi Cnt Igc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	- Toodit
LoaMgr_HwTqldptSigSV_Cnt_M_u08	4	4	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	3	3	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	1	1	<b>*</b>
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	-
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	-
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>~</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	-
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 175}	{170, 173, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~





Name	Actual Value	Expected Value	Result
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc.value	1	1	✓

Fest Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.3 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr lvtrLoaMtgtnEn Cnt M lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr MotAgldptSigSV Cnt M u08	1
LoaMgr MotAgLoaMtgtnEn Cnt M lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr MotCurrldptSigSV Cnt M u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr MotCurrRespSV Cnt M u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte Inst Ap LoaMgr	target Rte Inst Ap LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrivtrMtgtnScaZeroEn Cnt lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k LoaMgr HwTqldptSig1TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k LoaMgr HwTqldptSigFltThd Cnt u08	1
	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_UIs_f32	0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0





Name	Input Value		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	:_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lg	jc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	t lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0		
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target LoaMgr Per1 TloaAvail Cnt Igc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	- Toodit
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	-
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	-
LoaMgr_HwTqRespSV_Cnt_M_u08	3	3	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr lytrRespSV Cnt M u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr LoaScaleFctr Uls M f32	0	0	•
	5	5	
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	-
	1	1	
LoaMgr_MotAgRoapSV_Cat_M_u09	2	2	
LoaMgr_MotAgRespSV_Cnt_M_u08	1	1	
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	1	-
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	2	2	
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	
LoaMgr_TloaAvail_Cnt_M_lgc			
LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr_VitgMdSrcSV_Cnt_M_u08			•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170}	{170}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	<del>{1}</del>	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1}	{1} 500 + 0.04	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	•



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	•
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.4 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr MotAgldptSigSV Cnt M u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k LoaMgr MotAgldptSig2Resp Cnt u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc.value	1
J <u>J J J J J</u>	





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Ci	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	-	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target Rte Inst Ap LoaMgr.LoaMgr Per1 MtrPosldptSig Cnt u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M Igc	0	0	✓ ×
LoaMgr HwTqldptSiqSV Cnt M u08	2	2	_
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	-
LoaMgr lvtrldptSigSV Cnt M u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	·
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	_
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	-
LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	-
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 173, 175}	{170, 173, 175}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_endin)	{1, 1, 1}	{0, 0, 0, 0, 0}	W
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)		{1, 1, 1}	
Rte Write Ap LoaMgr LoaRateLimit UlspS f32(data)	{1, 1, 1} 500	500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_hw1qLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	
target LoaMgr Per1 LOASt State enum.value	5	5	_
· - ·	0	0	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	-
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	

Fest Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 3.5 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	5
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4 2
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k LoaMgr MotAgMtgtnRate UlspS f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k LoaMgr MotAgMtgtnSca Uls f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_UIs_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMigr_Per1_Loascabi_Crit_igc target_LoaMigr_Per1_MotAgLoaMigtnEn_Crit_igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
	,





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	5	5	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_IvtrRespSV_Cnt_M_u08	1	1	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	3	3	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Fest Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.6 (Repeat Count = 1)	Name of the state
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	5
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

2018-05-18, 17:54:37+0530

Input Value



Name



k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1		
k LoaMgr FadeOutStRate UlspS f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
	2		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
	0		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08			
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSig2Resp Cnt u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
	1		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc			
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
	4		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value			
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4 1 1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	4 1 1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtPosIdptSig_Cnt_u08.value	4 1 1 2		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	4 1 1 2 1		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_lgc.value	4 1 1 2 1	nt_lgc	
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target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	4 1 1 2 1 1 1 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt_lgc	
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target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc	1 1 1 2 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	nt_lgc  t_lgc gc  st_lgc gc st_lgc s	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrlOaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HvTqloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08	1 1 1 2 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	nt_lgc  t_lgc gc  at_lgc gc  at_lgc gc   Expected Value  0 0 0 5 1 1 1 2	***
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_HwTqLoaMtgtnEn_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrloaNtgtnEn_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	1 1 1 2 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	nt_lgc  t_lgc gc  at_lgc gc  at_lgc ga  Expected Value  0 0 0 5 1 1 1 2 5	***
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_HwTqltpSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaRtelmint_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	1 1 1 2 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Expected Value	***
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaRsteLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	4 1 1 1 2 1 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_LVrLoaMtgtnEn_Cnt target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_MotAgLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cttarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Cttarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Cttarget_LoaMgr_Per1_TioaAvail_Cnt_lgctarget_LoaMgr_Per1_TioaAvail_Cnt_lgctarget_LoaMgr_Per1_TioaDi_Cnt_lgc  Actual Value 0 0 0 5 1 1 2 5 500 1	nt_lgc t_lgc gc  st_lgc	***
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqlptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaRtelmit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Expected Value	***

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	✓
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	1	1 ± 0.001	✓
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	<b>~</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>~</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.7 (Repeat Count = 1)	<b>X</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





N	In most Malora		
Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k LoaMgr MotAgMtgtnRate UlspS f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32 k_LoaMgr_RedcdStRate_UlspS_f32	1		
k LoaMgr RedcdStSca Uls f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inact		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inact		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_IvtrLoaMtgtnEn_C target_LoaMgr_Per1_LOASt_State_enu		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEr		
target_Rte_Inst_Ap_LoaMgr_Der1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnl	_	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_en	um	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name			
TAILLY	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	Actual Value	Expected Value	Result
			Result
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1 2 1	1 2 1	· ·
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	1 2 1 4	1 2 1 4	~
LoaMgr_Ag\$V_Cnt_M_lgc LoaMgr_HwTqldptSig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_lvtrldptSig\$V_Cnt_M_u08	1 2 1 4 0	1 2 1 4 0	· ·
LoaMgr_Ag\$V_Cnt_M_lgc LoaMgr_HwTqldptSig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_lvtrldptSig\$V_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1 2 1 4 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	1 2 1 4 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	· ·
LoaMgr_Ag\$V_Cnt_M_lgc LoaMgr_HwTqldptSig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_lvtrldptSig\$V_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrResp\$V_Cnt_M_u08	1 2 1 4 0 1 5 5	1 2 1 4 0 1 1 5	~
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	1 2 1 4 0 1 5 5 5	1 2 1 4 0 1 5 5 5	• • • • • • • • • • • • • • • • • • •
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	1 2 1 4 0 1 5 5 5 500	1 2 1 4 0 1 5 5 5 500	
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32	1 2 1 4 0 0 1 5 5 5 500 1 1	1 2 1 4 0 1 5 5 5 500 1 1	
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaState_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 2 1 4 0 0 1 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5	1 2 1 4 0 0 1 5 5 5 500 1 5 5	
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaState_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	1 2 1 4 0 0 1 5 5 5 500 1 1	1 2 1 4 0 1 5 5 5 500 1 1	
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaState_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 2 1 4 0 0 1 5 5 5 5 5 0 0 0 0 0 0 0 0 0 0 0 0	1 2 1 4 0 0 1 5 5 5 500 1 5 0 0	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaState_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1 2 1 4 0 0 1 1 5 5 5 500 1 1 5 0 0 1 1	1 2 1 4 0 0 1 5 5 5 500 1 1 5 0 1	
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08	1 2 1 4 0 0 1 1 5 5 5 5 500 1 1 5 0 0 1 4 4	1 2 1 4 0 0 1 1 5 5 5 500 1 1 5 0 1 4	
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaSt_ate_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08	1 2 1 4 0 0 1 1 5 5 5 5 0 0 1 1 4 0 0	1 2 1 4 0 1 5 5 5 500 1 5 0 1 4 0 0	
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgCoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIoaMtgtnEn_Cnt_M_Igc	1 2 1 4 0 0 1 1 5 5 5 5 0 0 1 1 4 0 0 0 0 0 0 0	1 2 1 4 0 0 1 5 5 5 5 0 0 1 4 4 0 0 0 0 0 0 0	
LoaMgr_Ag\$V_Cnt_M_lgc LoaMgr_HwTqldptSig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_IvtrldptSig\$V_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrResp\$V_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_Ulsp\$S_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MoxAgtlqntSig\$V_Cnt_M_u08 LoaMgr_MoxAgtlqntSig\$V_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSig\$V_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrResp\$V_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrResp\$V_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	1 2 1 1 4 0 0 1 1 5 5 5 5 5 0 0 1 1 4 4 0 0 0 0 3 3 0 0 0 0	1 2 1 4 0 1 5 5 5 500 1 1 4 0 0 0 3 3 0 0 0 0	
LoaMgr_Ag\$V_Cnt_M_lgc LoaMgr_HwTqldptSig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_IvtrldptSig\$V_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrResp\$V_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSig\$V_Cnt_M_u08 LoaMgr_MotAgldptSig\$V_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSig\$V_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMd\$rc\$V_Cnt_M_u08	1 2 1 1 4 0 0 1 1 5 5 5 5 5 0 0 1 1 4 4 0 0 0 0 3 3 0 0 0 0 1 1	1 2 1 4 0 1 5 5 5 500 1 1 4 0 0 0 3 3 0 0 0 1 1	
LoaMgr_Ag\$V_Cnt_M_lgc LoaMgr_HwTqldptSig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_IvtrldptSig\$V_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrResp\$V_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_Ulsp\$_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSig\$V_Cnt_M_u08 LoaMgr_MotAgldptSig\$V_Cnt_M_u08 LoaMgr_MotAgResp\$V_Cnt_M_u08 LoaMgr_MotCurrldptSig\$V_Cnt_M_u08 LoaMgr_MotCurrldptSig\$V_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VtlgMdSrc\$V_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	1 2 1 4 0 0 1 5 5 5 5 500 1 1 5 5 0 0 1 1 4 4 0 0 0 0 3 3 0 0 0 0 1 1 {170, 173, 174}	1 2 1 4 0 0 1 5 5 5 500 1 1 4 0 0 0 3 3 0 0 0 1 {170, 173, 174}	
LoaMgr_Ag\$V_Cnt_M_lgc LoaMgr_HwTqldptSig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_IvtrldptSig\$V_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrResp\$V_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_Ulsp\$_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSig\$V_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrResp\$V_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMd\$rc\$V_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTC\$tatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTC\$tatus(Param_Cnt_T_u08)	1 2 1 4 0 0 1 5 5 5 5 500 1 1 5 5 0 0 1 1 4 4 0 0 0 0 3 3 0 0 0 0 1 1 {170, 173, 174}	1 2 1 1 4 0 0 1 1 5 5 5 500 1 1 5 0 0 0 1 1 4 0 0 0 0 3 0 0 0 1 1 {170, 173, 174} {0, 0, 0, 0, 0, 0}	
LoaMgr_Ag\$V_Cnt_M_lgc LoaMgr_HwTqldptSig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_IvtrldptSig\$V_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrResp\$V_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_Ulsp\$_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSig\$V_Cnt_M_u08 LoaMgr_MotAgldptSig\$V_Cnt_M_u08 LoaMgr_MotAgResp\$V_Cnt_M_u08 LoaMgr_MotCurrloptSig\$V_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMd\$rc\$V_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTC\$tatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTC\$tatus(Status_Cnt_T_enum)	1 2 1 4 0 0 1 5 5 5 5 500 1 1 5 5 0 0 1 1 4 4 0 0 0 0 3 3 0 0 0 0 1 {170, 173, 174} {1, 1, 1} {1, 1, 1}	1 2 1 4 0 1 5 5 5 500 1 5 0 1 4 0 0 1 4 0 0 1 4 0 0 1 4 0 0 1 {170, 173, 174} {0, 0, 0, 0, 0, 0} {1, 1, 1}	
LoaMgr_Ag\$V_Cnt_M_lgc LoaMgr_HwTqldptSig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_IvtrldptSig\$V_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrResp\$V_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_Ulsp\$_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSig\$V_Cnt_M_u08 LoaMgr_MotAgdLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSig\$V_Cnt_M_u08 LoaMgr_MotCurrldptSig\$V_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrc\$V_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1 2 1 4 0 0 1 5 5 5 5 500 1 1 5 5 0 0 1 1 4 4 0 0 0 0 3 3 0 0 0 0 1 {170, 173, 174} {1, 1, 1} {1, 1, 1} 500	1 2 1 4 0 1 5 5 5 500 1 5 0 1 4 0 0 1 4 0 0 1 4 0 0 1 4 0 0 1 {170, 173, 174} {0, 0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01	
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_TloaAvaii_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UIspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	1 2 1 4 0 1 5 5 5 500 1 5 0 1 4 0 0 1 4 1 4 0 0 0 1 4 1 4 1 1 1 1	1 2 1 4 0 1 5 5 5 500 1 5 0 1 4 0 0 1 4 0 0 1 4 0 0 1 4 1 7 1 7 1 7 1 8 1 8 1 8 1 8 1 8 1 8 1 8	
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotOurrIdptSigSV_Cnt_M_u08 LoaMgr_MotOurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotOurrRespSV_Cnt_M_u08 LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_StltpSigMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UIspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	1 2 1 4 0 0 1 5 5 5 5 500 1 1 5 5 0 0 1 1 4 4 0 0 0 0 3 3 0 0 0 0 1 {170, 173, 174} {1, 1, 1} {1, 1, 1} 500	1 2 1 4 0 1 5 5 5 500 1 5 0 1 4 0 0 1 4 0 0 1 4 0 0 1 4 0 0 1 {170, 173, 174} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 1	
LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_IwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UIspS_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_Igc  LoaMgr_MotOurrIdptSigSV_Cnt_M_u08  LoaMgr_MotOurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotOurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_Igc  LoaMgr_TloaDi_Cnt_M_Igc  LoaMgr_TloaDi_Cnt_M_Igc  LoaMgr_VttgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UIspS_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	1 2 1 4 0 1 5 5 5 500 1 5 0 1 4 0 0 1 4 1 4 0 0 0 1 4 170, 173, 174}  {1, 1, 1} 500 1	1 2 1 4 0 1 5 5 5 500 1 5 0 1 4 0 0 1 4 0 0 1 4 0 0 1 4 1 7 1 7 1 7 1 8 1 8 1 8 1 8 1 8 1 8 1 8	
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotOurrIdptSigSV_Cnt_M_u08 LoaMgr_MotOurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotOurrRespSV_Cnt_M_u08 LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_StltpSigMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UIspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	1 2 1 4 0 1 5 5 5 5 0 1 4 0 0 1 5 0 1 4 0 0 1 4 1 4 0 0 1 {170, 173, 174} {1, 1, 1} 500 1 1 1 1	1 2 1 4 0 1 5 5 5 500 1 5 0 1 4 0 0 1 4 0 0 1 4 0 0 1 {170, 173, 174} {0, 0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001 1 1	
LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_IwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UIspS_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgHogtSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_Igc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_Igc  LoaMgr_TloaDi_Cnt_M_Igc  LoaMgr_TloaDi_Cnt_M_Igc  LoaMgr_TloaDi_Cnt_M_Igc  LoaMgr_TloaDi_Cnt_M_Igc  LoaMgr_NtgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UIspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value  target_LoaMgr_Per1_LOASt_State_enum.value	1 2 1 4 0 1 5 5 5 5 0 1 4 0 0 1 5 0 1 4 0 0 1 4 1 4 0 0 1 {170, 173, 174} {1, 1, 1} 500 1 1 1 5	1 2 1 4 0 1 5 5 5 500 1 5 0 1 4 0 0 1 4 0 0 1 4 0 0 1 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 1 1 5	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	<b>~</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>~</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	<b>~</b>
AssignScale	1	AssignScale	1	<b>~</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>✓</b>

Name		_
Loadby_LAPV_CR_M_SP_	Test Step 3.8 (Repeat Count = 1)	
Loadby_PrivTqcbpSqSV_Crit_M_u08	Name	Input Value
Loading_ InvTrigency Com_M_units   Samuran	LoaMgr_AgSV_Cnt_M_lgc	
Loading   Hart   Face   Section   Loading   Hart   Har	LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
Loading   Introduction   Loading   Introduction   Loading   Introduction   Loading   Introduction   Loading   Introduction	LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	
Loading_riverRespSV_Cnt_M_u08	LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr   MoRajepsi   SV   Crit M   Lu98	LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
Loaking	LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	1
Loading MoAgnoshtginEn_Crt_M_Upe   1	LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr MACQRESSPX_CDT_M_U08   5	LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr MoCuridaptSigSV Cnt M	LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
Loading	LoaMgr_MotAgRespSV_Cnt_M_u08	5
Loading	LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_SnrishkotAgAvail_Cnt_M_lgc	LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
Loading_VilghdSrcSV_Cnt_M_u08	LoaMgr_MotCurrRespSV_Cnt_M_u08	1
Rile_Inst_Ap_LoaMgr	LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
k_LoaMgr_CurrivrMigtnRate_UlspS_f32         1           k_LoaMgr_CurrivrMigtnSacZeroEn_Cnt_Uge         0           k_LoaMgr_EndeOutSRate_UlspS_f32         1           k_LoaMgr_HwTqldpSigoThoaAvailResp_Cnt_U08         2           k_LoaMgr_HwTqldpSigoThoaAvailResp_Cnt_U08         2           k_LoaMgr_HwTqldpSigoThoaAvailResp_Cnt_U08         2           k_LoaMgr_HwTqldpSigoThoaAvailResp_Cnt_U08         2           k_LoaMgr_HwTqldpSigoTalCanCareSep_Cnt_U08         1           k_LoaMgr_HwTqldpSigoTalCanCareSep_Cnt_U08         0           k_LoaMgr_HwTqldpSigoTalCanCareSep_Cnt_U08         0           k_LoaMgr_HwTqldpSigoTalCanCareSep_Cnt_U08         0           k_LoaMgr_HwTqldpSigoTalCanCareSep_Cnt_U08         1           k_LoaMgr_HwTqldpSigoTalCanCareSep_Cnt_U08         0           k_LoaMgr_HwTqldpSigoTalCanCareSep_Cnt_U08         0           k_LoaMgr_IvridpSigoTalCanCareSep_Cnt_U08         0           k_LoaMgr_IvriMgtnSac_Uis_f32         1           k_LoaMgr_IvriMgtnSac_Uis_f32         1           k_LoaMgr_IvriMgtnSac_Uis_f32         1           k_LoaMgr_MotAgldpSigoFitTalCanCareSep_Cnt_U08         5           k_LoaMgr_MotAgldpSigoTalCareSep_Cnt_U08         1           k_LoaMgr_MotAgldpSignSac_Sep_Cnt_U08         2           k_LoaMgr_MotAgldpSignAsc_Uis_F32 <t< td=""><td>LoaMgr_VltgMdSrcSV_Cnt_M_u08</td><td>1</td></t<>	LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
k_LoaMgr_CurrivrMigtnRate_UlspS_f32         1           k_LoaMgr_CurrivrMigtnSacZeroEn_Cnt_Uge         0           k_LoaMgr_EndeOutSRate_UlspS_f32         1           k_LoaMgr_HwTqldpSigoThoaAvailResp_Cnt_U08         2           k_LoaMgr_HwTqldpSigoThoaAvailResp_Cnt_U08         2           k_LoaMgr_HwTqldpSigoThoaAvailResp_Cnt_U08         2           k_LoaMgr_HwTqldpSigoThoaAvailResp_Cnt_U08         2           k_LoaMgr_HwTqldpSigoTalCanCareSep_Cnt_U08         1           k_LoaMgr_HwTqldpSigoTalCanCareSep_Cnt_U08         0           k_LoaMgr_HwTqldpSigoTalCanCareSep_Cnt_U08         0           k_LoaMgr_HwTqldpSigoTalCanCareSep_Cnt_U08         0           k_LoaMgr_HwTqldpSigoTalCanCareSep_Cnt_U08         1           k_LoaMgr_HwTqldpSigoTalCanCareSep_Cnt_U08         0           k_LoaMgr_HwTqldpSigoTalCanCareSep_Cnt_U08         0           k_LoaMgr_IvridpSigoTalCanCareSep_Cnt_U08         0           k_LoaMgr_IvriMgtnSac_Uis_f32         1           k_LoaMgr_IvriMgtnSac_Uis_f32         1           k_LoaMgr_IvriMgtnSac_Uis_f32         1           k_LoaMgr_MotAgldpSigoFitTalCanCareSep_Cnt_U08         5           k_LoaMgr_MotAgldpSigoTalCareSep_Cnt_U08         1           k_LoaMgr_MotAgldpSignSac_Sep_Cnt_U08         2           k_LoaMgr_MotAgldpSignAsc_Uis_F32 <t< td=""><td></td><td>target Rte Inst Ap LoaMgr</td></t<>		target Rte Inst Ap LoaMgr
k_LoaMgr_CurrivfrMighrSca_ZeroEn_Cnt_Ige         0           k_LoaMgr_CurrivfrMighrSca_Uls_132         1           k_LoaMgr_HowTqidptSig9TotaAvailResp_Cnt_Uo8         2           k_LoaMgr_HwTqidptSig9TotaAvailResp_Cnt_Uo8         4           k_LoaMgr_HwTqidptSig9TotaCavailResp_Cnt_Uo8         2           k_LoaMgr_HwTqidptSig9ToaEnaResp_Cnt_Uo8         1           k_LoaMgr_HwTqidptSig9ToaEnaResp_Cnt_Uo8         0           k_LoaMgr_HwTqidptSig9ToaEnaResp_Cnt_Uo8         0           k_LoaMgr_HwTqidptSig9TeaEnaResp_Cnt_Uo8         0           k_LoaMgr_HwTqidptSig9TeaEnaResp_Cnt_Uo8         0           k_LoaMgr_HwTqidptSig9Resp_Cnt_Uo8         1           k_LoaMgr_HwTqidptSig9Resp_Cnt_Uo8         1           k_LoaMgr_HwTqidptSig9Resp_Cnt_Uo8         0           k_LoaMgr_IwTridptSig9Resp_Cnt_Uo8         0           k_LoaMgr_IwTridptSig9Resp_Cnt_Uo8         0           k_LoaMgr_IwTridptSig9Resp_Cnt_Uo8         0           k_LoaMgr_IwTridptSig9Resp_Cnt_Uo8         0           k_LoaMgr_IwTridptRisq2EvenEn_Cnt_Uo         0           k_LoaMgr_IwTridptSig9Tif1De_Cnt_Uo         0           k_LoaMgr_ModqidptSig2Resp_Cnt_Uo         1           k_LoaMgr_ModqidptSig3Resp_Cnt_Uo         2           k_LoaMgr_ModqidptSig3Resp_Cnt_Uo         2 <td< td=""><td></td><td></td></td<>		
		0
k_Loadigr_FadeOutSiRate_UispS_f32         1           k_Loadigr_HwTqloptSignTloaAvailResp_Cnt_u08         2           k_Loadigr_HwTqloptSignTloaResp_Cnt_u08         4           k_Loadigr_HwTqloptSignTloaResp_Cnt_u08         2           k_Loadigr_HwTqloptSignTloaResp_Cnt_u08         1           k_Loadigr_HwTqloptSignTloaResp_Cnt_u08         0           k_Loadigr_HwTqloptSignTloaResp_Cnt_u08         0           k_Loadigr_HwTqloptSignTloaResp_Cnt_u08         1           k_Loadigr_HwTqloptSignTloaResp_Cnt_u08         1           k_Loadigr_HwTqloptSignTloaResp_Cnt_u08         1           k_Loadigr_HwTqloptSignTloaResp_Cnt_u08         0           k_Loadigr_HwTqloptSignTloaResp_Cnt_u08         0           k_Loadigr_IwTrloptSignTloaLoaLug         0           k_Loadigr_IwTrloptSignTloaLoaLug         0           k_Loadigr_IwTrloptSignTloaLoaLug         0           k_Loadigr_IwTrloptSignTloaLoaLug         0           k_Loadigr_IwTrloptSignTloaLoaLug         0           k_Loadigr_IwTrlognTsignTloaLoaLug         1           k_Loadigr_MotAgloptSignTsosmaresp_Cnt_u08         1           k_Loadigr_MotAgloptSignTloaLoaLug         2           k_Loadigr_MotAgloptSignTloaLoaLug         2           k_Loadigr_MotAgloptSignTloaLug         2           k_Loa		1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08         4           k_LoaMgr_HwTqldptSig1ToaDiResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08         0           k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08         0           k_LoaMgr_HwTqldptSigFitThd_Cnt_u08         1           k_LoaMgr_HwTqldptSigFitThd_Cnt_u08         1           k_LoaMgr_HwTqldptSigFitThd_Cnt_u08         1           k_LoaMgr_HwTqldptSigFitThd_Cnt_u08         1           k_LoaMgr_HwTqldptSigFitThd_Cnt_u08         0           k_LoaMgr_HwTqldptSigFitThd_Cnt_u08         0           k_LoaMgr_IwtrldptSigFitThd_Cnt_u08         0           k_LoaMgr_IwtrldptSigFitThd_Cnt_u08         0           k_LoaMgr_IwtrldptSigFitThd_Cnt_u08         0           k_LoaMgr_IwtrldptSigFitThd_Cnt_u08         1           k_LoaMgr_IwtrldptSigFitThd_Cnt_u08         5           k_LoaMgr_MotAjdptSigTResp_Cnt_u08         1           k_LoaMgr_MotAjdptSigTResp_Cnt_u08         2           k_LoaMgr_MotAjdptSigTResp_Cnt_u08         2           k_LoaMgr_MotAjdptSigTresp_Cnt_u08         2           k_LoaMgr_MotAjdptSigTresp_Cnt_u08         2           k_LoaMgr_MotAjdptMgraca_U8p_SigTresp_Cnt_u08         2           k_LoaMgr_Mo		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08         4           k_LoaMgr_HwTqldptSig1TloaAvaiResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08         0           k_LoaMgr_HwTqldptSig1FitTnd_Cnt_u08         1           k_LoaMgr_HwTqldptSig1FitTnd_Cnt_u08         1           k_LoaMgr_HwTqMtgtnSca_Ulsp_Sr32         1           k_LoaMgr_HwTqMtgtnSca_Ulsp_Sr32         1           k_LoaMgr_IwtrdptSig1Resp_Cnt_u08         0           k_LoaMgr_IwtrdptSig1Resp_Cnt_u08         0           k_LoaMgr_Iwtrltgtsig2Resp_Cnt_u08         0           k_LoaMgr_IwtrltgtnSca_Ulsp_Sr32         1           k_LoaMgr_IwtrltgtnSca_Ulsp_Sr32         1           k_LoaMgr_IwtrltgtnSca_Ulsp_Sr32         1           k_LoaMgr_IwtrltgtnSca_Ulsp_Sr32         1           k_LoaMgr_IwtrltgtnSca_Ulsp_Sr32         1           k_LoaMgr_ModQldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_ModQldptSig2Resp_Cnt_u08         1           k_LoaMgr_ModQldptSig2Resp_Cnt_u08         2           k_LoaMgr_ModQldptSig2Resp_Cnt_u08         2           k_LoaMgr_ModQldptSig2Resp_Cnt_u08         2           k_LoaMgr_ModQldptSig2Resp_Cnt_u08         2           k_LoaMgr_ModQldptSig2Resp_Cnt_u08		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08         0           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         1           k_LoaMgr_HwTqMtghrSate_UlspS_f32         1           k_LoaMgr_HwTqMtghrSate_UlspS_f32         1           k_LoaMgr_WrtdptSig2Resp_Cnt_u08         0           k_LoaMgr_WrtdptSig2Resp_Cnt_u08         0           k_LoaMgr_WrtMglnSate_UlspS_f32         1           k_LoaMgr_WrtMglnSate_UlspS_f32         1           k_LoaMgr_WrtMglnSac_UlspS_f32         1           k_LoaMgr_WrtMglnSac_UlspS_f32         1           k_LoaMgr_WrtMglnSac_UlspS_f32         1           k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         5           k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         2           k_LoaMgr_MotAglMglnSac_UlspS_f32         1           k_LoaMgr_MotAglMglnSac_UlspS_f32         1           k_LoaMgr_MotAglMglnSac_UlspS_f32         1           k_LoaMgr_MotAglMglnSac_UlspS_f32         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSigFitThd_Cnt_u08         0		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         1           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         1           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         1           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqdsig4Resp_Cnt_u08         0           k_LoaMgr_IwtrldptSig4Resp_Cnt_u08         0           k_LoaMgr_IwtrldptSig4Resp_Cnt_u08         0           k_LoaMgr_IwtrldptSig4Resp_Cnt_u08         0           k_LoaMgr_IwtrldptSig4Resp_Cnt_u08         0           k_LoaMgr_IwtrldptSig4Resp_Cnt_u08         0           k_LoaMgr_MotAjdptSig3Resp_Cnt_u08         5           k_LoaMgr_MotAjdptSig3Resp_Cnt_u08         5           k_LoaMgr_MotAjdptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAjdptSig3Resp_Cnt_u08         2           k_LoaMgr_MotAjdptSig3Resp_Cnt_u08         2           k_LoaMgr_MotAjdptSig3Resp_Cnt_u08         2           k_LoaMgr_MotAjdptSig3Resp_Cnt_u08         2           k_LoaMgr_MotAjdptSig3Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig4Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig4Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig4Resp_Cnt_u08         0<		
k_LoaMgr_HwTqldptSig2TioaEnaResp_Cnt_u08         0           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSig1Fithd_Cnt_u08         1           k_LoaMgr_HwTqlMtglnRate_UlspS_f32         1           k_LoaMgr_IwtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig2Resp_Cnt_u08         0           k_LoaMgr_IvtrldgtSig2Resp_Cnt_u08         0           k_LoaMgr_IvtrldgtnRate_UlspS_f32         1           k_LoaMgr_IvtrldgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_IvtrldgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig1Resp_Cnt_u08         2           k_LoaMgr_MotAgMtgnRate_Ulsp_5132         1           k_LoaMgr_MotAgMtgnRate_Ulsp_5132         1           k_LoaMgr_MotAgMtgnRate_Ulsp_512         1           k_LoaMgr_MotAgMtgnRate_Ulsp_512         1           k_LoaMgr_MotAgMtgnRate_Ulsp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig7Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitThd_Cnt_u08		
k_Loaklgr_HwTqldptSig4Resp_Cnt_u08         0           k_Loaklgr_HwTqldptSigFitThd_Cnt_u08         1           k_Loaklgr_HwTqMtglmRate_UlspS_f32         1           k_Loaklgr_HwTqMtglmSca_Uls_f32         1           k_Loaklgr_IwTdqhtSig1Resp_Cnt_u08         0           k_Loaklgr_IwTdqhtSig2Resp_Cnt_u08         0           k_Loaklgr_IwTdqhtSig2Resp_Cnt_u08         0           k_Loaklgr_IwTMtghRate_UlspS_f32         1           k_Loaklgr_IwTMtghRScaZeroEn_Cnt_lgc         0           k_Loaklgr_IwTMtghtSca_Uls_f32         1           k_Loaklgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_Loaklgr_MotAgldptSig3Resp_Cnt_u08         1           k_Loaklgr_MotAgldptSig3Resp_Cnt_u08         1           k_Loaklgr_MotAgldptSigFitThd_Cnt_u08         2           k_Loaklgr_MotAgldptSigFitThd_Cnt_u08         2           k_Loaklgr_MotAgldptSigFitThd_Cnt_u08         2           k_Loaklgr_MotAgldptSigFitThd_Cnt_u08         2           k_Loaklgr_MotAgldptSigSep_Cnt_u08         2           k_Loaklgr_MotAgldptSigSep_Cnt_u08         2           k_Loaklgr_MotCurridptSigResp_Cnt_u08         2           k_Loaklgr_MotCurridptSigResp_Cnt_u08         0           k_Loaklgr_MotCurridptSigFitThd_Cnt_u08         0           k_Loaklgr_MotCurridptSigFitThd_Cnt_	1	
k_Loaklgr_HwTqldptSigFltTnd_Cnt_u08         1           k_Loaklgr_HwTqldtghrRate_UlspS_f32         1           k_Loaklgr_HwTqldtghrSca_Uls_f32         1           k_Loaklgr_IvtrldptSig1Resp_Cnt_u08         0           k_Loaklgr_IvtrldptSig1Resp_Cnt_u08         0           k_Loaklgr_IvtrldptSigFltTnd_Cnt_u08         0           k_Loaklgr_IvtrldptSigFltTnd_Cnt_u08         0           k_Loaklgr_IvtrldptSigFltTnd_Cnt_u08         0           k_Loaklgr_IvtrldtghrSca_ZeroEn_Cnt_ugc         0           k_Loaklgr_IvtrldtghrSca_Uls_f32         1           k_Loaklgr_NotAgldptSig1NoSmaResp_Cnt_u08         5           k_Loaklgr_MotAgldptSig2Resp_Cnt_u08         1           k_Loaklgr_MotAgldptSig3Resp_Cnt_u08         0           k_Loaklgr_MotAgldptSig3Resp_Cnt_u08         2           k_Loaklgr_MotAgldptSig3Resp_Cnt_u08         2           k_Loaklgr_MotAglktghrSca_UispS_f32         1           k_Loaklgr_MotAglktghrSca_UispS_f32         1           k_Loaklgr_MotAglktghrSca_UispS_f32         1           k_Loaklgr_MotOurrldptSig1Resp_Cnt_u08         2           k_Loaklgr_MotCurrldptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurrldptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurrldptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurrld		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32         1           k_LoaMgr_HwTqMtgtnSca_Uls_f32         1           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig2Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig1FitTd_Cnt_u08         0           k_LoaMgr_IvtrldptSig1FitTd_Cnt_u08         0           k_LoaMgr_IvtrldptSig2Resp_Cnt_ulsp         1           k_LoaMgr_IvtrldptSig2Resp_Cnt_ulsp         0           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         1           k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig1FitThd_Cnt_u08         2           k_LoaMgr_MotAgldptSigFitThd_Cnt_u08         2           k_LoaMgr_MotAgMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08		
k_LoaMgr_HvTqMtgtnSca_Uls_f32         1           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig1RiThd_Cnt_u08         0           k_LoaMgr_IvtrMtgtnRate_UlspS_f32         1           k_LoaMgr_IvtrMtgtnSca_ZeroEn_Cnt_lgc         0           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSigFltThd_Cnt_u08         2           k_LoaMgr_MotAglMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotCurrldptSigResp_Cnt_u08         2           k_LoaMgr_MotCurrldptSigResp_Cnt_u08         2           k_LoaMgr_MotCurrldptSigResp_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08		
k_LoaMgr_NtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig2Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSigFitThd_Cnt_u08         0           k_LoaMgr_IvtrMtgtnRate_UlspS_f32         1           k_LoaMgr_IvtrMtgtnSca_Uls_f32         0           k_LoaMgr_IvtrMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSigFitThd_Cnt_u08         2           k_LoaMgr_MotAgldptSigFitThd_Cnt_u08         2           k_LoaMgr_MotAgMtgnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgnScaZeroEn_Cnt_lgc         0           k_LoaMgr_MotAgMtgnSca_Uls_f32         1           k_LoaMgr_MotAgMtgnSca_Uls_f32         1           k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFitThd_Cnt_u08		
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSigFltThd_Cnt_u08         0           k_LoaMgr_IvtrMtgtnRate_UlspS_f32         1           k_LoaMgr_IvtrMtgtnSca_Uls_f32         0           k_LoaMgr_IvtrMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig1SqResp_Cnt_u08         1           k_LoaMgr_MotAgldptSig1StTnd_Cnt_u08         2           k_LoaMgr_MotAgldptSigFltTnd_Cnt_u08         2           k_LoaMgr_MotAgMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08<		
k_LoaMgr_\vtrldptSigFltThd_Cnt_u08         0           k_LoaMgr_\vtrMtgtnRate_UlspS_f32         1           k_LoaMgr_\vtrMtgtnSca_ZeroEn_Cnt_lgc         0           k_LoaMgr_\vtrMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSigFltThd_Cnt_u08         2           k_LoaMgr_MotAgMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnSca_ZeroEn_Cnt_lgc         0           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         2           k_LoaMgr_MotCurrldptSigResp_Cnt_u08         2           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32         1           k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_IvtrMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSigFitThd_Cnt_u08         2           k_LoaMgr_MotAgMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Thd_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08         0           k_LoaMgr_MotCurrMtgtnRate_UlspS_f32         1           k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         1		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_IvtrMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig1rltTnd_Cnt_u08         2           k_LoaMgr_MotAgMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         0           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgSmaAvailResp_Cnt_u08         2           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Resp_Cn		
k_LoaMgr_IvtrMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       5         k_LoaMgr_MotAgldptSig2Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFitThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFitThd_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFitTnd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08       5         k_LoaMgr_MotAgldptSig2Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFltThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08       1         k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFltThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08       0         k_LoaMgr_MotAgldptSigFitThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1		
k_LoaMgr_MotAgldptSigFitThd_Cnt_u08       2         k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_Ugc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1		
k_LoaMgr_MotAgNttgtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1		
k_LoaMgr_MotAgNtdgtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1		
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 1 k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc 1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc 1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32		
k_LoaMgr_RedcdStRate_UlspS_f32 1		
k_LoaMgr_RedcdStSca_Uls_f32 1		
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value 0	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	:nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr HwTqldptSigSV Cnt M u08	0	0	<b>~</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	<b>V</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	-
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	-
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	-
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	-
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172}	{170, 172}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace ✓				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 3.9 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2 4
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 k LoaMgr HwTqldptSig1TloaAvailResp Cnt u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1 0
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurridptSig1Resp_Cnt_u08	0 0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08 k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0 0
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_LoaMgr_Per1_SteenMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc



Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>~</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	<b>~</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>~</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.10 (Repeat Count = 1)		×
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	2	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_lvtrRespSV_Cnt_M_u08	3	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

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Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0 0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_UIs_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	2		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target Rte Inst Ap LoaMgr.LoaMgr Per1 LoaScaDi Cnt Igc	target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Perl_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Peri_LoaScaDi_Cnt_igc target_LoaMgr_Peri_MotAqLoaMtgtnEn_Cnt_igc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	<b>V</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	<b>*</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	-
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	-
LoaMgr_LoaScaleFctr_Uls_M_f32			
	0	0	✓
LoaMgr_MaxMtgtnResp_Cnt_M_u08		5	<b>*</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	0		
	0 5	5	~

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

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Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 175}	{170, 173, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace				, v
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.11 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Innut Value		
Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k LoaMgr MotAgMtgtnRate UlspS f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc.value	0		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target LoaMgr Per1 LoaScaDi Cnt Igc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inact	tv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enu		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgo target_LoaMgr_Per1_MotAgLoaMtgtnEr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>—</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	1	1	
LoaMgr_LoaNt_State_M_enum	5	5 500	
LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	
LoaMgr MaxMtgtnResp Cnt M u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr MotAgRespSV Cnt M u08	5	5	<b>✓</b>
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170}	{170}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1}	{1}	~
DI 14/1 A I AI I DIII 1/1 0 00/1/1			
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	500	1 ± 0.001	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	500 1 0	1 ± 0.001 0	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	500 1 0 0	1 ± 0.001 0 0	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_LOASt_State_enum.value	500 1 0 0 5	1 ± 0.001 0 0 5	· · · · · · · · · · · · · · · · · · ·
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	500 1 0 0	1 ± 0.001 0 0	~



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.12 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr MotAgldptSigSV Cnt M u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlytrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
	1
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_	u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr HwTqldptSigSV Cnt M u08	1	1	_
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	
LoaMgr MotCurrLoaMtgtnEn Cnt M Igc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 175}	{170, 175}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	-
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	1	1	•
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	-
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	·
	- I -	1 ·	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 3.13 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	0 0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target Rte Inst Ap LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32 k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	0
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k LoaMgr MotCurrldptSig0Resp Cnt u08	2
k LoaMgr MotCurrldptSig1Resp Cnt u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_LoaScabi_Crit_gc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LoASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMigr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
2 2 2 2 3 3 3 2 3 2 3 3 3 3 3 3 3 3 3 3	, 5 = 5 = 5 = 5 = 5





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.14 (Repeat Count = 1)	<b>12</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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LoaMgr\_Per1



Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
	2		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08			
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqIdptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
	1		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	0		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc			
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k LoaMgr MotCurrldptSig0Resp Cnt u08	2		
k_LoaMgr_MotCurridptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target LoaMgr Per1 MotCurrldptSig Cnt u08.value	1		
	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqLoaMtgtnEn Cnt Igc	target LoaMgr Per1 HwTqLoaMtgtnEn Cn	t lgc	
target Rte Inst Ap LoaMgr.LoaMgr Per1 IvtrLoaMtgtnEn Cnt Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_l		
target Rte Inst Ap LoaMgr.LoaMgr Per1 LOASt State enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target LoaMgr Per1 LoaScaDi Cnt lgc		
		nt lac	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ci		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_Loalvigi_Feri_floaAvaii_Crit_igc		
Name	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
		Expected Value	Result
LoaMar AgSV Cnt M lac	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0	1 0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 1	0 1	<b>*</b>
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 1 4	1 0 1 4	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1  0  1  4	1 0 1 4	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 1 4 1	1 0 1 4 1	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 1 4 1 1 3	1 0 1 4 1 1 3	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 1 4 1	1 0 1 4 1	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 1 4 1 1 3	1 0 1 4 1 1 3	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 1 4 1 1 3 5 500	1 0 1 4 1 1 3 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1  0  1  4  1  3  5  500  0	1 0 1 4 1 1 3 5 500	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 1 4 1 1 3 5 500 0 5	1 0 1 4 1 1 3 5 500 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 1 4 1 1 3 5 500 0 5 0	1 0 1 4 1 1 1 3 5 500 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 1 4 1 1 3 5 500 0 5	1 0 1 4 1 1 3 5 500 0	· · · · · · · · · · · · · · · · · · ·

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Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.15 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





N.			
Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSigFltThd Cnt u08	0		
k LoaMgr MotCurrMtgtnRate UlspS f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value			
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_load	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	Result
LoaMgr HwTqldptSigSV Cnt M u08	0	0	·
· · · · · · · · · · · · · · · · · · ·	0	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc			•
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>*</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
		{1, 1, 1}	~
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	{1, 1, 1}		
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	{1, 1, 1} 500		<b>V</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>4</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	500 0	500 ± 0.01 0 ± 0.001	<b>V</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	500 0 0	500 ± 0.01 0 ± 0.001 0	~ ~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	500 0 0 1	500 ± 0.01 0 ± 0.001 0	\( \frac{1}{2} \)
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	500 0 0 1 5	500 ± 0.01 0 ± 0.001 0 1	\( \frac{1}{2} \)
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	500 0 0 1	500 ± 0.01 0 ± 0.001 0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	<b>~</b>
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.16 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1.
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrlvtrMtgtnScaZeroEn Cnt lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	1
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
	·





Name	Input Value		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_	_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_	u08	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum	1	
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaAvail Cnt Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	_
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	<b>~</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	_
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170}	{170}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1}	{1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	-
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
CalcivtrState	2	CalclvtrState	2	~	
LatchInputs	1	LatchInputs	1	<b>✓</b>	
RqstRespConds	1	RqstRespConds	1	~	
RqstHwTqResp	1	RqstHwTqResp	1	~	
ArbResp	1	ArbResp	1	~	
ChooseFinalResp	1	ChooseFinalResp	1	~	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	~	
AssignScale	1	AssignScale	1	~	
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~	
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~	





Indicates   Indi	Test Step 3.17 (Repeat Count = 1)	13
Looking Hardquistign Cort Mg   1		
Looking Fund Country Come Louis 1  Looking Fund Country C		
Lander J. Principacing C. Del. J. J. S. Lander J. Principacing C. Del. J. S. Lander J. Principacing C. Del. J. J. S. Lander J. Del. J. S. Lander J. Principacing C. Del. J. J. S. Lander J. Del. J. J. S. Lander J. Del. J. Lander J. Del. J. S. Lander J. Del. J. Lander J. Del. J. Lander J. Del. J. Lander J. Del. J. Del. J. Lander J. Del. J. Del. J. Del. J. Del. J. Lander J. Del. J. Del. J. Del. J. Del. J. Del. J. Del. J. Lander J. Del. J. D	LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
Landary Landard (1997) Cert M. 198  Landary Landard (1997) Cert M.	LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
Landary Landardysens Cort M. 198  Landary Modylaptisky Cort M. 198  Landary Landary Modylaptisky Cort M. 198  Landary Landary Cort Modylaptisky Cort M. 198  Landary Landary Cort Modylaptisky Cort M. 198  Landary Landary Landary Cort Modylaptisky Cort M. 198  Landary Landary Landary Landary Cort Landary L	LoaMgr_HwTqRespSV_Cnt_M_u08	1
Landary Landang-Section (Landang Landang Landa	LoaMgr_lvtrldptSigSV_Cnt_M_u08	
Loading / MacApplanging C. Ord. M. Jud9	LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	
Coading - Manage and parties   Cort. M. 196		
Loading Muchanges (Co. M. V. US		
Coading McContraction   Coading		1
Landing Michael Septimization (Linding Landing		
Loadery Control Might Search Control Lipic Loadery Free Control Might Search Control Loadery Control Might Search Control Loadery Might Search Contro	LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	
Loading / Lindown   Loading / Load	LoaMgr_MotCurrRespSV_Cnt_M_u08	4
Rest   Inst. Ap. Leading	LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
L. Landary _	LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Luaday_Info@nest_us_nest_nest_nest_nest_nest_nest_nest_nes	Rte_Inst_Ap_LoaMgr	V =
L. Loading		
Laading   Artifologica   Data   Artifologica   California   Californ		
L. Landley_Mortglogis() Informations		
Labalign   Hardigotissign   Hardigotissign   Labalign   Labalign   Labalign   Labalign   Hardigotissign   Labalign   Labalign   Hardigotissign   Labalign   Labalign   Hardigotissign   Labalign   Lab		
K_Landsy_MrVingdisSg2TeanDiResp_Cnt_UB8		
Landsgr   Mr-OptiosSg2Tean Price   Cort   USB	k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	
L. Loading   HwTqdqsSigATesos Cott   L098	k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	
Loaking-HwTqdtgnSpFTHD_Cnt_U08	k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
K_Loakig-HwToMighRate_UspS_132	k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
K. Loadkg   HvirdpiSig Resp. Cht U88	k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
Leafley Purticips Sig Resp. Cnt. U08 Leafley Mode Sig Sig	k_LoaMgr_HwTqMtgtnRate_UlspS_f32	
LoaMgr_Intripsisig2Resp_Cnt_U08		
Loaklgr_IvrifdpSigFIRTid_Cnt_U08		
L. Loadley FurthflightRate UlspS _132         1           L. Loadley FurthflightSazeTechE_Crt_Ige         0           L. Loadley FurthflightSazeTechE_Crt_Ige         1           L. Loadley FurthflightSazeTechE_Crt_Ige         5           L. Loadley FurthflightSazeTechE_Crt_Ige         1           L. Loadley FurthflightSazeTechE_Crt_Ige         0           L. Loadley FurthflightSazeTechE_Crt_Ige         1           L. Loadley FurthflightSazeTechE_Crt_Ige value         0           Larget_Loadley Fert_Iber_IndexCatt_Ige_value         0           Larget_Loadley Fert		
k_Loadkgr_IntrifligtnSca_Us_f32         1           k_Loadkgr_IntrifligtnSca_Us_f32         1           k_Loadkgr_MoNdpidsig18finNsmaResp_Cnt_U08         5           k_Loadkgr_MoNdpidsig3Resp_Cnt_U08         1           k_Loadkgr_MoNdpidsig3Resp_Cnt_U08         0           k_Loadkgr_MoNdpidsig3Resp_Cnt_U08         2           k_Loadkgr_MoNdpidsig5aResp_Cnt_U08         2           k_Loadkgr_MoNdpidsignScaZeroEn_Cnt_Uge         0           k_Loadkgr_MoNdpidsignScaZeroEn_Cnt_Uge         0           k_Loadkgr_MoNdpidsignScaZeroEn_Cnt_Uge         0           k_Loadkgr_MoNdpidsignScaZeroEn_Cnt_Uge         2           k_Loadkgr_MoNdpidsignScaZeroEn_Cnt_Uge         2           k_Loadkgr_MoNdpidsignScaZeroEn_Cnt_Uge         2           k_Loadkgr_MoNdpidsignScaZeroEn_Cnt_Uge         0           k_Loadkgr_MolocurridpsigResp_Cnt_Uge         0           k_Loadkgr_MolocurridpsigResp_Cnt_Uge         0           k_Loadkgr_MolocurridpsigResp_Cnt_Uge         0           k_Loadkgr_MolocurridpsigResp_Cnt_Uge         1           k_Loadkgr_MolocurridpsigResp_Cnt_Uge         1           k_Loadkgr_MolocurridpsigResp_Cnt_Uge         1           k_Loadkgr_MolocurridpsigResp_Cnt_Uge         1           k_Loadkgr_MolocurridpsigResp_Cnt_Uge         1           k		
L. LoaMgr_MotAgliptSig1Ass_CUIs_132 L. LoaMgr_MotAgliptSig1Ass_Cont_U08 L. LoaMgr_MotAgliptSig2Ass_Cont_U08 L. LoaMgr_MotAgribtSig1Ass_Cont_U08 L. LoaMgr_MotContridptSig1Ass_Cont_U08 L. LoaMgr_MotContridgtSig1Ass_Cont_U08 L. LoaMgr_PotT_Diag2SstavtTinactv_Cont_Ugc_LoaMgr_PotT_Diag2SstavtTinactv_Cont_U08 Larget_LoaMgr_PotT_NotContridpSig1Ass_Cont_U08 Larget_LoaMgr_PotT_NotContridpSig1Ass_Cont_U08 Larget_LoaMgr_PotT_NotContridpSig1Ass_Cont_U08 Larget_LoaMgr_PotT_NotContridpSig1Cont_U08 Larget_LoaMgr_PotT_NotContridpSig1Cont_U08 Larget_LoaMgr_PotT_NotContridpSig1Cont_U08 Larget_LoaMgr_PotT_NotContridpSig1Cont_U08 Larget_LoaMgr_PotT_NotContridpSig1Cont_U08 Larget_LoaMgr_PotT_NotContridpSig1Cont_U08 Larget_LoaMgr_PotT_NotContridpSig1Cont_U08 Larget_LoaMgr_PotT_NotContridpSig1Cont_U08 Larget_LoaMgr_PotT_NotContridpSig1Cont_U08 Larget_LoaMgr_PotT_NotContridpSig1		
k_LoaMgr_MotApidpisig3rResp_Cnt_u08         1           k_LoaMgr_MotApidpisig3resp_Cnt_u08         0           k_LoaMgr_MotApidpisig3resp_Cnt_u08         0           k_LoaMgr_MotApidpisig3resp_Cnt_u08         2           k_LoaMgr_MotApidpisig3resp_Cnt_u08         2           k_LoaMgr_MotApidignReale_UispS_152         1           k_LoaMgr_MotApidignSea_Uis_132         1           k_LoaMgr_MotApidignSea_Uis_132         1           k_LoaMgr_MotQuridptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCuridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCuridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCuridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCuridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCuridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCuridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCuridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCuridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCuridptSig2Resp_Cnt_u08         1           k_LoaMgr_Pot_DiagsStstrinact_unct_Cnt_lgc_value         0           target_LoaMgr_Pot_DiagsStstrinact_Cnt_lgc_value         0           target_LoaMgr_Pot_LoaMgr_Diags_Cnt_u08 value         0           target_LoaMgr_Pot_LoaScap_LoaMgr_Diags_Cnt_u08 value         1 <tr< td=""><td></td><td></td></tr<>		
LoaMgr_MotAgldplSig3Resp_Cnt_u08	k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgidptSigFitThd_Cnt_u08         2           k_LoaMgr_MotAgMtghtRade_UlspS_132         1           k_LoaMgr_MotAgMtghtRade_UlspS_132         0           k_LoaMgr_MotAgMtghtSca_Uls_132         1           k_LoaMgr_MotAgMtghtSca_Uls_132         1           k_LoaMgr_MotCurridptSigResp_Cnt_u08         2           k_LoaMgr_MotCurridptSigResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigResp_Cnt_u08         0           k_LoaMgr_MotCurridtgtnSca_ZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurridtgtnSca_ZeroEn_Cnt_lgc         1           k_LoaMgr_RedodSiRa_ulls_152         1           k_LoaMgr_RedodSiRa_ulls_152         1           k_LoaMgr_Pert_DiagcSisIvtrInactv_Cnt_lgc.value         0           target_LoaMgr_Pert_DiagcSisIvtrInactv_Cnt_lgc.value         0           target_LoaMgr_Pert_IndCurridptSig_Cnt_u08 value         1           target_LoaMgr_Pert_IndCurridptSig_Cnt_u08 value         1           target_LoaMgr_Pert_IndCurridptSig_Cnt_u08 value         1           target_LoaMgr_Pert_IndCurridptSig_Cnt_u08 value         1           target_LoaMgr_Pert_IndCurridptSig_Cnt_u08 value	k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
LoaMgr_MotAgMighRate_UlspS_f32	k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgMtginSca_ZeroEn_Cnt_lgc         0           k_LoaMgr_MotAgMtginSca_Uls_732         1           k_LoaMgr_MotAgMtginSca_Uls_732         1           k_LoaMgr_MotGuridptSigGResp_Cnt_u08         2           k_LoaMgr_MotGuridptSigGResp_Cnt_u08         0           k_LoaMgr_MotGuridptSigGResp_Cnt_u08         0           k_LoaMgr_MotGuridptSigGResp_Cnt_u08         0           k_LoaMgr_MotGuridptSigGResp_Cnt_u08         0           k_LoaMgr_MotGuridptSigGresp_Cnt_u08         0           k_LoaMgr_MotGuridptSigGresp_Cnt_u08         1           k_LoaMgr_MotGuridptSigGresp_Cnt_u08         1           k_LoaMgr_MotGuridptsigGresp_Cnt_u08         1           k_LoaMgr_MotGuridptsigGresp_Cnt_u08         1           k_LoaMgr_MotGuridptsig_Cnt_u08         1           k_LoaMgr_RedcdStsca_Uls_52         1           k_LoaMgr_Pert_DiagcStslvtr1actv_Cnt_lgc_value         0           target_LoaMgr_Pert_DiagcStslvtr2inactv_Cnt_lgc_value         0           target_LoaMgr_Pert_MotGuridptSig_Cnt_u08 value         1           target_LoaMgr_Pert_MotGuridptSig_Cnt_u08 value         1           target_LoaMgr_Pert_MotGuridptSig_Cnt_u08 value         1           target_LoaMgr_Pert_MotGuridptSig_Cnt_u08 value         1           target_LoaMgr_Pert_MotGuridptSig_Cnt_u08 value <td< td=""><td>k_LoaMgr_MotAgldptSigFltThd_Cnt_u08</td><td></td></td<>	k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	
k_LoaMgr_MotAgMigtnSca_Uls_f32 k_LoaMgr_MotCurridptSignSesp_Cnt_u08 k_LoaMgr_PotT_DiagcStstvrtInactv_Cnt_gc_value target_LoaMgr_PotT_LoaSp_Cnt_u08 target_LoaMgr_PotT_LoaSp_Cnt_u08 target_LoaMgr_PotT_LoaSp_Cnt_u08 target_LoaMgr_PotT_MotCurridptSig_Cnt_u08 value 1 target_LoaMgr_PotT_SignSesp_Cnt_u08 target_LoaMgr_PotT_SignSesp_Cnt_u		
K_LoaMgr_MotAgSmaAvaiResp_Cnt_u08         2           K_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           K_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           K_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           K_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           K_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           K_LoaMgr_MotCurridptSig1Resp_Cnt_L08         1           K_LoaMgr_MotCurridptSig2Resp_Cnt_Cnt_lgc         1           K_LoaMgr_MotCurridptSig2Resp_Cnt_L08         1           K_LoaMgr_RedcdSiRea_Uls_32         1           K_LoaMgr_RedcdSiRea_Uls_32         1           K_LoaMgr_RedcdSiRea_Uls_32         1           K_LoaMgr_RedcdSiRea_Uls_32         1           Laguet_LoaMgr_Pert_DiagcStstvrtInactv_Cnt_lgc value         0           target_LoaMgr_Pert_HvTqlqtptSig_Cnt_u08.value         2           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08.value         1           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08.value         0           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08.value         1           target_LoaMgr_Pert_ToaDi_Cnt_lgc_value         0           target_LoaMgr_Pert_ToaDi_Cnt_lgc_value         1           target_LoaMgr_Pert_ToaDi_Cnt_lgc_value         1           target_LoaMgr_Pert_ToaDi_Cnt_lgc_value         1		1
K_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           K_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           K_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           K_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           K_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           K_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           K_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           K_LoaMgr_MotCurridptSig-Sa_Us_52         1           K_LoaMgr_RedodSiSca_Us_532         1           K_LoaMgr_RedodSiSca_Us_532         1           K_LoaMgr_Perd_DiagcStsivtr1lnactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc.value         0           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 value         1           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 value         1           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 value         0           target_LoaMgr_Per1_ToaAvail_Cnt_lgc.value         0           target_LoaMgr_Per1_ToaAvail_Cnt_lgc.value         0           target_LoaMgr_Per1_ToaDi_Cnt_lgc.value         1           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsivtr1lnactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoa		
k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1RiThd_Cnt_u08         0           k_LoaMgr_MotCurrMtgtnRsez_UlspS_132         1           k_LoaMgr_MotCurrMtgtnSea_UlspS_132         1           k_LoaMgr_MotCurrMtgtnSea_Uls_1532         1           k_LoaMgr_RedcdStRate_UlspS_132         1           k_LoaMgr_RedcdStSea_Uls_152         1           k_LoaMgr_Perl_DiagcStstvfrInactv_Cnt_lgc.value         0           target_LoaMgr_Perl_DiagcStstvfrInactv_Cnt_lgc.value         0           target_LoaMgr_Perl_MotGurrIdptSig_Cnt_u08.value         2           target_LoaMgr_Perl_MotCurrIdptSig_Cnt_u08.value         1           target_LoaMgr_Perl_MotCurrIdptSig_Cnt_u08.value         0           target_LoaMgr_Perl_MotCurrIdptSig_Cnt_u08.value         1           target_LoaMgr_Perl_SterMod_Cnt_enum.value         0           target_LoaMgr_Perl_TicaAvail_Cnt_lgc.value         1           target_LoaMgr_Perl_TicaDi_Cnt_lgc.value         1           target_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_DiagcStstvtrInactv_Cnt_lgc         target_LoaMgr_Perl_DiagcStstvtrInactv_Cnt_lgc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_HwTqLoaMtgrEn_Cnt_lgc         target_LoaMgr_Perl_HwTqLoaMtgrEn_Cnt_lgc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_HwTqLoaMtgtnEn_Cnt_lgc         tar		
k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         1           k_LoaMgr_MotCurridptSigReaCeroEn_Cnt_lgc         1           k_LoaMgr_MotCurridptSigScaZeroEn_Cnt_lgc         1           k_LoaMgr_RedodStRate_UlspS_f32         1           k_LoaMgr_RedodStRate_UlspS_f32         1           k_LoaMgr_Perd_DiagcStstvf1inactv_Cnt_lgc.value         0           target_LoaMgr_Perl_DiagcStstvf2inactv_Cnt_lgc.value         0           target_LoaMgr_Perl_DiagcStstvf2inactv_Cnt_lgc.value         1           target_LoaMgr_Perl_MotGridptSig_Cnt_u08.value         2           target_LoaMgr_Perl_MotGridptSig_Cnt_u08.value         1           target_LoaMgr_Perl_MtrPosldptSig_Cnt_u08.value         1           target_LoaMgr_Perl_MtrPosldptSig_Cnt_u08.value         1           target_LoaMgr_Perl_TioaAvail_Cnt_lgc.value         0           target_LoaMgr_Perl_TioaAvail_Cnt_lgc.value         1           target_LoaMgr_Perl_TioaAvail_Cnt_lgc.value         1           target_LoaMgr_Perl_TioaAvail_Cnt_lgc.value         1           target_LoaMgr_Perl_DiagcStslvtr1inactv_Cnt_lgc         target_LoaMgr_Perl_DiagcStslvtr2inactv_Cnt_lgc           target_Re_inst_Ap_LoaMgr_LoaMgr_Perl_HwTqLoaMigtnEn_Cnt_lgc         target_LoaMgr_Perl_HwTqLoaMigtnEn_Cnt_lgc </td <td></td> <td></td>		
k_LoaMgr_MolCurrIdptSigFitThd_Cnt_u08         0           k_LoaMgr_MolCurrMtghrRate_UlspS_f32         1           k_LoaMgr_MolCurrMtghrSca_Uls_f32         1           k_LoaMgr_RedcdSiRate_UlspS_f32         1           k_LoaMgr_RedcdSiRate_UlspS_f32         1           k_LoaMgr_RedcdSiRate_UlspS_f32         1           k_LoaMgr_Per1_DiagcStstvtr1nactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStstvtr2nactv_Cnt_lgc.value         0           target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value         2           target_LoaMgr_Per1_MolCurrldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MolCurrldptSig_Cnt_u08.value         0           target_LoaMgr_Per1_MolCurrldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_SteerMod_Cnt_enum.value         0           target_DoaMgr_Per1_ToaAvail_Cnt_lgc.value         0           target_Per1_ToaDi_Cnt_lgc.value         1           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStstvtr1nactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStstvtr2nactv_Cnt_lgc           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStstvtr2nactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStstvtr2nactv_Cnt_lgc           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgnEn_Cnt_lgc         target_LoaMgr_Per1_HwTqLoaMtgnEn_Cnt_lgc           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgnEn_Cnt_lgc         target_LoaMgr_Per1_WoltQaMgr_Per1_		
k_LoaMgr_MotCurrMigtnSca_Pins_Cat_ligc	k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_Perd_DiagcStsIvtrIInactv_Cnt_lgc.value 10 target_LoaMgr_Per1_DiagcStsIvtrIInactv_Cnt_lgc.value 21 target_LoaMgr_Per1_DiagcStsIvtrIInactv_Cnt_lgc.value 22 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 23 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 34 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 45 target_LoaMgr_Per1_MrPosldptSig_Cnt_u08.value 46 target_LoaMgr_Per1_MrPosldptSig_Cnt_u08.value 47 target_LoaMgr_Per1_SterMod_Cnt_enum.value 48 target_LoaMgr_Per1_ToaDi_Cnt_lgc.value 49 target_LoaMgr_Per1_ToaDi_Cnt_lgc.value 40 target_LoaMgr_Per1_ToaDi_Cnt_lgc.value 40 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtrIInactv_Cnt_lgc 40 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc 40 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc 40 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 40 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 40 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 40 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqlcaMtgtnEn_Cnt_lgc 40 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_IoaScaDi_Cnt_lgc 40 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 40 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 40 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 40 target_LoaMgr_Per1_MotQuoaMgr_Per1_LoaScaDi_Cnt_lgc 40 target_LoaMgr_Per1_MotQuoaMgr_Per1_LoaScaDi_Cnt_lgc 40 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 40 target_LoaMgr_Per1_MotQuoaMgr_Per1_LoaScaDi_Cnt_lgc 40 target_LoaMgr_Per1_MotQuoaMgr_Per1_LoaScaDi_Cnt_lgc 40 target_LoaMgr_Per1_MotQuoaMgr_Per1_LoaScaDi_Cnt_lgc 40 target_LoaMgr_Per1_MotQuoaMgr_Per1_LoaScaDi_Cnt_lgc 40 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 40 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1.
k_LoaMgr_RedcdStRate_UlspS_132	k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc.value 0 target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value 0 target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value 0 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_TloaAvaii_Cnt_Igc.value 1 target_LoaMgr_Per1_TloaAvaii_Cnt_Igc.value 1 target_LoaMgr_Per1_TloaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	k_LoaMgr_MotCurrMtgtnSca_Uls_f32	
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target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value 2 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaDi_cnt_lgc.value  target_LoaMgr_Per1_TloaDi_cnt_lgc.value  target_LoaMgr_Per1_TloaDi_cnt_lgc.value  target_LoaMgr_Per1_TloaDi_cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoASt_State_enum  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoASt_State_enum  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
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target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_loaSt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_InvTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_InvTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_InvTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaSt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum  target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	
target_LoaMgr_Per1_TloaDi_Cnt_igc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_igc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_igc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_igc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lwTqLoaMtgtnEn_Cnt_igc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_igc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_igc  target_LoaMgr_Per1_LoaScaDi_Cnt_igc  target_LoaMgr_Per1_LoaScaDi_Cnt_igc  target_LoaMgr_Per1_LoaScaDi_Cnt_igc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_igc  target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_igc  target_LoaMgr_Per1_LoaScaDi_Cnt_igc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_igc	target_LoaMgr_Per1_SteerMod_Cnt_enum.value	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LeaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LeaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LeaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	~
LoaMgr_LOASt_State_M_enum	4	4	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	4	4	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	3	3	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{171, 173, 174}	{171, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	4	4	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.18 (Repeat Count = 1)	<b>8</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Input Value



Name



Name	Input value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k LoaMgr HwTqMtgtnRate UlspS f32	1		
	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32			
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	0		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value			
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	В	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target LoaMgr Per1 MtrPosIdptSig Cnt u		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_TloaAvail_Cnt_Igc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	Even antical Value	Den II
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	2	2	~
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08			
	5	15	<b>✓</b>
Loamur MotAuldotSigSV Cnt M u08	5	5	
LoaMgr_MotAgloptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08			

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Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.19 (Repeat Count = 1)	N .
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	5
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	5
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSigFltThd Cnt u08	0		
k LoaMgr MotCurrMtgtnRate UlspS f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
	1		
k_LoaMgr_RedcdStSca_UIs_f32	0		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value			
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_	Cnt_lgc	
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqldptSig Cnt u08	target LoaMgr Per1 HwTqldptSig Cnt u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	-95	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
		Not les	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enun		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enun		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc		Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc	1	Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	Expected Value	Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	Expected Value	~
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 1	Expected Value 1	~
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1	Expected Value  1 1 1 0	· ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 1 0 1	Expected Value  1 1 1 0	· ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 1 0 1 1	Expected Value  1 1 1 0 1	· ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIcoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 1 1 1 1 1 1	Expected Value  1 1 0 1 1	· ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIcoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IOASt_State_M_enum	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 1 1 1 5	Expected Value  1 1 1 0 1 1 5	· ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_U08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 1 1 1 5 500	Expected Value  1 1 1 0 1 1 5 5 500	· ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_UIspS_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 1 1 5 500 1	Expected Value  1 1 1 0 1 1 5 500	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_1gc LoaMgr_LoaSt_State_M_enum LoaMgr_LoaScaleFctr_UIspS_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 1 1 5 500 1 5	Expected Value  1 1 1 0 1 1 5 500 1 5	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_UIspS_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 1 1 5 5 500 1 5 1	Expected Value  1 1 1 0 1 1 5 5 500 1 5 1	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_Igc	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 1 1 5 5 500 1 5 1 1 1	Expected Value  1 1 1 0 1 1 5 5 1 1 1 1 1 1 1 1 1 1 1	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaSt_ate_Limit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 1 1 5 500 1 5 1 1 1 3	Expected Value  1 1 1 0 1 1 5 500 1 5 1 1 1 3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_Igc	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 1 1 5 5 500 1 5 1 1 1 3 0	Expected Value  1 1 1 0 1 1 5 5 1 1 1 3 0	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaSt_ate_Limit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 1 1 5 500 1 5 1 1 1 3	Expected Value  1 1 1 0 1 1 5 500 1 5 1 1 1 3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 1 1 5 5 500 1 5 1 1 1 3 0	Expected Value  1 1 1 0 1 1 5 5 1 1 1 3 0	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 1 1 5 5 500 1 5 1 1 3 0 1	Expected Value  1 1 1 0 1 1 5 500 1 5 1 1 1 3 0 1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaSt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 1 1 1 5 500 1 5 1 1 1 3 0 1 5 1 1 5	Expected Value  1 1 1 0 1 1 5 500 1 1 1 1 1 1 1 5 5 5 1 1 1 5 1 1 5 1 1 5 1 1 5 1 1 1 5 1 1 1 5 5 1 1 1 5 5 1 1 1 5 5 6 6 6 6	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaSt_state_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgldahtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 1 1 1 5 5 500 1 1 1 3 0 1 1 5 1 1 5 1 1 5 1 1 1 5 1 1 1 1 1 1	Expected Value  1 1 1 0 1 1 5 500 1 1 1 1 3 0 1 5 1 1 1 5 1 1 1 1 1 1 1 1 1 1 1 1 1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_VltgMdSrcSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 1 1 1 5 5 500 1 1 5 1 1 1 3 0 1 5 1 1 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Expected Value  1 1 1 1 0 1 1 5 500 1 1 1 3 0 1 1 5 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrlOaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrlOaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgloaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrIoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 1 1 1 5 5 500 1 1 5 1 1 1 1 1 1 1 1	Expected Value  1 1 1 1 0 1 1 5 500 1 1 5 1 1 1 3 0 1 5 1 1 1 3 0 1 1 5 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_state_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 1 1 1 5 5 500 1 1 5 1 1 1 1 1 1 1 1	Expected Value  1 1 1 1 0 1 1 5 500 1 1 5 1 1 1 1 1 1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IwtrQtptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IoaState_M_enum LoaMgr_LoaState_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_NtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 1 1 1 5 5 500 1 1 5 1 1 1 3 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Expected Value  1 1 1 1 0 1 1 5 500 1 1 5 1 1 1 1 1 1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IwtrQptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIcptSigSV_Cnt_M_u08 LoaMgr_MotCurrIcptSigSV_Cnt_M_u08 LoaMgr_MotCurrIcptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 1 1 1 5 5 500 1 1 5 1 1 1 1 3 0 1 1 1 1 1 1 1 5 1 1 1 1 5 1 1 1 1 1 1	Expected Value  1 1 1 1 0 1 1 1 5 500 1 1 5 1 1 1 1 1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgNoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_Igc LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 1 1 1 5 5 500 1 5 1 1 1 1 1 1 1 1 1	Expected Value  1 1 1 1 0 1 1 1 5 500 1 5 1 1 1 1 3 0 1 1 5 1 1 1 3 0 1 {170, 172, 173} {0, 0, 0, 0, 0, 0} {1, 1, 1} 5500 ± 0.01 1 ± 0.001	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaStateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_StelTidaVail_Cnt_M_Igc LoaMgr_StelTidaVail_Cnt_M_Igc LoaMgr_StelTidaVail_Cnt_M_Igc LoaMgr_StelTidaVail_Cnt_M_Igc LoaMgr_NotCurrRespSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 1 1 1 5 5 500 1 5 1 1 1 1 1 1 1 1 1	Expected Value  1 1 1 1 0 1 1 1 5 500 1 5 1 1 1 1 3 0 1 1 5 1 1 1 3 0 1 {170, 172, 173} {0, 0, 0, 0, 0, 0} {1, 1, 1} 5500 ± 0.01 1 ± 0.001 1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LoASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_StelMgdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 1 1 1 5 5 500 1 5 1 1 1 1 1 1 1 1 1	Expected Value  1 1 1 1 0 1 1 1 5 500 1 5 1 1 1 1 3 0 1 1 5 1 0 1 {170, 172, 173} {0, 0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 1 1 1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgCont_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UIspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_LOASt_State_enum.value	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 1 1 1 5 5 500 1 5 1 1 1 1 1 1 1 1 1	Expected Value  1 1 1 1 0 1 1 1 5 500 1 5 1 1 1 1 3 0 1 1 5 1 0 1 {170, 172, 173} {0, 0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 1 1 5	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LoASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_StelMgdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 1 1 1 5 5 500 1 5 1 1 1 1 1 1 1 1 1	Expected Value  1 1 1 1 0 1 1 1 5 500 1 5 1 1 1 1 3 0 1 1 5 1 0 1 {170, 172, 173} {0, 0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 1 1 1	· · · · · · · · · · · · · · · · · · ·



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

T + 0+ 0 00 (D + 0 + 1)	
Test Step 3.20 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k LoaMgr CurrlvtrMtgtnSca Uls f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
	1
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	-	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	Cnt Igc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	- 100 u.i.
LoaMgr HwTqldptSigSV Cnt M u08	1	1	<b>V</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	<b>V</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	3	3	-
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	-
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>V</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	3	3	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	-
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	-
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(Param Cnt T u08)	{1, 1, 1}	{0. 0. 0. 0. 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	-
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target LoaMgr Per1 MotAgLoaMtgtnEn Cnt Igc.value	1	1	-
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	-

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>✓</b>





Test Step 3.21 (Repeat Count = 1)	12
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_Hw1qldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k LoaMgr HwTqldptSigFltThd Cnt u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1mactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc



Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170}	{170}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1}	{1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>✓</b>

Test Step 3.22 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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LoaMgr\_Per1



Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32 k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	1 2		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSig2Resp Cnt u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	2 0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2 0 2		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2 0 2 3		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2 0 2 3 2		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	2 0 2 3		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	2 0 2 3 2 1	nt Igc	
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	2 0 2 3 2 1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	2 0 2 3 2 1 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr	nt_lgc	
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target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	2 3 2 1 1 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_HwTqloptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrIcoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0	_lgc _lgc c t_lgc 08 int_lgc 8  Expected Value 0 1	<b>*</b>
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASc_Di_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_l08 LoaMgr_HwTqRespSV_Cnt_M_l08	2 3 2 1 1 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_HwTqloptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgt target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgt target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotOurrLoaMtgtnEn_Cnt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt target_LoaMgr_Per1_TloaDidtSig_Cnt_u0: target_LoaMgr_Per1_TloaDidtSig_Cnt_u0: target_LoaMgr_Per1_TloaDidtSig_Cnt_u0: target_LoaMgr_Per1_TloaDidt_Cnt_lgc Actual Value 0 1 0 0	int_lgc  _lgc c  t_lgc 08 int_lgc 8  Expected Value 0 1 0 0	· · · · · · · · · · · · · · · · · · ·
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target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsivtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08	2 3 2 1 1 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_log target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_log target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_log target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0i target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cn target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cn target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cn target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_SteerMod_Cnt_logc target_LoaMgr_Per1_TloaAvail_Cnt_logc Actual Value 0 1 0 0 0 5 5 5	Ligc	***
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_lwtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lwtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	2 3 2 1 1 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_HwTqlcptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0i target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0i target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cn target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cn target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 0 0 5 5 5 500	Ligc	****
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TsteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_lwtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LoaScaleFctr_Uls_M_632 LoaMgr_LoaScaleFctr_Uls_M_632	2 3 2 1 1 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_HwTqlcptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotQurrldptSig_Cnt_u0i target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0i target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0i target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 1 0 0 0 5 5 5 500 0	Ligc	***************************************
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotQurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqladptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	2 0 2 3 2 1 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0t_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0t_target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 0 0 5 5 5 500 0 0 5	Ligc   Ligc	***************************************
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TioaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TioaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TioaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TioaAvail_Cnt_lgc  Name  LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	2 3 2 1 1 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_HwTqlcptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotQurrldptSig_Cnt_u0i target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0i target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0i target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 1 0 0 0 5 5 5 500 0	Ligc	***************************************

2018-05-18, 17:54:37+0530



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 175}	{170, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.23 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





N	Immed Walter		
Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k LoaMgr MotAgMtgtnRate UlspS f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k LoaMgr MotAgMtqtnSca Uls f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inac		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inac		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LOASt_State_enutarget_LoaMgr_Per1_LoaScaDi_Cnt_lg		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScabi_Cit_gc	target_LoaMgr_Per1_MotAgLoaMtgtnE		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtn	_	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cr		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_er	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgo		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target LoaMgr Per1 TloaDi Cnt Igc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr HwTqRespSV Cnt M u08	4	4	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_IvtrRespSV_Cnt_M_u08	3	3	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	<b>✓</b>
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>~</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VitgMdSrcSV_Cnt_M_u08	1 (470, 470, 474)	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	<b>⊠</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1} 500	{1, 1, 1, 1} 500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	I		
	0	0	
	0	0	
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	7
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_LOASt_State_enum.value	1 5	1 5	7
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	7



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.24 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	.lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_	u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum	1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr HwTqldptSigSV Cnt M u08	2	2	•
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	3	3	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	_
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	3	3	_
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	_
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	_
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3	3	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	_
LoaMgr MotCurrLoaMtgtnEn Cnt M Igc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	_
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	_
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 174}	{170, 174}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	{1, 1}	{1, 1}	~
Rte Write Ap LoaMgr LoaRateLimit UlspS f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	-
target LoaMgr Per1 HwTqLoaMtgtnEn Cnt Igc.value	1	1	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	•
	- I -	1°	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 3.25 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	5
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	5
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Inst_Ap_LoaMgr	target Rte Inst Ap LoaMgr
k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0 0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08 k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurridptSig1Resp_Cnt_u08	0 0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08 k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k LoaMgr MotCurrMtgtnRate UlspS f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0 target LeaMar Port DisacStellytr1lpacty Cnt Inc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Peri_DiagcStsivtr2inactv_Cnt_igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Peri_HwTqldptSig_Cnt_u08	target_LoaMgr_Peri_Diagcststvtrzinactv_Cnt_igc target_LoaMgr_Peri_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_INtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target Rte Inst Ap LoaMgr.LoaMgr Per1 LoaScaDi Cnt Iqc	target Loawigi i ci i Loadcadi citi ige
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc





Name	Input Value			
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	arget_LoaMgr_Per1_SteerMod_Cnt_enum			
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc	rarget_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc			
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	0	0	~	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	•	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~	
LoaMgr_HwTqRespSV_Cnt_M_u08	5	5	•	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>	
LoaMgr_lvtrRespSV_Cnt_M_u08	2	2	~	
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	•	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~	
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	~	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~	
LoaMgr_MotCurrRespSV_Cnt_M_u08	5	5	~	
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~	
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~	
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.26 (Repeat Count = 1)	<b>13</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Name	Input Value			
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1			
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0			
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0			
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1			
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1			
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1			
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0			
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0			
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1			
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1			
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5			
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1			
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0			
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2			
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1			
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2			
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2			
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0			
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0			
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1			
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1			
k_LoaMgr_RedcdStRate_UlspS_f32	1			
k_LoaMgr_RedcdStSca_Uls_f32	1			
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1			
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1			
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1			
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1			
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2			
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1			
target LoaMgr Per1 SteerMod Cnt enum.value	2			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0			
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0			
target Rte Inst Ap LoaMgr.LoaMgr Per1 DiagcStslvtr1Inactv Cnt Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	Cnt Igc		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target LoaMgr Per1 DiagcStslvtr2Inactv	Cnt lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u	108		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_(			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target LoaMgr Per1 lvtrLoaMtgtnEn Cnt			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum			
target Rte Inst Ap LoaMgr.LoaMgr Per1 LoaScaDi Cnt Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_	Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target LoaMgr Per1 SteerMod Cnt enur			
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc			
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc			
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	0	0	ixesuii	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1		
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	0	0		
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0		
LoaMgr_IvtrldptSigSV_Cnt_M_u08	0	0		
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0		
	5	5		
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5		
LoaMgr_LoaState_M_enum				
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500		
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1		
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	•	
LoaMgr MotAgRespSV Cnt M u08	3	3	·	

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Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 175}	{170, 172, 173, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	1	1 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.27 (Repeat Count = 1)	N .
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Nama	Innut Value		
Name k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	Input Value		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08 k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	0		
k_LoaMgr_MotCurrMtgtnNate_otspo_joz	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target LoaMgr Per1 TloaDi Cnt Igc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Ir	nactv Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Ir		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_0	Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtr	nEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEr	n_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lge		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	✓
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	<b>V</b>
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1 2	2	
LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	5	5	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>~</b>
LoaMgr MaxMtgtnResp Cnt M u08	5	5	_
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc			
	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	•
LoaMgr_TloaAvail_Cnt_M_lgc	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	2 1 1	2 1 1	· ·
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	2 1 1 0	2 1 1 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	2 1 1 0 {170, 173, 174}	2 1 1 0 {170, 173, 174}	• • • • • • • • • • • • • • • • • • •
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	2 1 1 0	2 1 1 0	V V V V V V V V V V V V V V V V V V V
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2 1 1 0 {170, 173, 174} {1, 1, 1}	2 1 1 0 {170, 173, 174} {0, 0, 0, 0, 0, 0}	\(\frac{1}{2}\)
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	2 1 1 0 {170, 173, 174} {1, 1, 1} {1, 1, 1}	2 1 1 0 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001	
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	2 1 1 0 {170, 173, 174} {1, 1, 1} {1, 1, 1} 500 1	2 1 1 0 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001 0	\(\frac{1}{2}\)
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	2 1 1 0 {170, 173, 174} {1, 1, 1} {1, 1, 1} 500 1	2 1 1 0 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001 0	
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	2 1 1 0 {170, 173, 174}  {1, 1, 1} {1, 1, 1} 500 1 0 1 5	2 1 1 0 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001 0 1	\(\frac{1}{2}\)
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	2 1 1 0 {170, 173, 174} {1, 1, 1} {1, 1, 1} 500 1	2 1 1 0 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001 0	**************************************



Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	<b>~</b>
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.28 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr MotAgldptSigSV Cnt M u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
	1
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	:nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_le	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr HwTqldptSigSV Cnt M u08	2	2	~
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>~</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	3	3	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	•

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	<b>~</b>
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Pte Evit An LoaMar FalloaScaleAndRate	1	Pte Evit An LoaMar FalloaScaleAndPate	1	<b>V</b>





Test Step 3.29 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotApDoopSV_Cot_M_upg	0
LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	2
k_LoaMgr_Hw1qldptSig2TloaEnaResp_Cnt_u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1 0
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k LoaMgr MotAgMtgtnSca Uls f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_UIs_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMigr_Per1_Loascabi_Crit_igc target_LoaMigr_Per1_MotAgLoaMigtnEn_Crit_igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc



Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	1	1	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>~</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace			<b>✓</b>	
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.30 (Repeat Count = 1)	<b>12</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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LoaMgr\_Per1



Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32 k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k LoaMgr lvtrldptSig2Resp Cnt u08	0		
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32 k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cot_loa	target_LoaMgr_Per1_LoaScaDi_Cnt_lge		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_	Cnt lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEr		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enu		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	3	3	~
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32		1500	<b>✓</b>
	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	0 5	0 5	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	0 5 0	0 5 0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	0 5	0 5	~





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace				, v
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Name Input Value	
LoaMgr_AgSV_Cnt_M_lgc 0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc 0	
LoaMgr_HwTqRespSV_Cnt_M_u08 3	
LoaMgr_lvtrldptSigSV_Cnt_M_u08 0	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc 0	
LoaMgr_IvtrRespSV_Cnt_M_u08 3	
LoaMgr_MotAgldptSigSV_Cnt_M_u08 0	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc 0	
LoaMgr_MotAgRespSV_Cnt_M_u08	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08 2	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 0	
LoaMgr_MotCurrRespSV_Cnt_M_u08 0	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc 1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08 0	
Rte_Inst_Ap_LoaMgr target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32 1	
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc 0	
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32 1	
k_LoaMgr_FadeOutStRate_UlspS_f32 1	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08 2	
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 4	
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 2	
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 1	
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 0	
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 0	
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
k_LoaMgr_HwTqMtgtnRate_UlspS_f32 1	
k_LoaMgr_HwTqMtgtnSca_Uls_f32	
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08 0	
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08 0	
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08 0	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32 1	
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc 0	
k_LoaMgr_lvtrMtgtnSca_Uls_f32 1	
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 5	





Name	Innut Value		
Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k LoaMgr MotAgMtgtnRate UlspS f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target LoaMgr Per1 LoaScaDi Cnt Igc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LoASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_	Ont lac	
target_Rte_Inst_Ap_LoaMgr_Der1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cni		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEr	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enu	m	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>Y</b>
LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr LOASt State M enum	5	5	-
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
		300	
LoaMar LoaScaleEctr Llls M f32		0	<b>✓</b>
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	0 5	5	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	0		~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	0 5 0	5	, ,
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0 5 0 1	5 0 1	\rightarrow \frac{1}{2}
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_igc LoaMgr_MotAgRespSV_Cnt_M_u08	0 5 0 1 2	5 0 1 2	\rightarrow \frac{1}{2}
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0 5 0 1 2	5 0 1 2	\rightarrow \right
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0 5 0 1 2 1	5 0 1 2 1 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_igc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_igc LoaMgr_TloaDi_Cnt_M_igc	0 5 0 1 2 1 0 0	5 0 1 2 1 0 0 0	\rightarrow \frac{1}{2}
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_igc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_igc LoaMgr_TloaDi_Cnt_M_igc LoaMgr_TloaDi_Cnt_M_igc LoaMgr_VltgMdSrcSV_Cnt_M_u08	0 5 0 1 2 1 0 0 0	5 0 1 2 1 0 0 0 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0 5 0 1 2 1 0 0 0 0 1 0 (170, 173}	5 0 1 2 1 0 0 0 1 0 1 0 1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_igc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_igc LoaMgr_TloaDi_Cnt_M_igc LoaMgr_TloaDi_Cnt_M_igc LoaMgr_VtgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0 5 0 1 2 1 0 0 0 0 1 0 (170, 173}	5 0 1 2 1 0 0 0 1 0 (170, 173) {0, 0, 0, 0, 0}	
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VtlgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 5 0 1 2 1 0 0 0 0 1 0 (170, 173} (1, 1) {1, 1}	5 0 1 2 1 0 0 0 1 0 {170, 173} {0, 0, 0, 0, 0, 0} {1, 1}	
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	0 5 0 1 2 1 0 0 0 0 1 0 (170, 173) {1, 1} {1, 1} 500	5 0 1 2 1 0 0 0 0 1 0 {170, 173} {0, 0, 0, 0, 0, 0} {1, 1} 500 ± 0.01	
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_u08) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0 5 0 1 2 1 0 0 0 0 1 0 (170, 173} (1, 1) {1, 1} 500	5 0 1 2 1 0 0 0 0 1 0 {170, 173} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 0 ± 0.001	
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	0 5 0 1 2 1 0 0 0 0 1 0 (170, 173) {1, 1} {1, 1} 500	5 0 1 2 1 0 0 0 0 1 0 {170, 173} {0, 0, 0, 0, 0, 0} {1, 1} 500 ± 0.01	
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0 5 0 1 2 1 0 0 0 0 1 0 0 1 0 (170, 173) {1, 1} {1, 1} 500 0 0	5 0 1 2 1 0 0 0 1 0 {170, 173} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 0 ± 0.001 0	
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	0 5 0 1 2 1 0 0 0 0 0 1 1 0 (170, 173) {1, 1} {1, 1} 500 0 0 0	5 0 1 2 1 0 0 0 1 0 {170, 173} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 0 ± 0.001 0	
LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaVail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VttgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value  target_LoaMgr_Per1_LOASt_State_enum.value	0 5 0 1 2 1 0 0 0 0 0 1 1 0 (170, 173) {1, 1} {1, 1} 500 0 0 0 5	5 0 1 2 1 0 0 0 1 0 (170, 173) {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 0 ± 0.001 0 0 5	



「est Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.32 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr MotAgldptSigSV Cnt M u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnKate_Oisp3_i32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnKate_0isp3_i32 k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
	1
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotAgIdptSig3Resp_Cnt_u08 k_LoaMgr_MotAgIdptSigFltThd_Cnt_u08	2
	1
k_LoaMgr_MotAgMtgtnSoaZoroEn_Cot_loa	0
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
	2
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2 0
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	.lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt	u08	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum	1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	0	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	-
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	2	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	-
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	-
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	-
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~



Injust Value   Injus	Test Step 3.33 (Repeat Count = 1)	12
Looking   American   American		
Looking   Invited activity   Dec   Dec		
Loading   Junispies   Loading   Loading   Loading   Loading   Junispies   Loading		3
Landing - Mendenghage Cort M. 108  Loaning - Menden	LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
Landing Medigenesity Crit M. 108  Landing Medicensesity Crit Medicensesity Crit Medicensesity Crit Medicensesity Crit Medicensesity Crit Medicensesity Crit Medicenses Crit Medicensesity Crit Medicenses Crit Medice	LoaMgr_HwTqRespSV_Cnt_M_u08	2
Loading Mandages SV Cris M. vi0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	LoaMgr_lvtrldptSigSV_Cnt_M_u08	
Looking   MacApigenesis   Cort   M. 196		
Coadags   MacAge analoge   Corp.   12   15		
Loading MacGardings Cort M, USB  Loading MacGardings Cort M, USB  Loading MacGardings Cort M, Mag  Rein Find R, Loading C, Loading L, Loading L, Loading C, Loadings C, Loading MacGardings Cort M, Mag  Landing MacGardings L, Loading C, Loadings C, Loading C, Loadings C, Loa		
Loading / McContradigners Cott / M. 196   Loading / McContradigners / M. 196   Loading / McContradig		1
Leading MoConfreeDescription (M. Mgs  Leading MoConfreeDescription (M. Mgs  Leading MoConfreeDescription (M. Mgs  Leading Leading (M. Mgs  Leading		
Loadleg. Control Margines   Mar	LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	
Loadley_Carly Agriculture   Loadley_Carly Lines   Loadley_Carly	LoaMgr_MotCurrRespSV_Cnt_M_u08	0
Restands   Lander	LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
	LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Linable_Driftonthalpriss_Driftonthalpr	Rte_Inst_Ap_LoaMgr	V =
Loading-InvitoglisignTrans-Availance_Crit_UBB   Loading-InvitoglisignTrans-Availance_Crit_UBB   Loading-InvitoglisignTrans-Availance_Crit_UBB   Loading-InvitoglisignTrans-Availance_Crit_UBB   Loading-InvitoglisignTrans-Availance_Crit_UBB   Loading-InvitoglisignTrans-Availance_Crit_UBB   Loading-InvitoglisignTrans-Crit_UBB   Loading-		
L. Loading-In-Minigrosity-Transcription, Crit. (Job. 1)		
K. Loadey_HwTopicsSg2TranDirects_Crd_Lu08		
	k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	
	k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	
		0
	k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
K_LosMgr_MridghtSeap_Cnt_U08	k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
Loading Intrigistign Resp. Cort Lu08	k_LoaMgr_HwTqMtgtnRate_UlspS_f32	
K_LosMgr_Intringtsig2Resp_Cnt_U08		
L. Louking / Intriducising Fittind_Cnt_U08		
k_Looklogr_IvrinfignRate_UispS_132         1           k_Looklogr_IvrinfignScac_UispS_2         0           k_Looklogr_IvrinfignScac_UispS_2         1           k_Looklogr_MoldgidpSig2Resp_Cnt_u08         5           k_Looklogr_MoldgidpSig2Resp_Cnt_u08         0           k_Looklogr_MoldgidpSig2Resp_Cnt_u08         0           k_Looklogr_MoldgidpSig2Resp_Cnt_u08         2           k_Looklogr_MoldgidpSig2Resp_Cnt_u08         2           k_Looklogr_MoldgidpSig2Resp_Cnt_u08         2           k_Looklogr_MoldgidpSig2Resp_Cnt_u08         2           k_Looklogr_MoldgidpSig2Resp_Cnt_u08         2           k_Looklogr_MoldgidpSig2Resp_Cnt_u08         2           k_Looklogr_MoldcurridpSig1Resp_Cnt_u08         2           k_Looklogr_MoldcurridpSig1Resp_Cnt_u08         0           k_Looklogr_MoldcurridpSig1Resp_Cnt_u08         0 <t< td=""><td></td><td></td></t<>		
K, LouMgr_InvintignSca_Uis_IS2         1           K, LouMgr_InvintignSca_Uis_IS2         1           K, LouMgr_MoNajdipSig1NSignSresp_Cnt_U08         5           K, LouMgr_MoNajdipSig1NSignSresp_Cnt_U08         1           K, LouMgr_MoNajdipSig1NSignSresp_Cnt_U08         2           K, LouMgr_MoNajdipSigNSignSresp_Cnt_U08         2           K, LouMgr_MoNajdipSignSresp_Cnt_U08         2           K, LouMgr_MoNajdipSignSresp_Cnt_U08         0           K, LouMgr_MoNajdipSison_Uis_I32         1           K, LouMgr_MoNajdipSison_Uis_I32         1           K, LouMgr_MoNajdipSison_Uis_I32         2           K, LouMgr_MoldupSison_Uis_I32         1           K, LouMgr_MoldupSison_Uis_I32         1           K, LouMgr_MoldupSison_Uis_I32         2           K, LouMgr_MoldupSison_Uis_I32         0           K, LouMgr_MoldupSison_Uis_I32         0           K, LouMgr_MoldupSison_Uis_I32         0           K, LouMgr_MoldupSison_Uis_I32         1           K, Lo		
LoaMgr MoRajdiptSig3Resp_Cnt_U08		
k_LoaMgr_MorAgidptSig3Resp_Cnt_u08         0           k_LoaMgr_MorAgidptSig1FiThd_Cnt_u08         2           k_LoaMgr_MorAgidptSig1FiThd_Cnt_u08         1           k_LoaMgr_MorAgMignRate_UBsp5_f22         1           k_LoaMgr_MorAgMignSca_Uis_f32         1           k_LoaMgr_MorAgMignSca_Uis_f32         1           k_LoaMgr_MorAgSmaAvailResp_Cnt_u08         2           k_LoaMgr_MorCurridptSig1Resp_Cnt_u08         2           k_LoaMgr_MorCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MorCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MorCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MorCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MorCurridptSig1Resp_Sf122         1           k_LoaMgr_MorCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MorCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MorCurridptSig1Resp_Usp_Sf22         1           k_LoaMgr_MorCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_RedodSf3Res_Ulsp_Sf22         1           k_LoaMgr_RedodSf3Res_Ulsp_Sf22         1           k_LoaMgr_RedodSf3Res_Uls_Sf22         1           k_LoaMgr_Pert_DiagcStstvr1inactv_Cnt_lgc value         1           target_LoaMgr_Pert_LoaScaD_LoaT_lgc value         1           target_LoaMgr_Pert_SteerMod_	k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
K_LoaMgr_MotAgldplSigFitTnd_Cnt_u08         2           K_LoaMgr_MotAghtghtRate_UlspS_132         1           K_LoaMgr_MotAghtghtScace_Cnt_crt_ge         0           K_LoaMgr_MotAghtghtScace_Uls_132         1           K_LoaMgr_MotAghtghtScace_Cnt_u08         2           K_LoaMgr_MotCurridptSigGeRes_Cnt_u08         2           K_LoaMgr_MotCurridptSigGeRes_Cnt_u08         0           K_LoaMgr_MotCurridptSigGeRes_Cnt_u08         0           K_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           K_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           K_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           K_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           K_LoaMgr_MotCurridptsigSeZeroEn_Cnt_lgc         1           K_LoaMgr_MotCurridptsigSeZeroEn_Cnt_lgc         1           K_LoaMgr_RedcdSiRae_Uls_52         1           K_LoaMgr_RedcdSiRae_Uls_52         1           K_LoaMgr_RedcdSiRae_Uls_52         1           K_LoaMgr_Pert_DiagcSistvtTlnactv_Cnt_lgc_value         0           target_LoaMgr_Pert_DiagcSistvtTlnactv_Cnt_lgc_value         1           target_LoaMgr_Pert_HwTiQfstSig_Cnt_u08 value         1           target_LoaMgr_Pert_InCurridptSig_Cnt_u08 value         3           target_LoaMgr_Pert_MirrostoftSig_Cnt_u08 value         3	k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
K_LoaMgr_MotAyMightRate_UlspS_f32         1           K_LoaMgr_MotAyMightSacZencEn_Cnt_igc         0           K_LoaMgr_MotAyMightSacZencEn_Cnt_igc         1           K_LoaMgr_MotAyMightSac_Ulsp_f32         1           K_LoaMgr_MotCurridps/SigResp_Cnt_u08         2           K_LoaMgr_MotCurridps/SigResp_Cnt_u08         0           K_LoaMgr_MotCurridps/SigResp_Cnt_u08         0           K_LoaMgr_MotCurridps/SigResp_Cnt_u08         0           K_LoaMgr_MotCurridps/SigResp_Cnt_u08         0           K_LoaMgr_MotCurridps/SigResp_Sig2         1           K_LoaMgr_MotCurridps/SigResp_Sig2         1           K_LoaMgr_MotCurridps/SigResp_Sig2         1           K_LoaMgr_RedcdSisRet_ulsp_Sig2         1           K_LoaMgr_Perd_SigResp_Sig2         1           K_LoaMgr_Perd_DiagcSistvirlinactv_Cnt_igc value         0           Larget_LoaMgr_Perd_DiagcSistvirlinactv_Cnt_igc value         1           Larget_LoaMgr_Perl_DiagcSistvirlinactv_Cnt_igc value         1           Larget_LoaMgr_Perl_MotCuridpsig_Cnt_u08 value         1           Larget_LoaMgr_Perl_MotCuridpsig_Cnt_u08 value         1           Larget_LoaMgr_Perl_MotCuridpsig_Cnt_u08 value         1           Larget_LoaMgr_Perl_TioaDi_Cnt_igc value         0           Larget_LoaMgr_Perl_TioaQac_Cnt_igc_value	k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
K_LoaMgr_MotAgMtgthSca_ZeroEn_Cn_Igc         0           K_LoaMgr_MotAgMtgthSca_Uls_132         1           K_LoaMgr_MotAgMtgthSca_Uls_132         2           K_LoaMgr_MotCurridptSigGResp_Cnt_u08         2           K_LoaMgr_MotCurridptSigGresp_Cnt_u08         0           K_LoaMgr_MotCurridptSigFiltThd_Cnt_u08         0           K_LoaMgr_MotCurridptSigFiltThd_Cnt_u08         0           K_LoaMgr_MotCurridptSigFiltThd_Cnt_u08         0           K_LoaMgr_MotCurridptSigFiltThd_Cnt_u08         1           K_LoaMgr_MotCurridptSigFiltThd_Cnt_u08         1           K_LoaMgr_MotCurridptSigScreen_Cnt_u0g         1           K_LoaMgr_MotCurridptSig_Cnt_u08         1           K_LoaMgr_ReddSiSca_Uls_5122         1           K_LoaMgr_Pert_DiagcStsivtr1lnack_Cnt_lgc_value         1           Larget_LoaMgr_Pert_DiagcStsivtr2lnack_Cnt_lgc_value         1           Larget_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value         4           Larget_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value         1           Larget_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value         3           Larget_LoaMgr_Pert_Tolaqvail_Cnt_lgc_value         0           Larget_LoaMgr_Pert_Tolaqvail_Cnt_lgc_value         0           Larget_LoaMgr_Pert_Tolaqvail_Cnt_lgc_value         0           Larget_LoaMgr_Pert_Tolaqvail_C	k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	
k_LoaMgr_MotAgMiginSca_Ulis_f32         1           k_LoaMgr_MotAgSmaAvaiiResp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Sig2         1           k_LoaMgr_MotCurridptSig2Sig2Sig2         1           k_LoaMgr_MotCurridptSig2Sig2Sig2         1           k_LoaMgr_ReddSiSca_Uis_f32         1           k_LoaMgr_ReddSiSca_Uis_f32         1           k_LoaMgr_Pert_DiagcStsivfTinactv_Cnt_gc value         1           target_LoaMgr_Pert_DiagcStsivfTinactv_Cnt_gc value         1           target_LoaMgr_Pert_MrPosidptSig_Cnt_u08 value         1           target_LoaMgr_Pert_Tinach_Cnt_lgc value         0           target_LoaMgr_Pert_Tinach_Cnt_lgc value         0           targ	k_LoaMgr_MotAgMtgtnRate_UlspS_f32	
k_LoaMgr_MotOurdiptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurrdiptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurrdiptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrdiptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrdiptSig1FIThd_Cnt_u08         0           k_LoaMgr_MotCurrMigInSca_Usp_S_132         1           k_LoaMgr_MotCurrMigInSca_Usp_S_128         1           k_LoaMgr_MotCurrMigInSca_Usp_S_128         1           k_LoaMgr_MotCurrMigInSca_Usp_S_128         1           k_LoaMgr_RedGISSca_Usp_S_128         1           k_LoaMgr_RedGISSca_Usp_S_128         1           k_LoaMgr_Pert_DiagcSisivIrtInactv_Cnt_igc_value         0           target_LoaMgr_Pert_DiagcSisivIrtInactv_Cnt_igc_value         1           target_LoaMgr_Pert_HwTqidptSig_Cnt_u08_value         4           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08_value         1           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08_value         1           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08_value         1           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08_value         1           target_LoaMgr_Pert_TioaOl_Cnt_igc_value         0           target_LoaMgr_Pert_TioaOl_Cnt_igc_value         1           target_LoaMgr_Pert_TioaOl_Cnt_igc_value         0           target_LoaMgr_Pert_TioaOl_C		1
k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptRate_UlspS_132         1           k_LoaMgr_RedcdSiRate_UlspS_32         1           k_LoaMgr_RedcdSiRate_UlspS_132         1           k_LoaMgr_Perd_DiagcSistvir1nactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcSistvir2nactv_Cnt_lgc.value         1           target_LoaMgr_Per1_DiagcSistvir2nactv_Cnt_lgc.value         1           target_LoaMgr_Per1_Mr_LoaMgr_Der1_U08 value         4           target_LoaMgr_Per1_Mr_LoaMgr_Der1_U08 value         1           target_LoaMgr_Per1_MrPosliptSig_Cnt_u08 value         1           target_LoaMgr_Per1_ToaDr_Cnt_lgc.value         1           target_LoaMgr_Per1_ToaDr_Cnt_lgc.value         0           target_LoaMgr_Per1_ToaDr_Cnt_lgc.value         0           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMgr_Enc_nt_lgc         target_LoaMgr_Per1_HwTqLoaMgr_Enc_nt_lgc           <	_ 0_ 0	
k_LoaMgr_MolCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MolCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MolCurridptSigFilThd_Cnt_u08         0           k_LoaMgr_MolCurrMtgtnRste_UlspS_132         1           k_LoaMgr_MolCurrMtgtnSca_Uls_152         1           k_LoaMgr_RedcdStRate_UlspS_132         1           k_LoaMgr_RedcdStRate_UlspS_132         1           k_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc_value         0           target_LoaMgr_Perl_DiagcStsIvtrInactv_Cnt_lgc_value         1           target_LoaMgr_Perl_DiagcStsIvtrInactv_Cnt_lgc_value         1           target_LoaMgr_Perl_MolCurridptSig_Cnt_u08_value         4           target_LoaMgr_Perl_MolCurridptSig_Cnt_u08_value         1           target_LoaMgr_Perl_MolCurridptSig_Cnt_u08_value         1           target_LoaMgr_Perl_MolCurridptSig_Cnt_u08_value         3           target_LoaMgr_Perl_ToaDi_Cnt_lgc_value         0           target_LoaMgr_Perl_ToaDi_Cnt_lgc_value         0           target_LoaMgr_Perl_ToaDi_Cnt_lgc_value         0           target_LoaMgr_Perl_ToaDi_Cnt_lgc_value         0           target_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_DiagcStsIvtrInactv_Cnt_lgc         target_LoaMgr_Perl_DiagcStsIvtrInactv_Cnt_lgc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_Mort_DiagcStsIvtrInactv_Cnt_lgc         target_LoaMgr_Perl_HwTqloaMtgtnEn_Cnt_lgc<		
k_LoaMgr_MolCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MolCurridptSigFilThd_Cnt_u08         0           k_LoaMgr_MolCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MolCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MolCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdSlSca_Uls_f32         1           k_LoaMgr_Perd_DiagcStslvtr1nactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc.value         1           target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc.value         1           target_LoaMgr_Per1_MolCurrldptSig_Cnt_u08.value         4           target_LoaMgr_Per1_MolCurrldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MolCurrldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_TinaOaVail_Cnt_lgc.value         1           target_LoaMgr_Per1_TinaOaVail_Cnt_lgc.value         0           target_Re_inst_Ap_LoaMgr_Per1_DiagcStslvtr1inactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStslvtr2inactv_Cnt_lgc           target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_DiagcStslvtr2inactv_Cnt_lgc           target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgnEn_Cnt_lgc         target_LoaMgr_Per1_HvTqLoaMtgnEn_Cnt_lgc           target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgnEn_Cnt_lgc         target_LoaMgr_Per1_HvTqLoaMtgnEn_Cnt_lgc           target_LoaMgr_Per1_LoaAMgr_Per		
k_Loaklgr_MotCurridptSigFitThd_Cn_u08         0           k_Loaklgr_MotCurridptRate_UlspS_f32         1           k_Loaklgr_MotCurridptRate_UlspS_f32         1           k_Loaklgr_MotCurridptRisca_Uls_f32         1           k_Loaklgr_RedodStRate_UlspS_f32         1           k_Loaklgr_RedodStRate_UlspS_f32         1           k_Loaklgr_Per1_DiagcStsivtr1lnactv_Cnt_lgc.value         0           target_Loaklgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc.value         1           target_Loaklgr_Per1_HvTqldptSig_Cnt_u08.value         4           target_Loaklgr_Per1_HvTqldptSig_Cnt_u08.value         1           target_Loaklgr_Per1_MotCurridptSig_Cnt_u08.value         1           target_Loaklgr_Per1_MotCurridptSig_Cnt_u08.value         3           target_Loaklgr_Per1_SteerMod_Cnt_enum.value         1           target_Loaklgr_Per1_ToaAvail_Cnt_lgc.value         0           target_Rel_nst_Ap_Loaklgr_Loaklgr_Per1_DiagcStsivtr1lnactv_Cnt_lgc         target_Rel_nst_Ap_Loaklgr_Loaklgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc           target_Rel_nst_Ap_Loaklgr_Loaklgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc         target_Loaklgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc           target_Rel_nst_Ap_Loaklgr_Loaklgr_Per1_HvTqldptSig_Cnt_u08         target_Loaklgr_Per1_HvTqldptSig_Cnt_u08           target_Rel_nst_Ap_Loaklgr_Loaklgr_Per1_HvTqldptSig_Cnt_lgc         target_Loaklgr_Per1_HvTqLoakltgtnEn_Cnt_lgc           t		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc.value         1           target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc.value         4           target_LoaMgr_Per1_HvTqldptSig_Cnt_u08.value         4           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value         3           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value         3           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value         3           target_LoaMgr_Per1_ToaDd_Cnt_lgc.value         0           target_LoaMgr_Per1_ToaDaVall_Cnt_lgc.value         0           target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqloaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_HvTqloaMtgtnEn_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_McCurridptSig_Cnt_u08.value target_LoaMgr_Per1_MrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SterMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_NtrLoaMtgt	k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1.
k_LoaMgr_RedcdStRate_UlspS_f32	k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
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target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_ivtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc



Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	3	3	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	1	1	~
LoaMgr_LOASt_State_M_enum	4	4	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	4	4	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	0	0	•
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{171}	{171}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1}	{1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	4	4	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>✓</b>

Test Step 3.34 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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LoaMgr\_Per1



Namo	Input Value		
Name k LoaMar CurristrMtataScaZeroEn Cat Inc	Input Value		
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_CurrivtrMtgtnSca_UIs_f32 k_LoaMgr_FadeOutStRate_UIspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k LoaMgr HwTqldptSigFltThd Cnt u08	1		
k LoaMgr HwTqMtgtnRate UlspS f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_IvtrIdptSig1Resp_Cnt_u08	0		
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
	1		
k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target LoaMgr Per1 MotCurrldptSig Cnt u08.value	1		
target LoaMgr Per1 MtrPosldptSig Cnt u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target LoaMgr Per1 TloaDi Cnt Igc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cni		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 LOASt State enum	target LoaMgr Per1 LOASt State enum	,-	
target Rte Inst Ap LoaMgr.LoaMgr Per1 LoaScaDi Cnt Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
	1	1	result
LoaMgr_AgSV_Cnt_M_lgc	2	2	~
LoaMgr_HwTqlqaMtatxEp_Cot_M_lgc	1	1	-
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	3	3	~
	0	0	-
LoaMgr_lvtrldptSigSV_Cnt_M_u08			-
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_LoaRstol imit_UlanS_M_f22	5	5	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•

LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

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Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 175}	{170, 173, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.35 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_	_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum	1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	3	3	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>~</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc		0	~
	0	4	
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	1 1 1	1	<b>*</b>
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	1 1 1 0	1 1 0	, , , , , , , , , , , , , , , , , , ,
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	1 1 1 0 {170, 173, 175}	1 1 0 {170, 173, 175}	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1 1 1 0 {170, 173, 175} {1, 1, 1}	1 1 0 {170, 173, 175} {0, 0, 0, 0, 0}	✓ ✓ ✓
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1 1 1 0 {170, 173, 175} {1, 1, 1} {1, 1, 1}	1 1 0 {170, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1}	✓ ✓ ✓
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1 1 1 0 {170, 173, 175} {1, 1, 1} {1, 1, 1} 500	1 1 0 {170, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01	**************************************
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1 1 0 {170, 173, 175} {1, 1, 1} {1, 1, 1} 500	1 1 0 {170, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001	**************************************
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1 1 1 0 {170, 173, 175} {1, 1, 1} {1, 1, 1} 500	1 1 0 {170, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001	**************************************
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1 1 1 0 {170, 173, 175} {1, 1, 1} {1, 1, 1} 500	1 1 0 {170, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001	× × × × × × × × × × × × × × × × × × ×
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	1 1 1 0 {170, 173, 175} {1, 1, 1} {1, 1, 1} 500 1 1 1 5	1 1 0 {170, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001 1 1	× × × × × × × × × × × × × × × × × × ×
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1 1 1 0 {170, 173, 175} {1, 1, 1} {1, 1, 1} 500	1 1 0 {170, 173, 175} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	<b>✓</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.36 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrlytrMtgtnScaZeroEn Cnt Igc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
	2
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k LoaMgr RedcdStSca Uls f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
	<u> </u>





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	-	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0	Ont Igc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target LoaMgr Per1 SteerMod Cnt enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	0	0	· 100an
LoaMgr HwTqldptSiqSV Cnt M u08	1	1	_
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	·
LoaMgr lvtrldptSigSV Cnt M u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_IvtrRespSV_Cnt_M_u08	2	2	
LoaMgr_LOASt_State_M_enum	5	5	<b>V</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	_
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>V</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	·
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	
LoaMgr_TloaDi_Cnt_M_lgc	1	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 172, 173}	{170, 172, 173}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_CITE_1_endIII)	{1, 1, 1}	{0, 0, 0, 0, 0}	<b>B</b>
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	{1, 1, 1}	{1, 1, 1}	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	(1, 1, 1) 500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMigr_Per1_Intr_LoaMigtriEn_Cnt_lgc.value target_LoaMigr_Per1_lvtrLoaMigtriEn_Cnt_lgc.value	1	1	
	5	5	
target_LoaMgr_Per1_LOASt_State_enum.value	0	0	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	U	U	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>✓</b>





Injust Value	Test Step 3.37 (Repeat Count = 1)	12
Loading   Ambridge   Cort   Mage		
Leading Instructions Could by the Control of the Country Instruction Country Instructi		
Leading   Intrologistics   Crit M   Loss		1
Leading   Jendengley Cort   M. Judi	LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
Leading Fundangering Core Mu 198 Leading Managedering Core Mu 199 Leading Managedering Core Mu 199 Leading Managedering Core Mu 199 Leading Managedering Core Mu 198 Leading Managedering Core Mu 198 Leading Leading Leading Mu 198 Leading Le	LoaMgr_HwTqRespSV_Cnt_M_u08	4
Loading_Ambidgestagesty  Cort_M_Load	LoaMgr_lvtrldptSigSV_Cnt_M_u08	
Loading / MacAglaesia (SV Core, M. 100)		
Contagn_Managenesing_Cont_M_Sec_   Contagn_Manage		
Looking MacCure (1987) Co. M. 1, 100   Looking MacCure		
Londing MocContradge(Str. Crit. M. 196		1
Looking / Michael Post / March   March		
Lookings   Looking   Loo	LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	
Loading / Lindhigh Searce	LoaMgr_MotCurrRespSV_Cnt_M_u08	1
	LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
L. Loadings / Landings/Saczenian   Circli ge	LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Landing   Landing Residuation   Landing Residuation   Landing Residuation   Landing   La	Rte_Inst_Ap_LoaMgr	V =
L. Looking-  HardigotiSg0PTeackwalResp_Crt_U88		
L. Looking-In-WritingtisSg/Tellon Resp. Cnt. U08		
	k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	
	k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	
		0
K_Loshig_HwTqhtghRate_UlspS_122	k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
K_LosMgr_MritghtsgrResp_Cnt_u08	k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
Loakgr_MridptSigPResp_Cnt_u08	k_LoaMgr_HwTqMtgtnRate_UlspS_f32	
K_LosMgr_IntridptSig2Ptes_Cnt_U08		
k_LoaMgr_IvridptSigPIRTive_Cnt_U08         0           k_LoaMgr_IvridptRate_UbpS_122         1           k_LoaMgr_IvridptSica_Rep_Cnt_U08         1           k_LoaMgr_IvridptSica_Rep_Cnt_U08         5           k_LoaMgr_ModQptSigSigNsmRep_Cnt_U08         1           k_LoaMgr_ModQptSigSigRtTivd_Cnt_U08         2           k_LoaMgr_ModQptSigSigRtTivd_Cnt_U08         2           k_LoaMgr_ModQptSigSigRtTivd_Cnt_U08         2           k_LoaMgr_ModQptSigRtTivd_Cnt_U08         2           k_LoaMgr_ModQptSigRtTivd_Cnt_U08         2           k_LoaMgr_ModQptSigrsCazeOct_Cnt_U08         2           k_LoaMgr_ModQptSigrsAvaiRes_Cnt_U08         2           k_LoaMgr_ModQptSigrsAvaiRes_Cnt_U08         2           k_LoaMgr_ModCurridptSigRes_Cnt_U08         2           k_LoaMgr_ModCurridptSigRes_Cnt_U08         0           k_LoaMgr_ModCurridptSigRes_Cnt_U08         <		
K_Loakly_InvitMignRate_UlspS_132		
k, LoaMgr_IvriMiginScaZeroEn_Cnt_lgs         0           k, LoaMgr_IvriMiginSca_Lis_IS2         1           k, LoaMgr_MoNajdisSigNSimResp_Cnt_u08         5           k, LoaMgr_MoNajdisSigResp_Cnt_u08         1           k, LoaMgr_MoNajdisSigResp_Cnt_u08         2           k, LoaMgr_Molor_MortgrissigResp_Cnt_u08         2           k, LoaMgr_Molor_IndeptsigResp_Cnt_u08         2           k, LoaMgr_Molor_IndeptsigResp_Cnt_u08         0           k, LoaMgr_Molor_IndeptsigResp_Cnt_u08         1           k, LoaMgr_Molor_IndeptsigResp_Cnt_u08         1           k, LoaMgr_Molor_IndeptsigResp_Cnt_u08         1           k, LoaMgr_Molor_IndeptsigResp_Cnt_u08         1           k, LoaMgr_Molor_Indiglssid_Cnt_u08         1           k, LoaMgr_Molo		
k_LoaMgr_MorAgidpiSig/Resp_Cnt_U08         1           k_LoaMgr_MorAgidpiSig/Resp_Cnt_U08         0           k_LoaMgr_MorAgidpiSig/Resp_Cnt_U08         2           k_LoaMgr_MorAgidpiSig/Resp_Cnt_U08         2           k_LoaMgr_MorAgidpiSig/Resp_Cnt_U08         2           k_LoaMgr_MorAgidpiSig/Resp_Cnt_U08         0           k_LoaMgr_MorAgidpiSig/Resp_Cnt_U08         2           k_LoaMgr_MorAgidpiSig/Resp_Cnt_U08         2           k_LoaMgr_MorCurridpiSig/Resp_Cnt_U08         2           k_LoaMgr_MorCurridpiSig/Resp_Cnt_U08         0           k_LoaMgr_MorCurridpiSig/Resp_Cnt_U08         0           k_LoaMgr_MorCurridpiSig/Resp_Cnt_U08         0           k_LoaMgr_MorCurridpiSig/Resp_Cnt_U08         0           k_LoaMgr_MorCurridpiSig/Resp_Cnt_U08         0           k_LoaMgr_MorCurridpiSig/Resp_Cnt_U08         0           k_LoaMgr_MorCurridpiSig-Resp_Cnt_U08         0           k_LoaMgr_MorCurridpiSig-Resp_Cnt_U08         1           k_LoaMgr_MorCurridpiSig-Resp_Cnt_U08         1           k_LoaMgr_MorCurridpiSig-Resp_Cnt_U08         1           k_LoaMgr_MorCurridpiSig-Resp_Cnt_U08         1           k_LoaMgr_Pert_DiagosStivtrinactv_Cnt_Igc value         1           target_LoaMgr_Pert_DiagosStivtrinactv_Cnt_Igc value         1		
K_LoaMgr_MorAgidptSigSResp_Cnt_u08         0           K_LoaMgr_MorAgidptSigFiThd_Cnt_u08         2           K_LoaMgr_MorAgidptSigFiThd_Cnt_u08         1           K_LoaMgr_MorAgMighReate_Uis_f32         1           K_LoaMgr_MorAgMightSca_Uis_f32         1           K_LoaMgr_MorAgMightSca_Uis_f32         1           K_LoaMgr_MorAgMightSca_Uis_f32         2           K_LoaMgr_MorAgidpResp_Cnt_u08         2           K_LoaMgr_MorCurridptSigResp_Cnt_u08         0           K_LoaMgr_MorCurridptSigResp_Cnt_u08         0           K_LoaMgr_MorCurridptSigResp_Cnt_u08         0           K_LoaMgr_MorCurridptSigResp_Cnt_u08         0           K_LoaMgr_MorCurridptSigResp_Cnt_u08         0           K_LoaMgr_MorCurridptSigResp_Cnt_u08         1           K_LoaMgr_MorCurridptSigResp_Cnt_u08         1           K_LoaMgr_MorCurridptSigResp_Cnt_u08         1           K_LoaMgr_MorCurridptSigr_Cnt_u08         1           K_LoaMgr_MorCurridptSigr_Cnt_u08         1           K_LoaMgr_RedodSisca_Uis_f32         1           K_LoaMgr_RedodSisca_Uis_f32         1           Larget_LoaMgr_RedodSisca_Uis_f32         1           Larget_LoaMgr_Pert_DiagcSistvrTianctv_Cnt_lgc value         1           target_LoaMgr_Pert_LoaSca_Cot_Lot_u1gc value	k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSigFitTnd_Cnt_u08         2           k_LoaMgr_MotAghtglmScae_UlspS_f32         1           k_LoaMgr_MotAghtglmScae_Cnt_u08         0           k_LoaMgr_MotAghtgmScae_Uls_f32         1           k_LoaMgr_MotAghtgmScae_Cnt_u08         2           k_LoaMgr_MotCurridptSigGeRes_Cnt_u08         0           k_LoaMgr_MotCurridptSigGeRes_Cnt_u08         0           k_LoaMgr_MotCurridptSigFers_Cnt_u08         0           k_LoaMgr_MotCurridptSigFers_Cnt_u08         0           k_LoaMgr_MotCurridptSigFers_Cnt_u08         0           k_LoaMgr_MotCurridptSigFers_Cnt_u08         0           k_LoaMgr_MotCurridptSigFers_Cnt_u08         0           k_LoaMgr_MotCurridptSigSeRes_Cerc_Cnt_u08         1           k_LoaMgr_MotCurridptSigSeZeroEn_Cnt_u08         1           k_LoaMgr_MotCurridptSigSeZeroEn_Cnt_u08         1           k_LoaMgr_RedcdSiRa_Uls_S12         1           k_LoaMgr_RedcdSiRa_Uls_S12         1           k_LoaMgr_Pert_DiagcStsivtrInactv_Cnt_lgc_value         1           target_LoaMgr_Pert_DiagcStsivtrInactv_Cnt_lgc_value         1           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value         1           target_LoaMgr_Pert_SteerMod_Cnt_enum_value         1           target_LoaMgr_Pert_Toalval_Cnt_lgc_value         0	k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgMtgtnRate_UispS_f32         1           k_LoaMgr_MotAgMtgtnRate_UispS_f32         0           k_LoaMgr_MotAgMtgtnRate_UispS_f32         1           k_LoaMgr_MotAgMtgtnRate_UispS_f32         1           k_LoaMgr_MotCurridptSigtResp_Cnt_u08         2           k_LoaMgr_MotCurridptSigtResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigtResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigtResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigtResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigtResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigtResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigt_UispS_f32         1           k_LoaMgr_MotCurridptSigt_Sigt_UispS_f32         1           k_LoaMgr_ReddSiRate_UispS_f32         1           k_LoaMgr_ReddSiRate_UispS_f32         1           k_LoaMgr_Pert_DiagcSistritrinact_Cnt_lgc value         1           target_LoaMgr_Pert_DiagcSistritrinact_Cnt_lgc value         1           target_LoaMgr_Pert_MotCrotificition_Cnt_lgc value         1           target_LoaMgr_Pert_MotCrotificition_Cnt_lgc value         1           target_LoaMgr_Pert_MotCrotificition_Cnt_lgc value         1           target_LoaMgr_Pert_MotResp_Cnt_uox value         0           target_LoaMgr_Pert_MotResp_Cnt_loavalue	k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgMtgtnSca_ZeroEn_Cnt_lgc         0           k_LoaMgr_MotAgMtgtnSca_Uis_132         1           k_LoaMgr_MotAgmaAvailResp_Cnt_u08         2           k_LoaMgr_MotCurridptSigGResp_Cnt_u08         2           k_LoaMgr_MotCurridptSigGResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigGResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigGResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigGResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigGFitTnd_Cnt_u08         0           k_LoaMgr_MotCurridptSigGreep_Cnt_u08         1           k_LoaMgr_MotCurridptSigGreep_Cnt_u08         1           k_LoaMgr_MotCurridptSig_Creep_Cnt_u0g         1           k_LoaMgr_RedotSiRca_Uis_S122         1           k_LoaMgr_RedotSiRca_Uis_S122         1           t_LoaMgr_Pert_DiagcSistivtTinack_Cnt_lgc_value         1           target_LoaMgr_Pert_DiagcSistivtZinack_Cnt_lgc_value         1           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value         4           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value         1           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value         0           target_LoaMgr_Pert_TolaQual_Cnt_lgc_value         1           target_LoaMgr_Pert_TolaQual_Cnt_lgc_value         1           target_LoaMgr_Pert_TolaQual_Cnt_lgc_value <td>k_LoaMgr_MotAgldptSigFltThd_Cnt_u08</td> <td></td>	k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	
k_Loaklgr_MotAgSmaAvailResp_Cnt_u08         2           k_Loaklgr_MotAgSmaAvailResp_Cnt_u08         2           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         1           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         1           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         1           k_Loaklgr_MotCurridptSig2Aresp_Cnt_u08         1           target_Loaklgr_Pert_DiagcStsivfTlaactv_Cnt_lgc value         1           target_Loaklgr_Pert_MotCurridptSig_Cnt_u08 value         1           target_Loaklgr_Pert_MotCurridptSig_Cnt_u08 value         1           target_Loaklgr_Pert_TioaDi_Cnt_lgc value         1           target_Loaklgr_Pert_TioaDi_	k_LoaMgr_MotAgMtgtnRate_UlspS_f32	
k_LoaMgr_MotOuridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1FitThd_Cnt_u08         0           k_LoaMgr_MotCurridptSig1FitThd_Cnt_u08         0           k_LoaMgr_MotCurridptSig2FitThd_Cnt_u08         1           target_LoaMgr_Per1_LoaScaDl_Cnt_lgc-value         1           target_LoaMgr_Per1_MotCurridptSig_Cnt_u08 value         1           target_LoaMgr_Per1_MotCurridptSig_Cnt_u08 value         1           target_LoaMgr_Per1_ToaOl_Cnt_lgc-value         1           target_LoaMgr_Per1_ToaOl_Cnt_lgc-value         1           target_LoaMgr_Per1_ToaOl_Cnt_lgc-value         1           target_LoaMgr_Per1_ToaOl_Cnt_lgc-valu		1
k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMtgtnSca_Uls_52         1           k_LoaMgr_RedcdStRate_UlspS_32         1           k_LoaMgr_RedcdStRate_UlspS_32         1           k_LoaMgr_Pert_DiagcStslvtrInactv_Cnt_lgc.value         1           target_LoaMgr_Pert_DiagcStslvtr2lnactv_Cnt_lgc.value         1           target_LoaMgr_Pert_LoaScaDi_Cnt_lgc.value         0           target_LoaMgr_Pert_Mr_LoaColic_Cnt_lgc.value         1           target_LoaMgr_Pert_Mr_LoaColic_Cnt_lgc.value         1           target_LoaMgr_Pert_MrPosldptSig_Cnt_u08 value         1           target_LoaMgr_Pert_TicaScaDi_Cnt_lgc.value         0           target_LoaMgr_Pert_TicaDi_Cnt_lgc.value         1           target_LoaMgr_Pert_TicaDi_Cnt_lgc.value         1           target_Re_Inst_Ap_LoaMgr_Pert_DiagcStslvtrInactv_Cnt_lgc         target_LoaMgr_Pert_DiagcStslvtrInactv_Cnt_lgc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_HivTqldptSig_Cnt_u08         target_LoaMgr_Pert_HivTqldptSig_Cnt_u08           target_R		
k_LoaMgr_MolCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MolCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MolCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MolCurrMtgtnState_UlspS_132         1           k_LoaMgr_MolCurrMtgtnSca_Uls_1532         1           k_LoaMgr_RedcdStRate_UlspS_132         1           k_LoaMgr_RedcdStSca_Uls_1532         1           k_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc_value         1           target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc_value         1           target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc_value         0           target_LoaMgr_Per1_MolCurridptSig_Cnt_u08 value         4           target_LoaMgr_Per1_MolCurridptSig_Cnt_u08 value         1           target_LoaMgr_Per1_MolCurridptSig_Cnt_u08 value         1           target_LoaMgr_Per1_SteerMod_Cnt_enun_value         1           target_LoaMgr_Per1_ToaAvail_Cnt_lgc_value         0           target_LoaMgr_Per1_ToaDi_Cnt_lgc_value         1           target_Rel_nst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc           target_Rel_nst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc           target_Rel_nst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc		
k_LoaMgr_MolCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MolCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MolCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MolCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MolCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_Per1_DiagcStsIvtr1inactv_Cnt_lgc_value         1           target_LoaMgr_Per1_DiagcStsIvtr2inactv_Cnt_lgc_value         0           target_LoaMgr_Per1_MoldptSig_Cnt_u08 value         4           target_LoaMgr_Per1_MolCurridptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_ToaAvai_Cnt_lgc_value         1           target_LoaMgr_Per1_ToaAvai_Cnt_lgc_value         1           target_Rel_inst_Ap_LoaMgr_Per1_DiagcStsIvtr1inactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtr2inactv_Cnt_lgc           target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtr2inactv_Cnt_lgc           target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc           target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_LoaMtgtnE		
k_LoaMgr_MotCurridptSigFItThd_Cnt_u08         0           k_LoaMgr_MotCurridptRate_UlspS_f32         1           k_LoaMgr_MotCurrMigtmRate_UlspS_f32         1           k_LoaMgr_MotCurrMigtmSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_Perd_DiagcStsivtr1lnactv_Cnt_lgc.value         1           target_LoaMgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc.value         0           target_LoaMgr_Per1_HvTqldptSig_Cnt_u08.value         4           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         0           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         0           target_LoaMgr_Per1_SteerMod_Cnt_enum.value         1           target_LoaMgr_Per1_ToaDi_Cnt_lgc.value         0           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsivtr1lnactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsivtr1lnactv_Cnt_lgc           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqldptSig_Cnt_u08         target_LoaMgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqldptSig_Cnt_lge         target_Lo		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_Perd_DiagcStsIvtr1Inactv_Cnt_lgc_value         1           target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc_value         1           target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc_value         0           target_LoaMgr_Per1_HvTqldptSig_Cnt_u08_value         4           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08_value         1           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08_value         1           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08_value         0           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08_value         1           target_LoaMgr_Per1_ToaAvail_Cnt_lgc_value         1           target_LoaMgr_Per1_ToaAvail_Cnt_lgc_value         0           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc         target_LoaMgr_Per1_LoaMgr_Per1_LoaScaDi_Cnt_lgc         target_LoaMgr_		
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target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSi	g_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_C	nt_enum	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_l	lgc	
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotAgRespSV_Cnt_M_u08	3	3	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>~</b>

Test Step Call Trace	est Step Call Trace					
Actual Function	Count	Expected Function	Count	Result		
CalclytrState	2	CalclvtrState	2	~		
LatchInputs	1	LatchInputs	1	~		
RqstRespConds	1	RqstRespConds	1	~		
RqstHwTqResp	1	RqstHwTqResp	1	~		
ArbResp	1	ArbResp	1	~		
ChooseFinalResp	1	ChooseFinalResp	1	~		
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~		
AssignScale	1	AssignScale	1	~		
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~		
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~		
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~		
Rte Evit An LoaMor FalloaScaleAndRate	1	Rte Exit An LoaMar FalloaScaleAndRate	1	<b>V</b>		

Test Step 3.38 (Repeat Count = 1)		×
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	1	
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

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LoaMgr\_Per1



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Name	Input Value		
k_LoaMgr_CurrlytrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32 k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	0		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_Igc k_LoaMgr_IvtrMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k LoaMgr MotAgldptSig2Resp Cnt u08	1		
k LoaMgr MotAgldptSig3Resp Cnt u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_UIs_f32	0		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lg	gc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	t lee	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrloaMtotpEn_C		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_PerI_MotCurrLoaMtgtnEn_Cnt_igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_PerI_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MttPostaptsig_Crit_ubo target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Crit_enum	target LoaMgr Per1 SteerMod Cnt enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_Steenwou_Crit_endin		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08		I .	
zeamgmaxmig.m.teep_em_m_uee	1	1	~

1 2

LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc LoaMgr\_MotAgRespSV\_Cnt\_M\_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt lgc.value	1	1	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

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Test Case 4: PRM\_R Test

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### LoaMgr\_Per1 Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles: CPU Cycles:

TS4.1 5037.00 Cycles
TS4.2 4978.00 Cycles
TS4.3 5037.00 Cycles
TS4.3 5037.00 Cycles
TS4.4 5026.00 Cycles
TS4.5 5003.00 Cycles
TS4.6 5017.00 Cycles
TS4.7 5052.00 Cycles
TS4.8 4969.00 Cycles
TS4.9 5023.00 Cycles
TS4.10 5056.00 Cycles
TS4.11 5015.00 Cycles
TS4.12 5022.00 Cycles
TS4.13 4963.00 Cycles
TS4.14 4970.00 Cycles
TS4.15 4957.00 Cycles
TS4.16 5059.00 Cycles
TS4.17 4989.00 Cycles
TS4.17 4989.00 Cycles
TS4.19 4976.00 Cycles
TS4.19 4976.00 Cycles TS4.18 4976.00 Cycles TS4.19 4993.00 Cycles TS4.20 4987.00 Cycles TS4.21 4951.00 Cycles TS4.22 5066.00 Cycles TS4.22 4997.00 Cycles TS4.24 4997.00 Cycles TS4.25 4965.00 Cycles TS4.26 5000.00 Cycles TS4.27 4990.00 Cycles TS4.28 4961.00 Cycles TS4.29 5022.00 Cycles TS4.30 4966.00 Cycles TS4.30 4966.00 Cycles TS4.31 5011.00 Cycles TS4.33 4998.00 Cycles TS4.33 4998.00 Cycles TS4.33 4999.00 Cycles TS4.33 4999.00 Cycles TS4.34 4999.00 Cycles TS4.35 4975.00 Cycles TS4.36 5469.00 Cycles TS4.37 4975.00 Cycles TS4.37 4975.00 Cycles TS4.38 4954.00 Cycles TS4.38 4935.00 Cycles TS4.40 4996.00 Cycles TS4.41 4934.00 Cycles TS4.42 5044.00 Cycles TS4.43 5006.00 Cycles TS4.44 5041.00 Cycles TS4.45 4939.00 Cycles TS4.46 5008.00 Cycles TS4.47 5012.00 Cycles TS4.48 5022.00 Cycles TS4.49 4950.00 Cycles TS4.50 5022.00 Cycles TS4.51 5015.00 Cycles TS4.52 4959.00 Cycles TS4.52 4959.00 Cycles TS4.53 4956.00 Cycles TS4.54 4933.00 Cycles TS4.55 5008.00 Cycles TS4.56 5012.00 Cycles TS4.57 5039.00 Cycles TS4.57 5039.00 Cycles TS4.58 4975.00 Cycles TS4.59 4987.00 Cycles TS4.60 4952.00 Cycles TS4.61 4916.00 Cycles TS4.62 5008.00 Cycles TS4.63 5012.00 Cycles TS4.64 5022.00 Cycles TS4.65 4958.00 Cycles TS4.65 4958.00 Cycles TS4.66 4987.00 Cycles TS4.67 4938.00 Cycles TS4.68 4916.00 Cycles TS4.70 4997.00 Cycles TS4.70 4979.00 Cycles TS4.71 5022.00 Cycles TS4.72 4952.00 Cycles TS4.73 4976.00 Cycles TS4.74 4938.00 Cycles TS4.75 4916.00 Cycles TS4.76 4997.00 Cycles TS4.77 4979.00 Cycles TS4.78 5022.00 Cycles TS4.79 4952.00 Cycles TS4.80 4976.00 Cycles TS4.81 4938.00 Cycles TS4.82 4916.00 Cycles TS4.83 4997.00 Cycles TS4.84 4979.00 Cycles TS4.83 4997.00 Cycles
TS4.84 4979.00 Cycles
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TS4.86 4921.00 Cycles
TS4.86 4921.00 Cycles
TS4.87 4916.00 Cycles
TS4.88 4997.00 Cycles
TS4.99 4979.00 Cycles
TS4.90 5022.00 Cycles
TS4.91 4976.00 Cycles
TS4.91 4976.00 Cycles
TS4.93 4916.00 Cycles
TS4.94 4997.00 Cycles
TS4.95 4979.00 Cycles
TS4.96 5022.00 Cycles
TS4.96 5022.00 Cycles
TS4.97 5039.00 Cycles
TS4.97 5039.00 Cycles
TS4.98 4975.00 Cycles
TS4.101 4916.00 Cycles
TS4.102 5008.00 Cycles
TS4.103 5022.00 Cycles
TS4.104 4952.00 Cycles
TS4.105 4976.00 Cycles
TS4.106 4938.00 Cycles
TS4.106 4938.00 Cycles
TS4.107 4989.00 Cycles
TS4.108 4976.00 Cycles
TS4.109 4993.00 Cycles

TS4.109 4993.00 Cycles TS4.110 4987.00 Cycles TS4 109 4993 00

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TS4.111 4938.00 Cycles





**Description** Vector Description:

```
TS4.1k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08==>Min
TS4.2k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08==>Max
 TS4.3k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08==>Pos/default TS4.4k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08==>Min TS4.5k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08==>Max
 TS4.6k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08==>Pos/default TS4.7k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08==>Min TS4.8k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08==>Max
TS4.8k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08==>Max
TS4.9k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08==>Pos/default
TS4.10k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08==>Min/default
TS4.11k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08==>Max
TS4.12k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08==>Pos
TS4.13k_LoaMgr_HwTqldptSig4Resp_Cnt_u08==>Min/default
TS4.14k_LoaMgr_HwTqldptSig4Resp_Cnt_u08==>Max
TS4.15k_LoaMgr_HwTqldptSig4Resp_Cnt_u08==>Pos
TS4.16k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08==>Min
TS4.17k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08==>Max
TS4.18k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08==>Pos/default
TS4.19k_LoaMgr_RedcdStSca_Uls_f32==>Min
 TS4.19k_LoaMgr_RedcdStSca_Uls_f32==>Min
TS4.20k_LoaMgr_RedcdStSca_Uls_f32==>Max/default
TS4.21k_LoaMgr_RedcdStSca_Uls_f32==>Pos
TS4.21k_LoaMgr_RedcdStRate_UlspS_f32==>Min
TS4.22k_LoaMgr_RedcdStRate_UlspS_f32==>Max
TS4.23k_LoaMgr_RedcdStRate_UlspS_f32==>Defaut
TS4.25k_LoaMgr_RedcdStRate_UlspS_f32==>Pos
TS4.25k_LoaMgr_FadeOutStRate_UlspS_f32==>Min
TS4.27k_LoaMgr_FadeOutStRate_UlspS_f32==>Min
TS4.27k_LoaMgr_FadeOutStRate_UlspS_f32==>Max
IS4.27k_LoaMgr_FadeOutstRate_UlspS_f32==>Max
TS4.28k_LoaMgr_FadeOutstRate_UlspS_f32==>Default
TS4.29k_LoaMgr_FadeOutstRate_UlspS_f32==>Pos
TS4.30k_LoaMgr_HwTqldptSigFltTnd_Cnt_u08==>Min
TS4.31k_LoaMgr_HwTqldptSigFltTnd_Cnt_u08==>Max
TS4.32k_LoaMgr_HwTqldptSigFltTnd_Cnt_u08==>Pos/default
TS4.33k_LoaMgr_MotAgldptSigFltTnd_Cnt_u08==>Min
TS4.34k_LoaMgr_MotAgldptSigFltTnd_Cnt_u08==>Max
TS4.34k_LoaMgr_MotAgldptSigFltTnd_Cnt_u08==>Max
 TS4.35k_LoaMgr_MotAgldptSigFltThd_Cnt_u08==>Pos/default TS4.36k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08==>Min/default TS4.37k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08==>Max
 TS4.38k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08==>Pos
TS4.39k_LoaMgr_lvtrldptSigFltThd_Cnt_u08==>Min/default
TS4.40k_LoaMgr_lvtrldptSigFltThd_Cnt_u08==>Max
 TS4.41k_LoaMgr_lvtrldptSigFitThd_Cnt_u08==>Pos
TS4.41k_LoaMgr_lvtrldptSigFitThd_Cnt_u08==>Pos
TS4.42k_LoaMgr_MotAgSmaAvailResp_Cnt_u08==>Min
TS4.43k_LoaMgr_MotAgSmaAvailResp_Cnt_u08==>Max
TS4.44k_LoaMgr_MotAgSmaAvailResp_Cnt_u08==>Pos/default
TS4.44K_LoaMgr_MotAgSmaAvailResp_Cnt_u08==>Pos/default
TS4.45K_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08==>Min
TS4.46K_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08==>Max/default
TS4.47K_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08==>Pos
TS4.48K_LoaMgr_MotAgldptSig2Resp_Cnt_u08==>Pos
TS4.49K_LoaMgr_MotAgldptSig2Resp_Cnt_u08==>Min
TS4.49K_LoaMgr_MotAgldptSig2Resp_Cnt_u08==>Pos/default
TS4.51K_LoaMgr_MotAgldptSig3Resp_Cnt_u08==>Pos/default
TS4.52K_LoaMgr_MotAgldptSig3Resp_Cnt_u08==>Max
TS4.53K_LoaMgr_MotAgldptSig3Resp_Cnt_u08==>Pos
TS4.54K_LoaMgr_MotAgldptSig3Resp_Cnt_u08==>Pos
TS4.54K_LoaMgr_MotAgldptSig3Resp_Cnt_u08==>Pos
TS4.54K_LoaMgr_MotAgldptSig3Resp_Cnt_u08==>Pos
TS4.54K_LoaMgr_MotAgldptSig3Resp_Cnt_u08==>Pos
TS4.54K_LoaMgr_MotAgldptSig3Resp_Cnt_u08==>Pos
TS4.54K_LoaMgr_MotAgldptSig3Resp_Cnt_u08==>Min/default
 TS4.54k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08==>Min/default TS4.55k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08==>Max TS4.56k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08==>Pos
 TS4.57k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08==>Min/default TS4.58k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08==>Max TS4.59k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08==>Pos
 TS4.60k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08==>Min
 TS4.61k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08==>Max
TS4.62k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08==>Pos/default
TS4.63k_LoaMgr_lvtrldptSig1Resp_Cnt_u08==>Min/default
 TS4.64k_LoaMgr_lvtrldptSig1Resp_Cnt_u08==>Max
TS4.65k_LoaMgr_lvtrldptSig1Resp_Cnt_u08==>Pos
TS4.66k_LoaMgr_lvtrldptSig2Resp_Cnt_u08==>Min/default
 TS4.67k_LoaMgr_lvtrldptSig2Resp_Cnt_u08==>Max
TS4.68k_LoaMgr_lvtrldptSig2Resp_Cnt_u08==>Pos
TS4.69k_LoaMgr_HwTqMtgtnSca_Uls_f32==>Min
 TS4.70k_LoaMgr_HwTqMtgtnSca_Uls_f32==>Max/default TS4.71k_LoaMgr_HwTqMtgtnSca_Uls_f32==>Pos TS4.72k_LoaMgr_HwTqMtgtnRate_UlspS_f32==>Min
 TS4.73k_LoaMgr_HwTqMtgtnRate_UlspS_f32==>Max
TS4.74k_LoaMgr_HwTqMtgtnRate_UlspS_f32==>Default
TS4.75k_LoaMgr_HwTqMtgtnRate_UlspS_f32==>Pos
 TS4.76k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc==>Min
TS4.77k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc==>Max
TS4.78k_LoaMgr_MotAgMtgtnSca_Uls_f32==>Min
  TS4.79k_LoaMgr_MotAgMtgtnSca_Uls_f32==>Max/default
TS4.79k_LoaMg_MotAgMtgtnSca_Uls_f32==>Max/default TS4.80k_LoaMgr_MotAgMtgtnSca_Uls_f32==>Pos TS4.81k_LoaMgr_MotAgMtgtnRate_UlspS_f32==>Min TS4.82k_LoaMgr_MotAgMtgtnRate_UlspS_f32==>Max TS4.83k_LoaMgr_MotAgMtgtnRate_UlspS_f32==>Default TS4.84k_LoaMgr_MotAgMtgtnRate_UlspS_f32==>Default TS4.84k_LoaMgr_MotAgMtgtnRate_UlspS_f32==>Default TS4.86k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc==>Min TS4.86k_LoaMgr_MotCurrMtgtnSca_Uls_f32==>Min TS4.87k_LoaMgr_MotCurrMtgtnSca_Uls_f32==>Max TS4.87k_LoaMgr_MotCurrMtgtnSca_Uls_f32==>Pos TS4.90k_LoaMgr_MotCurrMtgtnSca_Uls_f32==>Pos TS4.90k_LoaMgr_MotCurrMtgtnRate_UlspS_f32==>Min TS4.91k_LoaMgr_MotCurrMtgtnRate_UlspS_f32==>Max TS4.91k_LoaMgr_MotCurrMtgtnRate_UlspS_f32==>Max TS4.92k_LoaMgr_MotCurrMtgtnRate_UlspS_f32==>Default TS4.93k_LoaMgr_MotCurrMtgtnRate_UlspS_f32==>Pos
 TS4.93k_LoaMgr_MotCurrMtgtnRate_UlspS_f32==>Pos
TS4.94k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_Igc==>Min
```

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TS4.95k\_LoaMgr\_lvtrMtgtnScaZeroEn\_Cnt\_lgc==>Max
TS4.96k\_LoaMgr\_lvtrMtgtnSca\_Uls\_f32==>Min
TS4.97k\_LoaMgr\_lvtrMtgtnSca\_Uls\_f32==>Max/default
TS4.97k\_LoaMgr\_lvtrMtgtnSca\_Uls\_f32==>Pos
TS4.99k\_LoaMgr\_lvtrMtgtnRate\_UlspS\_f32==>Min
TS4.100k\_LoaMgr\_lvtrMtgtnRate\_UlspS\_f32==>Max
TS4.101k\_LoaMgr\_lvtrMtgtnRate\_UlspS\_f32==>Pos
TS4.101k\_LoaMgr\_lvtrMtgtnRate\_UlspS\_f32==>Pos
TS4.103k\_LoaMgr\_lvtrMtgtnRate\_UlspS\_f32==>Pos
TS4.103k\_LoaMgr\_CurrlvtrMtgtnScaZeroEn\_Cnt\_lgc==>Min
TS4.104k\_LoaMgr\_CurrlvtrMtgtnScaZeroEn\_Cnt\_lgc==>Max
TS4.105k\_LoaMgr\_CurrlvtrMtgtnSca\_Uls\_f32==>Max/Default
TS4.105k\_LoaMgr\_CurrlvtrMtgtnSca\_Uls\_f32==>Max/Default
TS4.107k\_LoaMgr\_CurrlvtrMtgtnSca\_Uls\_f32==>Pos
TS4.108k\_LoaMgr\_CurrlvtrMtgtnRate\_UlspS\_f32==>Max
TS4.109k\_LoaMgr\_CurrlvtrMtgtnRate\_UlspS\_f32==>Max
TS4.109k\_LoaMgr\_CurrlvtrMtgtnRate\_UlspS\_f32==>Max
TS4.110k\_LoaMgr\_CurrlvtrMtgtnRate\_UlspS\_f32==>Default
TS4.111k\_LoaMgr\_CurrlvtrMtgtnRate\_UlspS\_f32==>Default

Name	Test Step 4.1 (Repeat Count = 1)	
Loading Java Tourishight See Cell J. U. 68 Loading Java Tourishight E. Ott. M. 100 Loa	Name	Input Value
Loading	LoaMgr_AgSV_Cnt_M_lgc	0
Loading   Introface   Loading   Lo		2
Loaking_ Heritangkings Cort_M_Digs	LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
Loading   Introduction   Loading	LoaMgr_HwTqRespSV_Cnt_M_u08	2
Loading   InfoRespix C nt M   UB	LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
Leading: MANAGRIPSESSY_CMT, M_UB	LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
Loaking   MoAglack  Cort M. Us9	LoaMgr_lvtrRespSV_Cnt_M_u08	2
Landary MonCarriss(SSV Crift M, USB   2	LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
Loahlgr   McCourtegsSgSV Cnt   Muse   2	LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
Leahlig: MOCUTA call tignific; Cnt. M. Jos   Leahlig: MOCUTA call tignific; Cnt. M. Jos   Leahlig: Scarish MoRayAvail Cnt. M. Jos   Leahlig: Scarish Classific; Scarish Cnt. M. Jos   Leahlig: Scarish Classific; Scarish Cnt. Jos   Leahlig: Java Jose Jose   Leahlig: Java Jose   Leahlig: J	LoaMgr_MotAgRespSV_Cnt_M_u08	4
Leahlg	LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
Leading	LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
Leahlgr_Vilght/SircSV_Cnt_M_U08	LoaMgr_MotCurrRespSV_Cnt_M_u08	3
Re_Inst_Ap_LoaMgr   LoaMgr   LoaMgr   LoaMgr   LoaMgr   LoaMgr   LoaMgr   CurrivrAftignRate_UlspS_132   1	LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
k_LoaMg_CurrivfMigInSca_ZerEn_Cnt_Job         0           k_LoaMg_CurrivfMigInSca_Us_S12         1           k_LoaMg_CurrivfMigInSca_Us_S12S         1           k_LoaMg_FaceOutSRate_Uss_S12S         1           k_LoaMg_FACEOUTSRate_Uss_S12S         2           k_LoaMg_FACEOUTSRate_Uss_S12S         2           k_LoaMg_FACEOUTSRATE_Uss_S12S         4           k_LoaMg_FACEOUTSRATE_Uss_S12S         0           k_LoaMg_FACEOUTSRATE_Uss_S12S         0           k_LoaMg_FACEOUTSRATE_Uss_S12S         0           k_LoaMg_FACEOUTSRATE_Uss_S12TOR_CN_USS         0           k_LoaMg_FACEOUTSRATE_Uss_S12TOR_CN_USS         0           k_LoaMg_FACEOUTSRATE_Uss_S12S         1           k_LoaMg_FACEOUTSRATE_CN_USS_S12S         1           k_LoaMg_FACEOUTSRATE_USS_S12S         1           k_LoaMg_FACEOUTSRATE_USS_S12S         1           k_LoaMg_FACEOUT	LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
k_Loaking_CurrinkritightSea_Uis_18_28         1           k_Loaking_HardeoutsiRate_Uisp6_192         1           k_Loaking_HardeoutsiRate_Uisp6_192         1           k_Loaking_HardeoutsiRate_Uisp6_192         1           k_Loaking_HardeoutsiRate_Uisp6_192         2           k_Loaking_HardeoutsiRate_Uisp6_192         4           k_Loaking_HardeoutsiResp_Cnt_u08         4           k_Loaking_HardeoutsiResp_Cnt_u08         1           k_Loaking_HardeoutsiResp_Cnt_u08         0           k_Loaking_HardeoutsiResp_Cnt_u08         0           k_Loaking_HardeoutsiResp_Cnt_u08         1           k_Loaking_HardeoutsiResp_Cnt_u08         1           k_Loaking_HardeoutsiResp_Cnt_u08         0           k_Loaking_HardeoutsiResp_Cnt_u08         0           k_Loaking_HardeoutsiResp_Cnt_u08         0           k_Loaking_HardeoutsiResp_Cnt_u08         0           k_Loaking_HardeoutsiResp_Cnt_u08         0           k_Loaking_HardeoutsiResp_Cnt_u08         0           k_Loaking_HardeoutsiResp_Cnt_u08         1           k_Loaking_MardeoutsiResp_Cnt_u08         1           k_Loaking_MardeoutsiResp_Cnt_u08         1           k_Loaking_MardeoutsiResp_Cnt_u08         2           k_Loaking_MardeoutsiResp_Cnt_u08         2      <	Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
	k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
K_LoaMgr_Hv7iqbfSig0TloaAvailResp_Cnt_U08	k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_HwTqlsplSg\0TioaAvaiResp_Cnt_u08         2           k_LoaMgr_HwTqlsplSg\0TioaTioaResp_Cnt_u08         4           k_LoaMgr_HwTqldplSg\0TioaDResp_Cnt_u08         0           k_LoaMgr_HwTqldplSg\0TioaDResp_Cnt_u08         1           k_LoaMgr_HwTqldplSg\0TioaDResp_Cnt_u08         0           k_LoaMgr_HwTqldplSg\0TioaDResp_Cnt_u08         0           k_LoaMgr_HwTqldp\0Tisq\0Tida\0T	k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
K_LoaMgr_HvTqlpfSig1nOTioaReap_Cnt_u08	k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSigTtDoaNailResp_Cnt_u08         1           k_LoaMgr_HwTqldptSigTabaBResp_Cnt_u08         0           k_LoaMgr_HwTqldptSigTabaBResp_Cnt_u08         0           k_LoaMgr_HwTqldptSigFResp_Cnt_u08         0           k_LoaMgr_HwTqldptSigFResp_Cnt_u08         1           k_LoaMgr_HwTqMtghrsate_UlspS_192         1           k_LoaMgr_HwTqMtghrsate_UlspS_192         1           k_LoaMgr_HwTqMtghrSig-Resp_Cnt_u08         0           k_LoaMgr_IwtridptSigResp_Cnt_u08         0           k_LoaMgr_IwtridptSigResp_Cnt_u08         0           k_LoaMgr_IwtridptSigResp_Cnt_u08         0           k_LoaMgr_IwtridptSigResp_Cnt_u08         0           k_LoaMgr_IwtridptSigResp_Cnt_u08         1           k_LoaMgr_IwtridptSigResp_Cnt_u08         1           k_LoaMgr_IwtridptSigResp_Cnt_u08         5           k_LoaMgr_MotAgldptSigResp_Cnt_u08         1           k_LoaMgr_MotAgldptSigResp_Cnt_u08         1           k_LoaMgr_MotAgldptSigResp_Cnt_u08         2           k_LoaMgr_MotAgldptSigResp_Cnt_u08         2           k_LoaMgr_MotAgldptSigResp_Cnt_u08         2           k_LoaMgr_MotAgldptSigResp_Cnt_u08         2           k_LoaMgr_MotCurridptSigNesp_Cnt_u08         2           k_LoaMgr_MotCurridptSigNesp_Cnt_u08         0<	k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_Loadigr_HwTqlqtSig2TtoaDiResp_Cnt_u08         1           k_Loadigr_HwTqlqtSig2TtoaEnaResp_Cnt_u08         0           k_Loadigr_HwTqlqtSig2Resp_Cnt_u08         0           k_Loadigr_HwTqlqtSig2Resp_Cnt_u08         1           k_Loadigr_HwTqlqtSig1Resp_UspS_132         1           k_Loadigr_HwTqhtgtSig1Resp_Cnt_u08         0           k_Loadigr_IvridptSig1Resp_Cnt_u08         5           k_Loadigr_MotAgldptSig1Resp_Cnt_u08         1           k_Loadigr_MotAgldptSig1Resp_Cnt_u08         0           k_Loadigr_MotAgldptSig1Resp_Cnt_u08         2           k_Loadigr_MotAgldptSig1Resp_Cnt_u08         2           k_Loadigr_MotAgldptSig1Resp_Cnt_u08         2           k_Loadigr_MotAgldptSig2Resp_Cnt_u08         2           k_Loadigr_MotAgldptSig2Resp_Cnt_u08         2           k_Loadigr_MotAgldptSigResp_Cnt_u08         2           k_Loadigr_MotCurridptSig1Resp_Cnt_u08         0           k_Loadigr_MotCurridptSig1Resp_C	k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HvTqlqhSig2TloaEnaResp_Cnt_u08         0           k_LoaMgr_HvTqlqhSig2FlTtQ-ft_u08         1           k_LoaMgr_HvTqlqhSig2FlTtQ-ft_u08         1           k_LoaMgr_HvTqMlqhtRate_UlspS_ft22         1           k_LoaMgr_HvTqMlqhtRate_UlspS_ft22         1           k_LoaMgr_IvtrlqhSigTResp_Cnt_u08         0           k_LoaMgr_IvtrlqhSigTResp_Cnt_u08         0           k_LoaMgr_IvtrlqhSigFltThd_Cnt_u08         0           k_LoaMgr_IvtrlqhSigFltThd_Cnt_u08         0           k_LoaMgr_IvtrlqhSigAca_Coen_Cnt_lge         0           k_LoaMgr_IvtrlqhSisa_Clus_ft22         1           k_LoaMgr_IvtrlqhSisa_Clus_ft22         1           k_LoaMgr_IvtrlqhSisa_Clus_ft22         1           k_LoaMgr_MotAglqtpSisqTrases_Cnt_u08         5           k_LoaMgr_MotAglqtpSisqTsses_Cnt_u08         1           k_LoaMgr_MotAglqtpSisqTsses_Cnt_u08         2           k_LoaMgr_MotAglqtpSisqTsa_Uls_ft22         1           k_LoaMgr_MotAglqtpSisqTsa_Uls_ft22         1           k_LoaMgr_MotAglqtpSisqTsa_Uls_ft2         1           k_LoaMgr_MotCurrlqtpSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurrlqtpSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurrlqtpSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrlqtpSig1Resp_Cnt_u08 <t< td=""><td>k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08</td><td>0</td></t<>	k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	0
k_LoaMgr_HwTqlqbtSig4Resp_Cnt_u08         1           k_LoaMgr_HwTqlqbtSig4Resp_Cnt_u08         1           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         1           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqbtSig1Resp_Cnt_u08         0           k_LoaMgr_IwTrdptSig1Resp_Cnt_u08         0           k_LoaMgr_IwTrdptSig1Resp_Cnt_u08         0           k_LoaMgr_IwTrdptSig1Resp_Cnt_u08         0           k_LoaMgr_IwTrdptSig1Resp_Cnt_u08         1           k_LoaMgr_IwTrdptSig2Resp_Cnt_u08         5           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         2           k_LoaMgr_MotAgldptSig1Resp_Cnt_u08         2           k_LoaMgr_MotAgldptSig1Resp_Cnt_u08         2           k_LoaMgr_MotAgldptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08	k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqlqtpSigFlTrh_Cnt_u08         1           k_LoaMgr_HwTqMtglRstae_UlspS_132         1           k_LoaMgr_HwTqMtglRstae_UlspS_132         1           k_LoaMgr_IvtrlqtpSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrlqtpSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrlqtpSigFlTrhd_Cnt_u08         0           k_LoaMgr_IvtrldtpSigFlTrhd_Cnt_u08         0           k_LoaMgr_IvtrldtgnSqLB_UlspS_132         1           k_LoaMgr_IvtrldtgnSqLB_UlspS_132         1           k_LoaMgr_MotAgldptSig1Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig1Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig1Resp_Cnt_u08         2           k_LoaMgr_MotAgldptSig1Resp_Cnt_u08         2           k_LoaMgr_MotAgldtptSig2Resp_Cnt_u08         2           k_LoaMgr_MotAgltgtrlSca_UlspS_32         1           k_LoaMgr_MotAgltgtrlSca_UlspS_32         1           k_LoaMgr_MotAgltgtrlSca_UlspS_32         1           k_LoaMgr_MotAgltgtrlSca_UlspS_32         1           k_LoaMgr_MotCurrldptSigTesp_Cnt_u08         2           k_LoaMgr_MotCurrldptSigTesp_Cnt_u08         2           k_LoaMgr_MotCurrldptSigTesp_Cnt_u08         0           k_LoaMgr_MotCurrldptSigTesp_Cnt_u08         0 <td>k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08</td> <td></td>	k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	
k_LoaMgr_HwTqMtgtnRate_UlspS_f32         1           k_LoaMgr_HwTqMtgtnSca_Uls_f32         1           k_LoaMgr_WtrIpMtgtsig1Resp_Cnt_u08         0           k_LoaMgr_WtrIpdtSig1Resp_Cnt_u08         0           k_LoaMgr_WtrIpdtSig1Resp_Cnt_u08         0           k_LoaMgr_WtrMtgtnSca_Uls_f32         1           k_LoaMgr_WtrMtgtnSca_Uls_f32         1           k_LoaMgr_WtrMtgtnSca_Uls_f32         1           k_LoaMgr_WtrMtgtnSca_Uls_f32         1           k_LoaMgr_Mthdjqdslysig1NoSmaResp_Cnt_u08         5           k_LoaMgr_Mthdjqdslysig1Pdt_Cnt_u08         1           k_LoaMgr_Mthdjqdslysig1Pdt_Cnt_u08         2           k_LoaMgr_Mthdjqdslysig1Pdt_Cnt_u08         2           k_LoaMgr_Mthdjqdslysig1Pdt_Cnt_u08         2           k_LoaMgr_Mthdjqdslysig1Resp_Cnt_u08         2           k_LoaMgr_MthdQfqdslysig1Resp_Cnt_u08         2           k_LoaMgr_MthCurridptSig1Resp_Cnt_u08         2           k_LoaMgr_MthCurridptSig2Resp_Cnt_u08         2           k_LoaMgr_MthCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MthCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MthCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MthCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MthCurridptSig2Resp_Cnt_u08         0 </td <td>k_LoaMgr_HwTqIdptSig4Resp_Cnt_u08</td> <td></td>	k_LoaMgr_HwTqIdptSig4Resp_Cnt_u08	
k_Loaklgr_HwTqMtgmSca_Uls_f32         1           k_Loaklgr_HwTqMtghSigResp_Cnt_u08         0           k_Loaklgr_HwTqMtghSigResp_Cnt_u08         0           k_Loaklgr_HwTdghSigResp_Cnt_u08         0           k_Loaklgr_HwTdghSigResp_Cnt_u08         1           k_Loaklgr_HwTdghSca_Uls_f32         1           k_Loaklgr_MwTdgldtsligitNoSmaResp_Cnt_u08         5           k_Loaklgr_MotAgldtsligitSmSesp_Cnt_u08         1           k_Loaklgr_MotAgldtsligisFirtThd_Cnt_u08         2           k_Loaklgr_MotAgldtsligisFirtThd_Cnt_u08         2           k_Loaklgr_MotAgldtsligisFirtThd_Cnt_u08         2           k_Loaklgr_MotAgldtsligisFirtThd_Cnt_u08         2           k_Loaklgr_MotAgldtsligSesp_Cnt_u08         2           k_Loaklgr_MotAgldtsligSesp_Cnt_u08         2           k_Loaklgr_MotCurridptSigResp_Cnt_u08         2           k_Loaklgr_MotCurridptSigResp_Cnt_u08         2           k_Loaklgr_MotCurridptSigResp_Cnt_u08         0           k_Loaklgr_MotCurridptSigResp_Cnt_u08         0           k_Loaklgr_MotCurridptSigResp_Cnt_u08         0           k_Loaklgr_MotCurridptSigResp_Cnt_u08         0           k_Loaklgr_MotCurridptSigResp_Cnt_u08         0           k_Loaklgr_MotCurridptSigResp_Cnt_u08         0           k_Loaklgr_MotCur	k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
k_Loaklgr_IvridptSig1Resp_Cnt_u08         0           k_Loaklgr_IvridptSig2Resp_Cnt_u08         0           k_Loaklgr_IvridptSigFITrhd_Cnt_u08         0           k_Loaklgr_IvriMignRate_UlspS_732         1           k_Loaklgr_IvriMignScazeroEn_Cnt_lgc         0           k_Loaklgr_IvriMignScazeroEn_Cnt_lgc         0           k_Loaklgr_IvriMignScazeroEn_Cnt_u08         1           k_Loaklgr_MotAgidptSig1RoSmaResp_Cnt_u08         5           k_Loaklgr_MotAgidptSig1Resp_Cnt_u08         0           k_Loaklgr_MotAgidptSig1Resp_Cnt_u08         2           k_Loaklgr_MotAgidptSig1Resp_Cnt_u08         2           k_Loaklgr_MotAgidptSig1Resp_Cnt_u08         2           k_Loaklgr_MotAgMignRate_UlspS_32         1           k_Loaklgr_MotAgMignSca_Uls_732         1           k_Loaklgr_MotAgMignSca_Uls_732         1           k_Loaklgr_MotAgMignSca_Uls_732         2           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         2           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptResp_Cls_	k_LoaMgr_HwTqMtgtnRate_UlspS_f32	
k_LoaMgr_IvrirlighSig/2Resp_Cn_Lu08         0           k_LoaMgr_IvrirlighSig/ItTind_Cnt_u08         0           k_LoaMgr_IvrirlighSig-ItTind_Cnt_u08         1           k_LoaMgr_IvrirlighSig-CareCn_Cnt_lgc         0           k_LoaMgr_MortAgidptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MortAgidptSig3Resp_Cnt_u08         1           k_LoaMgr_MortAgidptSig3Resp_Cnt_u08         0           k_LoaMgr_MortAgidptSig3Resp_Cnt_u08         2           k_LoaMgr_MortAgidptSig3Resp_Cnt_u08         2           k_LoaMgr_MortAgMtgnRsca_Ulsp_S12         1           k_LoaMgr_MortAgMtgnRsca_Ulsp_S2         1           k_LoaMgr_MortAgMtgnRsca_Uls_S2         1           k_LoaMgr_MortAgMtgnRsca_Uls_S2         1           k_LoaMgr_MortAgMtgnRsca_Uls_S2         1           k_LoaMgr_MortCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MortCurridptSig1Resp_Cnt_u08         2           k_LoaMgr_MortCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MortCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MortCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MortCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MortCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MortCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MortCurrid		
k_Loaklgr_lvrtrldptSigFitTnd_Cnt_u08         0           k_Loaklgr_lvrtrldptScazeroEn_Cnt_lgc         0           k_Loaklgr_lvrtrldptSca_Uls_52         1           k_Loaklgr_lvrtrldptSca_Uls_132         1           k_Loaklgr_lvrtrldptSca_Uls_132         1           k_Loaklgr_MotAgldptSigTResp_Cnt_u08         5           k_Loaklgr_MotAgldptSigFitTnd_Cnt_u08         0           k_Loaklgr_MotAgldptSigFitTnd_Cnt_u08         2           k_Loaklgr_MotAgldptSigFitTnd_Cnt_u08         2           k_Loaklgr_MotAglktgtnScaZeroEn_Cnt_lgc         0           k_Loaklgr_MotAglktgtnScaZeroEn_Cnt_Ugc         0           k_Loaklgr_MotAglktgtnSca_Uls_132         1           k_Loaklgr_MotAglktgtnSca_Uls_132         1           k_Loaklgr_MotAglktgtnSca_Uls_132         2           k_Loaklgr_MotCurrldptSigResp_Cnt_u08         2           k_Loaklgr_MotCurrldptSigResp_Cnt_u08         0           k_Loaklgr_MotCurrldptSigFitTnd_Cnt_u08         0           k_Loaklgr_MotCurrldptSigFitTnd_Cnt_u08         0           k_Loaklgr_MotCurrldptSigFitTnd_Cnt_u08         0           k_Loaklgr_MotCurrldptSigFitTnd_Cnt_u08         1           k_Loaklgr_MotCurrldptSigFitTnd_Cnt_u08         1           k_Loaklgr_MotCurrldptSig-Loaklgr_u08         1           k_Loaklgr_Pert_Di		
k_LoaMgr_lvtrMigtnRate_UlspS_f32         1           k_LoaMgr_lvtrMigtnScazeroEn_Cnt_lgc         0           k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         2           k_LoaMgr_MotAgldptSigFltThd_Cnt_u08         2           k_LoaMgr_MotAgldptSigFltThd_Cnt_u08         1           k_LoaMgr_MotAgMitgInSca_Uls_f32         1           k_LoaMgr_MotAgMitgInSca_Uls_f32         1           k_LoaMgr_MotAgMitgInSca_Uls_f32         1           k_LoaMgr_MotCuridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCuridptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCuridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCuridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCuridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCuridptSigFltThd_Cnt_u08         0           k_LoaMgr_MotCuridptSigFltThd_Cnt_u08         1           k_LoaMgr_MotCuridptSigSigItTinact_Cnt_lgc         1           k_LoaMgr_RedcdSitSa_Uls_f32         1           k_LoaMgr_Per1_DiagcSisIvt7lnacty_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcS		
k_LoaMgr_VrtrMigtnSca_ZeroEn_Cnt_lgc         0           k_LoaMgr_MotAglidptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAglidptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAglidptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAglidptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAglidptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAglidptSig-IiThd_Cnt_u08         2           k_LoaMgr_MotAglidptSig-IiThd_Cnt_u08         2           k_LoaMgr_MotAglidptSca_ZeroEn_Cnt_lgc         0           k_LoaMgr_MotAgMigtnSca_Uls_f32         1           k_LoaMgr_MotAgMigtnSca_Uls_f32         1           k_LoaMgr_MotAgMigtnSca_Uls_f32         2           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig1Rest_UlspS_f32         1           k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig2Resp_Cnt_Ulsg_ca_Ulsg_f32         1           k_LoaMgr_MotCurrldptSig_Sa_Sa_Uls_f32         1           k_LoaMgr_RedcdSixSa_Uls_f32         1           k_LoaMgr_RedcdSixSa_Uls_f32         1           target_LoaMgr_Per1		
k_LoaMgr_MotAglidptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAglidptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAglidptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAglidptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAglidptSigFliThd_Cnt_u08         2           k_LoaMgr_MotAgMitghRate_UispS_f32         1           k_LoaMgr_MotAgMitghtSca_ZeroEn_Cnt_lgc         0           k_LoaMgr_MotAgMitghtSca_Uis_f32         1           k_LoaMgr_MotAgMitghtSca_Uis_f32         1           k_LoaMgr_MotQmftpSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Sig1Thd_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_RedcdStSca_Uis_f32         1           k_LoaMgr_RedcdStSca_Uis_f32         1           k_LoaMgr_RedcdStSca_Uis_f32         1           k_LoaMgr_Per1_DiagcStslvtrInactv_Cnt_igc.value         0           target_LoaMgr_Per1_DiagcSt		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08         5           k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSigFltThd_Cnt_u08         2           k_LoaMgr_MotAgldptSigFltThd_Cnt_u08         2           k_LoaMgr_MotAgMtghrRate_UlspS_f32         1           k_LoaMgr_MotAgMtghrSca_Uls_f32         1           k_LoaMgr_MotAgMtghrSca_Uls_f32         1           k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08         0           k_LoaMgr_MotCurrldptSig-S_f32         1           k_LoaMgr_MotCurrldptSig-S_f32         1           k_LoaMgr_MotCurrldptSig-S_f32         1           k_LoaMgr_RedcdSfRate_UlspS_f32         1           k_LoaMgr_RedcdSfRate_UlspS_f32         1           k_LoaMgr_Perd_DiagcStStvtrInlactv_Cnt_lgc.value         0           target_LoaMgr_Perl_DiagcStStvtrInlactv_Cnt_lgc.value         0           target_LoaMgr_Perl_MotCurrldptSig_Cnt_u08.value         1           target_Lo		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig1RTnd_Cnt_u08         2           k_LoaMgr_MotAgMtgnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgnRate_UlspS_f32         0           k_LoaMgr_MotAgMtgnSca_Uls_f32         1           k_LoaMgr_MotAgMtgnSca_Uls_f32         1           k_LoaMgr_MotOcurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurrMtghRate_UlspS_f32         1           k_LoaMgr_MotCurrMtghRate_UlspS_f32         1           k_LoaMgr_MotCurrMtghRate_UlspS_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStSca_Uls_f32         1           k_LoaMgr_Per1_DiagcStstvtTlnactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStstvtTlnactv_Cnt_lgc.value         0           target_LoaMgr_Per1_LoaScaDi_Cnt_uo8.value         1           target_LoaMgr_Per1_MotCurrldptSig_Cnt_uo8.value         0		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSigFitThd_Cnt_u08         2           k_LoaMgr_MotAgMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgSmaAvailResp_Cnt_u08         2           k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_Perd_DiagcStslvtr1Inactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value         0           target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value         1           target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value         0           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         0 <td></td> <td></td>		
k_LoaMgr_MotAgldptSigFitThd_Cnt_u08         2           k_LoaMgr_MotAgMtgtnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_MotAgMtgtnSca_Uls_f32         1           k_LoaMgr_MotAgSmaAvailResp_Cnt_u08         2           k_LoaMgr_MotCurrldptSigOResp_Cnt_u08         2           k_LoaMgr_MotCurrldptSigOResp_Cnt_u08         0           k_LoaMgr_MotCurrldptSigPitTnd_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFitTnd_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFitTnd_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFitTnd_Cnt_u08         0           k_LoaMgr_MotCurrldtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrldtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrldtgtnSca_Uls_f32         1           k_LoaMgr_MotCurrldtgtnSca_Uls_f32         1           k_LoaMgr_McCurrldtgtnSca_Uls_f32         1           k_LoaMgr_MotCurrldtgtnSca_Uls_f32         1           k_LoaMgr_Perd_DiagcStslvtrlnactv_Cnt_lgc.value         0           k_LoaMgr_Perd_DiagcStslvtrlnactv_Cnt_lgc.value         0           target_LoaMgr_Perl_HwTqldptSig_Cnt_u08.value         1           target_LoaMgr_Perl_LoaScaDi_Cnt_lgc.value         0           target_LoaMgr_Perl_MotCurrldptSig_Cnt_u08.value         0		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32       1         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgMtgtnSca_Uls_f32       1         k_LoaMgr_MotAgMtgtnSca_Uls_f32       2         k_LoaMgr_MotCurrldptSigResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFitThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_Perd_DiagcStstvtrlnactv_Cnt_lgc.value       0         target_LoaMgr_Per1_DiagcStstvtrlnactv_Cnt_lgc.value       0         target_LoaMgr_Per1_DiagcStstvtrloactv_Cnt_lgc.value       0         target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value       1         target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value       0         target_LoaMgr_Per1_LoaScaDi_Cnt_u08.value       0		
k_LoaMgr_MotAgMigtnScaZeroEn_Cnt_lgc       0         k_LoaMgr_MotAgMigtnSca_Uls_f32       1         k_LoaMgr_MotAgSmaAvaiiResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltrhd_Cnt_u08       0         k_LoaMgr_MotCurrlMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value       0         target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value       0         target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value       1         target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value       0         target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value       0		
k_LoaMgr_MotAgMtgtnSca_UIs_132       1         k_LoaMgr_MotAgSmaAvaiiResp_Cnt_u08       2         k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08       2         k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrIdptSigFitTnd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UIspS_f32       1         k_LoaMgr_MotCurrMtgtnSca_UIs_532       1         k_LoaMgr_RedcdStRate_UIspS_f32       1         k_LoaMgr_RedcdStRate_UIspS_f32       1         k_LoaMgr_RedcdStSca_UIs_f32       1         k_LoaMgr_RedcdStSca_UIs_f32       1         k_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value       0         target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value       0         target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value       1         target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value       0         target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value       0		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08       2         k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1         target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value       0         target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value       0         target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value       1         target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value       0         target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value       0		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08       2         k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrIdptSigFliThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1         target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value       0         target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value       0         target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value       1         target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value       0         target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value       0		
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08       0         k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1         target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value       0         target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value       0         target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value       1         target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value       0         target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value       0         target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value       0		
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08       0         k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1         target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value       0         target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value       0         target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value       1         target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value       0         target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value       0         target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value       0		
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08       0         k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1         target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value       0         target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value       0         target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value       1         target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value       0         target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value       0         target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value       0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1         target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value       0         target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value       0         target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value       1         target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value       0         target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value       0         target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value       0		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc       1         k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1         target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value       0         target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value       0         target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value       1         target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value       0         target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value       0		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1         target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value       0         target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value       0         target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value       1         target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value       0         target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value       0		
k_LoaMgr_RedcdStRate_UlspS_i32       1         k_LoaMgr_RedcdStSca_Uls_i32       1         target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value       0         target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value       0         target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value       1         target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value       0         target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value       0		
k_LoaMgr_RedcdStSca_Uls_f32     1       target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value     0       target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value     0       target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value     1       target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value     0       target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value     0		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value 0 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value 0 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 0 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value 0 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 0 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 0 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 0 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 0		
target_Loamgr_Per1_MtrPosidptSig_Cnt_u08.value 3	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3





Name	Input Value		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsl	vtr1Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsl	vtr2Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldpt	Sig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoa	MtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMt	gtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_S	tate_enum	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaD	i_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoa	aMtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrlo	dptSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrL	oaMtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldp	otSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod	I_Cnt_enum	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_C	nt_lgc	
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr HwTqldptSigSV Cnt M u08	1	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr lvtrLoaMtgtnEn Cnt M lgc	1	1	•
LoaMgr lvtrRespSV Cnt M u08	2	2	
LoaMgr_LOASt_State_M_enum	5	5	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	
LoaMgr MaxMtgtnResp Cnt M u08	5	5	
LoaMgr MotAgldptSigSV Cnt M u08	3	3	
LoaMgr MotAgLoaMtgtnEn Cnt M Igc	1	1	
LoaMgr MotAgRespSV Cnt M u08	4	4	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr MotCurrRespSV Cnt M u08	3	3	
LoaMgr TloaAvail Cnt M lgc	1	1	
LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr VltgMdSrcSV Cnt M u08	0	0	
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 172, 174}	{170, 172, 174}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	3
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	
target LoaMgr Per1 HwTqLoaMtgtnEn Cnt Igc.value	1	1	
target LoaMgr Per1 lvtrLoaMtgtnEn Cnt lgc.value	1	1	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	<b>~</b>

Test Step 4.2 (Repeat Count = 1)	<b>□</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4





Name			
	Input Value		
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1		
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0		
LoaMgr_lvtrRespSV_Cnt_M_u08	5		
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2		
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0		
LoaMgr_MotAgRespSV_Cnt_M_u08	1		
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1		
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0		
LoaMgr_MotCurrRespSV_Cnt_M_u08	5		
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1		
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1		
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr		
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	5		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_UIs_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_IvtrIdptSigFltThd_Cnt_u08			
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_IvtrMtgtnSca_UIs_f32			
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k LoaMgr MotCurrldptSig0Resp Cnt u08	2		
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSigFltThd Cnt u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k LoaMgr MotCurrMtgtnSca Uls f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k LoaMgr RedcdStSca Uls f32	1		
target LoaMgr Per1 DiagcStslvtr1Inactv Cnt Igc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lq		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HoaDi_Cnt_Igc  Name	Actual Value	Expected Value	Result





Name	Actual Value	Expected Value	Result
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>~</b>
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_LOASt_State_M_enum	5	5	<b>~</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	<b>~</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	<b>~</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	5	5	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>~</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 175}	{170, 172, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte Exit An LoaMor FalloaScaleAndRate	1	Rte Exit An LoaMor FalloaScaleAndRate	1	<b>V</b>

Test Step 4.3 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2





Name	Input Value		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k LoaMgr lvtrMtgtnRate UlspS f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
	0		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurridgtSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08			
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc			
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqIdptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt	_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_	_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_	u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enun	n	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	0	0	~

target_LoaMgr_Per1_HoaDI_Cnt_igc		
Actual Value	Expected Value	Result
0	0	~
1	1	~
1	1	~
0	0	~
1	1	~
1	1	~
3	3	~
5	5	~
500	500	~
0	0	~
5	5	~
1	1	~
1	1	~
5	5	~
0	0	~
1	1	~
4	4	~
1	1	~
1	1	~
0	0	~
	Actual Value  0 1 1 1 0 1 1 3 5 500 0 5 1 1 1 1 5 0 1 1 1 1 5	Actual Value         Expected Value           0         0           1         1           0         0           1         1           1         1           1         1           3         3           5         5           500         0           0         0           5         5           1         1           1         1           5         5           0         0           1         1           4         4           1         1           1         1           1         1

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Name	Actual Value	Expected Value	Result
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	✓
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	•
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.4 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1





Name	Input Value		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inac	tv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inac	tv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt	_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_C	cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enu	ım	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lge	С	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEr	n_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_C	:nt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnl	En_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cr	nt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_er	num	
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc	:	
target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value	Expected Value	Result
		Expected Value	Result
Name	Actual Value	· ·	Result
Name LoaMgr_AgSV_Cnt_M_lgc	Actual Value	1	•
Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08	Actual Value  1 0	1 0	~
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	Actual Value  1 0 0	1 0 0	<i>y</i>
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08	Actual Value  1 0 0 4	1 0 0 0 4	· · · · · · · · · · · · · · · · · · ·
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08	Actual Value  1 0 0 4 0	1 0 0 4 0	*
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	Actual Value  1 0 0 4 0 0	1 0 0 4 0	· · · · · · · · · · · · · · · · · · ·
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08	Actual Value  1 0 0 4 0 5	1 0 0 4 0 0 5	***
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08	Actual Value  1 0 0 4 0 5 5	1 0 0 4 0 0 5 5	· · · · · · · · · · · · · · · · · · ·
Name  LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32	Actual Value  1 0 0 4 0 5 5 5	1 0 0 4 0 0 5 5 5	> > > > > > > > > > > > > > > > > > >
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LoaSt_State_M_enum  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32	Actual Value  1 0 0 0 4 0 5 5 5 500	1 0 0 4 0 0 0 5 5 5 500	***************************************
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08	Actual Value  1 0 0 0 4 0 5 5 5 500 1 5	1 0 0 4 0 0 0 5 5 5 500	> > > > > > > > > > > > > > > > > > >
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	Actual Value  1 0 0 0 4 0 0 5 5 5 500 1 5 0	1 0 0 4 0 0 0 5 5 5 5 500 1 5	***************************************
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08	Actual Value  1 0 0 0 4 0 5 5 5 0 0 0	1 0 0 4 0 0 0 5 5 5 5 500 1 5	***************************************
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08	Actual Value  1 0 0 0 4 0 5 5 5 0 0 1 5 0 0 5 5 5 5 5 5 5 5 5 5	1 0 0 4 0 0 0 5 5 5 5 500 1 5 0 0	**************************************
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc	Actual Value  1 0 0 0 4 0 5 5 5 0 0 1 5 0 0 1 1 1 1 1 1 1 1 1 1	1 0 0 4 0 0 0 5 5 5 5 500 1 5 0 0	> > > > > > > > > > > > > > > > > > >
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_gc  LoaMgr_LOASt_State_M_enum  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgCoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08	Actual Value  1 0 0 0 4 0 0 5 5 5 0 0 0 1 5 0 0 0 5 0 0	1 0 0 4 0 0 0 5 5 5 5 500 1 5 0 0 0	**************************************
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrlAgtnEn_Cnt_M_lgc  LoaMgr_MotCurrlAgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08	Actual Value  1 0 0 0 4 0 0 5 5 5 0 0 1 5 0 0 1 2 1	1 0 0 4 0 0 0 5 5 5 5 500 1 5 0 0 0 1 5 0	> > > > > > > > > > > > > > > > > > >
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc	Actual Value  1 0 0 0 4 0 5 5 5 0 0 1 5 0 0 1 2	1 0 0 4 0 0 0 5 5 5 5 500 1 5 0 0 0 1 5	**************************************
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_LOASt_State_M_enum  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrlCoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08	Actual Value  1 0 0 0 4 0 0 5 5 5 0 0 1 5 0 0 1 2 1 1 1	1 0 0 4 0 0 5 5 5 500 1 5 0 0 0 5 0 1 2 1 2	**************************************
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_LoASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	Actual Value  1 0 0 0 4 0 0 5 5 5 0 0 1 5 0 0 1 1 5 1 1 1 1 1 1	1 0 0 0 4 0 0 5 5 5 500 1 5 0 0 1 2 1 1 1 1 {170, 172, 173, 175}	**************************************
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LOaSt_State_M_enum  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	Actual Value  1 0 0 0 4 0 0 5 5 5 0 0 1 1 2 1 1 1 1 {170, 172, 173, 175} {1, 1, 1, 1}	1 0 0 0 4 0 0 5 5 5 500 1 5 0 0 1 2 1 1 1 {170, 172, 173, 175}	**************************************
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LOaSt_State_M_enum  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotOurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotOurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	Actual Value  1 0 0 0 4 0 0 5 5 5 0 0 1 2 1 1 1 1 {170, 172, 173, 175} {1, 1, 1, 1} {1, 1, 1, 1}	1 0 0 0 4 0 0 5 5 5 500 1 5 0 0 1 2 1 1 1 (170, 172, 173, 175) {0, 0, 0, 0, 0, 0} {1, 1, 1, 1}	V V V V V V V V V V V V V V V V V V V
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_LoaSt_State_M_enum  LoaMgr_LOaSt_State_M_enum  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	Actual Value  1 0 0 0 4 0 0 5 5 5 500 1 5 0 0 1 2 1 1 1 {170, 172, 173, 175} {1, 1, 1, 1} {1, 1, 1, 1} 500	1 0 0 0 4 0 0 5 5 5 500 1 5 0 0 1 2 1 1 {170, 172, 173, 175} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1, 1} 500 ± 0.01	V V V V V V V V V V V V V V V V V V V
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	Actual Value  1 0 0 0 4 0 0 5 5 5 500 1 5 0 0 1 2 1 1 1 {170, 172, 173, 175} {1, 1, 1, 1} {1, 1, 1, 1} 500	1 0 0 0 4 0 0 5 5 5 500 1 5 0 0 1 2 1 1 {170, 172, 173, 175} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1, 1} 500 ± 0.01 1 ± 0.001	V V V V V V V V V V V V V V V V V V V
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRcaleLimit_UlspS_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	Actual Value  1 0 0 0 4 0 0 5 5 5 500 1 5 0 0 1 2 1 1 1 (170, 172, 173, 175) {1, 1, 1, 1} {1, 1, 1, 1} 500 1 0	1 0 0 0 4 0 0 5 5 5 500 1 5 0 0 1 2 1 1 {170, 172, 173, 175} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 0	V V V V V V V V V V V V V V V V V V V
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	Actual Value  1 0 0 0 4 0 0 5 5 5 500 1 5 0 0 1 2 1 1 1 (170, 172, 173, 175) {1, 1, 1, 1} {1, 1, 1, 1} 500 1 0 0	1 0 0 0 4 0 0 5 5 5 500 1 5 0 0 1 2 1 1 1 {170, 172, 173, 175} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 0 0	V V V V V V V V V V V V V V V V V V V
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRcaleLimit_UlspS_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	Actual Value  1 0 0 0 4 0 0 5 5 5 500 1 5 0 0 1 2 1 1 1 (170, 172, 173, 175) {1, 1, 1, 1} {1, 1, 1, 1} 500 1 0	1 0 0 0 4 0 0 5 5 5 500 1 5 0 0 1 2 1 1 {170, 172, 173, 175} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 0	V V V V V V V V V V V V V V V V V V V



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	•
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•

Test Step 4.5 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2
LoaMgr MotCurrLoaMtgtnEn Cnt M lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	5
k LoaMgr HwTqldptSig1TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt Igc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target LoaMgr Per1 MtrPosldptSig Cnt u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	· 100an
LoaMgr HwTqldptSigSV Cnt M u08	1	1	_
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	·
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	<b>V</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	_
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	<b>V</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	
LoaMgr MotCurrLoaMtgtnEn Cnt M Igc	1	1	<b>V</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	·
LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 173}	{170, 173}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_endin)	{1, 1}	{0, 0, 0, 0, 0}	<b>B</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	·
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_IvtiLoaMigtriEr1_Crit_igc.value target_LoaMgr_Per1_LOASt_State_enum.value	5	5	-
target_LoaMgr_Per1_LOASt_State_enum.value target LoaMgr_Per1_MotAgLoaMtgtnEn Cnt lgc.value	1	1	
		1	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	

Test Step Call Trace   ✓				
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 4.6 (Repeat Count = 1)	<b>13</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	1 5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 k LoaMgr MotCurrMtgtnScaZeroEn Cnt lgc	1
k LoaMgr MotCurrMtgtnSca Uls f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtrInlactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_ui	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	<b>✓</b>
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>~</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.7 (Repeat Count = 1)		E E
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_lvtrRespSV_Cnt_M_u08	4	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	4	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

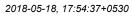
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LoaMgr\_Per1



Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32 k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqIdptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_	_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	Ont Inc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrloptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_ target_LoaMgr_Per1_MtrPosIdptSig_Cnt_t		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPostoptSig_Cnt_uu8 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_MtrPosidptSig_Cnt_t target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Peri_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Peri_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc	·	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	result
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	<b>~</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08		4	~
	4	4	_
LoaMgr_LOASt_State_M_enum	4	4	~
LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32			
	4	4	•
LoaMgr_LoaRateLimit_UlspS_M_f32	4 500 0 4	4 500 0 4	· · · · · · · · · · · · · · · · · · ·
LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	4 500 0 4 0	4 500 0 4 0	· ·
LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	4 500 0 4	4 500 0 4	· · · · · · · · · · · · · · · · · · ·

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

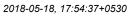




Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{171, 172, 173}	{171, 172, 173}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	✓
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	4	4	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt lgc.value	0	0	✓

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.8 (Repeat Count = 1)	<b>18</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	5
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	0		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Ina	actv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Ina	actv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Ci	nt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnB	En_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_er	num	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_l		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgl		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_MtrPosIdptSig_0 target_LoaMgr_Per1_SteerMod_Cnt_o	_	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_l		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	90	
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	✓
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc			~
Eddings_iva_Eddinagalen_en_ing	0	0	•
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	, , , , , , , , , , , , , , , , , , ,
LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	4 5	4 5	*
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	4 5 500	4 5 500	*
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	4 5 500 0	4 5 500 0	
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	4 5 500 0 5	4 5 500 0 5	•
LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	4 5 500 0 5 2	4 5 500 0 5 2	· · · · · · · · · · · · · · · · · · ·
LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	4 5 500 0 5 2 1	4 5 500 0 5 2	· · · · · · · · · · · · · · · · · · ·
LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	4 5 500 0 5 2 1	4 5 500 0 5 2 1	· · · · · · · · · · · · · · · · · · ·
LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	4 5 500 0 5 2 1 4	4 5 500 0 5 2 1 4	· ·
LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	4 5 500 0 5 2 1	4 5 500 0 5 2 1	· ·
LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	4 5 500 0 5 2 1 4 1	4 5 500 0 5 2 1 4 1	*
LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	4 5 500 0 5 2 1 4 1 0	4 5 500 0 5 2 1 4 1 0 3	*
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc	4 5 500 0 5 2 1 4 1 0 3	4 5 500 0 5 2 1 4 1 0 3	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	4 5 500 0 5 2 1 4 1 0 3	4 5 500 0 5 2 1 4 1 0 3 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrRespSV_Cnt_N_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4 5 500 0 5 2 1 4 1 0 3 0 0 0 (170, 173)	4 5 500 0 5 2 1 4 1 0 3 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	4 5 500 0 5 2 1 4 1 0 3 0 0 0 {170, 173} {1, 1} {1, 1}	4 5 500 0 5 2 1 4 1 0 3 0 0 0 0 {170, 173} {0, 0, 0, 0, 0} {1, 1}	
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrNespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	4 5 500 0 5 2 1 4 1 0 3 0 0 {170, 173} {1, 1} 500	4 5 500 0 5 2 1 4 1 0 3 0 0 (170, 173) {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOaSt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	4 5 500 0 5 2 1 4 1 0 3 0 0 {170, 173} {1, 1} {1, 1} 500	4 5 500 0 5 2 1 4 1 0 3 0 0 0 (170, 173) {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 0 ± 0.001	· · · · · · · · · · · · · · · · · · ·
LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_igc LoaMgr_TloaAvail_Cnt_M_igc LoaMgr_TloaDi_Cnt_M_igc LoaMgr_TloaDi_Cnt_M_igc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	4 5 500 0 5 2 1 4 1 0 3 0 0 {170, 173} {1, 1} {1, 1} 500 0 1	4 5 500 0 5 2 1 4 1 0 3 0 0 0 (170, 173) {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 0 ± 0.001 1	· · · · · · · · · · · · · · · · · · ·
LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rto_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	4 5 500 0 5 2 1 4 1 0 3 0 0 {170, 173} {1, 1} {1, 1} 500 0 1	4 5 500 0 5 2 1 4 1 0 3 0 0 {170, 173} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 0 ± 0.001 1 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_igc LoaMgr_TloaAvail_Cnt_M_igc LoaMgr_TloaDi_Cnt_M_igc LoaMgr_TloaDi_Cnt_M_igc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	4 5 500 0 5 2 1 4 1 0 3 0 0 {170, 173} {1, 1} {1, 1} 500 0 1	4 5 500 0 5 2 1 4 1 0 3 0 0 0 (170, 173) {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 0 ± 0.001 1	· · · · · · · · · · · · · · · · · · ·



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.9 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr MotAgldptSigSV Cnt M u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k LoaMgr HwTqldptSig1TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C	:nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_l	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ci	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr HwTqldptSigSV Cnt M u08	1	1	<b>~</b>
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	<b>~</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>~</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	<b>~</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>~</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>V</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr MotCurrLoaMtgtnEn Cnt M lgc	1	1	<b>V</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	0	0	-
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 174}	{170, 174}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Section	Test Step 4.10 (Repeat Count = 1)	8
Analysis   Profession   Cont. M.   1	Name	
analog for inglandings Co. M. vis 8 analog for inglandings Co. M. vis 9 analog for inglandings Co. M.	LoaMgr_AgSV_Cnt_M_lgc	·
analog - IntroSpecial Cort M - 100	LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
analog - Michalippin Coff M. Julia  analog - Michalippin Coff M. Julia  analog - Michalippin Coff M. Julia  2 analog - Michael M. Julia  analog - Michael M. Julia  3 analog - Michael M. Julia  2 analog - Michael M. Julia  3 analog - Michael M. Julia  3 analog - Michael M. Julia  3 analog - Michael M. Julia  4 analog - Michael M. Julia  4 analog - Michael M. Julia  4 analog - Michael M. Julia  5 analog - Michael M. Julia  6 analog - Michael M. Julia  7 analog - Michael M. Julia  8 analog - Michael M. Juli	LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
analog principality Chris Muse  analog Montphase  an	LoaMgr_HwTqRespSV_Cnt_M_u08	
andley_bridge(s)_CD_M_V08  andley_bridge(s)_CD_M	LoaMgr_lvtrldptSigSV_Cnt_M_u08	
2048 MANGRISHIPS CM M 100 2048 MANGRISHIPS CM 100 2048 MANGRIS	LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	
andley_Monde_control_Con_Muse  andley_Monde_control_Con_Muse  andley_Monde_control_Con_Muse  andley_Monde_control_Con_Muse  andley_Monde_control_Con_Muse  andley_Monde_control_Con_Muse  andley_Monde_control_Con_Muse  andley_Monde_control_Con_Muse  andley_Monde_con_Muse  andle_con_Muse  an		
andley John John Service Victor III July 2 andley John John Service Victor III July 2 andley John John Service Victor III July 3 andley John John Service Victor III July 3 andley John John Service Victor III July 3 andley John John John John John John July 4 andley John John John John John John John John		
2 and/org McDurchards (Principle Control Miles) 3 and/org McDurchards (Principle Control Miles) 4 and/org McDurchards (Principle Control Miles) 4 (and/org Control Miles) 5 (and/org Control Miles) 6 (and/org Mil		
analog Mortanian Magnetic Cent M. 198  analog Mortanian Magnetic Cent M. 198  1		
. analogy . Smothanic playani Lour July 19:		
Landay CurrivitAgrica   Land	LoaMgr_MotCurrRespSV_Cnt_M_u08	4
	LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
Landing_currivalingsaca_wells_Critique   0    -Landing_currivalingsaca_wells_Critique   0    -Landing_currivalingsaca	LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Landarg_Internationalistics_Description_Col_Upic_   Landarg_Landarge_Description_Col_Upic_   Landarge_Description_Col_Upic_Col_Upic_   Landarge_Internation_Col_Upic_Col_Upi	Rte_Inst_Ap_LoaMgr	· · - ·
Loading   February		
Leahig   Friority   Disposition   Leahig   Lea		
Landing hwiTrigings [WT Triples of Liu 08		
Leading / Horifolds 17 Teachers   Chr. U8		
Loading   Miroflot(Sty)  TransvariResp. Cnt. U08		
Loading   Mind publishing Zince   Direct   Direct	k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	
Loading: HVT-00058/SPTEND COT. U08	k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	
Loading	k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
Loaking	k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
Loading   Mrt   Mightis   Loading   Loading   Mrt   Mightis   Mi	k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
Laaking   Miritapisig   Resp. Cnt_U8	k_LoaMgr_HwTqMtgtnRate_UlspS_f32	
Loaking   Invitigistig:   Resp.   Crt.   U88		
Loading:   Lord   Loading:   Lo		
Loaking_ru/Mitignisca_UispS_q2		
Loaking/ IntriktginSca/ Us		
Loaklgr_InvirkignSca_Uis_f32		
Loaking/MoNajdiptiSig2Resp_Cnt_u08	k_LoaMgr_lvtrMtgtnSca_Uls_f32	
Loaking MotAglidptSig4Resp_Cnt_u08	k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
LoaMgr_MoNAglidpiSigFilThd_Cnt_u08	k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
LoaMgr_MotAgMtginRate_UlspS_f32	k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
LoaMgr_MortAgMightScazZeroEn_Cnt_lgc	k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	
LoaMgr_MotAgMtgnSca_Uls_132		
LoaMgr_MotCurridptSig1Resp_Cnt_u08		
LoaMgr_MotCurridptSig1Resp_Cnt_u08		
LoaMgr_MotCurridptSig2Resp_Cnt_u08		
LoaMgr_MotCurridptSig2Resp_Cnt_u08		
LoaMgr_MotCurrMtgtnRate_UlspS_132	k LoaMgr MotCurrldptSig2Resp Cnt u08	
LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
LoaMgr_RedcdStRate_UlspS_f32	k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	f.
LoaMgr_RedcdStRate_UtspS_f32 LoaMgr_Per1_DiagcStstvtr1Inactv_Cnt_lgc_value 1 arget_LoaMgr_Per1_DiagcStstvtv2Inactv_Cnt_lgc_value 1 arget_LoaMgr_Per1_HvTqldptSig_Cnt_u08_value 1 arget_LoaMgr_Per1_HvTqldptSig_Cnt_u08_value 1 arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08_value 1 arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08_value 1 arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08_value 1 arget_LoaMgr_Per1_SteerMod_Cnt_enum_value 1 arget_LoaMgr_Per1_ToaAvail_Cnt_lgc_value 1 arget_LoaMgr_Per1_DiagcStstvtrInactv_Cnt_lgc arget_Rie_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStstvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStstvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStstvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStstvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_HvTqldptSig_Cnt_u08 target_Rie_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMitgInEn_Cnt_lgc target_LoaMgr_Per1_HvTqLoaMitgInEn_Cnt_lgc target_LoaMgr_Per1_HvTqLoaMitgInEn_Cnt_lgc target_LoaMgr_Per1_HvTqLoaMitgInEn_Cnt_lgc target_LoaMgr_Per1_HvTqLoaMitgInEn_Cnt_lgc target_LoaMgr_Per1_LoASc_aDi_Cnt_lgc target_LoaMgr_Per1_LoASc_aDi_Cnt_lgc target_LoaMgr_Per1_LoASc_aDi_Cnt_lgc target_LoaMgr_Per1_LoASc_aDi_Cnt_lgc target_LoaMgr_Per1_LoASc_aDi_Cnt_lgc target_LoaMgr_Per1_LoASc_aDi_Cnt_lgc target_LoaMgr_Per1_LoASc_aDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMitgInEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMitgInEn_Cnt_lgc target_LoaMgr_Per1_LoASc_aDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMitgInEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMitgInEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMitgInEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMitgInEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMitgInEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMitgInEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMitgInEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMitgInEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMitgInEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMit	k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
LoaMgr_RedodStSca_Uls_f32  arget_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value  arget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value  1  arget_LoaMgr_Per1_HwTqldptSig_Cnt_u08 value  4  arget_LoaMgr_Per1_HwTqldptSig_Cnt_u08 value  arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 value  1  arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  1  arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  1  arget_LoaMgr_Per1_SteerMod_Cnt_enum.value  1  arget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  1  arget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  1  arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value  1  arget_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  arget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  arget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LvtLoaMtgtnEn_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc  arget_Re_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arg	k_LoaMgr_MotCurrMtgtnSca_Uls_f32	
arget_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value  1 arget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value  1 arget_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value  2 arget_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  3 arget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value  4 arget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value  1 arget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value  1 arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  1 arget_LoaMgr_Per1_SteerMod_Cnt_enum.value  1 arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value  1 arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value  1 arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value  1 arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08  arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrAoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum  arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum  arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DoaScaDi_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DoaScaDi_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DoaScaDi_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DoaScaDi_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DoaScaDi_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DoaScaDi_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DoaScaDi_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DoaScaDi_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DoaScaDi_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DoaScaDi_Cnt_lgc  arget_LoaMgr_Per1_LoaScaDi_Cnt_lgc  arget_LoaMgr_Per1_LoaScaDi_Cnt_lgc  arget_LoaMgr_Per1_LoaScaDi_Cnt_lgc  arget_LoaMgr_Per1_LoaScaDi_Cnt_lgc  arget_LoaMgr_Per1_LoaScaDi_Cnt_lgc  arget_LoaMgr_Per1_LoaScaDi_Cnt_lgc  arget_LoaMgr_Per1_LoaScaDi_Cnt_lgc  arget_LoaMgr_Per1_LoaScaDi_Cnt_lgc	k_LoaMgr_RedcdStRate_UlspS_f32	
arget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value  arget_LoaMgr_Per1_LoaScaDi_Cnt_uo8.value  arget_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  arget_LoaMgr_Per1_MotCurrldptSig_Cnt_uo8.value  arget_LoaMgr_Per1_MotCurrldptSig_Cnt_uo8.value  1  arget_LoaMgr_Per1_MtrPosldptSig_Cnt_uo8.value  1  arget_LoaMgr_Per1_SteerMod_Cnt_enum.value  1  arget_LoaMgr_Per1_SteerMod_Cnt_enum.value  1  arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value  1  arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value  1  arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value  1  arget_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_uo8  arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IncaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_uo8  arget_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_uo8  arget_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_uo8  arget_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_uo8  arget_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_uo8  arget_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_uo8  arget		
arget_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value		
arget_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 0 arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 arget_LoaMgr_Per1_SteerMod_Cnt_enum.value 1 arget_LoaMgr_Per1_SteerMod_Cnt_enum.value 1 arget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value 0 arget_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Net_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
arget_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value  arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  arget_LoaMgr_Per1_SteerMod_Cnt_enum.value  arget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value  arget_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08  arget_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08  arget_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_LoASt_State_enum  arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum  arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 arget_LoaMgr_Per1_SteerMod_Cnt_enum.value 1 arget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value 0 arget_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	
arget_LoaMgr_Per1_SteerMod_Cnt_enum.value  arget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value  arget_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08  arget_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_LoASt_State_enum  arget_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
arget_LoaMgr_Per1_TioaDi_Cnt_lgc.value  arget_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08  arget_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_LoASt_State_enum  arget_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_HwTqLoaMgr_Per1_Igc target_LoaMgr_Per1_IvtrLoaMggr_Per1_Igc target_LoaMgr_Per1_IvtrLoaMggr_Per1_Igc target_LoaMgr_Per1_IvtrLoaMggr_Per1_Igc target_LoaMgr_Per1_IvtrLoaMggr_Per1_Igc target_LoaMgr_Per1_LoaSc_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum  arget_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LOASt_State_enum  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	
arget_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc     target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc       arget_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum     target_LoaMgr_Per1_LOASt_State_enum       arget_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc     target_LoaMgr_Per1_LoaScaDi_Cnt_Igc       arget_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc     target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc       arget_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08     target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
arget_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum       target_LoaMgr_Per1_LOASt_State_enum         arget_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc       target_LoaMgr_Per1_LoaScaDi_Cnt_lgc         arget_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc       target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc         arget_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08       target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc       target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc         arget_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08       target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	
arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 175}	{170, 173, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.11 (Repeat Count = 1)		X
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_lvtrRespSV_Cnt_M_u08	2	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	4	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

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LoaMgr\_Per1



Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	5		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k LoaMgr HwTqMtgtnSca Uls f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08			
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k LoaMgr MotAgMtgtnRate UlspS f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc			
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target LoaMgr Per1 MotCurrldptSig Cnt u08.value	0		
target LoaMgr Per1 MtrPosldptSig Cnt u08.value	0		
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target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ci	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Ci	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target LoaMgr Per1 HwTqldptSig Cnt u08		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqLoaMtgtnEn Cnt Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt	t lac	
target Rte Inst Ap LoaMgr.LoaMgr Per1 lvtrLoaMtgtnEn Cnt lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg		
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target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_ui		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaAvail Cnt Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
		Power and Malice	D16
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
Loanigi_i wiqLoanigiiLi_Ont_w_igc		4	<b>✓</b>
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	
	1	1	<b>✓</b>
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08	1		
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08	1 1 2	1 1 2	•
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	1 1 2 5	1 1 2 5	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	1 1 2 5 500	1 1 2 5 500	~
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	1 1 2 5 500 0	1 1 2 5 500 0	~
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	1 1 2 5 500	1 1 2 5 500	\(\frac{1}{2}\)
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	1 1 2 5 500 0	1 1 2 5 500 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 1 2 5 500 0 5	1 1 2 5 500 0 5	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \

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Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc.value	0	0	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.12 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	3
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Institute Value		
Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	1		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k LoaMgr MotAgMtgtnSca Uls f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSigFltThd Cnt u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	ent lac	
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqldptSig Cnt u08	target LoaMgr Per1 HwTqldptSig Cnt u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr_Der1_lvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	<del>5</del> -	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	-
LoaMgr HwTqldptSigSV Cnt M u08	0	0	<b>~</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	<b>~</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr LOASt State M enum	5	5	<b>~</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>✓</b>
LoaMgr MaxMtgtnResp Cnt M u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr MotAgLoaMtgtnEn Cnt M lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08		0	-
LoaMgr MotCurrLoaMtgtnEn Cnt M Igc	0	0	
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	1	~
·			<b>V</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	· ·
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	1 3	1 3	· · · · · · · · · · · · · · · · · · ·
	1 3 1	1 3 1	\rightarrow \frac{1}{2}
LoaMgr_TloaDi_Cnt_M_lgc	1 3 1 1	1 3 1 1	-
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	1 3 1 1	1 3 1 1	<b>y</b>
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	1 3 1 1 1 1 {170, 173, 174, 175}	1 3 1 1 1 1 {170, 173, 174, 175}	<b>y</b>
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1 3 1 1 1 1 {170, 173, 174, 175} {1, 1, 1, 1}	1 3 1 1 1 1 {170, 173, 174, 175} {0, 0, 0, 0, 0}	× ×
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1 3 1 1 1 1 {170, 173, 174, 175} {1, 1, 1, 1} {1, 1, 1, 1}	1 3 1 1 1 1 {170, 173, 174, 175} {0, 0, 0, 0, 0} {1, 1, 1, 1}	× ×
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1 3 1 1 1 (170, 173, 174, 175) (1, 1, 1, 1) (1, 1, 1, 1) 500	1 3 1 1 1 {170, 173, 174, 175} {0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01	× ×
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1 3 1 1 1 {170, 173, 174, 175} {1, 1, 1, 1} {1, 1, 1, 1} 500	1 3 1 1 1 {170, 173, 174, 175} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1, 1} 500 ± 0.01 1 ± 0.001	× ×
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1 3 1 1 1 (170, 173, 174, 175) (1, 1, 1, 1) (1, 1, 1, 1) 500 1	1 3 1 1 1 {170, 173, 174, 175} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 1	× ×
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1 3 1 1 1 1 {170, 173, 174, 175} {1, 1, 1, 1} {1, 1, 1, 1} 500 1 1 1 0	1 3 1 1 1 {170, 173, 174, 175} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 1	**************************************



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	<b>~</b>
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>~</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.13 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	4
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr MotAgldptSigSV Cnt M u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnKate_Oisp3_132	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k LoaMgr MotAgldptSig2Resp Cnt u08	1
	0
k_LoaMgr_MotAgIdptSig3Resp_Cnt_u08 k_LoaMgr_MotAgIdptSigFltThd_Cnt_u08	2
	1
k_LoaMgr_MotAgMtgtnSoaZoroEn_Cot_loa	0
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotAgMtgtnSca_Uls_f32	2
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	1
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Ci	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	-	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target LoaMgr Per1 LoaScaDi Cnt Igc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target LoaMgr Per1 MtrPosldptSig Cnt u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr HwTqldptSigSV Cnt M u08	0	0	_
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	_
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	_
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	
LoaMgr MotCurrLoaMtgtnEn Cnt M Igc	1	1	
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	-
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	
LoaMgr_TloaDi_Cnt_M_lgc	1	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 172}	{170, 172}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_endin)	{1, 1}	{0, 0, 0, 0, 0}	<b>X</b>
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	{1, 1} {1, 1}	{1, 1}	~
Rte Write Ap LoaMgr LoaRateLimit UlspS f32(data)	500	500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	-
target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	
target_LoaMgr_Per1_LOASt_State_enum.value target LoaMgr_Per1_MotAgLoaMtgtnEn Cnt lgc.value	1	1	
	1	1	-
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	I I	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~



Test Step 4.14 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4 2
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	5
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k LoaMgr MotAgMtgtnRate UlspS f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnSca_Ule_f32	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32 k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1 torget LeoMar Port DiagoStalutri Inpaty Cet Inc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_nw1qLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	<b>~</b>
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.15 (Repeat Count = 1)		E
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_lvtrRespSV_Cnt_M_u08	1	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

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Input Value



Name



k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
	2		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
	0		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08			
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	2		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSig2Resp Cnt u08	0		
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32			
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
toward Landau Bard Diagraphy Oct Income	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1 0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1 0 1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1 0 1 1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1 0 1 1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1 0 1 1 1 0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1 0 1 1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1 0 1 1 1 0	int_lgc	
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1 0 1 1 1 0 0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc	1 0 1 1 1 0 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	:nt_lgc	
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	1 0 1 1 1 0 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ctarget_LoaMgr_Per1_DiagcStslvtr2Inactv_Ctarget_LoaMgr_Per1_DiagcStslvt	ent_lgc 3	
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	1 0 1 1 1 0 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ctarget_LoaMgr_Per1_DiagcStslvtr2Inactv_Ctarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08	int_lgc 3 t_lgc	
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	1 0 1 1 1 0 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ctarget_LoaMgr_Per1_DiagcStslvtr2Inactv_Ctarget_LoaMgr_Per1_HwTqldptSig_Cnt_u0⌖_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	int_lgc 3 t_lgc	
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	1 0 1 1 1 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_larget_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_larget_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_l	int_lgc 3 t_lgc	
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	1 0 1 1 1 0 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_l target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_l target_LoaMgr_Per1_loASt_State_enum	int_igc 3 t_lgc gc	
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc	1 0 1 1 1 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u0& target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_learget_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_learget_LoaMgr_Per1_loASt_State_enum target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	int_igc 3 t_lgc gc nt_lgc	
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	1 0 1 1 1 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u0& target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_letarget_LoaMgr_Per1_lvtLoaMtgtnEn_Cnt_letarget_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_C.c.	int_igc 3 t_lgc gc nt_igc 08	
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target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc	1 0 1 1 1 1 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_letarget_LoaMgr_Per1_IvTLoaMtgtnEn_Cnt_letarget_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u6 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_target_LoaMgr_Per1_MtrPosldptSig_Cnt_u6 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0	int_lgc int_lgc it_lgc gc int_lgc int_	~
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target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvaii_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc	1 0 1 1 1 1 0 ctarget_LoaMgr_Per1_DiagcStsIvtr1Inactv_Ctarget_LoaMgr_Per1_DiagcStsIvtr2Inactv_Ctarget_LoaMgr_Per1_DiagcStsIvtr2Inactv_Ctarget_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lotarget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lotarget_LoaMgr_Per1_LoASt_State_enumtarget_LoaMgr_Per1_LoASt_State_enumtarget_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lotarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lotarget_LoaMgr_Per1_MtrPosIdptSig_Cnt_lotarget_LoaMgr_Per1_MtrPosIdptSig_Cnt_lotarget_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_Loa	Expected Value  0 1 0 4 1 0 0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
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target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_HwTqloptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	1 0 1 1 1 1 0 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_u0t target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_u0t target_LoaMgr_Per1_LoASt_State_num target_LoaMgr_Per1_LoASt_State_num target_LoaMgr_Per1_LoASt_Dicnt_lgc target_LoaMgr_Per1_MotCurrloptSig_Cnt_u0t target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0t target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0t target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaDi_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 4 1 0 1 5	Expected Value	***
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqlcaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	1 0 1 1 1 1 1 0 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_le target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_le target_LoaMgr_Per1_LoaSt_state_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAQLoaMtgtnEn_Cnt_le target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_le target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_le target_LoaMgr_Per1_MtrPosldptSig_Cnt_ucl target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 4 1 0 1 5 5000	Expected Value  0 1 0 4 1 0 1 5 5 5 0 0	***
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum	1 0 1 1 1 1 0 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_u0t target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_u0t target_LoaMgr_Per1_LoASt_State_num target_LoaMgr_Per1_LoASt_State_num target_LoaMgr_Per1_LoASt_Dicnt_lgc target_LoaMgr_Per1_MotCurrloptSig_Cnt_u0t target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0t target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0t target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaDi_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 4 1 0 1 5	Expected Value	***
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_En_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrLoaRtgtnEn_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	1 0 1 1 1 1 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_letarget_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_u08 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_u08 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 4 1 0 1 5 500 0 5	Expected Value  0 11 0 14 10 0 11 5 5 500 0 5	***
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASc_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_En_Cnt_M_u08 LoaMgr_HwTqloaMtgtnEn_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrloaNtgtnEn_Cnt_M_u08 LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	1 0 1 1 1 1 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_letarget_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 4 1 0 1 5 500 0 5	Expected Value  0 11 0 14 10 0 11 5 5 500 0 5 0	***
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaNtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaNtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaNtgtnEn_Cnt_M_lgc LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 0 1 1 1 1 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_letarget_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_u08 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_u08 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 4 1 0 1 5 500 0 5	Expected Value  0 11 0 14 10 0 11 5 5 500 0 5	***

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc.value	1	1	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.16 (Repeat Count = 1)	<b>13</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	5
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv		
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enur		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cr	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	l=	
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	5	5	
LoaMgr_HwTqRespSV_Cnt_M_u08			
LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn Cnt M lgc	1	1	_
LoaMgr_lvtrRespSV_Cnt_M_u08	1	1	
LoaMgr_LOASt_State_M_enum	5	5	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr LoaScaleFctr Uls M f32	1	1	_
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	_
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	3	3	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	_
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	•
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•

Test Step 4.17 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	5
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr MotAgldptSigSV Cnt M u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	5
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u		
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M Igc	1	1	
LoaMgr HwTqldptSigSV Cnt M u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	5	5	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	<b>~</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 172, 173, 174, 175}	{170, 172, 173, 174, 175}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1, 1}	{0, 0, 0, 0, 0}	<b>1</b> 12 12 12 12 12 12 12 12 12 12 12 12 12
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Farani_Cnt_T_enum)	{1, 1, 1, 1, 1} {1, 1, 1, 1, 1}	{1, 1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc.value	0	0	-
target_LoaMgr_Per1_IvtiLoaMigtriEr1_Crit_igc.value target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
· - ·	0	0	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_Loawigi_Ferri_wotourrLoawitgtriEff_Offt_lgc.value	U	0	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	~
AssignScale	1	AssignScale	1	<b>✓</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Sealing	Test Step 4.18 (Repeat Count = 1)	
Sealing John Compiler Compil		
Leading - Infriquentiagin - Cort M - 199  Leading - Mandraganiagin - Cort M - 199  Leading - Mandragania - 199  Leadin		•
Leading Interligency Cmt M 100	LoaMgr_HwTqldptSigSV_Cnt_M_u08	
Landary Puriosophics Cort. M. 200  Londary Puriosophics M. 200  Londary Puriosophics Cort. M. 200  Londary Puriosophics M	LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
Landary   Land	LoaMgr_HwTqRespSV_Cnt_M_u08	4
Loading_Modelpathspinc_Cort_M_second_cort_Maps_cort_Ma	LoaMgr_lvtrldptSigSV_Cnt_M_u08	
Leading   Mandage Registry   Celt M, under	LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	
Loading   Machigan   March   March		
Landary, ManCardes (Sty) Cor, M., 108  Landary, ManCardes (Sty) Cor, M., 108  Landary, ManCardes (Sty) Cor, M., 108  Landary, Saminakondy, Noval Cord, M. 109  Landary, Landary Cord, M., 108  1 Step Lead Age Landary  Landary Landary Cord, M., 108  1 Landary, Landary Cord, M., 108  1 Landary, Landary Cord, M., 108  1 Landary, Landary  Landary  Landary Landary  Landary		
Loading McControllogist Cost M Isp 0  Loading McControllogist Cost M Isp 0  Loading McControllogist Cost M Isp 0  Loading McControllogist Cost M Isp 1  Loading Sprinkfasticy Lost M Isp 1  Loading MacControllogist Lost M Isp 1  Loading MacControllogist Lost M Isp 1  Loading MacControllogist Lost M Isp 2  Loading MacControllogist M Isp 3  Loading MacControllogist Loading M Isp 3  Loading MacControllogist Resp Cort Loading M Isp 4  Loading MacControllogi		
Leading - Michael Carlo M. Jose  Leading - Michael Carlo M. Jose  1		
Loading   Load		•
Loading Control Micro   Loading		
Loading / Contribution   Loading		1
Landarg.ContrividaginSea.2eeEnC.ntl.ge	LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Loading	Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
Loading / Faller Staffaller Users   12	k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	
Loading Hort Option (Control of the Control of th		
Landary Harrichetisp NotTooleans, Chil. 1988   2		
Loading   Mrd gloss   Na Diade Rep. Cht. U88		
Loading   Mindigs   Zimba   Resp. Cht   U88		
Loading   Mind projects   2011		
Loaking		
Loaking-Mir/TolipinSigNPRep_Cnt_U08		
Loading-HwTqdhgrisRepLulips   192	k LoaMgr HwTqldptSig4Resp Cnt u08	•
Loaking		
Loading   NirdipSigi Resp Cnt   U08	k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
Loaking	k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
Loaking   Nurtidiptising   Linch   Loaking   Linch	k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
Loaking	k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
Loading	k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
Loading	k_LoaMgr_IvtrMtgtnRate_UlspS_f32	
LoaMgr_MotAgloptSig1NoSmaResp_Cnt_u08		
LoaMgr_MotAgloptsig2Resp_Cnt_u08		
LoaMgr_MotAgloptSig3Resp_Cnt_u08		
LoaMgr_MotAgldptSigFitThd_Cnt_u08		
LoaMgr_MotAgMtghtRate_UlspS_f32		
k_LoaMgr_MotAgMtginScazeroEn_Cnt_lgc         0           k_LoaMgr_MotAgMtginScaz Uis_122         1           k_LoaMgr_MotAgMtginScaz Uis_122         2           k_LoaMgr_MotGuridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotGuridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotGuridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotGuridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotGuridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotGuridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotGuridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotGuridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotGuridptSig2Resp_Cnt_u1gc         1           k_LoaMgr_RedcdSitRate_UispS_132         1           k_LoaMgr_RedcdSitRate_UispS_132         1           k_LoaMgr_Pert_DiagcSitshtr1inactv_Cnt_lgc_value         0           target_LoaMgr_Pert_DiagcSitshtr2inactv_Cnt_lgc_value         1           target_LoaMgr_Pert_MotGuridptSig_Cnt_u08 value         1           target_LoaMgr_Pert_MotCuridptSig_Cnt_u08 value         1           target_LoaMgr_Pert_MotPostQtSigtSig_Cnt_u08 value         1           target_LoaMgr_Pert_Time_DiagcSistvtr1inactv_Cnt_lgc         1           target_LoaMgr_Pert_Time_DiagcSistvtr2inactv_Cnt_lgc         1           target_LoaMgr_Pert_Time_Diagc		
LoaMgr_MotAgSmaAvaiResp_Cnt_u08         2           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_Cnt_lgc         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_Cnt_lgc         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_Cnt_lgc         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Sig1Sig1Sig2         1           k_LoaMgr_MotCurridptSig2Sig1Sig2         1           k_LoaMgr_RedodSiRea_Uls_52         1           k_LoaMgr_RedodSiRea_Uls_52         1           k_LoaMgr_RedodSiRea_Uls_52         1           araget_LoaMgr_Per1_DiagcSistvir1inactv_Cnt_lgc value         1           araget_LoaMgr_Per1_HariqlqtpSig_Cnt_u08.value         1           araget_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value         1           araget_LoaMgr_Per1_ToaAvail_Cnt_lgc.value         1           araget_LoaMgr_Per1_ToaAvail_Cnt_lgc.value         1           araget_LoaMgr_Per1_ToaAvail_Cnt_lgc.value         1           araget_LoaMgr_LoaMgr_LoaMgr_Per1_DiagcSistvir1inactv_Cnt_lgc	k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         1           k_LoaMgr_MotCurridptSigaZesorEn_Cnt_lgc         1           k_LoaMgr_MotCurridptSigaZesorEn_Cnt_lgc         1           k_LoaMgr_RedcdSiStade_Uls_f32         1           k_LoaMgr_RedcdSiStade_Uls_f32         1           k_LoaMgr_RedcdSiStade_Uls_f32         1           k_LoaMgr_RedcdSiStade_Uls_f32         1           k_LoaMgr_Per1_DiagcStstvtr2Inactv_Cnt_lgc.value         0           larget_LoaMgr_Per1_DiagcStstvtr2Inactv_Cnt_lgc.value         1           larget_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value         1           larget_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value         2           larget_LoaMgr_Per1_TloaDi_Cnt_lgc.value         1           larget_LoaMgr_Per1_TloaDi_Cnt_lgc.value         1           larget_LoaMgr_Per1_TloaDi_Cnt_lgc.value         1           larget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc         larget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc           <	k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_U08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_Un_gc         1           k_LoaMgr_MotCurridptSig2SeroEn_Cnt_Igc         1           k_LoaMgr_MotCurridptSig2SeroEn_Cnt_Igc         1           k_LoaMgr_MotCurridptSig2SeroEn_Cnt_Igc         1           k_LoaMgr_MotCurridptSig2SeroEn_Cnt_Igc         1           k_LoaMgr_MotCurridptSig2SeroEn_Cnt_Igc         1           k_LoaMgr_MotCurridptSig2SeroEn_Cnt_Igc         1           k_LoaMgr_RedcdSiSea_Uis_f32         1           k_LoaMgr_RedcdSiStat_Uis_f32         1           k_LoaMgr_Per1_DiagcStsivtrInactv_Cnt_Igc.value         0           target_LoaMgr_Per1_DiagcStsivtrViloactv_Cnt_Igc.value         1           target_LoaMgr_Per1_MctCurridptSig_Cnt_u08.value         1           target_LoaMgr_Per1_SteerMod_Cnt_enum.value         1           target_LoaMgr_Per1_ToaAvail_Cnt_Igc.value         1           target_LoaMgr_Per1_Diag-CstsivtrInactv_Cnt_Igc         target_LoaMgr_Per1_Diag-CstsivtrInactv_Cnt_Igc           target_LoaMgr_Per1_Diag-CstsivtrInactv_Cnt_Igc         target_LoaMgr_Per1_Diag-CstsivtrInactv_Cnt_Igc           target_LoaMgr_LoaMgr_LoaMgr	k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitThd_Cnt_u08         1           k_LoaMgr_MotCurridptRiscaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurridptSigScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurridptSigSca_Uls_f32         1           k_LoaMgr_RedcdSiRate_UlspS_f32         1           k_LoaMgr_Perd_DiagcStsIvtr1Inactv_Cnt_lgc.value         0           larget_LoaMgr_Perl_DiagcStsIvtr2Inactv_Cnt_lgc.value         1           larget_LoaMgr_Perl_DiagcStsIvtr2Inactv_Cnt_lgc.value         1           larget_LoaMgr_Perl_MotCurridptSig_Cnt_u08.value         3           larget_LoaMgr_Perl_MotCurridptSig_Cnt_u08.value         1           larget_LoaMgr_Perl_MotCurridptSig_Cnt_u08.value         1           larget_LoaMgr_Perl_SteerMod_Cnt_enum.value         1           larget_LoaMgr_Perl_TicaAvail_Cnt_lgc.value         1           larget_LoaMgr_Perl_TicaAvail_Cnt_lgc.value         1           larget_Re_Inst_Ap_LoaMgr_Perl_DiagcStsIvtr1Inactv_Cnt_lgc         larget_LoaMgr_Perl_DiagcStsIvtr2Inactv_Cnt_lgc           larget_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_HwTqLoaMtgInEn_Cnt_lgc         larget_LoaMgr_Perl_DiagcStsIvtr2Inactv_Cnt_lgc           larget_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_HwTqLoaMtgInEn_Cnt_lgc         larget_LoaMgr_Perl_HwTqLoaMtgInEn_Cnt_lgc	k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
LoaMgr_MotCurrtqptSigFitTh_Cn_u08	k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	
LoaMgr_MotCurrMtgthRate_UlspS_32		
LoaMgr_MolCurrMtgtnScaZeroEn_Cnt_lgc		
LoaMgr_NotCurrMigtnSca_Uls_f32		
K_LoaMgr_RedcdStRate_UispS_f32  k_LoaMgr_Perd_DiagcStsIvtr1Inactv_Cnt_lgc.value  larget_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value  larget_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value  larget_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value  larget_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  larget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value  larget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value  larget_LoaMgr_Per1_SteerMod_Cnt_enum.value  larget_LoaMgr_Per1_SteerMod_Cnt_enum.value  larget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  larget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  larget_LoaMgr_Per1_TloaDi_Cnt_lgc.value  larget_LoaMgr_Per1_TloaDigc.value  larget_Rie_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc  larget_Rie_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc  larget_Rie_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  larget_Rie_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  larget_Rie_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  larget_Rie_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  larget_Rie_Inst_Ap_LoaMgr_LoaMgr_Per1_MotQuridptSig_Cnt_u08  larget_LoaMgr_Per1_MotQuridptSig_Cnt_u08		
k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Perl_DiagcStslvtr1Inactv_Cnt_Igc.value target_LoaMgr_Perl_DiagcStslvtr2Inactv_Cnt_Igc.value target_LoaMgr_Perl_DiagcStslvtr2Inactv_Cnt_Igc.value target_LoaMgr_Perl_HwTqldptSig_Cnt_u08.value target_LoaMgr_Perl_MctCurrldptSig_Cnt_u08.value target_LoaMgr_Perl_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Perl_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Perl_SteerMod_Cnt_enum.value target_LoaMgr_Perl_TloaAvail_Cnt_Igc.value target_LoaMgr_Perl_TloaAvail_Cnt_Igc.value target_LoaMgr_Perl_TloaAvail_Cnt_Igc.value target_LoaMgr_Perl_TloaMgr_LoaMgr_Perl_DiagcStslvtr1Inactv_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_DiagcStslvtr2Inactv_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_DiagcStslvtr2Inactv_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_HwTqLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_HwTqLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_LoaScaDi_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_LoaScaDi_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_LoaScaDi_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_LoaScaDi_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_LoaScaDi_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_MotAgLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_MotAgLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_MotAgLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_MotAgLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Perl_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Perl_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Perl_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Perl_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Perl_MotCurrldptSig_Cnt_u08		
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_Indextine_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoASt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc		
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target_LoaMgr_Per1_SteerMod_Cnt_enum.value  1 target_LoaMgr_Per1_TloaDavail_Cnt_lgc.value  1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1
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target_LoaMgr_Per1_TioaDi_Cnt_lgc.value  1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_SteerMod_Cnt_enum.value	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LOAScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08  target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	<b>~</b>
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.19 (Repeat Count = 1)		
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	3	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_lvtrRespSV_Cnt_M_u08	0	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

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Name	Input Value			
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1			
k_LoaMgr_FadeOutStRate_UlspS_f32	1			
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2			
k LoaMgr HwTgldptSig1NoTloaResp Cnt u08	4			
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2			
	1			
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08				
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0			
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0			
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1			
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1			
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1			
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0			
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0			
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1			
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1			
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5			
	1			
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08				
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0			
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2			
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1			
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2			
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2			
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0			
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0			
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1			
	1			
k_LoaMgr_MotCurrMtgtnSca_Uls_f32				
k_LoaMgr_RedcdStRate_UlspS_f32	1			
k_LoaMgr_RedcdStSca_Uls_f32	0			
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0			
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1			
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0			
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0			
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2			
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1			
target LoaMgr Per1 TloaDi Cnt Igc.value	1			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C	ent lac		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C			
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqldptSig Cnt u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08			
V =				
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr			
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_l	gc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	108		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc			
Name	Actual Value	Expected Value	Result	
		· ·	ivesuit	
LoaMgr_AgSV_Cnt_M_lgc	1	1		
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~	
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	✓	
LoaMgr_LOASt_State_M_enum	5	5	~	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5		
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	<b>y</b>	

LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

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LoaMgr\_Per1



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	Ī	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.20 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Nama	Input Value		
Name k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08 k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	0		
k_LoaMgr_MotCurrMtgtnNate_otspo_joz	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0 2		
target_LoaMgr_Per1_SteenMod_Crit_enum.value target_LoaMgr_Per1_TloaAvail_Crit_lgc.value	0		
target LoaMgr Per1 TloaDi Cnt Igc.value	0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1In	actv Cnt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2In		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_C	Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtn	En_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_e		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrIdptSig target_LoaMgr_Per1_MotCurrLoaMtg		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	;	
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	<b>V</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	· ·
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr LOASt State M enum	5	5	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>*</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08			
		1	<b>✓</b>
	1	1 {170, 173}	· ·
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)		1 {170, 173} {0, 0, 0, 0, 0}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	1 {170, 173}	{170, 173}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1 {170, 173} {1, 1}	{170, 173} {0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1 {170, 173} {1, 1} {1, 1} 500 1	{170, 173} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001	¥
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1 {170, 173} (1, 1) {1, 1} 500 1 0	{170, 173} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1 {170, 173} {1, 1} {1, 1} 500 1	{170, 173} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0	¥
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value  target_LoaMgr_Per1_LOASt_State_enum.value	1 {170, 173} {1, 1} {1, 1} 500 1 0 1 5	{170, 173} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0 1	¥
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1 {170, 173} {1, 1} {1, 1} 500 1	{170, 173} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0	**************************************



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	-
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.21 (Repeat Count = 1)	E Company of the Comp
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1.
LoaMgr_MotCurrRespSV_Cnt_M_u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4_
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	0.258599997
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target LoaMgr Per1 HwTqldptSig Cnt u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
target LoaMgr Per1 SteerMod Cnt enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target LoaMgr Per1 DiagcStslvtr1Ir	nacty Cnt loc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Ir		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqldptSig Cnt u08	target_LoaMgr_Per1_HwTqldptSig_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtr	_	
target Rte Inst Ap LoaMgr.LoaMgr Per1 lvtrLoaMtgtnEn Cnt Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_o		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMt		
target Rte Inst Ap LoaMgr.LoaMgr Per1 MtrPosldptSig Cnt u08	target_LoaMgr_Per1_MtrPosIdptSig_		
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target_LoaMgr_Per1_SteerMod_Cnt		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaAvail Cnt Igc	target_LoaMgr_Per1_TloaAvail_Cnt_		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lge		
Name	Actual Value	Expected Value	Result
	1	1	Result
LoaMgr_AgSV_Cnt_M_lgc	2	2	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	5	5	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	Ž
LoaMgr_LOASt_State_M_enum			
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgloptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotAgBoogS\/ Cot M v08	5	5	Ž
LoaMgr_MotAgRespSV_Cnt_M_u08		1	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	- V
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	·		
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	
LoaMgr_TloaDi_Cnt_M_lgc	0		
LoaMgr_VitgMdSrcSV_Cnt_M_u08		1 (470, 470)	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1} (4, 4)	{0, 0, 0, 0, 0}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1} 500	{1, 1} 500 + 0.01	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	Y
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	· ·
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>•</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>*</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	<b>V</b>
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	<b>✓</b>



Test Step 4.22 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2 4
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 k LoaMgr HwTqldptSig1TloaAvailResp Cnt u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k_LoaMgr_HwTqldptSig2TloaDilxesp_Cnt_u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32 k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	1 0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08 k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	0.0099999978
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0
target LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target LoaMgr_Per1 SteerMod Cnt enum.value	0
target_LoaMgr_Per1_SteenMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.23 (Repeat Count = 1)	E
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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LoaMgr\_Per1



Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_Igc	0		
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	1		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_eroEn_Cnt_igc k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k LoaMgr MotCurrldptSig0Resp Cnt u08	2		
k LoaMgr MotCurrldptSig1Resp Cnt u08	0		
k LoaMgr MotCurrldptSig2Resp Cnt u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	500		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
and the second s	4		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0 1 1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0 1 1 0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc	0 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	0 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqIdptSig_Cnt_u08	0 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	nt_lgc	
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target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc	0 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoASc_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotQLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1	nt_lgc 3 t_lgc gc nt_lgc 08 Cnt_lgc 08 Expected Value	~
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target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_tgtarget_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrloaMtgtnEn_Crt target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1	nt_lgc 3 t_lgc gc nt_lgc 08 Cnt_lgc 08 Expected Value 1	<b>*</b>
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08	0 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Crt_uarget_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0	nt_lgc 3 t_lgc gc nt_lgc 08 Cnt_lgc 08 Expected Value 1 0	<b>*</b>
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target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08	0 1 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_tgt target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cr target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaAvail_Cnt_lgc Actual Value 1 0 1 2 1 0 1 5	nt_lgc st_lgc gc st_lgc gc st_lgc st_	***
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgd_LoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqloaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOaSt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	0 1 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1 2 1 0 1 5 500	nt_lgc st_lgc st_lgc gc  nt_lgc 08 Cnt_lgc 08 Expected Value 1 0 1 2 1 0 1 5 500	***
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgsV_Cnt_M_lgc LoaMgr_HwTqltptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	0 1 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 1 0 1 2 1 5 500 1	nt_lgc  st_lgc  gc  nt_lgc  gc  nt_lgc  8  Expected Value  1 0 1 2 1 0 1 5 5 500 1	******
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaRtgtnEn_Cnt_M_lgc LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	0 1 1 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crt target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 1 0 1 2 1 0 1 5 500 1 5	nt_lgc st_lgc gc  nt_lgc gc  nt_lgc 08  Cnt_lgc 08  Expected Value 1 0 1 2 1 0 1 5 5000 1 5	***************************************

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

2018-05-18, 17:54:37+0530

LoaMgr\_Per1



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.24 (Repeat Count = 1)	N .
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inac	ctv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inac	ctv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cn	t_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnE	n_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_en	um	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lg	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnE	En_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_0	Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtr	nEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_C	nt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_e	num	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lg	С	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
target_Rte_Inst_Ap_Loamgr.Loamgr_Perl_HoaDI_Cnt_igc  Name	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value	Expected Value	Result
		Expected Value	Result
Name	Actual Value		Result
Name LoaMgr_AgSV_Cnt_M_lgc	Actual Value	0	~
Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08	Actual Value 0 1	0	,
Name  LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc	Actual Value 0 1	0 1 1	•
Name  LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_HwTqRespSV_Cnt_M_u08	Actual Value 0 1 1 4	0 1 1 4	•
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08	Actual Value  0 1 1 1 4 1 0 5	0 1 1 4 1 0 5	•
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	Actual Value  0 1 1 1 4 1 0 5 5	0 1 1 1 4 1 0 5	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08	Actual Value  0 1 1 1 4 1 0 5	0 1 1 4 1 0 5	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum	Actual Value  0 1 1 1 4 1 0 5 5	0 1 1 1 4 1 0 5	
Name  LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqIdptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrIdptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UIspS_M_f32	Actual Value  0 1 1 1 4 1 0 5 5 5 500 0 5	0 1 1 4 1 0 5 5 5 5 500 0 5	
Name  LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqIdptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrIdptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UIspS_M_f32  LoaMgr_LoaScaleFctr_UIs_M_f32	Actual Value  0 1 1 1 4 1 0 5 5 5 5 0 0	0 1 1 4 1 0 5 5 5 5 500 0 5	
Name  LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqIdptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrIdptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_logc	Actual Value  0 1 1 1 4 1 0 5 5 5 500 0 0 0	0 1 1 4 1 0 5 5 5 5 500 0 0 5	
Name  LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqIdptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrIdptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08	Actual Value  0 1 1 1 4 1 0 5 5 5 5 0 0	0 1 1 4 1 0 5 5 5 5 500 0 5	
Name  LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqIdptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrIdptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	Actual Value  0 1 1 1 4 1 0 5 5 5 500 0 0 5 1 1	0 1 1 4 1 0 5 5 5 5 500 0 0 5 1	
Name  LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqIdptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrIdptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LoASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	Actual Value  0 1 1 1 4 1 0 5 5 5 0 0 0 5 1 0 0 0 5 1 0 0	0 1 1 4 1 0 5 5 5 5 500 0 0 5 5	
Name  LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqIdptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrIdptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LoASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08	Actual Value  0  1  1  4  1  0  5  5  5  0  0  5  1  0  3	0 1 1 1 4 1 0 5 5 5 5 500 0 0 5 5 0 0 0 5	
Name  LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqIdptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrIdptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LoASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	Actual Value  0 1 1 1 4 1 0 5 5 5 0 0 0 5 1 0 3 1	0 1 1 1 4 1 0 5 5 5 5 500 0 0 5 5 0 0 0 5 1	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc	Actual Value  0 1 1 1 4 1 0 5 5 5 0 0 0 5 1 0 3 1 1	0 1 1 1 4 1 0 5 5 5 500 0 0 5 5 0 0 0 5 1 0 0 3 1	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRaleLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldotSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc	Actual Value  0 1 1 1 4 1 0 5 5 5 0 0 0 5 1 1 0 3 1 1 1 0	0 1 1 1 4 1 0 5 5 5 500 0 0 5 5 0 0 0 5 1 0 0 3 1	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VitgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	Actual Value  0 1 1 1 4 1 0 5 5 5 0 0 0 5 1 1 0 3 1 1 1 0 (170)	0 1 1 1 4 1 0 5 5 5 5 500 0 0 5 5 1 0 0 3 1 1 1 0 4 1 1 0 1 1 0 1 1 1 1 1 1 1 1 1	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VitgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	Actual Value  0 1 1 1 4 1 0 5 5 5 500 0 0 5 1 1 0 3 1 1 1 0 {170}	0 1 1 1 4 1 0 5 5 5 500 0 0 5 1 0 0 3 1 1 1 0 {170}	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VitgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	Actual Value  0 1 1 1 4 1 0 5 5 5 5 00 0 0 5 1 1 0 3 1 1 1 0 {170} {1}	0 1 1 1 4 1 0 5 5 5 500 0 0 5 1 0 0 3 1 1 0 {170} {0,0,0,0,0} {1}	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_LOASt_State_M_enum  LoaMgr_LOaSt_State_M_enum  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VitgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	Actual Value  0 1 1 1 4 1 0 5 5 5 5 0 0 0 5 1 1 0 3 1 1 1 0 {170} {1} {1} 500	0 1 1 1 4 1 0 5 5 5 500 0 0 5 1 0 0 3 1 1 1 0 {170} {0,0,0,0,0} {1} 500 ± 0.01	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotOurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	Actual Value  0 1 1 1 4 1 0 5 5 5 5 0 0 0 5 1 1 0 3 1 1 1 0 {170} {1} {1} 500 0	0 1 1 1 4 1 0 5 5 5 500 0 0 5 1 1 0 3 1 1 1 0 {170} {0, 0, 0, 0, 0} {1} 500 ± 0.01 0 ± 0.001	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LOaSt_State_M_enum  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotOurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvaii_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VtgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	Actual Value  0 1 1 1 4 1 0 5 5 5 5 00 0 0 5 1 1 0 3 1 1 1 0 {170} {1} {1} 500 0 1	0 1 1 1 4 1 0 5 5 5 500 0 0 5 1 1 0 3 1 1 0 {170} {0, 0, 0, 0, 0} {1} 500 ± 0.01 0 ± 0.001 1	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	Actual Value  0 1 1 1 4 1 0 5 5 5 5 0 0 0 5 1 1 0 3 1 1 1 0 {170} {1} 500 0 1	0 1 1 1 4 1 0 5 5 5 500 0 0 5 1 1 0 3 1 1 0 {170} {0, 0, 0, 0, 0} {1} 500 ± 0.01 0 ± 0.001 1 0	
Name  LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqIdptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrIdptSigSV_Cnt_M_u08  LoaMgr_IvtrIdptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaScaleFctr_UIs_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgIcoaMtgtnEn_Cnt_M_Igc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_TloaAvail_Cnt_M_Igc  LoaMgr_TloaDi_Cnt_M_Igc  LoaMgr_VtgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UIspS_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value  target_LoaMgr_Per1_LOASt_State_enum.value	Actual Value  0 1 1 1 4 1 0 5 5 5 5 0 0 0 5 1 1 0 3 1 1 1 0 {170} {1} 5 0 0 1 0 5 1 0 5 1 0 0 5 1 0 0 5 1 0 0 5 1 0 0 5 1 0 0 5 1 1 0 5 5 1 1 0 6 1 1 0 5 5 5 6 6 6 7 7 8 7 8 8 8 8 8 8 8 8 8 8 8 8 8	0 1 1 1 4 1 0 5 5 5 500 0 0 5 1 1 0 3 1 1 1 0 {170} {0, 0, 0, 0, 0} {1} 500 ± 0.01 0 ± 0.001 1 0 5	
Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	Actual Value  0 1 1 1 4 1 0 5 5 5 5 0 0 0 5 1 1 0 3 1 1 1 0 {170} {1} 500 0 1	0 1 1 1 4 1 0 5 5 5 500 0 0 5 1 1 0 3 1 1 0 {170} {0, 0, 0, 0, 0} {1} 500 ± 0.01 0 ± 0.001 1 0	



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	~
AssignScale	1	AssignScale	1	<b>✓</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.25 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr MotAgldptSigSV Cnt M u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlytrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
	1
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	251.257599
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	:nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr HwTqldptSigSV Cnt M u08	1	1	<b>~</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	-
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	<b>V</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	-
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	-
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	-
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170}	{170}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1}	{1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~



Test Step 4.26 (Repeat Count = 1)	x
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlytrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	0.0099999978
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k LoaMgr HwTgldptSig1NoTloaResp Cnt u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08 k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k LoaMgr IvtrMtgtnRate UlspS f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	0
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0
target LoaMgr Per1 LoaScaDi Cnt Igc.value	1
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_inst_Ap_LoaMgr.LoaMgr_Per1_ivirLoaMtgtnEn_Cnt_igc target_Rte_inst_Ap_LoaMgr.LoaMgr_Per1_ivirLoaMtgtnEn_Cnt_igc	target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc



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Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174, 175}	{170, 172, 173, 174, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1, 1}	{1, 1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	•
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•

Test Step 4.27 (Repeat Count = 1)		*
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	2	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_lvtrRespSV_Cnt_M_u08	3	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	4	
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

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Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	500		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_UIs_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08 k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0	0.11	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_ut		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_	_igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LoaScaDi_Cot_loc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_(	Cnt lac	
target Rte Inst Ap LoaMgr.LoaMgr Per1 MotCurrldptSig Cnt u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target LoaMgr Per1 MotCurrLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target LoaMgr Per1 MtrPosldptSig Cnt i		
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target_LoaMgr_Per1_SteerMod_Cnt_enun		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaAvail Cnt Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	Result
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	·
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	·
LoaMgr_lvtrRespSV_Cnt_M_u08	3	3	
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr MotAgldptSigSV Cnt M u08	0	0	<b>✓</b>
V			
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

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Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	<b>~</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>~</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.28 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Nama	Input Value		
Name k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	Input Value		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k LoaMgr MotAgMtgtnRate UlspS f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32 k_LoaMgr_RedcdStRate_UlspS_f32	1		
k LoaMgr RedcdStSca Uls f32	1		
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Ina		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Ina		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cn	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnE		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_IvtrLoaMtgtnEn_v target_LoaMgr_Per1_LOASt_State_en		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_loaScaDi_Cnt		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnE		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_t		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtr	_	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_C		
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_e	num	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lg	c	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	<b>~</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	<b>Y</b>
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr LOASt State M enum	5	5	-
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	_
LoaMgr MaxMtgtnResp Cnt M u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	✓
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1} 500	{1, 1} 500 + 0.01	· ·
Rte_Write_Ap_LoaMgr_LoaScaleEctr_UIs_f32(data)	500	500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0 ± 0.001	Ž
target_LoaMgr_Per1_HW1qLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	
0 0			
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	



「est Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.29 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr MotAgldptSigSV Cnt M u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	159.248901
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_ui	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr HwTqRespSV Cnt M u08	0	0	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	-
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	-
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	-
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	-
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1.	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Section   Sect	Test Step 4.30 (Repeat Count = 1)	13
Looking   Ferting order   Looking		
Loader   L		·
Londong   London		2
Leading   Lead	LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
Looking Junifergook Core M. 198 Looking Junifergook Core M. 19	LoaMgr_HwTqRespSV_Cnt_M_u08	2
Leading_Mediaperisty Cort_M_upin   Leading_Mediap	LoaMgr_lvtrldptSigSV_Cnt_M_u08	
Loading MacAgnipering Corpl. Mg   1		
Loading_Monting_cast_Com_M_size   Load		
Loading MacCure Design Cort M 1 100   Loading MacCure Design Cort M 100   Loading M 100   Loadin		
Londing MicroCurred (1985)		
Lookings		
Loading Sentina Nation   Loading		
Loadings_ Institution_Company_Com_M_ 100		
		0
	LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Lacking	Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
	k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
Londry-InvTodictSignTrianAvaiReap_Crt_U08		
K.Lookig/HwTqdxSisgArtDocKig08880		
K_Loskig-HwTqdtgiSpfITth_Cnt_u08		
K. Loahlyr_HviTqAhtginRate_UispS_122		
K. LooMgr - InviriptingSir Resp., Crt. U08		
K_LoaMgr_IntridpSisg2Res_Cnt_U08         0           K_LoaMgr_IntridpSisg2Res_Cnt_U08         0           K_LoaMgr_IntridpSisg2Res_Cnt_U08         0           K_LoaMgr_IntridpSisg2Res_Cnt_U08         5           K_LoaMgr_MortAgipSisg1Res_Cnt_U08         5           K_LoaMgr_MortAgipSisg1Res_Cnt_U08         1           K_LoaMgr_MortAgipSisg1Res_Cnt_U08         0           K_LoaMgr_MortAgipSisg1Res_Cnt_U08         2           K_LoaMgr_MortAgipSisg1Res_Cnt_U08         2           K_LoaMgr_MortAgipSisg1Res_Cnt_U08         2           K_LoaMgr_MortAgipSisg1Res_Cnt_U08         2           K_LoaMgr_MortAgipSisg1Res_Cnt_U08         2           K_LoaMgr_MortAgipSisg2Res_Cnt_U08         2           K_LoaMgr_MortAgipSisg2Res_Cnt_U08         2           K_LoaMgr_MortCurridpSisg1Res_Cnt_U08         2           K_LoaMgr_MortCurridpSisg1Res_Cnt_U08         0           K_LoaMgr_MortCurridpSisg1Res_Cnt_U08         0           K_LoaMgr_MortCurridpSisg1Res_Cnt_U08         0           K_LoaMgr_MortCurridpSisg1Res_Cnt_U08         0           K_LoaMgr_MortCurridpSisg1Res_Cnt_U08         0           K_LoaMgr_MortCurridpSisg1Res_Cnt_U08         0           K_LoaMgr_MortCurridpsisg2Res_Cnt_U08         0           K_LoaMgr_MortCurridpsisg2Res_Cnt_U08		1
k_LoaMgr_IntridptSigPITTRd_Cnt_u08         0           k_LoaMgr_IntridignSacZeroEn_Cnt_lge         1           k_LoaMgr_IntridignSacZeroEn_Cnt_lge         0           k_LoaMgr_IntridignSacZeroEn_Cnt_lge         1           k_LoaMgr_MortAgloptSigPISTMS arResp_Cnt_u08         5           k_LoaMgr_MortAgloptSigPITTen_Cnt_u08         1           k_LoaMgr_MortAgloptSigPITTen_Cnt_u08         2           k_LoaMgr_MortAgloptSigPITTen_Cnt_u08         2           k_LoaMgr_MortAgloptSigPITTen_Cnt_u08         2           k_LoaMgr_MortAgloptSigPITTen_Cnt_u08         2           k_LoaMgr_MortAgloptSigPITTen_Cnt_u08         2           k_LoaMgr_MortAgloptSigPITTen_Cnt_u08         2           k_LoaMgr_MortCurridptSigPITTen_Cnt_u08         2           k_LoaMgr_MortCurridptSigPresp_Cnt_u08         2           k_LoaMgr_MortCurridptSigPresp_Cnt_u08         0           k_LoaMgr_MortCurridptSigPresp_Cnt_u08         0           k_LoaMgr_MortCurridptSigPresp_Cnt_u08         0           k_LoaMgr_MortCurridptSigPresp_Cnt_u08         0           k_LoaMgr_MortCurridptSigPresp_Cnt_u08         1           k_LoaMgr_MortCurridptSigPresp_Cnt_u08         0           k_LoaMgr_MortCurridptSigPresp_Cnt_u08         0           k_LoaMgr_MortCurridptSigPresp_Cnt_u08         1 <tr< td=""><td>k_LoaMgr_lvtrldptSig1Resp_Cnt_u08</td><td>0</td></tr<>	k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_IvhtMigtnRate_UlspS_132	k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MriftginSca_Uis_1S2         1           k_LoaMgr_MrAlginSca_Uis_1S2         1           k_LoaMgr_MoAlgidSis_1SinSmResp_Cnt_u08         5           k_LoaMgr_MoAlgidSis_2Resp_Cnt_u08         1           k_LoaMgr_MoAlgidSis_2Resp_Cnt_u08         0           k_LoaMgr_MoAlgidSis_2Resp_Cnt_u08         2           k_LoaMgr_MoAlgidSis_2Resp_Cnt_u08         2           k_LoaMgr_MoAlgidInsca_Zenc_Cnt_y0         0           k_LoaMgr_MoAlgidInsca_Zenc_Cnt_y0         0           k_LoaMgr_MoAlgidInsca_Uis_1S2         1           k_LoaMgr_MoAlgidInsca_Uis_1S2         1           k_LoaMgr_MoCurridptSigResp_Cnt_u08         2           k_LoaMgr_MoCurridptSigResp_Cnt_u08         2           k_LoaMgr_MoCurridptSigResp_Cnt_u08         0           k_LoaMgr_MoCurridptSigResp_Cnt_u08         0           k_LoaMgr_MoCurridptSigResp_Cnt_u08         0           k_LoaMgr_MoCurridptSigResp_Cnt_u08         0           k_LoaMgr_MoCurridptSigResp_Cnt_u08         1           k_LoaMgr_MoCurridptSigResp_Cnt_u08         1           k_LoaMgr_MoCurridptSigResp_Cnt_u08         1           k_LoaMgr_MoCurridptSigResp_Cnt_u08         1           k_LoaMgr_MoCurridptSigResp_Cnt_u08         1           k_LoaMgr_MoCurridptSigResp_Cnt_u08         1	k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
	k_LoaMgr_IvtrMtgtnRate_UlspS_f32	
k_LoaMgr_MotAgidptSig1NoSmaResp_Cnt_u08         1           k_LoaMgr_MotAgidptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgidptSig2Resp_Cnt_u08         0           k_LoaMgr_MotAgidptSig1Resp_Cnt_u08         2           k_LoaMgr_MotAgidptSig1Resp_Cnt_u08         2           k_LoaMgr_MotAgidptSig2Resp_Cnt_u08         0           k_LoaMgr_MotAgidptSig2Resp_Cnt_u08         2           k_LoaMgr_MotQmSanaVarilEsp_Cnt_u08         2           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_P		
k_LoaMgr_MotAgidptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgidptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgidptSig3Resp_Cnt_u08         2           k_LoaMgr_MotAghtghtsle_UtspS_132         1           k_LoaMgr_MotAghtghtsle_UtspS_132         1           k_LoaMgr_MotAghtghtsle_UtspS_132         1           k_LoaMgr_MotAghtghtsle_Dct_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig		
k_Loakigr_MotAgldptSig3Resp_Cnt_u08         0           k_Loakigr_MotAgldptSig1FiThd_Cnt_u08         2           k_Loakigr_MotAgldptRiafea_Upsbg_f32         1           k_Loakigr_MotAgldptRiafea_Upsbg_f32         1           k_Loakigr_MotAgldptRiafea_Upsg_Cnt_u08         2           k_Loakigr_MotAgldptSigAResp_Cnt_u08         2           k_Loakigr_MotCurridptSig1Resp_Cnt_u08         0           k_Loakigr_MotCurridptSig1Resp_Cnt_u08         1           k_Loakigr_MotCurridptSig2Scate_Cnt_Cnt_u0c         1           k_Loakigr_ReddSiSae_Uss_72         1 </td <td></td> <td></td>		
k_LoaMgr_MotAgldptSigFilTnd_Cnt_u08         2           k_LoaMgr_MotAgMtginState_UlspS_132         1           k_LoaMgr_MotAgMtginScac_Dit_132         0           k_LoaMgr_MotAgMtginScac_Uls_132         1           k_LoaMgr_MotAgMtginScac_Uls_132         1           k_LoaMgr_MotCuridptSigGers_Cnt_u08         2           k_LoaMgr_MotCuridptSigGers_Cnt_u08         0           k_LoaMgr_MotCuridptSigFase_Cnt_u08         0           k_LoaMgr_MotCuridptSigFitTnd_Cnt_u08         0           k_LoaMgr_MotCuridptSigFitTnd_Cnt_u08         0           k_LoaMgr_MotCuridptSigFitTnd_Cnt_u08         0           k_LoaMgr_MotCuridptSigFitTnd_Cnt_u08         0           k_LoaMgr_MotCuridptSigFitTnd_Cnt_u08         0           k_LoaMgr_MotCuridptSigFitTnd_Cnt_u08         1           k_LoaMgr_MotCuridptSigSigTs_Us_5/2         1           k_LoaMgr_RedcdSisCa_Uls_5         122           k_LoaMgr_RedcdSisCa_Uls_5         122           t_LoaMgr_Pert_DiagcStsivtrInactv_Cnt_lgc value         0           target_LoaMgr_Pert_DiagcStsivtrInactv_Cnt_lgc value         1           target_LoaMgr_Pert_MotCuridptSig_Cnt_u08 value         2           target_LoaMgr_Pert_MotCuridptSig_Cnt_u08 value         3           target_LoaMgr_Pert_ToaloaVal_Cnt_lgc value         1		
k_Loakigr_MotAgMtginRate_UispS_f32         1           k_Loakigr_MotAgMtginScaZeroEn_Cn_Ligc         0           k_Loakigr_MotAgMtginSca_Uis_f32         1           k_Loakigr_MotAgMtginSca_Uis_f32         1           k_Loakigr_MotAgSmaAvailResp_Cnt_u08         2           k_Loakigr_MotCurridptSigResp_Cnt_u08         0           k_Loakigr_MotCurridptSigResp_Cnt_u08         1           k_Loakigr_MotCurridptSigResp_Cnt_u08         1           k_Loakigr_MotCurridptSigResp_Cnt_u08         1           k_Loakigr_MotCurridptSigResp_Cnt_u08         1           k_Loakigr_MotCurridptSigr_Cnt_u08         1           k_Loakigr_MotCurridptSigr_Cnt_u08         1           k_Loakigr_MotCurridptSigr_Cnt_u08         1           target_Loakigr_Per1_MotCurridptSigr_Cnt_u08         1           target_Loakig		
k_LoaMgr_MotAgMtgtnSca_ZeroEn_Cnt_Igc         0           k_LoaMgr_MotAgMtgtnSca_Uis_f32         1           k_LoaMgr_MotAgMtgtnSca_Uis_f32         2           k_LoaMgr_MotCurridptSigOResp_Cnt_U08         2           k_LoaMgr_MotCurridptSigSigResp_Cnt_U08         0           k_LoaMgr_MotCurridptSigSigResp_Cnt_U08         0           k_LoaMgr_MotCurridptSigSigResp_Cnt_U08         0           k_LoaMgr_MotCurridptSigSigResp_Cnt_U08         0           k_LoaMgr_MotCurridptSigSigResp_Cnt_U08         0           k_LoaMgr_MotCurridptSigSigResp_Cnt_U08         1           k_LoaMgr_MotCurridptSig_Cnt_U1gSig_Cnt_U08         1           k_LoaMgr_RedGSISca_Uis_F32         1           k_LoaMgr_RedGSISca_Uis_F32         1           k_LoaMgr_Pert_DiagcSistvirtInack_Cnt_Igc_value         0           target_LoaMgr_Pert_DiagcSistvirtClack_Cnt_Igc_value         1           target_LoaMgr_Pert_MotGSig_Cnt_U08 value         2           target_LoaMgr_Pert_MotGridptSig_Cnt_U08 value         1           target_LoaMgr_Pert_MotGridptSig_Cnt_U08 value         3           target_LoaMgr_Pert_MotGridptSig_Cnt_U08 value         1           target_LoaMgr_Pert_ToaAyail_Cnt_Igc_value         1           target_LoaMgr_Pert_ToaAyail_Cnt_Igc_value         1           target_LoaMgr_Pert_ToaAyail_Cnt_Igc_va		
k_LoaMgr_MotAgMtghSca_Uis_f32         1           k_LoaMgr_MotAgSmaAvailResp_Cnt_u08         2           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2All_sig2         1           k_LoaMgr_Per1_DiagStsivtTilactV_Cnt_lgc value         0           target_LoaMgr_Per1_MotCurridptSig_Cnt_u08 value         1           target_LoaMgr_Per1_MrPosidptSig_Cnt_u08 value         1           target_LoaMgr_Per1_		
k_Loaklgr_MotCurridptSigOResp_Cnt_u08         2           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig-Resp_Cnt_u08         1           k_Loaklgr_MotCurridptSig_Cnt_u1g_S         1           k_Loaklgr_MotCurridptSig_Cnt_u5         1           k_Loaklgr_MotCurridptSig_Cnt_u5         1           k_Loaklgr_MotCurridptSig_Cnt_u5         1           k_Loaklgr_MotCurridptSig_Cnt_u5         1           k_Loaklgr_MotCurridptSig_Cnt_u5         1           k_Loaklgr_ReddStRate_UlspS_32         1           k_Loaklgr_ReddStRate_UlspS_32         1           k_Loaklgr_Per1_DiagcStslvtr1inactv_Cnt_lgc_value         0           target_Loaklgr_Per1_DiagcStslvtr2inactv_Cnt_lgc_value         0           target_Loaklgr_Per1_MrPosldptSig_Cnt_u08_value         1           target_Loaklgr_Per1_Tioabl_Cnt_lgc_value         0           target_Loaklgr_Per1_Tioabl_Cnt_lgc_value         0           target_Loaklgr_Per1_Tioabl_Cnt_lgc_value         1           target_Re_Inst_Ap_Loaklgr_Loaklgr_Per1_DiagcStslvtr2inactv_Cnt_lgc		1
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurrldptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurrlMtglnSeate_UlspS_132         1           k_LoaMgr_MotCurrMtglnSea_UlspS_132         1           k_LoaMgr_MotCurrMtglnSea_UlspS_132         1           k_LoaMgr_RedcdStRate_UlspS_132         1           k_LoaMgr_RedcdStSeat_UlspS_132         1           k_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_Igc.value         0           target_LoaMgr_Per1_DiagcStsIvtrZinactv_Cnt_Igc.value         1           target_LoaMgr_Per1_DiagcStsIvtrZinactv_Cnt_Igc.value         2           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 value         2           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 value         1           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 value         3           target_LoaMgr_Per1_SteerMod_Cnt_enun.value         1           target_LoaMgr_Per1_TioaDri_Cnt_Igc.value         0           target_LoaMgr_Per1_TioaDri_Cnt_Igc.value         1           target_LoaMgr_Per1_TioaDri_Cnt_Igc.value         1           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_Igc         target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_Igc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqlofSig_Cnt_u08         target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_Igc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_wTqLoaMtglnEn_Cnt_Igc <td>k_LoaMgr_MotAgSmaAvailResp_Cnt_u08</td> <td>2</td>	k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSigFitThd_Cnt_u08         0           k_Loaklgr_MotCurridptSig_FitThd_Cnt_u08         1           k_Loaklgr_MotCurridptSig_CazeroEn_Cnt_lgc         1           k_Loaklgr_MotCurridptSig_CazeroEn_Cnt_lgc         1           k_Loaklgr_RedddSiRsac_Uls_f32         1           k_Loaklgr_RedddSiRsac_Uls_f32         1           target_Loaklgr_Per1_DiagcStslvtrInactv_Cnt_lgc_value         0           target_Loaklgr_Per1_DiagcStslvtr2inactv_Cnt_lgc_value         1           target_Loaklgr_Per1_HvTqldptSig_Cnt_u08_value         2           target_Loaklgr_Per1_MotCurridptSig_Cnt_u08_value         1           target_Loaklgr_Per1_MrPosidptSig_Cnt_u08_value         3           target_Loaklgr_Per1_TioaAvail_Cnt_lgc_value         1           target_Loaklgr_Per1_TioaAvail_Cnt_lgc_value         1           target_Loaklgr_Per1_TioaAvail_Cnt_lgc_value         1           target_Rel_Inst_Ap_Loaklgr_Loaklgr_Per1_DiagcStslvtrInactv_Cnt_lgc         target_Loaklgr_Per1_DiagcStslvtrInactv_Cnt_lgc           target_Rel_Inst_Ap_Loaklgr_Loaklgr_Per1_HvTqLoaklgtnch_Cnt_lgc         target_Loaklgr_Per1_DiagcStslvtrInactv_Cnt_lgc           target_Rel_Inst_Ap_Loaklgr_Loaklgr_Per1_HvTqLoaklgtnch_Cnt_lgc         target_Loaklgr_Per1_HvTqLoakllgtnch_Cnt_lgc           target_Rel_Inst_Ap_Loaklgr_Per1_HvTqLoakllgtnch_Cnt_lgc	k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_Loaklyr_MotCurridptSigFitThd_Cnt_u08         0           k_Loaklyr_MotCurridptSigFitThd_Cnt_u08         1           k_Loaklyr_MotCurridptSigS_22eoEn_Cnt_lgc         1           k_Loaklyr_MotCurridptSisca_Uls_522         1           k_Loaklyr_MotCurridptSisca_Uls_52         1           k_Loaklyr_RedddSiState_UlspS_f32         1           k_Loaklyr_RedddSiState_UlspS_f32         1           k_Loaklyr_Per1_DiagcStsivtr1lnactv_Cnt_lgc.value         0           target_Loaklyr_Per1_DiagcStsivtr2lnactv_Cnt_lgc.value         1           target_Loaklyr_Per1_HvTqldptSig_Cnt_u08.value         2           target_Loaklyr_Per1_MotCurrldptSig_Cnt_u08.value         1           target_Loaklyr_Per1_MotCurrldptSig_Cnt_u08.value         3           target_Loaklyr_Per1_SteerMod_Cnt_enum.value         1           target_Loaklyr_Per1_ToaAvail_Cnt_lgc.value         0           target_Rel_inst_Ap_Loaklyr_Loaklyr_Per1_DiagcStsivtr1lnactv_Cnt_lgc         target_Loaklyr_Per1_DiagcStsivtr1lnactv_Cnt_lgc           target_Rel_inst_Ap_Loaklyr_Loaklyr_Per1_DiagcStsivtr2lnactv_Cnt_lgc         target_Loaklyr_Per1_DiagcStsivtr2lnactv_Cnt_lgc           target_Rel_inst_Ap_Loaklyr_Loaklyr_Per1_HvTqlaklyfis_Cnt_u08         target_Loaklyr_Per1_HvTqlaklyfis_Cnt_u08           target_Rel_inst_Ap_Loaklyr_Per1_HvTqlaklyfis_Cnt_lgc         target_Loaklyr_Per1_HvTqloaklyfis_Cnt_u08           target_Rel_inst_Ap_Loakl	k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32         1           k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStSca_Uls_f32         1           k_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value         1           target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value         2           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         0           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value         3           target_LoaMgr_Per1_SteerMod_Cnt_enum.value         1           target_LoaMgr_Per1_ToaDvail_Cnt_lgc.value         0           target_LoaMgr_Per1_ToaDvail_Cnt_lgc.value         1           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc           target_LoaMgr_Per1_LoaScaDi_Cnt_lgc </td <td></td> <td></td>		
k_LoaMgr_MotCurrMtgtnSca_Uis_f32 k_LoaMgr_RedcdStRate_UispS_f32 k_LoaMgr_RedcdStRate_UispS_f32 t_LoaMgr_RedcdStSca_Uis_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc_value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc_value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc_value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc_value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc_value target_LoaMgr_Per1_MotCurridptSig_Cnt_u08_value target_LoaMgr_Per1_MotCurridptSig_Cnt_u08_value target_LoaMgr_Per1_MotCurridptSig_Cnt_u08_value target_LoaMgr_Per1_MotCurridptSig_Cnt_u08_value target_LoaMgr_Per1_SteerMod_Cnt_enum_value target_LoaMgr_Per1_TioaDi_Cnt_lgc_value target_LoaMgr_Per1_TioaDi_Cnt_lgc_value target_LoaMgr_Per1_TioaDi_Cnt_lgc_value target_LoaMgr_Per1_TioaDi_Cnt_lgc_value target_LoaMgr_Per1_TioaDi_Cnt_lgc_value target_LoaMgr_Per1_TioaMgr_LoaMgr_Per1_DiagcStslvtr1inactv_Cnt_lgc target_Re_inst_Ap_LoaMgr_Per1_DiagcStslvtr2inactv_Cnt_lgc target_Re_inst_Ap_LoaMgr_Per1_DiagcStslvtr2inactv_Cnt_lgc target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_IoAScaDi_Cnt_lgc target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Re_inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Re_inst_Ap_LoaMgr_Per1_MotAgloaMtgtnEn_Cnt_lgc target_Re_inst_Ap_LoaMgr_Per1_M		
k_LoaMgr_NotCurrNtgtnSca_UIs_132		
k_LoaMgr_RedcdStRate_UlspS_f32		
k_LoaMgr_RedcdStSca_Uls_f32 1 target_LoaMgr_Per1_DiagcStstvtr1Inactv_Cnt_lgc.value 0 target_LoaMgr_Per1_DiagcStstvtr2Inactv_Cnt_lgc.value 1 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 value 2 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 0 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 3 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStstvtr1Inactv_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStstvtr2Inactv_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStstvtr2Inactv_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqlcaMtgtnEn_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_LoaAst_State_enum 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_LoaMgr_Per1_MotAgl_CaMtgtnEn_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_MotAgl_CaMtgtnEn_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_Per1_MotAgl_CaMtgtnEn_Cnt_lgc		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value 2 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_McCurrldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_McCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_McCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_McCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_McCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtne_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtne_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtne_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtne_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtne_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtne_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtne_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtne_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtne_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtne_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtne_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtne_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtne_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtne_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtne_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtne_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtne_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtne_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtne_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtne_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotCurrldptSig_Cnt_Uo8		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_te_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_te_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_te_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum  target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum  target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoASt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_NotCurrldptSig_Cnt_uo8		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaSt_State_enum  target_LoaMgr_Per1_LoaSt_State_enum  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		0
target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaSt_State_enum  target_LoaMgr_Per1_LoaSt_State_enum  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaSt_State_enum  target_LoaMgr_Per1_LoaSt_State_enum  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_NotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_ivtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_ivtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc		
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170}	{170}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1}	{1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>~</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.31 (Repeat Count = 1)		*
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_lvtrRespSV_Cnt_M_u08	4	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	3	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

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LoaMgr\_Per1



			1 12 1 16 16	
Name	Input Value			
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_CurrlytrMtgtnSca_Uls_f32	1			
k_LoaMgr_FadeOutStRate_UlspS_f32	1			
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2			
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4			
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2			
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1			
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0			
k LoaMgr HwTqldptSig4Resp Cnt u08	0			
,				
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08		4		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1			
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1			
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0			
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0			
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1			
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1			
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5			
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1			
k_LoaMgr_MotAgIdptSig3Resp_Cnt_u08	0			
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2			
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1			
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2			
k LoaMgr MotCurrldptSig0Resp Cnt u08	2			
k LoaMgr MotCurrldptSig1Resp Cnt u08	0			
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0			
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1			
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1			
k_LoaMgr_RedcdStRate_UlspS_f32	1			
k_LoaMgr_RedcdStSca_Uls_f32	1			
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	0			
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0			
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4			
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0			
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1			
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1			
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0			
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv	_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv	_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u	108		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_0			
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cni			
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScab1_cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaDl_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_motAgLoaMigtnEn_Cnt_igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08			
target Rte Inst Ap LoaMgr.LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc		_		
		target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_			
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enu	III		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc		target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	0	0	<u> </u>	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•	
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	•	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	•	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•	
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	•	
LoaMgr_LOASt_State_M_enum	5	5	•	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500		
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5		
		1		
LoaMgr_MotAgloptSigSV_Cnt_M_u08	0	0		
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc		T U	· · · · · · · · · · · · · · · · · · ·	
LoaMgr_MotAgRespSV_Cnt_M_u08	3	3		





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.32 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





N	Inner Meline			
Name	Input Value			
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0			
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2			
k LoaMgr MotAgMtgtnRate UlspS f32	1			
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1			
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2			
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2			
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0			
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0			
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1			
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1			
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32	1			
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc.value	0			
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value	0			
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2			
target LoaMgr Per1 LoaScaDi Cnt Igc.value	0			
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2			
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1			
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Ina	actv_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Ina	actv_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LoaScaDi_Cat_			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_l target_LoaMgr_Per1_MotAgLoaMtgtn			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgt	_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_0			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_e	_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc			
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	0	0	~	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	<b>✓</b>	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	✓	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>~</b>	
LoaMgr_lvtrRespSV_Cnt_M_u08	2	2	· ·	
LoaMgr_LoaNt_State_M_enum	5 500	5 500	- J	
LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr LoaScaleFctr Uls M f32	0	0		
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5		
LoaMgr MotAgldptSigSV Cnt M u08	2	2		
LoaMgr MotAgLoaMtgtnEn Cnt M lgc	1	1		
LoaMgr MotAgRespSV Cnt M u08	5	5	<b>✓</b>	
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	1	<b>✓</b>	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>	
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	~	
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~	
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓	
		(4.70)	<b>✓</b>	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170}	{170}		
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	(1)	{0, 0, 0, 0, 0}	×	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	<b>(1) (1)</b>	{0, 0, 0, 0, 0} {1}	**************************************	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	{1} {1} 500	{0, 0, 0, 0, 0} {1} 500 ± 0.01	**************************************	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	(1) {1} 500 0	{0, 0, 0, 0, 0} {1} 500 ± 0.01 0 ± 0.001	× ×	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	(1) {1} 500 0	{0, 0, 0, 0, 0} {1} 500 ± 0.01 0 ± 0.001 0	**************************************	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	(1) (1) 500 0 0	{0, 0, 0, 0, 0} {1} 500 ± 0.01 0 ± 0.001 0	× ×	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	(1) (1) 500 0 0 1 5	{0, 0, 0, 0, 0} {1} 500 ± 0.01 0 ± 0.001 0 1	**************************************	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	(1) (1) 500 0 0	{0, 0, 0, 0, 0} {1} 500 ± 0.01 0 ± 0.001 0	× ×	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	~
AssignScale	1	AssignScale	1	<b>✓</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.33 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	5
LoaMgr_IvtrldptSigSV_Cnt_M_u08	1
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	5
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrivtrMtgtnScaZeroEn Cnt lgc	0
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	1
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1 0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	int lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	0	0	/ v
LoaMgr HwTqldptSiqSV Cnt M u08	1	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	
LoaMgr lvtrldptSigSV Cnt M u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08	1	1	
LoaMgr_LOASt_State_M_enum	5	5	
	500	500	
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	
LoaMgr_LoaScaleFctr_Uls_M_f32	5	5	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	0	0	~
LoaMgr_MotAglaptSigSV_Cnt_M_u08		1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	3	3	~
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	5	5	
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	
LoaMgr_TloaAvail_Cnt_M_lgc			
LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08		-	<b>V</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{170, 172, 173}	{170, 172, 173}	<b>✓</b>
	{1, 1, 1}	{0, 0, 0, 0, 0}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1} 500 + 0.01	· ·
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	•
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	1	1	
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>V</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	<b>Y</b>
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<u> </u>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Contage   Cont	Test Step 4.34 (Repeat Count = 1)	8
Leading   Mariques   December   Mariques   December   Mariques   December   Mariques   December   Mariques   December   Mariques   December		
Leading - Infrarigation Creat No. 196 Leading - Infrarigation Creat No		·
Looking   Jincoping   Looking   Jincoping   Looking   Jincoping		1
Loadey - Merchanger (Lord L.)  Loadey - Loadey - Merchanger (Lord L.)  Loadey - Loadey - Merchanger (Lord L.)  Loadey - Merc	LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
Loading   Mediglapting Cort   M.   196   2   1   1   1   1   1   1   1   1   1	LoaMgr_HwTqRespSV_Cnt_M_u08	0
Loading_ Junkelegosity Cent_M_usis   2		
Looking   Marking principals   Control   Light		
Loading - Manufage and Ingeries - Core - Ly - Ly - Loading - Manufage - Core - Ly - L		
Loading- Mochangesign- Cert M, USB  Loading- Seminobio-power Cert M, USB  Loading- Vigoria-Cert M, USB  Declary- Vigoria-Cert M, USB  Loading- Cert Mochanges- Cert M, USB  Loading- Mochanges-		
Londing / Michard Segret (Londing )		
Leading_LindorTareSetSys_Cor_M_Lings		
Loading-Sentification-Williams   4		
Loadings   Loadings   Loadings		
Reg.   Tel.   Loading   Loading		0
	LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Licable_Lorinfolialistica_colin_Col_Lige	Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
	k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	
Labs/put-Marade/Sign/TransAvaiRese_Crit_U88		
Loading-InvitationSignTranswarinesp. Cnt. U08		
Looking / Hw7iqtsSigAfrape, Crt, U88		
Loakgr_HwTqiqtsSipRifteD_Cmt_u08		
K_LosMg_HwTqlqtSigPiTTMC_Crt_U08		
L. Loading _ Intripolispia Resp. Cnt _ U08		
Loadig_NirdighSigResp_Crd_u08		1
K_Loakly_Intripolisig2Ples_Cnt_U08	k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_ LoaMgr_IndridpSigPITTRE_Cnt_U08         0           k_ LoaMgr_IndridpSigPITTRE_Cnt_U08         1           k_ LoaMgr_IndridpSicaZenoEn_Cnt_Uge         0           k_ LoaMgr_IndridpSicaZenoEn_Cnt_U08         5           k_ LoaMgr_MoNApdipSigNSigNSmaResp_Cnt_U08         1           k_ LoaMgr_MoNApdipSigNSigNSmaResp_Cnt_U08         1           k_ LoaMgr_MoNApdipSigNSigNSmaPesp_Cnt_U08         3           k_ LoaMgr_MoNApdipSigNSigNSmaPesp_Cnt_U08         3           k_ LoaMgr_MoNApdipSigNSmaPesp_Cnt_U08         3           k_ LoaMgr_MoNApdipSigNSmaPesp_Cnt_U08         2           k_ LoaMgr_MoNApdipSigNSmaPesp_Cnt_U08         2           k_ LoaMgr_MoCurridpSigNEasp_Cnt_U08         2           k_ LoaMgr_MoCurridpSigNEasp_Cnt_U08         2           k_ LoaMgr_MoCurridpSigNEasp_Cnt_U08         0	k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
K_LoaMgr_WriMgnRate_UlspS_132	k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0
	k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MorAgidptSig1NoSmaResp_Cnt_u08         1           k_LoaMgr_MorAgidptSig2Resp_Cnt_u08         1           k_LoaMgr_MorAgidptSig2Resp_Cnt_u08         0           k_LoaMgr_MorAgidptSig1Resp_Cnt_u08         3           k_LoaMgr_MorAgidptSig1Resp_Cnt_u08         1           k_LoaMgr_MorAgidptSig2Resp_Cnt_u08         2           k_LoaMgr_MorAgidptSig2Resp_Cnt_u08         2           k_LoaMgr_MorCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MorCurridptSig0Resp_Cnt_u08         0           k_LoaMgr_MorCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_Port_DiagcStstvr1inactv_Cnt_lgc value         0 <t< td=""><td></td><td></td></t<>		
K_LoaMgr_MotAgidptSig2Resp_Cnt_u08		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig1FitTnd_Cnt_u08         3           k_LoaMgr_MotAgldptRiag1FitTnd_Cnt_u08         3           k_LoaMgr_MotAglMgrRate_Ups_572         1           k_LoaMgr_MotAglMgrRate_Ups_572         1           k_LoaMgr_MotAglMgrRate_Ups_572         1           k_LoaMgr_MotAglMgrRate_Ups_572         1           k_LoaMgr_MotAglMgrRate_Ups_572         2           k_LoaMgr_MotCurridptSig1SqResp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1SqResp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1SqResp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1SqResp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1SqResp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1SqResp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Sqresp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Sqresp_Cnt_u08         1           k_LoaMgr_ReddSiSrate_Usp_S 72         1           k_LoaMgr_ReddSiSrate_Usp_S 72         1           k_LoaMgr_ReddSiSrate_Usp_S 72         1           k_LoaMgr_ReddSiSrate_Usp_S 72         1           k_LoaMgr_RedfSiSrate_Usp_S 72         1           k_LoaMgr_RedfSiSrate_Usp_S 72         1           k_LoaMgr_RedfSiSrate_Usp_S 72         1		
k_LoaMgr_MotAgldptSigFilThd_Cnt_u08         3           k_LoaMgr_MotAgMtgtRate_UlspS_132         1           k_LoaMgr_MotAgMtgtRate_UlspS_132         0           k_LoaMgr_MotAgMtgtRate_UlspS_122         1           k_LoaMgr_MotAgMtgtRate_UlspS_Cnt_u08         2           k_LoaMgr_MotCurridptSigTResp_Cnt_u08         2           k_LoaMgr_MotCurridptSigTResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigTResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigTResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigTrbd_Cnt_u08         0           k_LoaMgr_MotCurridptSigTrbd_Cnt_u08         0           k_LoaMgr_MotCurridptSigTrbd_Cnt_u08         1           k_LoaMgr_MotCurridptSigTst LispS_132         1           k_LoaMgr_RedcdSiRae_UlspS_152         1           k_LoaMgr_RedcdSiRae_UlspS_152         1           k_LoaMgr_Pert_DiagcStstvirInactv_Cnt_lgc value         0           target_LoaMgr_Pert_DiagcStstvirInactv_Cnt_lgc value         0           target_LoaMgr_Pert_LoaScaDi_Cnt_u08 value         4           target_LoaMgr_Pert_More_Undrigsig_Cnt_u08 value         1           target_LoaMgr_Pert_SteenMod_Cnt_enum_value         1           target_LoaMgr_Pert_TipLoaAval_Cnt_lgc value         0           target_LoaMgr_Pert_TipLoaAval_Cnt_lgc value         0		
k_Loakigr_MotAghtigtnRate_UispS_f32         1           k_Loakigr_MotAghtigtnSca_Us_f32         1           k_Loakigr_MotAghtigtnSca_Us_f32         1           k_Loakigr_MotAghtigtnSca_Us_f32         1           k_Loakigr_MotAghtigtnSca_Us_f32         2           k_Loakigr_MotCurridptSigtSep_Cnt_u08         2           k_Loakigr_MotCurridptSigtSep_Cnt_u08         0           k_Loakigr_MotCurridptSigtSep_Cnt_u08         0           k_Loakigr_MotCurridptSigtSep_Scn_Cnt_u08         0           k_Loakigr_MotCurridptSigtSep_Scn_Cnt_u08         0           k_Loakigr_MotCurridptSigtSep_Scn_Cnt_u08         1           k_Loakigr_MotCurridptSigtSep_Scn_Cnt_u08         1           k_Loakigr_MotCurridptSigtSep_UispS_f32         1           k_Loakigr_MotCurridptSigtSep_Cn_Cnt_u08         1           k_Loakigr_RedodSiRate_UispS_f32         1           k_Loakigr_RedodSiRate_UispS_f32         1           k_Loakigr_RedodSiRate_UispS_f32         1           k_Loakigr_RedodSiRate_UispS_f32         1           k_Loakigr_Per_Loakigr_Loakigr_Cnt_u08.value         0           target_Loakigr_Per_Loakigr_Loakigr_Cnt_u08.value         0           target_Loakigr_Per_Loakigr_Loakigr_Cnt_u08.value         1           target_Loakigr_Per_Loakigr_Loakigr_Per_Loakigr_Loakigr_Loakigr_Loakigr_Loakigr_P		
k_Loadigr_MotAgMtginSca_ZeroEn_Cnt_lgc         0           k_Loadigr_MotAgMtginSca_Uis_132         1           k_Loadigr_MotAgMtginSca_Uis_132         2           k_Loadigr_MotCurridptSigOResp_Cnt_U08         2           k_Loadigr_MotCurridptSigGResp_Cnt_U08         0           k_Loadigr_MotCurridptSigGResp_Cnt_U08         0           k_Loadigr_MotCurridptSigGResp_Cnt_U08         0           k_Loadigr_MotCurridptSigGResp_Cnt_U08         0           k_Loadigr_MotCurridptSigGResp_Cnt_U08         0           k_Loadigr_MotCurridptSigGresp_Cnt_U08         1           k_Loadigr_MotCurridptSigGresp_Cnt_U08         1           k_Loadigr_MotCurridptSig_Cnt_U182         1           k_Loadigr_RedGSISca_Uis_S122         1           k_Loadigr_RedGSISca_Uis_S122         1           1 arget_Loadigr_Pert_DiagcSistvirtInack_Cnt_lgc_value         0           1 arget_Loadigr_Pert_DiagcSistvirtInack_Cnt_lgc_value         0           1 arget_Loadigr_Pert_MotCurridptSig_Cnt_U08 value         1           1 arget_Loadigr_Pert_MotCurridptSig_Cnt_U08 value         1           1 arget_Loadigr_Pert_MotCurridptSig_Cnt_U08 value         1           1 arget_Loadigr_Pert_MotCurridptSig_Cnt_U08 value         1           1 arget_Loadigr_Pert_Toalo_Lot_lgc_value         0           1 arget_Loadigr_Pert		
k_LoaMgr_MotAgMtginSca_Ulis_f32         1           k_LoaMgr_MotAgSmaAvailResp_Cnt_u08         2           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Acrefn_Cnt_lgc         1           k_LoaMgr_Per1_DiagStsivfrInactv_Cnt_lgc         1           target_LoaMgr_Per1_DiagStsivfrInactv_Cnt_lgc         1           target_LoaMgr_Per1_MotCurridptSig_Cnt_u08 value         1           target_LoaMgr_Per1_MrPosidptSig_Cnt_u08 value         1           target_LoaMgr_Per1_ToaDi_Cnt_lgc value		
k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Rest_UispS_132         1           k_LoaMgr_MotCurrMtgtnSca_Uis_52         1           k_LoaMgr_MotCurrMtgtnSca_Uis_52         1           k_LoaMgr_RedcdStRea_UispS_132         1           k_LoaMgr_RedcdStSca_Uis_52         1           k_LoaMgr_Perd_DiagcStslvtrInactv_Cnt_lgc.value         0           target_LoaMgr_Perl_DiagcStslvtrInactv_Cnt_lgc.value         0           target_LoaMgr_Perl_DiagcStslvtr2inactv_Cnt_lgc.value         0           target_LoaMgr_Perl_MotCurrldptSig_Cnt_u08.value         4           target_LoaMgr_Perl_MotCurrldptSig_Cnt_u08.value         1           target_LoaMgr_Perl_MirPosldptSig_Cnt_u08.value         1           target_LoaMgr_Perl_TioaDi_Cnt_lgc.value         0           target_LoaMgr_Perl_TioaDi_Cnt_lgc.value         0           target_LoaMgr_Perl_TioaDi_Cnt_lgc.value         0           target_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_DiagcStslvtrInactv_Cnt_lgc         target_LoaMgr_Perl_DiagcStslvtrInactv_Cnt_lgc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_DiagcStslvtrInactv_Cnt_lgc         target_LoaMgr_Perl_Mirposltsig_Cnt_u08		1
k_LoaMgr_MolCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MolCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MolCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MolCurridptSigFitThd_Cnt_u08         1           k_LoaMgr_MolCurridptSigSpEitSpS_132         1           k_LoaMgr_MolCurridptSca_Uls_1532         1           k_LoaMgr_RedcdStRate_UlspS_132         1           k_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc.value         0           target_LoaMgr_Per1_MolCurridptSig_Cnt_u08 value         4           target_LoaMgr_Per1_MolCurridptSig_Cnt_u08 value         1           target_LoaMgr_Per1_MolCurridptSig_Cnt_u08 value         1           target_LoaMgr_Per1_SteerMod_Cnt_enun.value         1           target_LoaMgr_Per1_ToaAvail_Cnt_lgc.value         0           target_LoaMgr_Per1_ToaDi_Cnt_lgc.value         0           target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc           target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc           target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_wrtLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_wrtLoaMtgtnEn_Cnt_lgc	k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurrMigtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMigtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMigtnSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           target_LoaMgr_Per1_DiagcStslvtrInactv_Cnt_lgc_value         0           target_LoaMgr_Per1_DiagcStslvtr2inactv_Cnt_lgc_value         0           target_LoaMgr_Per1_MotCurridptSig_Cnt_u08_value         4           target_LoaMgr_Per1_MotCurridptSig_Cnt_u08_value         1           target_LoaMgr_Per1_MrPosldptSig_Cnt_u08_value         1           target_LoaMgr_Per1_MrPosldptSig_Cnt_u08_value         1           target_LoaMgr_Per1_ToaAvail_Cnt_lgc_value         1           target_LoaMgr_Per1_ToaAvail_Cnt_lgc_value         0           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1inactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStslvtr2inactv_Cnt_lgc           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2inactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStslvtr2inactv_Cnt_lgc           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMlgtnEn_Cnt_lgc         target_LoaMgr_Per1_HvTqLoaMlgtnEn_Cnt_lgc           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMlgtnEn_Cnt_lgc         target_	k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_Loaklgr_MotCurridptSigFitThd_Cnt_u08         0           k_Loaklgr_MotCurridptSigFitThd_Cnt_u08         1           k_Loaklgr_MotCurridptSigS_22eoEn_Cnt_lgc         1           k_Loaklgr_MotCurridptSisca_Uls_132         1           k_Loaklgr_RedcdStRate_UlspS_r32         1           k_Loaklgr_RedcdStRate_UlspS_r32         1           k_Loaklgr_Per1_DiagcStsivtr1lnactv_Cnt_lgc.value         0           target_Loaklgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc.value         0           target_Loaklgr_Per1_HvTqldptSig_Cnt_u08.value         4           target_Loaklgr_Per1_MotCurrldptSig_Cnt_u08.value         1           target_Loaklgr_Per1_MotCurrldptSig_Cnt_u08.value         1           target_Loaklgr_Per1_MotCurrldptSig_Cnt_u08.value         1           target_Loaklgr_Per1_SteerMod_Cnt_enum.value         1           target_Loaklgr_Per1_ToaAvail_Cnt_lgc.value         0           target_Rel_Inst_Ap_Loaklgr_Loaklgr_Per1_DiagcStsivtr1lnactv_Cnt_lgc         target_Loaklgr_Per1_DiagcStsivtr1lnactv_Cnt_lgc           target_Rel_Inst_Ap_Loaklgr_Loaklgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc         target_Loaklgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc           target_Rel_Inst_Ap_Loaklgr_Loaklgr_Per1_HvTqldptSig_Cnt_u08         target_Loaklgr_Per1_HvTqldptSig_Cnt_u08           target_Rel_Inst_Ap_Loaklgr_Loaklgr_Per1_HvTqloakltgtnen_Cnt_lgc         target_Loaklgr_Per1_HvTqLoakltgtnen_Cnt_lgc	k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_Loaklyr_MotCurrMtgtnRate_UlspS_f32         1           k_Loaklyr_MotCurrMtgtnScaZeroEn_Cnt_lgc         1           k_Loaklyr_MotCurrMtgtnSca_Uls_f32         1           k_Loaklyr_RedcdStRate_UlspS_f32         1           k_Loaklyr_RedcdStRsca_Uls_f32         1           k_Loaklyr_Perd_DiagcStsIvtrInactv_Cnt_lgc.value         0           target_Loaklyr_Perd_DiagcStsIvtrInactv_Cnt_lgc.value         0           target_Loaklyr_Perd_HwTqldptSig_Cnt_u08.value         4           target_Loaklyr_Perd_MotCurrldptSig_Cnt_u08.value         1           target_Loaklyr_Perd_MotCurrldptSig_Cnt_u08.value         1           target_Loaklyr_Perd_MtPosldptSig_Cnt_u08.value         1           target_Loaklyr_Perd_SteerMod_Cnt_enum.value         1           target_Loaklyr_Perd_ToaAvail_Cnt_lgc.value         0           target_Re_Inst_Ap_Loaklyr_Loaklyr_Perd_DiagcStsIvtrInactv_Cnt_lgc         target_Loaklyr_Perd_DiagcStsIvtrInactv_Cnt_lgc           target_Re_Inst_Ap_Loaklyr_Loaklyr_Perd_DiagcStsIvtrInactv_Cnt_lgc         target_Loaklyr_Perd_DiagcStsIvtrInactv_Cnt_lgc           target_Re_Inst_Ap_Loaklyr_Loaklyr_Perd_HwTqLoakltgtnEn_Cnt_lgc         target_Loaklyr_Perd_HwTqLoakltgtnEn_Cnt_lgc           target_Re_Inst_Ap_Loaklyr_Loaklyr_Perd_Intractor_Cnt_lgc         target_Loaklyr_Perd_HwTqLoakltgtnEn_Cnt_lgc           target_Re_Inst_Ap_Loaklyr_Loaklyr_Perd_Intractor_Cnt_lgc         target_Loaklyr_Perd_Loakl_State_enum		
k_LoaMgr_MotCurrMtgtnSca_Uis_f32 k_LoaMgr_RedcdStRate_UispS_f32 k_LoaMgr_RedcdStSca_Uis_f32 k_LoaMgr_RedcdStSca_Uis_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc_value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc_value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc_value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc_value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc_value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc_value target_LoaMgr_Per1_MotCurridptSig_Cnt_u08_value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08_value target_LoaMgr_Per1_MotCurridptSig_Cnt_u08_value target_LoaMgr_Per1_SteerMod_Cnt_enum_value target_LoaMgr_Per1_TloaAvail_Cnt_lgc_value target_LoaMgr_Per1_TloaAvail_Cnt_lgc_value target_LoaMgr_Per1_TloaDi_Cnt_lgc_value target_LoaMgr_Per1_TloaDi_Cnt_lgc_value target_LoaMgr_Per1_TloaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_Nrt_LoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_IoaScaDi_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_Nrt_OaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurridptSig_Cnt_u08		
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MrPosldptSig_Cnt_u08_value target_LoaMgr_Per1_MrPosldptSig_Cnt_u08_value target_LoaMgr_Per1_MrPosldptSig_Cnt_u08_value target_LoaMgr_Per1_MrPosldptSig_Cnt_u08_value target_LoaMgr_Per1_MrPosldptSig_Cnt_u08_value target_LoaMgr_Per1_SteerMod_Cnt_enum_value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldpSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldpSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldpMgrEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NTLOaMgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_IndaMgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NTLOaMgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NTLOaMgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NTLOaMgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NTLOaMgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NTLOaMgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NTLOaMgtnEn_Cnt_lgc target_LoaMgr_Per1_NTLOaMgr_Per1_NTLOaMgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NTLOaMgtnEn_Cnt_lgc target_LoaMgr_Per1_NTLOaMgr_Ner1_NTLOaMgtnEn_Cnt_lgc target_LoaMgr_Per1_NTLOaMgr_Ner1_NTLOaMgtnEn_Cnt_lgc target_LoaMgr_Per1_NTLOaMgr_Ner1_NTLOaMgtnEn_Cnt_lgc target_LoaMgr_Per1_NTLOaMgr_Ner1_NTLOaMgtnEn_Cnt_lgc target_LoaMgr_Per1_NTLOASCaDi_Cnt_lgc target_LoaMgr_Per1_NTLOASCaDi_Cnt_lgc target_LoaMgr_Per1_NTLOASCaDi_Cnt_lgc target_LoaMgr_Per1_NTLOASCaDi_Cnt_lgc target_LoaMgr_Per1_NTLOASCaDi_Cnt_lgc target_LoaMgr_Per1_NTLOASCADi_Cn		
k_LoaMgr_RedcdStRate_UlspS_132 1  k_LoaMgr_RedcdStSca_Uls_132 1  target_LoaMgr_Per1_DiagcStstvtr1Inactv_Cnt_lgc.value 0  target_LoaMgr_Per1_DiagcStstvtr2Inactv_Cnt_lgc.value 0  target_LoaMgr_Per1_DiagcStstvtr2Inactv_Cnt_lgc.value 0  target_LoaMgr_Per1_LoaScaDl_Cnt_lgc.value 0  target_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value 1  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1  target_LoaMgr_Per1_SterrMod_Cnt_enum.value 1  target_LoaMgr_Per1_SterrMod_Cnt_lgc.value 0  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 0  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1  target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStstvtr1Inactv_Cnt_lgc 1  target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStstvtr2Inactv_Cnt_lgc 1  target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStstvtr2Inactv_Cnt_lgc 1  target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc 1  target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc 1  target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_LvtrLoaMtgtnEn_Cnt_lgc 1  target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_LvtrLoaMtgtnEn_Cnt_lgc 1  target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_LvtrLoaMtgtnEn_Cnt_lgc 1  target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1  target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1  target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1  target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1  target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAg		
k_LoaMgr_RedcdStSca_Uls_f32 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value 0 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value 0 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value 4 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 4 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 0 target_LoaMgr_Per1_MtcUrrldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_DaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_LoAScaDi_Cnt_lgc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 1 target_LoaMgr_Per		
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target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaSt_State_enum  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_IvrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaSt_State_enum  target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_ivtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_ivtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08	3	3	~
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	0	0	•
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.35 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Input Value



Name



k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
	2		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
	0		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08			
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
tornet LeaMay Days DiagoCtalyteQlacety Cat leavalue			
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0 1 1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0 1 1 0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0 1 1 0 1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0 1 1 0 1 0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0 1 1 0 1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	0 1 1 0 1 0	nt_lgc	
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	0 1 1 0 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Ctarget_LoaMgr_Per1_DiagcStsIvtr2Inactv_C	nt_lgc	
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	0 1 1 0 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	nt_lgc	
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target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HvTqldptSigSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08	0 1 1 0 1 0 target_LoaMgr_Per1_DiagcStsivtr1Inactv_C target_LoaMgr_Per1_DiagcStsivtr2Inactv_C target_LoaMgr_Per1_DiagcStsivtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u0& target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgt target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0& target_LoaMgr_Per1_MotCurrldptSig_Cnt_u1gt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cr target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0& target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 1 4 1	nt_lgc t_lgc gc st_lgc 08 Cnt_lgc 18  Expected Value 0 0 1 4 1	· · · · · · · · · · · · · · · · · · ·
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrl.oaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrl.oaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrl.oaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrl.oaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc	0 1 1 0 1 0 target_LoaMgr_Per1_DiagcStsivtr1Inactv_C target_LoaMgr_Per1_DiagcStsivtr2Inactv_C target_LoaMgr_Per1_DiagcStsivtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u0& target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoASt_Diagt_loaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0& target_LoaMgr_Per1_MtrPosidptSig_Cnt_u0& target_LoaMgr_Per1_MtrPosidptSig_Cnt_u0& target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 1 4 1	nt_lgc  t_lgc gc  st_lgc gc st_lgc s	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08	0 1 1 0 1 0 target_LoaMgr_Per1_DiagcStsivtr1Inactv_C target_LoaMgr_Per1_DiagcStsivtr2Inactv_C target_LoaMgr_Per1_DiagcStsivtr2Inactv_C target_LoaMgr_Per1_HwTqloaMtgtnEn_Cn_t target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn_t target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrloptSig_Cnt_uc target_LoaMgr_Per1_MotCurrloaMtgtnEn_C target_LoaMgr_Per1_MtrPosIdptSig_Cnt_uc target_LoaMgr_Per1_MtrPosIdptSig_Cnt_uc target_LoaMgr_Per1_TloaDi_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 1 4 1 0 0	nt_lgc  t_lgc gc  at_lgc gc  at_lgc ga  Expected Value  0 0 1 4 1 0 0	***
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqloaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum	0 1 1 0 1 0 target_LoaMgr_Per1_DiagcStsivtr1Inactv_C target_LoaMgr_Per1_DiagcStsivtr2Inactv_C target_LoaMgr_Per1_DiagcStsivtr2Inactv_C target_LoaMgr_Per1_HwTqlqbtSig_Cnt_u0& target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_uget_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoASt_Diagntgtnen_Crt_uget_LoaMgr_Per1_MotCurrloptSig_Cnt_u0& target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0& target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0& target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 1 4 1 0 0 0 5	nt_lgc  t_lgc gc  at_lgc gc  at_lgc ga  Expected Value  0 0 1 4 1 0 0 5	***
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TioaDi_Cnt_lgc.value target_LoaMgr_Per1_TioaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqloaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	0 1 1 0 1 0 target_LoaMgr_Per1_DiagcStsivtr1Inactv_C target_LoaMgr_Per1_DiagcStsivtr2Inactv_C target_LoaMgr_Per1_DiagcStsivtr2Inactv_C target_LoaMgr_Per1_HwTqloaMtgtnEn_Cn_t target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn_t target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrloptSig_Cnt_uC target_LoaMgr_Per1_MotCurrloaMtgtnEn_C target_LoaMgr_Per1_MtrPosIdptSig_Cnt_uC target_LoaMgr_Per1_MtrPosIdptSig_Cnt_uC target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 1 4 1 0 0 5 5000	Expected Value	***
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqloaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum	0 1 1 0 1 0 target_LoaMgr_Per1_DiagcStsivtr1Inactv_C target_LoaMgr_Per1_DiagcStsivtr2Inactv_C target_LoaMgr_Per1_DiagcStsivtr2Inactv_C target_LoaMgr_Per1_HwTqlqbtSig_Cnt_u0& target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_uget_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoASt_Diagntgtnen_Crt_uget_LoaMgr_Per1_MotCurrloptSig_Cnt_u0& target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0& target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0& target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 1 4 1 0 0 0 5	nt_lgc  t_lgc gc  at_lgc gc  at_lgc ga  Expected Value  0 0 1 4 1 0 0 5	***
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqloaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	0 1 1 0 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotQLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrloaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cttarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Cttarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Cttarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0ttarget_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 1 4 1 0 0 5 5000 1 5	Expected Value	***
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_En_Cnt_M_u08 LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	0 1 1 0 1 0 target_LoaMgr_Per1_DiagcStsivtr1Inactv_C target_LoaMgr_Per1_DiagcStsivtr2Inactv_C target_LoaMgr_Per1_DiagcStsivtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u0& target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrloptSig_Cnt_u0& target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0& target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0& target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaDi_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 1 4 1 0 0 5 5000 1	Expected Value	***
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaNtgtnEn_Cnt_M_lgc LoaMgr_LoaScaleFctr_Uis_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	0 1 1 0 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotQLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrloaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cttarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Cttarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Cttarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0ttarget_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 1 4 1 0 0 5 5000 1 5	Expected Value	***

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc.value	1	1	<b>✓</b>

Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.36 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k LoaMgr RedcdStRate UlspS f32	1		
k LoaMgr RedcdStSca Uls f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	9-	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	<b>Y</b>
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr HwTqRespSV Cnt M u08	2	1 2	-
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	·
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	•
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	<b>Y</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	1	<b>*</b>
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_LOASt_State_enum.value target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	5 0 1	5 0 1	~



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	<b>✓</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

	_
Test Step 4.37 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_IvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1.
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrlvtrMtgtnScaZeroEn Cnt Igc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	2
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lc	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0		
target Rte Inst Ap LoaMgr.LoaMgr Per1 MtrPosldptSig Cnt u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M Igc	1	1	- Nosuit
LoaMgr HwTgldptSigSV Cnt M u08	0	0	-
	0	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	4	4	-
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr lvtrldptSigSV Cnt M u08	1	1	
	1	1	-
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	3	3	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	-
LoaMgr_LOASt_State_M_enum			
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	-
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	<b>V</b>
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	<b>V</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	<b>V</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	<b>Y</b>
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>Y</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	<b>*</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>~</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>✓</b>





Test Step 4.38 (Repeat Count = 1)	x
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlytrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08 k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k LoaMgr IvtrMtgtnRate UlspS f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	0
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	1
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedodStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	0
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3
target LoaMgr Per1 LoaScaDi Cnt Igc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr_Dead_Res_Dead_Res_Rte_Inst_Ap_LoaMgr_Dead_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_Ap_Res_Rte_Inst_A	target_LoaMgr_Per1_DiagcStslvtr1lnactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_Invr_QLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Perl_IntrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc



Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	3	3	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>~</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>✓</b>

Test Step 4.39 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	<del></del>	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enur		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MaxwiginResp_Cnt_iw_u06 LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotAgloptSigSv_Cnt_im_uu8 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_igc	1	1	
LoaMgr_MotAgRespSV Cnt M u08	5	5	
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LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

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Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174, 175}	{170, 172, 173, 174, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1, 1}	{1, 1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.40 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	2
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5



Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0		
k LoaMgr MotCurrMtgtnRate UlspS f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value			
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_loan	ус	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	Kesuit
LoaMgr HwTqldptSigSV Cnt M u08	0	0	_
	0		•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc			
LoaMgr_HwTqRespSV_Cnt_M_u08		0	<b>*</b>
LoaMgr_IvtrldptSigSV_Cnt_M_u08	4	4	•
	4	4	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1 1	1 1	
LoaMgr_lvtrRespSV_Cnt_M_u08	4 1 1 3	4 1 1 3	
LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	4 1 1 3 5	4 1 1 3 5	
LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	4 1 1 3 5 500	4 1 1 3 5 500	
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	4 1 1 3 5 500 0	4 1 1 3 5 500 0	
LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	4 1 1 3 5 500 0 5	4 1 1 3 5 500 0 5	
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	4 1 1 3 5 500 0	4 1 1 3 5 500 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	4 1 1 3 5 500 0 5	4 1 1 3 5 500 0 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	4 1 1 3 5 500 0 5 0	4 1 1 3 5 500 0 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	4 1 1 3 5 500 0 5 0	4 1 1 3 5 500 0 0 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	4 1 1 3 5 5 500 0 5 0 0 5 5	4 1 1 3 5 500 0 0 5 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	4 1 1 3 5 5 500 0 5 0 0 5 0	4 1 1 3 5 5 500 0 0 5 0 0 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	4 1 1 3 5 5 500 0 5 0 0 5 0 0	4 1 1 3 5 5 500 0 0 5 0 0 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaSt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc	4 1 1 3 5 500 0 5 0 0 5 0 0 1 2	4 1 1 3 5 5000 0 5 0 0 5 0 0 1	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	4 1 1 3 5 5 500 0 5 0 0 5 0 0 1 2 0	4 1 1 3 5 5000 0 5 0 0 5 0 0 1 2	
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	4 1 1 3 5 5 500 0 0 5 0 0 5 0 0 1 2 0 0	4 1 1 3 5 5000 0 5 0 0 5 0 1 1 2 0 0	
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaAst_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	4 1 1 3 5 500 0 5 0 0 5 0 1 2 0 0 0 1 2 0 0 1 2 0 0 1 2 0 0 1 1 2 0 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1	4 1 1 3 5 5000 0 5 0 0 5 0 1 2 0 0 0 (1 2 0 0 0 (1 2 0 0 0 1 2 0 0 0 1 1 2 0 0 0 0 0 0 0 0 0 0 0 0 0	
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	4 1 1 3 5 500 0 5 0 0 5 0 1 2 0 0 0 (170, 172, 173, 174, 175) {1, 1, 1, 1, 1}	4 1 1 3 5 500 0 5 0 0 5 0 1 2 0 0 0 {170, 172, 173, 174, 175} {0, 0, 0, 0, 0, 0}	
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaAst_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	4 1 1 3 5 5 500 0 5 0 0 1 2 0 0 {170, 172, 173, 174, 175} {1, 1, 1, 1, 1} {1, 1, 1, 1, 1}	4 1 1 3 5 500 0 5 0 0 0 5 0 0 1 2 0 0 0 {170, 172, 173, 174, 175} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1, 1}	
LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LoaSt_State_M_enum  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgloptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	4 1 1 3 5 5 500 0 5 0 0 5 0 1 2 0 0 {170, 172, 173, 174, 175} {1, 1, 1, 1, 1} {1, 1, 1, 1, 1} 500	4 1 1 3 5 5 500 0 0 5 0 0 1 2 0 0 (170, 172, 173, 174, 175) {0, 0, 0, 0, 0} {1, 1, 1, 1, 1} 500 ± 0.01	
LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LoaSt_State_M_enum  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgloptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	4 1 1 3 5 5 500 0 5 0 0 5 0 1 2 0 0 {170, 172, 173, 174, 175} {1, 1, 1, 1, 1} {1, 1, 1, 1, 1} 500 0	4 1 1 1 3 5 5 500 0 0 5 0 0 1 2 0 0 {170, 172, 173, 174, 175} {0, 0, 0, 0, 0} {1, 1, 1, 1, 1} 500 ± 0.001	
LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	4 1 1 3 5 5 500 0 5 0 0 1 2 0 0 {170, 172, 173, 174, 175} {1, 1, 1, 1, 1} {1, 1, 1, 1, 1} 500 0 0	4 1 1 1 3 5 5 500 0 0 5 0 0 1 2 0 0 {170, 172, 173, 174, 175} {0, 0, 0, 0, 0} {1, 1, 1, 1, 1} 500 ± 0.01 0 ± 0.001	
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_igc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_igc LoaMgr_TloaDi_Cnt_M_igc LoaMgr_VitgMdSrcsV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	4 1 1 3 5 5 500 0 5 0 0 5 0 1 2 0 0 {170, 172, 173, 174, 175} {1, 1, 1, 1, 1, 1} {1, 1, 1, 1, 1} 500 0 1	4 1 1 3 5 5 500 0 5 0 0 5 0 1 2 0 0 {170, 172, 173, 174, 175} (0, 0, 0, 0, 0) {1, 1, 1, 1, 1} 500 ± 0.01 0 1	
LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value  target_LoaMgr_Per1_lotrLoaMtgtnEn_Cnt_lgc.value  target_LoaMgr_Per1_LOASt_State_enum.value	4 1 1 3 5 5 500 0 5 0 0 5 0 1 2 0 0 {170, 172, 173, 174, 175} {1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1	4 1 1 3 5 5 500 0 0 5 0 0 1 2 0 0 {170, 172, 173, 174, 175} (0, 0, 0, 0, 0) {1, 1, 1, 1, 1, 1} 500 ± 0.01 0 1 5	
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_igc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_igc LoaMgr_TloaDi_Cnt_M_igc LoaMgr_VitgMdSrcsV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	4 1 1 3 5 5 500 0 5 0 0 5 0 1 2 0 0 {170, 172, 173, 174, 175} {1, 1, 1, 1, 1, 1} {1, 1, 1, 1, 1} 500 0 1	4 1 1 3 5 5 500 0 5 0 0 5 0 1 2 0 0 {170, 172, 173, 174, 175} (0, 0, 0, 0, 0) {1, 1, 1, 1, 1} 500 ± 0.01 0 1	



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>~</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	•
AssignScale	1	AssignScale	1	<b>~</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.41 (Repeat Count = 1)	The state of the s
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr MotAgldptSigSV Cnt M u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
	1
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	1
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1 0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	1
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u(	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0	Ont Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn	Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enun		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>~</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	3	3	
LoaMgr_LOASt_State_M_enum	5	5	<b>~</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	<b>~</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	•
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174, 175}	{170, 172, 173, 174, 175}	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1, 1}	{1, 1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target LoaMgr Per1 MotAgLoaMtgtnEn Cnt Igc.value	0	0	•
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
		·	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 4.42 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRcaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlytrMtgtnSca_UIs_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0 0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08 k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0
k_LoaMgr_MotCurridptSig0Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0
k LoaMgr MotCurrMtgtnRate UlspS f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k LoaMgr MotCurrMtgtnSca Uls f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1 0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	2	2	~
LoaMgr_LOASt_State_M_enum	4	4	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	4	4	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	3	3	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{171, 172, 173}	{171, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	4	4	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.43 (Repeat Count = 1)	<b>2</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	5
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	5
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k LoaMgr HwTqMtgtnRate UlspS f32	1		
k_LoaMgr_HwTqMtgtnSca_UIs_f32	1		
k LoaMgr lvtrldptSig1Resp Cnt u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	5		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0		
k_LoaMgr_MotCurridptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08 k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0	2.1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_DiagcStslvtr2Inactv_ target_LoaMgr_Per1_HwTqldptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target LoaMgr Per1 HwTqLoaMtgtnEn C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target LoaMgr Per1 lvtrLoaMtgtnEn Cnt		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target LoaMgr Per1 LOASt State enum	.90	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0	:nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	5	5	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	<b>~</b>
LoaMgr_LOASt_State_M_enum		5	
LoaMar LoaDateLimit IllenS M f22	5		
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	500	500	~
LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	500 1 5	500 1 5	, , , , , , , , , , , , , , , , , , ,
LoaMgr_LoaScaleFctr_Uls_M_f32	500	500	~

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 175}	{170, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.44 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k LoaMgr MotAgMtgtnSca Uls f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSig2Resp Cnt u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqIdptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	ıt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cl	nt lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	J8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr HwTqldptSigSV Cnt M u08	1	1	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	<b>~</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_lvtrRespSV_Cnt_M_u08	1	1	
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	-
LoaMgr_MotAgRespSV_Cnt_M_u08	3	3	<b>✓</b>
LoaMgr MotCurrldptSigSV Cnt M u08	1	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>~</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	
	1	1	
LoaMgr_TloaAvail_Cnt_M_lgc			
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>~</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	V
	0	0	-
target LoaMgr Per1 lytrLoaMtgtnEn Cnt lgc.value		1.5	_
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	5	5	-
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>V</b>
	5 1 1	5 1 1	<b>V</b>



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>~</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	<b>~</b>
AssignScale	1	AssignScale	1	<b>✓</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.45 (Repeat Count = 1)	i de la companya de
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	0
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Ci	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	-	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target LoaMgr Per1 SteerMod Cnt enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	1	1	/ v
LoaMgr HwTqldptSiqSV Cnt M u08	0	0	
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr lytrldptSigSV Cnt M u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	
	500	500	
LoaMgr_LoaRateLimit_UlspS_M_f32	0	0	
LoaMgr_LoaScaleFctr_Uls_M_f32	5	5	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	1	1	~
LoaMgr_MotAglaptSigSV_Cnt_M_u08		1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	2	2	
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	
LoaMgr_TloaAvail_Cnt_M_lgc			
LoaMgr_TloaDi_Cnt_M_lgc	1	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	(470, 470, 475)	· .	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{170, 172, 175}	{170, 172, 175}	<b>✓</b>
	{1, 1, 1}	{0, 0, 0, 0, 0}	· ·
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>V</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	· ·
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	0	0	
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>V</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	<b>V</b>
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>✓</b>



Name	Test Step 4.46 (Repeat Count = 1)	
Loadings   Mary   Loadings   Lo		
Looking_ Invirtigationspace_Cont_M_L08		·
Landage Junifragologico, Com, July Jan Landage Junifragology, Com, July Jan Landage J		
Loading   Jave   Jave   Loading		
Loading   Michaeling Co.   M.   103   1   1   1   1   1   1   1   1   1		
Colangs   Technologisty Col. M. 108   Colangs   Technologisty Col. M. 108   Colangs   Technologisty Col. M. 108   Colangs   Technologisty Col. M. 109   Technologisty Co		1
Looking_Mode)gest(StyCon_M_106		0
Loading Journal Control of the Contr	LoaMgr_lvtrRespSV_Cnt_M_u08	3
Loading InfoContrigitacy Cost M	LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
Loading Michael and Supplies On 1 Loading Michael and Supplies On 1 Loading Michael and Supplies On 1 Loading	LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
Loading_ InfoCompany		
Loading Smith Modes   Marie		
Loading   Semination Ayun   Loading   Loadin		
Loading / Load		
Reg.   Imal Ap.   Loadley	· - · · ·	
		V = V
KLOMAGy I-MYTQIDISQYTTOAAVAIRESP, CMTLUBS   2		
K_LOMING_PM*CIGNSG*PICADRERSE_CRL_U08		2
		1
Loadigr_HviTqbqSigPiTRid_Cri_u08	k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
	k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
K, LoaMgr_HwTqMtgnSca_Uis_128         1           K, LoaMgr_HwTqMtgnSca_Pcn_U08         0           K, LoaMgr_HwTqMtgnSca_Pcn_U08         0           K, LoaMgr_HwTqMtgnSca_Pcn_Cn_U08         0           K, LoaMgr_HwTqmtgnSca_Pcn_Cn_U08         0           K, LoaMgr_HwTqmtgnSca_Pcn_Cn_U1gc         0           K, LoaMgr_MwTqdtgnStgnTexp_Cn_U08         5           K, LoaMgr_MwTqdtgnStgnTexp_Cn_U08         5           K, LoaMgr_MwTqdtgnStgnStexp_Cn_U08         1           K, LoaMgr_MwTqdtgnStgnStexp_Cn_U08         0           K, LoaMgr_MwTqdtgnStgnStexp_Cn_U08         2           K, LoaMgr_MwTqdtgnStgnStexp_Cn_U08         2           K, LoaMgr_MwTqdtgnStgnStexp_Cn_U1ge         0           K, LoaMgr_MwTqdtgnTsca_Pcn_Cn_U1ge         0           K, LoaMgr_MwCourtqtstsignTexp_Cn_U1ge         0           K, LoaMgr_MwCourtqtstsignTexp_Cn_U1ge         0           K, LoaMgr_MwCourtqtstsignTexp_Cn_U1ge         0           K, LoaMgr_MwCourtqtstsignTexp_Cn_U1ge         0           K, LoaMgr_MwCourtqtstpSca_U1ge_Stg_2 <t< td=""><td>k_LoaMgr_HwTqldptSigFltThd_Cnt_u08</td><td></td></t<>	k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
K_LoaMgr_IviridpiSig2RResp_Cnt_u08         0           K_LoaMgr_IviridpiSig2RResp_Cnt_u08         0           K_LoaMgr_IviridpiSig2Resp_Cnt_u08         1           K_LoaMgr_IviridpiSac_ulis_132         1           K_LoaMgr_MortAgitpiSig1NesmaResp_Cnt_u08         5           K_LoaMgr_MortAgitpiSig1Resp_Cnt_u08         1           K_LoaMgr_MortAgitpiSig1Resp_Cnt_u08         0           K_LoaMgr_MortAgitpiSig1Resp_Cnt_u08         2           K_LoaMgr_MortAgitpiSig1Resp_Cnt_u08         2           K_LoaMgr_MortAgitpiSig1Resp_Cnt_u08         2           K_LoaMgr_MortAgitpiSig2Resp_Cnt_u08         2           K_LoaMgr_MortAgitg1Resp_Cnt_u08         2           K_LoaMgr_MortAgitg1Resp_Cnt_u08         2           K_LoaMgr_MortCurridptSig1Resp_Cnt_u08         2           K_LoaMgr_MortCurridptSig1Resp_Cnt_u08         2           K_LoaMgr_MortCurridptSig2Resp_Cnt_u08         0           K_LoaMgr_MortCurridptSig1Resp_Cnt_u08         0           K_LoaMgr_MortCurridptSig2Resp_Cnt_u08         0           K_LoaMgr_MortCurridptSig2Resp_Cnt_u08         0           K_LoaMgr_MortCurridptSig2Resp_Cnt_u08         0           K_LoaMgr_MortCurridptSig2Resp_Cnt_u08         0           K_LoaMgr_MortCurridptSig2Resp_Cnt_u08         0           K_LoaMgr_MortCur		
k_LoaMgr_IvirtigniSalp/Itind_Cn_U08         0           k_LoaMgr_IvirtigniRale_UispS_132         1           k_LoaMgr_IvirtigniSalp/IndigniSalp/Iti		
K_Loaklgr_IvfMighnRate_UispS_R2		
K_LoaMgr_IvhfMghSsazeroEn_Cnt_lgs		
k_LoaMgr_Vrt/MigtnSca_Uls_f32         1           k_LoaMgr_MotAgloptSig/Resp_Cnt_u08         5           k_LoaMgr_MotAgloptSig/Resp_Cnt_u08         1           k_LoaMgr_MotAgloptSig/Resp_Cnt_u08         0           k_LoaMgr_MotAgloptSig/FitThc_Cnt_u08         2           k_LoaMgr_MotAgloptSig-FitThc_Cnt_u08         2           k_LoaMgr_MotAgloptSig-FitThc_Cnt_u08         1           k_LoaMgr_MotAgloptSig-Sig_Uls_f32         1           k_LoaMgr_MotAgloptSig-Resp_Cnt_u08         2           k_LoaMgr_MotAgloptSig-Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig-Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig-Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig-Sig-Sig-Sig_2         1           k_LoaMgr_MotCurridptSig-Sig-Sig_2         1           k_LoaMgr_Pot-Diag-Sist-Sig_Sig_2         1           k_LoaMgr_Pot-Diag-Sist-Sig_2         1           k_LoaMgr_Pot-Diag-Sist-Sig_2		
k_Loaklgr_MotAgldptSig1NoSmaResp_Cnt_u08         1           k_Loaklgr_MotAgldptSig2Resp_Cnt_u08         1           k_Loaklgr_MotAgldptSig2Resp_Cnt_u08         0           k_Loaklgr_MotAgldptSig1FitTnd_Cnt_u08         2           k_Loaklgr_MotAgldptSig1FitTnd_Cnt_u08         2           k_Loaklgr_MotAgldptSig2FitTnd_Cnt_u08         0           k_Loaklgr_MotAgldptSig2Resp_Cnt_u08         2           k_Loaklgr_MotAghtIgnSca_Uls_f32         1           k_Loaklgr_MotCurridptSig0Resp_Cnt_u08         2           k_Loaklgr_MotCurridptSig0Resp_Cnt_u08         2           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         1           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         1           k_Loaklgr_ReddSiSca_uls_f32         1           k_Loaklgr_ReddSiSca_uls_f32         1           k_Loaklgr_ReddSiSca_uls_f32         1           k_Loaklgr_ReddSi		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08         1           k_LoaMgr_MotAgldptSig3Resp_Cnt_u08         0           k_LoaMgr_MotAgldptSig1Resp_Cnt_u08         2           k_LoaMgr_MotAgMtgnRate_UispS_f32         1           k_LoaMgr_MotAgMtgnRate_UispS_f32         1           k_LoaMgr_MotAgMtgnRsca_Uis_f32         1           k_LoaMgr_MotAgSmaNavilResp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Cnt_u08         1           k_LoaMgr_ReddSiRate_Uisp_S_132         1           k_LoaMgr_ReddSiRate_Uisp_S_132         1           k_LoaMgr_Pert_DiagcStslvtrInact_Cnt_lgc-value         1           target_LoaMgr_Pert_DiagcStslvtrInact_Cnt_lgc-value         1           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value         1           targ		
k_Loaklgr_MotAgldptSig3Resp_Cnt_u08         2           k_Loaklgr_MotAgldptSig4FiTh_d_Cnt_u08         2           k_Loaklgr_MotAgltptRate_Usbg_5t22         1           k_Loaklgr_MotAgltptRate_Usbg_5t22         0           k_Loaklgr_MotAgltptRate_Usbg_5t22         1           k_Loaklgr_MotAgsma-AvailResp_Cnt_u08         2           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         2           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         1           k_Loaklgr_RedGSIStag_Usb_5t32         1           k_Loaklgr_RedGSIStag_Usb_5t32         1           k_Loaklgr_RedGSIStag_Usb_5t32         1           k_Loaklgr_RedGSIStag_Usb_5t32         1           target_Loaklgr_Perl_DiagcStstvtrInactv_Cnt_lgc_value         1           target_Loaklgr_Perl_LoaScaDL_Cnt_lgc_value         1           targ		
k_LoaMgr_MotAgMtgnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgnRate_UlspS_f32         0           k_LoaMgr_MotAgMtgnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgnRate_UlspS_f32         1           k_LoaMgr_MotAgMtgnRate_UlspS_f0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptsigNesp_Cnt_u08         0           k_LoaMgr_MotCurridptsigFitThd_Cnt_u08         0           k_LoaMgr_MotCurridptsigFitThd_Cnt_u08         0           k_LoaMgr_MotCurridtgnRate_UlspS_f32         1           k_LoaMgr_MotCurridtgnSac_ZeroEn_Cnt_u1gc         1           k_LoaMgr_MotCurridtgnSac_ZeroEn_Cnt_u1gc         1           k_LoaMgr_MotCurridtgnSac_ZeroEn_Cnt_u1gc         1           k_LoaMgr_RedcdSiRate_UlspS_f32         1           k_LoaMgr_RedcdSiRate_UlspS_f32         1           k_LoaMgr_RedcdSiSac_Uls_f32         1           target_LoaMgr_Per1_DiagcSistvrtInactv_Cnt_lgc_value         1           target_LoaMgr_Per1_DiagcSistvrtInactv_Cnt_lgc_value         1           target_LoaMgr_Per1_LoaScaD_Cnt_lgc_value         1           target_LoaMgr_Per1_LoaScaD_Cnt_lgc_value         1           target_LoaMgr_Per1_MotCurridptsig_Cnt_u08 value         3           target_LoaMgr_Per1_SteerMod_Cnt_enum_value         0           target_LoaMgr_Per1_SteerMod_Cnt_enum_value		0
k_Loaklgr_MotAgMignSca_ZeroEn_Cnt_lgc         0           k_Loaklgr_MotAgMignSca_Uis_f32         1           k_Loaklgr_MotCurridptSignResp_Cnt_u08         2           k_Loaklgr_MotCurridptSignResp_Cnt_u08         2           k_Loaklgr_MotCurridptSignResp_Cnt_u08         0           k_Loaklgr_MotCurridptSignResp_Cnt_u08         1           k_Loaklgr_Pert_DiagcStsivtrilnactv_Cnt_lgc_value         1           target_Loaklgr_Pert_HT_HTQlptSig_Cnt_u08 value         1           target_Loaklgr_Pert_MotCurtdptSig_Cnt_u08 value         0           target_Loaklgr_Pert_MotCurtdptSig_Cnt_u08 value	k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_Loaklgr_MotAgMiginSca_Uls_132         1           k_Loaklgr_MotAgSmaAvailResp_Cnt_u08         2           k_Loaklgr_MotCurridptSigOResp_Cnt_u08         0           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig1Resp_UispS_132         1           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         1           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         1           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         1           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         1           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         1           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         1           k_Loaklgr_Resp_Cnt_u08         1           k_Loaklgr_Resp_Cnt_u08         1           k_Loaklgr_Resp_Cnt_u08         1           k_Loaklgr_Resp_Cnt_u08         1           target_Loaklgr_Per1_DiagcStstvirtInactv_Cnt_lgc value         1           target_Loaklgr_Per1_MrPostqtsig_Cnt_u08 value         3           target_Loaklgr_Per1_Tioabri_Cnt_lgc_value         0           target_Loaklgr_Per1_Tioabri_Cnt_lgc_value         0           target_Loak	k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_Loaklgr_MotAgSmaAvailResp_Cnt_u08         2           k_Loaklgr_MotCurrldptSigOResp_Cnt_u08         2           k_Loaklgr_MotCurrldptSigTesp_Cnt_u08         0           k_Loaklgr_MotCurrldptSigFitThd_Cnt_u08         0           k_Loaklgr_MotCurrldptSigFitThd_Cnt_u08         0           k_Loaklgr_MotCurrldptSigFitThd_Cnt_u08         0           k_Loaklgr_MotCurrldptSigFitThd_Cnt_u08         0           k_Loaklgr_MotCurrldptSig-ZeroEn_Cnt_lgc         1           k_Loaklgr_MotCurrldptSig-ZeroEn_Cnt_lgc         1           k_Loaklgr_MotCurrldptSig-ZeroEn_Cnt_lgc         1           k_Loaklgr_MotCurrldptSig-ZeroEn_Cnt_lgc         1           k_Loaklgr_MotCurrldptSig-ZeroEn_Cnt_lgc         1           k_Loaklgr_MotCurrldptSig-ZeroEn_Cnt_lgc         1           k_Loaklgr_ReddSiSca_Uls_132         1           target_Loaklgr_ReddSiSca_Uls_132         1           target_Loaklgr_Per1_DiagcStstvtrZinactv_Cnt_lgc value         1           target_Loaklgr_Per1_DiagcStstvtrZinactv_Cnt_lgc value         4           target_Loaklgr_Per1_MirpotlptSig_Cnt_u08 value         3           target_Loaklgr_Per1_Tioab/Cnt_lgc value         0           target_Loaklgr_Per1_Tioab/Cnt_lgc value         0           target_Loaklgr_Per1_Tioab/Cnt_lgc_value         0           target_Re_Inst_Ap_Loaklgr	k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1FitTnd_Cnt_u08         0           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStSca_Uls_f32         1           k_LoaMgr_Pert_DiagcStstvtrInactv_Cnt_lgc.value         1           target_LoaMgr_Pert_DiagcStstvtrInactv_Cnt_lgc.value         1           target_LoaMgr_Pert_HarTqldptSig_Cnt_u08 value         4           target_LoaMgr_Pert_MotCurrldptSig_Cnt_u08 value         3           target_LoaMgr_Pert_MitPosldptSig_Cnt_u08 value         3           target_LoaMgr_Pert_MitPosldptSig_Cnt_u08 value         3           target_LoaMgr_Pert_MitPosldptSig_Cnt_u08 value         3           target_LoaMgr_Pert_TloaDi_Cnt_lgc.value         0           target_LoaMgr_Pert_TloaDi_Cnt_lgc.value         0           target_LoaMgr_Pert_TloaDi_Cnt_lgc.value         0           target_LoaMgr_Pert_TloaDi_Cnt_lgc.value         0           target_LoaMgr_Pert_DiagcStstvtrIn	k_LoaMgr_MotAgMtgtnSca_UIs_f32	
k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurrMtgtsigFitTnd_Cnt_u08         0           k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_u05         1           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdSiRste_UlspS_f32         1           k_LoaMgr_RedcdSiSca_Uls_f32         1           k_LoaMgr_RedcdSiSca_Uls_f32         1           k_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value         1           target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value         1           target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value         4           target_LoaMgr_Per1_MrTqdptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value         3           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value         3           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value         3           target_LoaMgr_Per1_ToaDval_Cnt_lgc.value         0           target_LoaMgr_Per1_ToaDval_Cnt_lgc.value         0           target_LoaMgr_Per1_ToaDval_Cnt_lgc.value         0           target_LoaMgr_Per1_ToaDval_Cnt_lgc.value         0           target_LoaMgr_Per1_ToaDval_Cnt_lgc.value         0           target_LoaMgr_Per1_ToaDval_Cnt_lgc.value         0           target_LoaMgr_Per1_T		
k_LoaMgr_MotCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurrMtgtnRate_UlspS_132         1           k_LoaMgr_MotCurrMtgtnSca_ZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMtgtnSca_UlspS_132         1           k_LoaMgr_RedcdStRate_UlspS_132         1           k_LoaMgr_RedcdStSca_Uls_132         1           k_LoaMgr_Per1_DiagCstStVtr1Inactv_Cnt_lgc.value         1           target_LoaMgr_Per1_DiagCstStVtr2Inactv_Cnt_lgc.value         1           target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value         4           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         3           target_LoaMgr_Per1_MtPostdptSig_Cnt_u08.value         3           target_LoaMgr_Per1_TloaDc_Cnt_lgc.value         0           target_LoaMgr_Per1_TloaDc_Cnt_lgc.value         0           target_LoaMgr_Per1_TloaDc_Cnt_lgc.value         0           target_LoaMgr_Per1_TloaDc_Cnt_lgc.value         0           target_Re_Inst_Ap_LoaMgr_Per1_Per1_DiagcStsivtr2Inactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08         target_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc         target_LoaMgr_Per1_HwTqldptSi		
k_LoaMgr_MotCurrNtgtnScaZeroEn_Cnt_lgc		
k_LoaMgr_MotCurrMtgtmRate_UlspS_f32       1         k_LoaMgr_MotCurrMtgtmSca_Uls_f32       1         k_LoaMgr_RedcdStRate_UlspS_f32       1         k_LoaMgr_RedcdStSca_Uls_f32       1         k_LoaMgr_Perd_DiagcStslvtr1lnactv_Cnt_lgc.value       1         target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc.value       1         target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc.value       1         target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value       4         target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 value       0         target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value       3         target_LoaMgr_Per1_SteerMod_Cnt_enum.value       0         target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value       0         target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value       0         target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1lnactv_Cnt_lgc       target_LoaMgr_Per1_DiagcStslvtr1lnactv_Cnt_lgc         target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc       target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc         target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTQloaMtgtnEn_Cnt_lgc       target_LoaMgr_Per1_HwTqlofptSig_Cnt_u08         target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTQLoaMtgtnEn_Cnt_lgc       target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc         target_Ret_Inst_Ap_LoaMgr_Per1_HwTQLoaMtgtnEn_Cnt_lgc       target_LoaMgr_Per1_HvTQloaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_LoaMgr_LoaMgr_		
k_LoaMgr_MotCurrMigtnSca_Vis_f32		
k_LoaMgr_MclCurrMtgtnSca_Uls_f32		
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_TioaAvail_Cnt_lgc.value target_LoaMgr_Per1_TioaDi_Cnt_lgc.value target_LoaMgr_Per1_TioaDi_Cnt_lgc.value target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_wTqLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_wTqLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_wTqLoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_LoASt_State_enum target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
k_LoaMgr_RedcdStSca_Uls_f32  target_LoaMgr_Per1_DiagcStslvtr1lnactv_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc.value  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enun.value  0  target_LoaMgr_Per1_SteerMod_Cnt_enun.value  0  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  0  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  1  target_LoaMgr_Per1_DiagcStslvtr1lnactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value 1 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 3 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value 0 target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc		1
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LOASt_State_enum  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1lnactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaSt_State_enum  target_LoaMgr_Per1_LoaSt_State_enum  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaSt_State_enum  target_LoaMgr_Per1_LoaSt_State_enum  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaSt_State_enum  target_LoaMgr_Per1_LoaSt_State_enum  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaSt_State_enum  target_LoaMgr_Per1_LoaSt_State_enum  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc		
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc		



Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 174, 175}	{170, 174, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•

Test Step 4.47 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Input Value



Name



Name	Input value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k LoaMgr HwTqMtgtnRate UlspS f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
	0		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08			
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	3		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc.value	1		
	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value			
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt	_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enur		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc	 	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
		Expected Volume	Desuit
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	-
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	•
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	•
	500		
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>✓</b>
		1 5	<b>*</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 5	5	
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	1		~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 5 0	5	·

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 174, 175}	{170, 174, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.48 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqIdptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum	1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	<b>_</b>
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	3	3	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	· ·
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>V</b>
LoaMgr_IvtrRespSV_Cnt_M_u08	3	3	<b>V</b>
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	✓ ✓
LoaMgr_LoaScaleFctr_Uls_M_f32	1	5	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5 2	2	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	-
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	-
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	-
LoaMgr_TloaDi_Cnt_M_lgc	1	1	-
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	[,,]	{0, 0, 0, 0, 0}	*
	{1, 1, 1}		
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	{1, 1, 1} {1, 1, 1}		
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte Write Ap LoaMgr LoaRateLimit UlspS f32(data)	{1, 1, 1} {1, 1, 1} 500	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	{1, 1, 1}		~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	{1, 1, 1} 500	{1, 1, 1} 500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	{1, 1, 1} 500 1	{1, 1, 1} 500 ± 0.01 1 ± 0.001	<b>*</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	{1, 1, 1} 500 1 0	{1, 1, 1} 500 ± 0.01 1 ± 0.001	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	{1, 1, 1} 500 1 0	{1, 1, 1} 500 ± 0.01 1 ± 0.001 0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	{1, 1, 1} 500 1 0 1 5	{1, 1, 1} 500 ± 0.01 1 ± 0.001 0 1 5	· · · · · · · · · · · · · · · · · · ·



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.49 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrlvtrMtgtnScaZeroEn Cnt Igc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	1
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	5
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_	.lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u		
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target LoaMgr Per1 SteerMod Cnt enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M Igc	1	1	/ (Count
LoaMgr HwTqldptSiqSV Cnt M u08	1	1	_
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	-
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	·
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	_
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	-
LoaMgr_TloaDi_Cnt_M_lgc	1	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	-
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 173, 174}	{170, 173, 174}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_endin)	{1, 1, 1}	{0, 0, 0, 0, 0}	<b>X</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)		{1, 1, 1}	
Rte Write Ap LoaMgr LoaRateLimit UlspS f32(data)	{1, 1, 1} 500	500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_hw1qLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	
target LoaMgr Per1 LOASt State enum.value	5	5	
· - ·	1	1	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value		1	-
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	<b>✓</b>
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~



Test Step 4.50 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32 k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	1 0
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1 0
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k LoaMgr MotAgldptSig3Resp Cnt u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32 k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_tvtLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaSt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_Loascabi_Cht_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
2 2 2 1 2 1 2 1 2 1 2 1 2 1 2 1 2 1 2	, 5 = 5 = 5 = 5 = 5 = 5



Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotAgRespSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172}	{170, 172}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.51 (Repeat Count = 1)		*
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	3	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	2	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_lvtrRespSV_Cnt_M_u08	1	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	0	
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

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Nama	Innut Value		
Name	Input Value		
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32 k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k LoaMgr lvtrldptSig1Resp Cnt u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc		nt lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target LoaMgr Per1 HwTqldptSig Cnt u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_k		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target LoaMgr Per1 LOASt State enum	9~	
target Rte Inst Ap LoaMgr.LoaMgr Per1 LoaScaDi Cnt Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	3	3	<b>~</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	<b>~</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	<b>V</b>
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>~</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	1	1	~
LoaMgr_LOASt_State_M_enum	4	4	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	4	4	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
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LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

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Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{171, 173}	{171, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	•
target_LoaMgr_Per1_LOASt_State_enum.value	4	4	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

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Name Input Value	
LoaMgr_AgSV_Cnt_M_lgc 0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 2	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc 1	
LoaMgr_HwTqRespSV_Cnt_M_u08 3	
LoaMgr_IvtrldptSigSV_Cnt_M_u08 1	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc 1	
LoaMgr_IvtrRespSV_Cnt_M_u08 4	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc 1	
LoaMgr_MotAgRespSV_Cnt_M_u08	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08 2	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 0	
LoaMgr_MotCurrRespSV_Cnt_M_u08 1	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc 0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08 0	
Rte_Inst_Ap_LoaMgr target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32 1	
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc 0	
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32 1	
k_LoaMgr_FadeOutStRate_UlspS_f32 1	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08 2	
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 4	
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 2	
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 1	
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 0	
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
k_LoaMgr_HwTqMtgtnRate_UlspS_f32 1	
k_LoaMgr_HwTqMtgtnSca_Uls_f32 1	
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08 0	
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08 0	
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08 0	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32 1	
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc 0	
k_LoaMgr_lvtrMtgtnSca_Uls_f32 1	
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 5	

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Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	2		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k LoaMgr MotAgMtgtnSca Uls f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target LoaMgr Per1 TloaDi Cnt Igc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inact	/ Cnt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inact		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_	u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enur	n	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cr	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnE		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_MtrPosIdptSig_Cnt target_LoaMgr_Per1_SteerMod_Cnt_ent		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc	4111	
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	3	3	<b>~</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_IvtrRespSV_Cnt_M_u08	4	4	~
LoaMgr_LOASt_State_M_enum	5	5	<b>Y</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr MotAgRespSV Cnt M u08	5	5	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	-
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	_
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaScaleEntr_Ulg f32(data)	500	500 ± 0.01	<b>*</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>✓</b>
target LoaMar Peri HwTal caMtathEn Cot Isourchio	0		
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1 1	1	· ·
	1	1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_LOASt_State_enum.value	1 1 5	1 1 5	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \



Test Step Call Trace   ✓				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	-
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.53 (Repeat Count = 1)	<b>1</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr MotAgLoaMtgtnEn Cnt M Igc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr MotCurrIdptSigSV Cnt M u08	2
	0
LoaMgr_MotCurrPoanSV/_Cot_M_u08	1
LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	-
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	3
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	.lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_	u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u	108	
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target_LoaMgr_Per1_SteerMod_Cnt_enum	1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr HwTqldptSigSV Cnt M u08	2	2	<b>V</b>
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	3	3	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	
LoaMgr MotCurrLoaMtgtnEn Cnt M Igc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	-
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 173}	{170, 173}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target LoaMgr Per1 HwTqLoaMtgtnEn Cnt lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	-
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
5 _			

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~



Test Step 4.54 (Repeat Count = 1)	x
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlytrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08 k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k LoaMgr IvtrMtgtnRate UlspS f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	0
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	0
target LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc.value target LoaMgr Per1 DiagcStslvtr2Inactv Cnt Igc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2
target LoaMgr Per1 LoaScaDi Cnt Igc.value	1
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1lnactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_inst_Ap_LoaMgr.LoaMgr_Per1_ivirLoaMtgtnEn_Cnt_igc target_Rte_inst_Ap_LoaMgr.LoaMgr_Per1_ivirLoaMtgtnEn_Cnt_igc	target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	3	3	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170}	{170}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1}	{1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

「est Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•

Test Step 4.55 (Repeat Count = 1)		E E
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	1	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

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LoaMgr\_Per1



Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32 k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2		
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	5		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k LoaMgr MotAgMtgtnRate UlspS f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	5		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEr		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enu	III	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
	1	1	Result
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	-
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	-
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	<b>→</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	1	1	-

LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

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LoaMgr\_Per1



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	Ī	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 175}	{170, 173, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Name -	
Name Input Value	
LoaMgr_AgSV_Cnt_M_lgc 1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 2	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc 1	
LoaMgr_HwTqRespSV_Cnt_M_u08 4	
LoaMgr_lvtrldptSigSV_Cnt_M_u08 2	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc 1	
LoaMgr_IvtrRespSV_Cnt_M_u08 5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08 3	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc 0	
LoaMgr_MotAgRespSV_Cnt_M_u08 1	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08 0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 1	
LoaMgr_MotCurrRespSV_Cnt_M_u08 3	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc 1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08 1	
Rte_Inst_Ap_LoaMgr target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32 1	
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc 0	
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32 1	
k_LoaMgr_FadeOutStRate_UlspS_f32 1	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08 2	
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 4	
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 2	
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 1	
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 0	
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08 1	
k_LoaMgr_HwTqMtgtnRate_UlspS_f32 1	
k_LoaMgr_HwTqMtgtnSca_Uls_f32 1	
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08 0	
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08 0	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32 1	
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc 0	
k_LoaMgr_lvtrMtgtnSca_Uls_f32 1	
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 5	





Landary Michigal Street, Crit. 108   1	Nama	Input Value		
Leading Anti-options prince Comp. 18	Name	•		
Leading Mindignishing United   1				
Landay Markedymines Usep (12)				
Landary Moniformaniane		1		
Leading MacGrandwalPresp Ort USB	k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
	k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
Landary_MonCorrispsignitions_Cont_108	k_LoaMgr_MotAgSmaAvailResp_Cnt_u08			
Licology_McCountrySignifies_Cort_106   Licology_McCount	k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08			
Landing   MoCarding Seq Print   Cort   USE				
Looking MoCorthight Seaze Use State				
Loading McContingNessZelling				
Loading   RedotStrate_Uings_Taze   1				
Supple   Loading / Pert   DiagosStativiTracko, Crit   government				
Single   Loady Pert   Hardqridgs   Ort   US value		1		
Supple   Loading   Pert   Loading		1		
	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
Image   Landing   Pert   Methodological   Cmrum value   2	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2		
Integral Landary_PerT_TONDI_COT_Lip cavalue	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value			
Integrical Loading	target_LoaMgr_Per1_SteerMod_Cnt_enum.value			
Image Rel Inst. Ap. Loading Loading Pert_DiagoSsishrid Inacky_Cnt_Upc   Image Rel_Inst. Ap_Loading Loading_Pert_DiagoSsishrid Inacky_Cnt_Upc   Image Rel_Inst. Ap_Loading_Loading_Pert_HwiTodoSing_Cnt_Upc   Image Rel_Inst. Ap_Loading_Loading_Pert_Loading_Dert_Diago_Cnt_Upc   Image Rel_Inst. Ap_Loading_Loading_Pert_Loading_Cnt_Upc   Image Rel_Inst. Ap_Loading_Loading_Pert_Loading_Cnt_Upc   Image Rel_Inst. Ap_Loading_Loading_Pert_MotOrading_Cnt_Upc   Image Rel_Inst. Ap_Loading_Loading_Pert_Toadin_Cnt_Upc   Image Rel_Inst. Ap_Loading_Pert_Toadin_Cnt_Upc   Image		1		
target. Rel. Inst. Ap. Loadlyr Loadlyr. Pert. Dispositivit/laned. Cnt. Igo target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. J. HviTqlostStip. Cnt. U8 target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. J. HviTqlostStip. Cnt. U8 target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. J. HviTqlostStip. Cnt. U8 target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. J. Vitt. State unum target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. J. Vitt. State unum target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. J. Vitt. State unum target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. J. Vitt. State unum target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. J. Vitt. State unum target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. J. Vitt. State unum target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. J. Vitt. State unum target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. J. Vitt. State unum target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. J. Vitt. State unum target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. J. Vitt. State unum target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. J. Vitt. State unum target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. J. Vitt. State unum target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. J. Vitt. State unum target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. J. Vitt. State unum target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. J. Vitt. State unum target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. J. Vitt. State unum target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. J. Vitt. State unum target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. J. Vitt. State unum target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. J. Vitt. State unum target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. J. Vitt. State unum target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. J. Vitt. State unum target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. J. Vitt. State unum target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. J. Vitt. State unum target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. J. Vitt. State unum target. Loadlyr. Pert. J. Vitt. State unum target. Loadlyr. Pert. J. Vitt. State unum target. Loadlyr. J. Vitt. State unum target. Loadlyr. J.	0 = 0 = = = = = = = = = = = = = = = = =			
large_Re_Inst_Ap_LoaMyr_LoaMyr_Per1   HvTqlopSig_Cnt_U8				
target_Rel_Inst_Ap_LoaMyr_LoaMyr_Per1_NirtQuaMtgrifien_Cnt_lgc   target_Rel_Inst_Ap_LoaMyr_LoaMyr_Per1_LOASS_state_enum   target_Rel_Inst_Ap_LoaMyr_LoaMyr_Per1_LOASS_state_enum   target_Rel_Inst_Ap_LoaMyr_LoaMyr_Per1_LOASS_state_enum   target_Rel_Inst_Ap_LoaMyr_LoaMyr_Per1_LOASS_state_enum   target_Rel_Inst_Ap_LoaMyr_LoaMyr_Per1_LOASS_state_enum   target_Rel_Inst_Ap_LoaMyr_LoaMyr_Per1_MoR_CoaMyr_End_Inst_Ap_LoaMyr_LoaMyr_Per1_MoR_CoaMyr_End_Inst_Ap_LoaMyr_LoaMyr_Per1_MoR_CoaMyr_End_Inst_Ap_LoaMyr_LoaMyr_Per1_MoR_CoaMyr_End_Inst_Ap_LoaMyr_LoaMyr_Per1_MoR_CoaMyr_End_Inst_Ap_LoaMyr_LoaMyr_Per1_MoR_CoaMyr_End_Inst_Ap_LoaMyr_LoaMyr_Per1_MoR_CoaMyr_End_Inst_Ap_LoaMyr_LoaMyr_Per1_MoR_CoaMyr_End_Inst_Ap_LoaMyr_End_Inst_Ap_LoaMyr_End_Inst_Ap_LoaMyr_End_Inst_Ap_LoaMyr_Per1_MoR_CoaMyr_End_Inst_Ap_LoaMyr_End_I				
target_Rel_inst_Ap_LoaMgr_LoaMgr_Pert_LOASE_State_enum larget_Rel_inst_Ap_LoaMgr_LoaMgr_Pert_LoaSeaDi_Cnt_lige target_Rel_inst_Ap_LoaMgr_LoaMgr_Pert_MotOutploaMtgnEn_Cnt_lige target_Rel_inst_Ap_LoaMgr_LoaMgr_Pert_MotOutploaMtgnEn_Cnt_lige target_Rel_inst_Ap_LoaMgr_LoaMgr_Pert_MotOutploaMtgnEn_Cnt_lige target_Rel_inst_Ap_LoaMgr_LoaMgr_Pert_MotOutploaMtgnEn_Cnt_lige target_Rel_inst_Ap_LoaMgr_LoaMgr_Pert_MotOutploaMgr_MotOutploaMgr_MotOutp				
larget_Rie_Inst_Ap_LoaMgr.LoaMgr_Pert_LoaScaDi_Cnt_Ige   larget_LoaMgr_Pert_MolAgLoaMignEn_Cnt_Ige   larget_LoaMgr_Pert_SteenMod_Cnt_enum   larget_Rie_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_Ige   larget_LoaMgr_Pert_ToaAvail_Cnt_Ige   larget_LoaMgr_Pert_Igevallen   larget_L				
target_Rie_Inst_Ap_LoaMgr_LoaMgr_Perf_MitrPosidptSig_Cnt_u08   target_LoaMgr_Perf_NewMod_Cnt_enum   target_LoaMgr_Perf_NewMod_Cnt_enum   target_LoaMgr_Perf_NewMod_Cnt_lgc   target_LoaMgr_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_Perf_ToaMgr_Perf_ToaMgr_Perf_ToaMgr_Perf_ToaMgr_Perf_ToaMgr_Perf_ToaMgr_Perf_ToaMgr_Perf_ToaMgr_Perf_ToaMgr_Perf_ToaMgr_Perf_ToaMgr_Perf_ToaMgr_Perf_ToaMgr_Perf_ToaMgr_Perf_ToaMgr_Perf_ToaMgr_Perf_ToaMgr_Perf_ToaMgr_Perf_ToaMgr_Perf_ToaMg				
target_Rie_Inst_Ap_LoaMgr.LoaMgr.Pert_TtoaAvail_Cnt_Igo         target_LoaMgr.Pert_TtoaAvail_Cnt_Igo           target_Rie_Inst_Ap_LoaMgr.DeaMigr.Pert_TtoaAvail_Cnt_Igo         target_LoaMgr.Pert_TtoaAvail_Cnt_Igo           Name         Actual Value         Expected Value         Result           LoaMgr_HarTqloghSigsV_Cnt_M_Ugo         1         1         1         4           LoaMgr_HarTqloghSigsV_Cnt_M_Ugo         2         2         2         LoaMgr_HarTqloghSigsV_Cnt_M_Ugo         0         0         0         0         0         LoaMgr_HarTqloghSigsV_Cnt_M_Ugo         0         0         0         0         LoaMgr_HarTqloghSigsV_Cnt_M_Ugo         0         0         0         0         0         LoaMgr_HarTqloghSigsV_Cnt_M_Ugo         0 <t< td=""><td>target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc</td><td>target_LoaMgr_Per1_MotCurrLoaMtgtn</td><td>En_Cnt_lgc</td><td></td></t<>	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtn	En_Cnt_lgc	
target_Rie_Inst_Ap_LoaMgr_LoaMgr_Pert_TloaAvail_Cnt_Igc   target_LoaMgr_Pert_TloaDi_Cnt_Igc   target_LoaMgr_Pert_TloaDi_Cnt_Igc   target_LoaMgr_Pert_TloaDi_Cnt_Igc   target_LoaMgr_Pert_TloaDi_Cnt_Igc   target_LoaMgr_Pert_TloaDi_Cnt_Igc   Result   LoaMgr_AgSV_Cnt_M_Igc   1   1   1   1   1   LoaMgr_HwTiqbtSigSV_Cnt_M_U08   2   2   2   2   2   LoaMgr_HwTiqbtSigSV_Cnt_M_U08   0   0   0   0   LoaMgr_HwTiqbtSigSV_Cnt_M_U08   0   0   0   0   LoaMgr_HwTiqBespSV_Cnt_M_U08   0   0   0   0   LoaMgr_HwTiqBespSV_Cnt_M_U08   0   0   0   0   LoaMgr_IwridepSigSV_Cnt_M_U08   5   5   5   5   LoaMgr_IwridepSigSV_Cnt_M_U08   5   5   5   LoaMgr_LoaSt_State_M_enum   6   5   5   LoaMgr_LoaSt_State_M_enum   6   5   5   LoaMgr_LoaSt_State_M_enum   7   7   LoaMgr_LoaSt_State_M_enum   7   7   7   LoaMgr_LoaSt_State_M_enum   7   7   7   7   LoaMgr_MoxNtglightSigSV_Cnt_M_u08   5   5   5   7   LoaMgr_MoxNtglightSigSV_Cnt_M_u08   7   7   7   7   7   LoaMgr_MoxD_RoaSt_State_M_enum   7   7   7   7   7   LoaMgr_MoxD_RoaSt_State_M_u08   7   7   7   7   7   LoaMgr_MoxD_RoaSt_State_M_u08   7   7   7   7   7   7   LoaMgr_MoxD_RoaSt_State_M_u08   7   7   7   7   7   7   7   LoaMgr_MoxD_RoaSt_State_M_u08   7   7   7   7   7   7   7   Rle_Wite_D_LoaMgr_NxtrDiagMgr_State_	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_C	nt_u08	
target_Rie_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_el	num	
Name         Actual Value         Expected Value         Result           LoaMgr_AgSV_Cnt_M_lgc         1         1         1         1           LoaMgr_HwTqLoaMitghEn_Cnt_M_lgc         1         1         1         2           LoaMgr_HwTqLoaMitghEn_Cnt_M_u08         0				
LoaMgr_HvTqldplSigSV_Cnt_M_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
LoaMgr_HwTqlpdISigSV_Cnt_M_u08	Name		· ·	Result
LoaMgr_HWTqLoaMtgtnEn_Cnt_M_lge				
LoaMgr_HwTqRespSV_Cnt_M_u08				
LoaMgr_Ivit/dptSigSV_Cnt_M_u08				
LoaMgr_IvtrLoaMtghEn_Cnt_M_loc				
LoaMgr_IvtRespSV_Cnt_M_u08				
LoaMgr_LoaRsteLimit_UlspS_M_f32				
LoaMgr_LoaRateLimit_UispS_M_f32         500         500           LoaMgr_LoaScaleFctr_UIs_M_f32         0         0           LoaMgr_MaxMigtnResp_Cnt_M_u08         5         5           LoaMgr_MotAgIdptSigSV_Cnt_M_u08         2         2           LoaMgr_MotAgRespSV_Cnt_M_u08         1         1           LoaMgr_MotCurrlotSigSV_Cnt_M_u08         0         0           LoaMgr_MotCurrlotSigSV_Cnt_M_u08         1         1           LoaMgr_MotCurrRespSV_Cnt_M_u08         3         3           LoaMgr_TloaAvail_Cnt_M_lgc         1         1           LoaMgr_TloaDi_Cnt_M_lgc         1         1           LoaMgr_TloaDi_Cnt_M_lgc         1         1           LoaMgr_VitgMSrcSV_Cnt_M_u08         1         1           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 173, 175}         {170, 173, 175}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0				<b>*</b>
LoaMgr_LoaScaleFct_Uls_M_f32         0         0           LoaMgr_MaxMtgtnResp_Cnt_M_u08         5         5           LoaMgr_MotAgldptSigsV_Cnt_M_u08         2         2           LoaMgr_MotAglcanMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_MotAgRespSV_Cnt_M_u08         1         1           LoaMgr_MotCurrtLoaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_MotCurrtLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrtLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_TloaAvail_Cnt_M_lgc         1         1           LoaMgr_TloaDi_Cnt_M_lgc         1         1           LoaMgr_VltgMdSrcSV_Cnt_M_u08         1         1           Rte_Call_Ap_LoaMgr_NtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         (170, 173, 175)         (170, 173, 175)           Rte_Call_Ap_LoaMgr_NtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         (1, 1, 1)         (0, 0, 0, 0, 0)         X           Rte_Call_Ap_LoaMgr_NtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         (1, 1, 1)         (1, 1, 1)         (1, 1, 1)           Rte_Write_Ap_LoaMgr_NtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         (1, 1, 1)         (1, 1, 1)         (1, 1, 1)           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0         0         0         0				
LoaMgr_MotAgldptSigSV_Cnt_M_u08         2         2           LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_MotAgRespSV_Cnt_M_u08         1         1           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrRespSV_Cnt_M_u08         3         3           LoaMgr_TloaAvail_Cnt_M_lgc         1         1           LoaMgr_TloaDi_Cnt_M_lgc         1         1           LoaMgr_VitgMdSrcsV_Cnt_M_u08         1         1           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 173, 175}         {170, 173, 175}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}         **           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}         **           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}         **           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1, 1}         {1, 1, 1, 1}         **           Rte_Call_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         500         500 ± 0.01         **           rte_get_LoaMgr_Per1_lwTqLoaMtgtnEn_Cnt_lgc.value         1				<b>✓</b>
LoaMgr_MotAgLoaMtgrnEn_Cnt_M_lgc         0         0           LoaMgr_MotAgRespSV_Cnt_M_u08         1         1           LoaMgr_MotCurrldptSigSV_Cnt_M_u08         0         0           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrRespSV_Cnt_M_u08         3         3           LoaMgr_TloaAvail_Cnt_M_lgc         1         1           LoaMgr_TloaDi_Cnt_M_lgc         1         1           LoaMgr_TloaDi_Cnt_M_lgc         1         1           LoaMgr_NtxtMgrcSV_Cnt_M_u08         1         1           Rte_Call_Ap_LoaMgr_ScSV_Cnt_M_u08         1         1           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 173, 175}         {170, 173, 175}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}         X           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}         X           Rte_Wite_Ap_LoaMgr_LoaRateLimit_UlspS_52/data)         500         500 ± 0.01         X           Rte_Wite_Ap_LoaMgr_LoaScaleFctr_Uls_f32/data)         0         0 ± 0.001         X           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         1         1         1           target_LoaMgr_Per1_LoASc_State_enum	LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotAgRespSV_Cnt_M_u08         1         1           LoaMgr_MotCurrIdptSigSV_Cnt_M_u08         0         0           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrRespSV_Cnt_M_u08         3         3           LoaMgr_TloaAvail_Cnt_M_lgc         1         1           LoaMgr_TloaDi_Cnt_M_lgc         1         1           LoaMgr_VltgMdSrcSV_Cnt_M_u08         1         1           Rte_Call_Ap_LoaMgr_NtxrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 173, 175}         {170, 173, 175}           Rte_Call_Ap_LoaMgr_NtxrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}         **           Rte_Call_Ap_LoaMgr_NtxrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}         **           Rte_Wite_Ap_LoaMgr_NtxrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}         **           Rte_Wite_Ap_LoaMgr_LoaRateLimit_Ulsps_f32(data)         500         500 ± 0.01         **           Rte_Wite_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         **           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         1         1         1           target_LoaMgr_Per1_LoASt_State_enum.value         5         5           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08         0         0           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrRespSV_Cnt_M_u08         3         3           LoaMgr_TloaAvail_Cnt_M_lgc         1         1           LoaMgr_TloaDi_Cnt_M_lgc         1         1           LoaMgr_VltgMdSrcSV_Cnt_M_u08         1         1           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 173, 175}         {170, 173, 175}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_Tu08)         {1, 1, 1}         {0, 0, 0, 0, 0}         X           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}         {1, 1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01         X           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         X           rtaget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         1         1         1           rtaget_LoaMgr_Per1_LOASt_State_enum.value         5         5         X           rtaget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0         0         X	LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1         2           LoaMgr_MotCurrRespSV_Cnt_M_u08         3         3         3           LoaMgr_TloaAvail_Cnt_M_lgc         1         1         1           LoaMgr_VltgMdSrcSV_Cnt_M_u08         1         1         1           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 173, 175}         {170, 173, 175}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}         **           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}         {1, 1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01         **           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         **           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         1         1         1           target_LoaMgr_Per1_LOASt_State_enum.value         5         5         **           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0         **	LoaMgr_MotAgRespSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08       3       3         LoaMgr_TloaAvail_Cnt_M_lgc       1       1         LoaMgr_VltgMdSrcSV_Cnt_M_u08       1       1         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       {170, 173, 175}       {170, 173, 175}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       {1, 1, 1}       {0, 0, 0, 0, 0}       X         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       {1, 1, 1}       {1, 1, 1}       {1, 1, 1}       X         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01       X         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001       X         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001       X         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       1       1       1         target_LoaMgr_Per1_LOASt_State_enum.value       5       5       X         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0       X	LoaMgr_MotCurrIdptSigSV_Cnt_M_u08			~
LoaMgr_TloaAvail_Cnt_M_lgc         1         1         1           LoaMgr_TloaDi_Cnt_M_lgc         1         1         1           LoaMgr_VltgMdSrcSV_Cnt_M_u08         1         1         1           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 173, 175}         {170, 173, 175}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001           Rte_Write_Ap_LoaMgr_PcadMtgtnEn_Cnt_lgc.value         1         1           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         1         1           target_LoaMgr_Per1_LOASt_State_enum.value         5         5           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0				~
LoaMgr_TloaDi_Cnt_M_lgc       1       1         LoaMgr_VltgMdSrcSV_Cnt_M_u08       1       1         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       {170, 173, 175}       {170, 173, 175}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       {1, 1, 1}       {0, 0, 0, 0, 0}       X         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       {1, 1, 1}       {1, 1, 1}       {1, 1, 1}       X         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01       X         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001       X         Rte_Write_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       1       1       X         target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value       1       1       X         target_LoaMgr_Per1_LOASt_State_enum.value       5       5       X         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0       X				~
LoaMgr_VitgMdSrcSV_Cnt_M_u08       1       1         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       {170, 173, 175}       {170, 173, 175}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       {1, 1, 1}       {0, 0, 0, 0, 0}       X         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       {1, 1, 1}       {1, 1, 1}       {1, 1, 1}       X         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01       X         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001       X         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       1       1       X         target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value       1       1       X         target_LoaMgr_Per1_LOASt_State_enum.value       5       5       X         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0       X				
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       {170, 173, 175}       {170, 173, 175}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       {1, 1, 1}       {0, 0, 0, 0, 0}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       {1, 1, 1}       {1, 1, 1}         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       1       1         target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value       1       1         target_LoaMgr_Per1_LOASt_State_enum.value       5       5         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0				<b>Y</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         1         1           target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value         1         1           target_LoaMgr_Per1_LOASt_State_enum.value         5         5           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0				
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       {1, 1, 1}       {1, 1, 1}         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       1       1         target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value       1       1         target_LoaMgr_Per1_LOASt_State_enum.value       5       5         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0				
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         1         1           target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value         1         1           target_LoaMgr_Per1_LOASt_State_enum.value         5         5           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0				
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       1       1         target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value       1       1         target_LoaMgr_Per1_LOASt_State_enum.value       5       5         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0			· · ·	-
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1				~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value				~
target_LoaMgr_Per1_LOASt_State_enum.value 5 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0				·
0 = 0 = 0 = 0 = 0		5	5	•
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value 1	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
	target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt lgc.value	1	1	<b>✓</b>



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

	_
Test Step 4.57 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	5
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_UIs_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	
k LoaMgr MotCurrldptSig2Resp Cnt u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value	0		
target LoaMgr Per1 HwTqldptSig Cnt u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3		
target LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1l	Inacty Cnt Igc	
target Rte Inst Ap LoaMgr.LoaMgr Per1 DiagcStslvtr2Inactv Cnt Igc	target_LoaMgr_Per1_DiagcStslvtr2l		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqldptSig Cnt u08	target_LoaMgr_Per1_HwTqldptSig_		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqLoaMtgtnEn Cnt Igc	target_LoaMgr_Per1_HwTqLoaMtgt	_	
target Rte Inst Ap LoaMgr.LoaMgr Per1 lvtrLoaMtgtnEn Cnt Igc	target_LoaMgr_Per1_lvtrLoaMtgtnE		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtg		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSi		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaM		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cn		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lg		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	0	0	Result
	1	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	4	4	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	5	5	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	Ž
LoaMgr_LOASt_State_M_enum			
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgloptSigSV_Cnt_M_u08			
LoaMgr_MotAgBoogS\/ Cot M v08	0	0	Ž
LoaMgr_MotAgRespSV_Cnt_M_u08		1	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1 0	0	Ž
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc			
LoaMgr_MotCurrRespSV_Cnt_M_u08	5	5	
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	
LoaMgr_TloaDi_Cnt_M_lgc	0		
LoaMgr_VitgMdSrcSV_Cnt_M_u08		0	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172}	{170, 172}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	Y
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	· ·
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	<b>V</b>
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	✓

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	•
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	<b>✓</b>





Test Step 4.58 (Repeat Count = 1)	<b></b>
Name LoaMgr_AgSV_Cnt_M_lgc	Input Value
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32 k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1 0
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	5
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32 k LoaMgr RedcdStRate UlspS f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_Ner1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Ner1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_igc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_igc	target_LoaMigr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
2 1 _ 0 _ 0 _ 0 _ 0 _ 0 _ 0 _ 0 _ 0	, , , , , , , , , , , , , , , , , , , ,





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 175}	{170, 172, 173, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	•
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•

Test Step 4.59 (Repeat Count = 1)		E C
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	2	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_lvtrRespSV_Cnt_M_u08	3	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	4	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

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LoaMgr\_Per1



Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k LoaMgr MotAgMtgtnSca Uls f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32			
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lo	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	
	0	0	_
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	
LoaMgr_lvtrldptSigSV_Cnt_M_u08		1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1		
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	<b>V</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	<b>V</b>
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	<b>V</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	<b>V</b>
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	-

LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

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LoaMgr\_Per1



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 175}	{170, 172, 173, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.60 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSig1Resp Cnt u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSigFltThd Cnt u08	0		
k LoaMgr MotCurrMtgtnRate UlspS f32	1		
k LoaMgr MotCurrMtgtnScaZeroEn Cnt Igc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value	0		
	2		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value			
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt	_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_	_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn	_Cnt_lgc	
	tarret Lander Dani MtrDanidatica Out	.00	
target Rte Inst Ap LoaMgr.LoaMgr Per1 MtrPosIdptSig Cnt u08	target Loawigr Peri witrPosiaptSig Cnt	JU0	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_i target_LoaMgr_Per1_SteerMod_Cnt_enun		
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enun		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc	n	Rosult
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	Expected Value	Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0	Expected Value	~
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1	Expected Value 0 1	~
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 1	Expected Value  0 1 1	•
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 1 2	Expected Value  0 1 1 2	~
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IwtrIqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 1 1 2 0	Expected Value  0 1 1 2 0	•
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 1 1 2 0 1	Expected Value  0 1 1 2 0 1	• • • • • • • • • • • • • • • • • • •
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 1 1 2 0 1 5	Expected Value  0 1 1 2 0 1 5	• • • • • • • • • • • • • • • • • • •
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 1 1 2 0 1 5 5	Expected Value  0 1 1 2 0 1 5 5	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 1 1 2 0 1 5 5 5	Expected Value  0 1 1 2 0 1 5 5 5	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 1 1 2 0 1 5 5 5 500 0	Expected Value  0 1 1 2 0 1 5 5 5 500 0	• • • • • • • • • • • • • • • • • • •
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 1 1 2 0 1 5 5 5 500 0 5	Expected Value  0 1 1 2 0 1 5 5 5 500 0 5	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrQaMtgtnEn_Cnt_M_Igc LoaMgr_LoaSt_State_M_enum LoaMgr_LoaScaleFctr_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 1 1 2 0 1 5 5 5 500 0 5 2	Expected Value  0 1 1 2 0 1 5 5 5 500 0 5 2	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdoaMtgtnEn_Cnt_M_Igc	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 1 2 0 1 5 5 5 500 0 5 2 1	Expected Value  0 1 1 2 0 1 5 5 5 500 0 5 2 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrQaMtgtnEn_Cnt_M_Igc LoaMgr_LoaSt_State_M_enum LoaMgr_LoaScaleFctr_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 1 1 2 0 1 5 5 5 500 0 5 2 1 2	Expected Value  0 1 1 2 0 1 5 5 5 500 0 5 2 1 2	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdoaMtgtnEn_Cnt_M_Igc	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 1 2 0 1 5 5 5 500 0 5 2 1	Expected Value  0 1 1 2 0 1 5 5 5 500 0 5 2 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_ivtrloaMtgtnEn_Cnt_M_lgc LoaMgr_ivtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_ivtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 1 1 2 0 1 5 5 5 500 0 5 2 1 2	Expected Value  0 1 1 2 0 1 5 5 5 500 0 5 2 1 2	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_ivtrloaMtgtnEn_Cnt_M_lgc LoaMgr_ivtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_ivtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurridptSigSV_Cnt_M_u08 LoaMgr_MotCurridptSigSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 1 1 2 0 1 5 5 5 500 0 5 2 1 2 0	Expected Value  0 1 1 2 0 1 5 5 5 5 2 1 2 0	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaAsateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 1 1 2 0 1 5 5 5 500 0 5 2 1 2 0 1	Expected Value  0 1 1 2 0 1 5 5 5 5 2 1 2 0 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 1 1 2 0 1 5 5 500 0 0 5 2 1 2 0 1 1 1	Expected Value  0 1 1 2 0 1 5 5 5 500 0 5 2 1 2 0 1 1 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotAglcaMtgtnEn_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 1 2 0 1 5 5 5 500 0 0 5 2 1 2 0 1 1 1 1 1 1	Expected Value  0 1 1 2 0 1 5 5 5 500 0 5 2 1 2 0 1 1 1 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LoASt_State_M_enum LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 1 2 0 1 5 5 5 500 0 0 5 2 1 1 2 0 1 1 1 2	Expected Value  0 1 1 2 0 1 5 5 5 500 0 5 2 1 2 0 1 1 1 1 0	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LoASt_State_M_enum LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VtgMdSrcSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enun target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 1 2 0 1 5 5 5 00 0 1 1 2 0 1 1 1 2 0 0 1 1 0 0 0 0	Expected Value  0 1 1 2 0 1 5 5 5 00 0 1 2 1 1 2 0 1 1 1 2 0 1 1 0 0	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LOASt_State_M_enum LoaMgr_LoASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgCaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgNespSV_Cnt_M_u08 LoaMgr_MotAgNespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_NtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	target_LoaMgr_Per1_SteerMod_Cnt_enuntarget_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 1 2 0 1 5 5 5 00 0 1 1 2 0 1 1 1 2 0 1 1 2 0 1 1 1 2 0 1 1 1 2 0 1 1 1 1	Expected Value  0 1 1 1 2 0 1 5 5 5 500 0 1 1 2 0 1 1 1 2 0 1 1 1 0 0 0 {170, 173, 174}	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAglotSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgCaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgCaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIcaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrIcaMtgtnEn_Cnt_M_Igc LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_NtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	target_LoaMgr_Per1_SteerMod_Cnt_enuntarget_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 1 2 0 1 5 5 5 500 0 1 1 1 2 0 1 1 1 0 0 0 {170, 173, 174} {1, 1, 1}	Expected Value  0 1 1 1 2 0 1 5 5 5 500 0 1 1 1 2 0 1 1 1 0 0 {170, 173, 174} {0, 0, 0, 0, 0}	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgloaMtgtnEn_Cnt_M_log LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_NotCurrlapMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_LoaMgr_Per1_SteerMod_Cnt_enuntarget_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  1  2  0  1  5  5  5  500  0  1  1  1  2  0  1  1  1  2  1  2  1  2  1  2  1  1  2  1  1	Expected Value  0 1 1 1 2 0 1 5 5 5 500 0 0 5 2 1 1 2 0 1 1 1 0 0 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1}	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LoASt_State_M_enum LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_Igc LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_StgMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_LoaMgr_Per1_SteerMod_Cnt_enuntarget_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  1  2  0  1  5  5  5  500  0  1  1  1  2  0  1  1  1  2  1  2  1  2  1  2  1  2  1  2  1  2  1  2  1  2  1  2  1  2  1  2  1  1	Expected Value  0 1 1 1 2 0 1 5 5 5 500 0 1 1 1 2 0 1 1 1 0 0 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1} 5500 ± 0.01	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_StyltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	target_LoaMgr_Per1_SteerMod_Cnt_enuntarget_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  1  2  0  1  5  5  5  500  0  5  2  1  1  1  0  0  (170, 173, 174) {1, 1, 1} {1, 1, 1} 500  0	Expected Value  0 1 1 1 2 0 1 5 5 5 500 0 5 2 1 1 2 0 1 1 1 0 0 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1} 5500 ± 0.01 0 ± 0.001	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_StyD_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	target_LoaMgr_Per1_SteerMod_Cnt_enuntarget_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  1  2  0  1  5  5  5  500  0  1  1  1  2  0  (1)  1  2  0  1  1  2  0  1  1  1  2  0  1  1  1  1  1  1  1  1  1  1  1  1	Expected Value  0 1 1 1 2 0 1 5 5 5 500 0 5 2 1 1 2 0 1 1 1 0 0 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_U08 LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_StelDaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_LOASt_State_enum.value	target_LoaMgr_Per1_SteerMod_Cnt_enuntarget_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  1  2  0  1  5  5  5  500  0  5  2  1  1  1  0  0  (170, 173, 174) {1, 1, 1} {1, 1, 1} 500  0  1  1  1  5  6	Expected Value  0 1 1 1 2 0 1 5 5 5 500 0 5 2 1 1 2 0 1 1 1 0 0 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001 1 1 1 5	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_StyD_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	target_LoaMgr_Per1_SteerMod_Cnt_enuntarget_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  1  2  0  1  5  5  5  500  0  5  2  1  1  1  0  0  (170, 173, 174) {1, 1, 1} {1, 1, 1} 500  0  1  1  1	Expected Value  0 1 1 1 2 0 1 5 5 5 500 0 5 2 1 1 2 0 1 1 1 0 0 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.001 1 1 1 1 1	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

	_
Test Step 4.61 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_UIs_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
	1
k_LoaMgr_MotAgMtgtnSca_UIs_f32	2
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	
k_LoaMgr_MotCurridptSig0Resp_Cnt_u08	5
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0 0
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrNtottPate_UlenS_f32	1
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_le	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_(		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target LoaMgr Per1 SteerMod Cnt enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	- 100 Life
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr IvtrIdptSigSV Cnt M u08	0	0	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	-
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	-
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	_
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	5	5	_
LoaMgr TloaAvail Cnt M lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	_
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	_
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(Param Cnt T u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	·
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	-
target LoaMgr Per1 HwTqLoaMtgtnEn Cnt Igc.value	0	0	•
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	-
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>*</b>
target LoaMgr Per1 MotAgLoaMtgtnEn Cnt Igc.value	1	1	-
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~
2			

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 4.62 (Repeat Count = 1)	X .
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlytrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08 k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k LoaMgr lvtrMtgtnRate UlspS f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	0
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedodStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	0
target LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3
target LoaMgr Per1 LoaScaDi Cnt Igc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr_Dead_Res_Dead_Rts_Inst_Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1lnactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_inst_Ap_LoaMgr.LoaMgr_Per1_ivirLoaMtgtnEn_Cnt_igc target_Rte_inst_Ap_LoaMgr.LoaMgr_Per1_ivirLoaMtgtnEn_Cnt_igc	target_LoaMgr_Perl_IntrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	~
LoaMgr_LOASt_State_M_enum	4	4	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	4	4	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{171, 173}	{171, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	4	4	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•

Test Step 4.63 (Repeat Count = 1)		*
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_lvtrRespSV_Cnt_M_u08	4	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	4	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

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LoaMgr\_Per1



Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k LoaMgr MotAgldptSig3Resp Cnt u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08 k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1 2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Ci	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Ci	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt	:_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg	jc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	t lao	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cn target_LoaMgr_Per1_MotCurrldptSig_Cnt_ui		
target Rte Inst Ap LoaMgr.LoaMgr Perl MotCurrLoaMtgtnEn Cnt Igc	target LoaMgr Per1 MotCurrLoaMtgtnEn C		
target Rte Inst Ap LoaMgr.LoaMgr Per1 MtrPosldptSig Cnt u08		···	
	target LoaMgr Per1 MtrPosldntSig Cnt u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum	8	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc		8	
	target_LoaMgr_Per1_SteerMod_Cnt_enum	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc	8  Expected Value	Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc		Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0	Expected Value 0 0	<b>Y</b>
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0	Expected Value 0 0 1	<b>*</b>
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 0 1	Expected Value 0 0 1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 0 1 0 1	Expected Value 0 0 1 0 1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 0 1 0 1 0	Expected Value 0 0 1 0 1 0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 0 1 0 1 0 1	Expected Value 0 0 1 0 1 0 4	***
target_Rte_Inst_Ap_LoaMgr,LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr,LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IOASt_State_M_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 0 1 0 1 0 1 5	Expected Value 0 0 1 0 1 0 4 5	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 0 1 0 1 0 1	Expected Value 0 0 1 0 1 0 4	***
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 0 1 0 1 0 4 5 5000	Expected Value 0 0 1 0 1 0 4 5 500	***
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaSt_State_M_enum LoaMgr_LoaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 0 1 0 1 0 4 5 5000	Expected Value 0 0 1 0 1 0 4 5 500	******
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvaii_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 0 1 0 1 5 5 500 1 5	Expected Value 0 0 1 0 1 0 4 5 500 1 5	***************************************

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

2018-05-18, 17:54:37+0530

LoaMgr\_Per1



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	1	<b>~</b>
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.64 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	5
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k LoaMgr MotAgMtgtnSca Uls f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08			
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_l	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_t		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr HwTqldptSigSV Cnt M u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr HwTqRespSV Cnt M u08	4	4	<b>~</b>
LoaMgr lytrldptSigSV Cnt M u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	
1 - 1 - 1			
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr MotAgRespSV Cnt M u08	5	5	<b>✓</b>
LoaMgr MotCurrldptSigSV Cnt M u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	
	1	1	
LoaMgr_TloaAvail_Cnt_M_lgc			
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>~</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	V
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	-
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	
target LoaMar Peri MotAal caMtathEn Cot lacyalus	0		
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

	_
Test Step 4.65 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	1
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1 3
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_l	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	-	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_ui		
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	1	1	- Ttoouit
LoaMgr HwTqldptSiqSV Cnt M u08	1	1	
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	-
LoaMgr lvtrldptSigSV Cnt M u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	-
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	-
	500	500	
LoaMgr_LoaRateLimit_UlspS_M_f32	0	0	-
LoaMgr_LoaScaleFctr_Uls_M_f32	5	5	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	1	1	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	5	5	-
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	-
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	2	2	
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	-
LoaMgr_TloaAvail_Cnt_M_lgc	· ·		
LoaMgr_TloaDi_Cnt_M_lgc	1	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	•
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>Y</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	<b>Y</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<u> </u>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	<b>✓</b>
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~



Test Step 4.66 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	1 0
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1 0
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k LoaMgr MotAgldptSig3Resp Cnt u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32 k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_tvtLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScabl_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_Loascabi_Cht_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
2 2 2 1 2 1 3 1 3 2 1 2 1 2 1 2 1 2 1 2	, 5 = 5 = 5 = 5 = 5 = 5



LoaMgr\_Per1

Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08	2	2	~
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	<b>{1, 1, 1}</b>	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.67 (Repeat Count = 1)	<b>8</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	5		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	1 0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08 k LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_Igc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg		
	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	nt_lgc 08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt_lgc 08 Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrldptSig_Cnt_utarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_C	nt_lgc 08 Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrldptSig_Cnt_ut target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct target_LoaMgr_Per1_MtrPosldptSig_Cnt_ut	nt_lgc 08 Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrldptSig_Cnt_ut target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct target_LoaMgr_Per1_MtrPosIdptSig_Cnt_ut target_LoaMgr_Per1_SteerMod_Cnt_enum	nt_lgc 08 Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrldptSig_Cnt_ut target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct target_LoaMgr_Per1_MtrPosIdptSig_Cnt_ut target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc	nt_lgc 08 Cnt_lgc	Result
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc	target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrldptSig_Cnt_ut target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct target_LoaMgr_Per1_MtrPosldptSig_Cnt_ut target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1	t_lgc 08 Cnt_lgc 08 Expected Value	~
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrldptSig_Cnt_ut target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct target_LoaMgr_Per1_MtrPosldptSig_Cnt_ut target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1	Expected Value  1 0	~
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvaii_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc	target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrldptSig_Cnt_ut target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct target_LoaMgr_Per1_MtrPosldptSig_Cnt_ut target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1	Expected Value  1 0 1	<b>*</b>
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvaii_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08	target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrloptSig_Cnt_ut target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct target_LoaMgr_Per1_MtrPosldptSig_Cnt_ut target_LoaMgr_Per1_MtrPosldptSig_Cnt_ut target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1	Expected Value  1 0 1 4	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvaii_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08	target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrldptSig_Cnt_ut target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct target_LoaMgr_Per1_MtrPosldptSig_Cnt_ut target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1 4	Expected Value  1 0 1 4 1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvaii_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIoaMtgtnEn_Cnt_M_Igc	target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrldptSig_Cnt_ut target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct target_LoaMgr_Per1_MtrPosldptSig_Cnt_ut target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1 4 1	Expected Value  1 0 1 4 1 0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvaii_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08	target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrloptSig_Cnt_ut target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct target_LoaMgr_Per1_MtrPosldptSig_Cnt_ut target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1 4 1 0 4	Expected Value	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrldptSig_Cnt_ut target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct target_LoaMgr_Per1_MtrPosldptSig_Cnt_ut target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1 4 1	Expected Value  1 0 1 4 1 0	***
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvaii_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08	target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrloptSig_Cnt_ut target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct target_LoaMgr_Per1_MtrPosldptSig_Cnt_ut target_LoaMgr_Per1_MtrPosldptSig_Cnt_ut target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 1 4 1 0 4 5	Expected Value	***************************************
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaSt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrldptSig_Cnt_ut target_LoaMgr_Per1_MtrPosldptSig_Cnt_ut target_LoaMgr_Per1_MtrPosldptSig_Cnt_ut target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1 4 1 0 4 5 500	Expected Value 1 0 1 4 1 0 4 5 5 500	***************************************
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvaii_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uis_M_f32 LoaMgr_LoaScaleFctr_Uis_M_f32	target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrldptSig_Cnt_ut target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct target_LoaMgr_Per1_MtrPosldptSig_Cnt_ut target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1 4 5 500 0 5 0	Expected Value 1 0 1 4 1 0 4 5 5 00 0 5 0	***
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvaii_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HvTrloaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrldptSig_Cnt_ut target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct target_LoaMgr_Per1_MtrPosldptSig_Cnt_ut target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1 4 1 0 4 5 500 0 5	Expected Value 1 0 1 4 1 0 4 5 500 0 5	***

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	<b>~</b>
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	Ī	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.68 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	2
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_UIs_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k LoaMgr RedcdStSca Uls f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target LoaMgr Per1 SteerMod Cnt enum.value	1		
target LoaMgr Per1 TloaAvail Cnt Igc.value	1		
target LoaMgr Per1 TloaDi Cnt Igc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Ina	actv Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Ina		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqldptSig Cnt u08	target_LoaMgr_Per1_HwTqldptSig_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnl	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_e		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtn	•	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtg	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_I		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	3	
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	Result
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1.	!	
	0	0	
	0	0	-
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	-
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	1 4	1 4	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08	1 4 1	1 4 1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1 4 1 0	1 4 1 0	, , , , , , , , , , , , , , , , , , ,
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08	1 4 1 0 4	1 4 1 0 4	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	1 4 1 0 4 5	1 4 1 0 4 5	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	1 4 1 0 4 5 5 500	1 4 1 0 4 5 500	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	1 4 1 0 4 5 5 500 0	1 4 1 0 4 5 500	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 4 1 0 4 5 5 500 0 5 5	1 4 1 0 4 5 500 0 5	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	1 4 1 0 4 5 5 500 0 5 0 0	1 4 1 0 4 5 5 500 0 5	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc	1 4 1 0 4 5 5 500 0 5 5 0 0 0	1 4 1 0 4 5 5 500 0 5 0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	1 4 1 0 4 1 0 4 4 5 5 500 0 5 5 0 0 5 5	1 4 1 0 4 5 5 500 0 5 0 0 5	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1 4 1 0 4 1 0 4 4 5 5 500 0 0 5 5 0 0 0 5 5 0 0	1 4 1 0 4 5 5 500 0 5 0 0 5	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1 4 1 0 4 1 0 0 4 4 5 5 500 0 0 5 5 0 0 0 5 5 0 1 1	1 4 1 0 4 5 5 500 0 5 0 0 5 0 0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	1 4 1 0 4 1 0 0 4 4 5 5 500 0 0 5 5 0 0 0 5 5 0 0 1 1 3	1 4 1 0 4 5 5 500 0 5 0 0 5 0 0 1 3	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc	1 4 1 0 4 1 0 0 4 4 5 5 500 0 0 5 5 0 0 0 5 5 0 0 1 1 3 1 1	1 4 1 0 4 5 5 500 0 5 0 0 5 0 0 1 3 1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	1 4 1 0 4 5 5 500 0 5 0 0 5 0 1 3 1 1	1 4 1 0 4 5 5 500 0 5 0 0 5 0 1 3 1 1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VtgMdSrcSV_Cnt_M_u08	1 4 1 0 4 5 5 500 0 0 5 0 0 1 3 1 1	1 4 1 0 4 5 5 500 0 5 0 0 5 0 1 3 1 1 1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VttgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	1 4 1 0 4 5 5 500 0 0 5 0 0 0 5 0 1 3 1 1 1 1 1 170, 172, 173}	1 4 1 0 4 5 5 500 0 5 0 0 5 0 1 3 1 1 1 11 170, 172, 173}	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1 4 1 0 4 5 5 500 0 5 0 0 1 3 1 1 1 1 {170, 172, 173} {1, 1, 1}	1 4 1 0 4 5 5 500 0 5 0 0 5 0 1 3 1 1 1 {170, 172, 173} {0, 0, 0, 0, 0}	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOARst_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VttgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1 4 1 0 4 1 0 4 5 5 500 0 5 0 0 1 3 1 1 1 1 {170, 172, 173} {1, 1, 1} {1, 1, 1}	1 4 1 0 4 5 5 500 0 5 0 0 5 0 1 1 3 1 1 1 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1}	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaSt_ate_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotOcurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_StgMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1 4 1 0 4 1 0 4 5 5 500 0 5 0 0 1 1 3 1 1 1 {170, 172, 173} {1, 1, 1} 500	1 4 1 0 4 5 5 500 0 5 0 0 5 0 1 1 3 1 1 1 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrlotSigSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrlotPsigSV_Cnt_M_u08 LoaMgr_MotCurrlotPsigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_StyP_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRcaleFctr_Uls_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1 4 1 0 4 1 0 4 5 5 500 0 5 0 0 1 1 3 1 1 1 {170,172,173} {1,1,1} 500 0	1 4 1 0 4 5 5 500 0 5 0 0 5 0 1 1 3 1 1 1 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1 4 1 0 4 1 0 4 5 5 500 0 5 0 0 1 1 3 1 1 1 {170,172,173} {1,1,1} 500 0 1	1 4 1 0 4 5 5 500 0 5 0 0 5 0 1 1 3 1 1 {170,172,173} {0,0,0,0,0} {1,1,1} 500±0.01 0±0.001 1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1 4 1 0 4 1 0 4 5 5 500 0 5 0 0 1 1 3 1 1 1 {170,172,173} {1,1,1} {1,1,1} 500 0 1 0	1 4 1 0 4 5 5 500 0 5 0 0 5 0 1 1 3 1 1 1 {170,172,173} {0,0,0,0,0} {1,1,1} 500±0.01 0±0.001 1 0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrlotSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VtlgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lotrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	1 4 1 0 4 1 0 4 5 5 500 0 5 0 0 1 1 3 1 1 1 {170, 172, 173} {1, 1, 1} {1, 1, 1} 500 0 1 0 5	1 4 1 0 4 5 5 500 0 5 0 0 5 0 1 1 3 1 1 1 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001 1 0 5	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1 4 1 0 4 1 0 4 5 5 500 0 5 0 0 1 1 3 1 1 1 {170,172,173} {1,1,1} {1,1,1} 500 0 1 0	1 4 1 0 4 5 5 500 0 5 0 0 5 0 1 1 3 1 1 1 {170,172,173} {0,0,0,0,0} {1,1,1} 500±0.01 0±0.001 1 0	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	•
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•

Test Step 4.69 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr MotAgldptSigSV Cnt M u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnKate_Oisp3_132	0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnKate_olspo_ioz k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k LoaMgr MotAgldptSig3Resp Cnt u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k LoaMgr MotAgMtgtnSca Uls f32	1
	2
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	2 0
	0
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08 k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	
k_LoaMgr_RedodStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C	:nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_l	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	0	0	_
LoaMgr HwTqldptSigSV Cnt M u08	0	0	<b>~</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	<b>~</b>
LoaMgr lvtrldptSigSV Cnt M u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>~</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	<b>~</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	<b>~</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	-
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	0	0	-
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	-
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 4.70 (Repeat Count = 1)	
Name LoaMgr_AgSV_Cnt_M_lgc	Input Value
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnKate_Olsp3_l32 k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlytrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08 k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k LoaMgr MotCurrMtgtnRate UlspS f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k LoaMgr MotCurrMtgtnSca UIs f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.71 (Repeat Count = 1)	<b>8</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_IvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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LoaMgr\_Per1



Nome	Input Volus				
Name	Input Value				
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0				
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1				
k_LoaMgr_FadeOutStRate_UlspS_f32	1				
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2				
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08		4			
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2				
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	1 0				
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0				
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1				
k LoaMgr HwTqMtgtnRate UlspS f32	1				
k LoaMgr HwTqMtgtnSca Uls f32	0.589399993				
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0				
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0				
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0				
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1				
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0				
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1				
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5				
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1				
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0				
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2				
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1				
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0				
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1				
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2				
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2				
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0				
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0				
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0				
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1				
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1				
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1				
k_LoaMgr_RedcdStRate_UlspS_f32	1				
k_LoaMgr_RedcdStSca_Uls_f32	1				
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0				
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1				
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3				
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1				
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0				
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2				
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0				
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1				
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1				
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ci				
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Ci				
target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08				
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt				
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg	lC			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LoASt_State_enum				
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	t lac			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cn				
	target_LoaMgr_Per1_MotCurrloaMtotpEn_C				
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0				
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MttPostaptSig_Crit_uboo target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Crit_enum	target_LoaMgr_Per1_MitrPosiaptSig_Crit_uo target_LoaMgr_Per1_SteerMod_Crit_enum	<u> </u>			
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_SteenMod_Crit_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Perl_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_SteenMod_Crit_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc				
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc	target LoaMgr Per1 TloaDi Cnt Igc				
Name	Actual Value	Expected Value	Result		
	Actual Value	LANGUEU VAIUE	Nesult		
LoaMgr_AgSV_Cnt_M_lgc		· ·			
THE PROPERTY OF THE PROPERTY O	0	0	<b>4</b>		
LoaMgr_HwTqlgaMtatpEp_Cot_M_u08	0	0	\rightarrow \right		
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0 0 0	0 0 0	~		
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	0 0 0 4	0 0 0 4			
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08	0 0 0 4 0	0 0 0 4 0	<b>*</b>		
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0 0 0 4 0	0 0 0 4 0	<b>y</b>		
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08	0 0 0 4 0 0 5	0 0 0 4 0 0 5	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \		
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	0 0 0 4 0 0 5	0 0 0 4 0 0 5 5	\rightarrow \right		
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	0 0 0 4 0 0 5	0 0 0 4 0 0 5	· · · · · · · · · · · · · · · · · · ·		
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	0 0 0 4 0 0 5 5 5	0 0 0 4 0 0 5 5 5	***		
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	0 0 0 4 0 0 5 5 5 500	0 0 0 4 0 0 0 5 5 5 500	***		
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	0 0 0 4 0 0 0 5 5 5 500 1	0 0 0 4 0 0 0 5 5 5 5 500 1	***		

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	Ī	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.72 (Repeat Count = 1)	<b>8</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	0.0099999978
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inac	ctv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inac	ctv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cn	t_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnE	n_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_en	um	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lg	IC .	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnE	n_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_0	Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtr	En_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_C	nt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_e	num	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lg		
- 3-2	target_Loaningr_r or r_rroa/ train_ont_ig	C	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	C	
		Expected Value	Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	Expected Value	Result
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1	Expected Value	~
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_Igc  Actual Value  1 0	Expected Value 1 0	~
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_Igc  Actual Value  1 0 0	Expected Value  1 0 0	~
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_Igc  Actual Value  1 0 0 0	Expected Value  1 0 0 0	•
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 0 2 1 4	Expected Value  1 0 0 0 2	•
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 0 2 1	Expected Value  1 0 0 0 2 1	• • • • • • • • • • • • • • • • • • •
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_lvtrRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 0 2 1 4	Expected Value  1 0 0 0 2 1 4	• • • • • • • • • • • • • • • • • • •
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 0 2 1 4 4	Expected Value  1 0 0 0 2 1 4 4	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 0 2 1 4 4 500	Expected Value  1 0 0 0 2 1 4 4 500	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOaSt_State_M_enum  LoaMgr_LoaScaleFctr_Uls_M_f32	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 0 0 2 1 4 4 500 0	Expected Value  1 0 0 0 2 1 4 4 500 0	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_ivtrldptSigSV_Cnt_M_u08  LoaMgr_ivtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_ivtrRespSV_Cnt_M_u08  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LoaSt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 0 0 2 1 4 4 500 0 4 1	Expected Value  1 0 0 0 2 1 4 4 500 0 0 1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_ivtrldptSigSV_Cnt_M_u08  LoaMgr_ivtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_ivtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 0 0 2 1 4 500 0 4 1 0 4	Expected Value  1 0 0 0 2 1 4 4 500 0 4 1 0 4	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_ivtrldptSigSV_Cnt_M_u08  LoaMgr_ivtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_ivtrRespSV_Cnt_M_u08  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LoaSt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 0 0 2 1 4 4 500 0 4 1 0 4 2	Expected Value  1 0 0 0 2 1 4 4 500 0 4 1 0 4 2	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LoASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 0 0 2 1 4 4 500 0 4 1 0 4 2 0	Expected Value  1 0 0 0 2 1 4 4 500 0 4 1 0 4 2	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LoASt_State_M_enum  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 0 0 2 1 4 4 500 0 4 1 0 4 2 0 3	Expected Value  1 0 0 0 2 1 4 4 500 0 4 1 0 4 2 0 3	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LoASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 0 0 2 1 4 4 500 0 4 1 0 4 2 0 3 0	Expected Value  1 0 0 0 2 1 4 4 500 0 4 1 0 0 3 0	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgltoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 0 0 2 1 4 4 500 0 4 1 0 4 2 0 3 0 0 0	Expected Value  1 0 0 0 2 1 4 4 500 0 4 1 0 0 3 0 0 0	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldotSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VtgMdSrcSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 0 0 2 1 4 4 500 0 4 1 0 4 2 0 3 0 0 1	Expected Value  1 0 0 0 2 1 4 4 500 0 4 1 0 3 0 0 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRaleLimit_UlspS_M_f32 LoaMgr_LoaRaleLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VttgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 0 0 2 1 4 500 0 4 1 0 0 4 2 0 3 0 0 1 {171, 172, 173}	Expected Value  1 0 0 0 0 2 1 4 4 500 0 4 1 0 0 4 1 1 0 1 1 1 1 1 1 1 1 1 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 0 0 2 1 4 4 500 0 4 1 0 0 3 0 1 {171, 172, 173} {1, 1, 1}	Expected Value  1 0 0 0 0 2 1 4 4 500 0 4 1 0 0 4 1 1 0 1 1 0 4 2 0 3 0 0 1 {171, 172, 173} {0, 0, 0, 0, 0}	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LoASt_State_M_enum LoaMgr_LoaSt_elertr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrtCaMftgnEn_Cnt_M_lgc LoaMgr_MotCurrtCaMftgnEn_Cnt_M_lgc LoaMgr_MotCurrtCaMftgnEn_Cnt_M_lgc LoaMgr_MotCurrtCaMftgnEn_Cnt_M_lgc LoaMgr_MotCurrtCaMftgnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 0 0 2 1 4 4 500 0 4 1 0 0 4 2 0 3 0 1 {171, 172, 173} {1, 1, 1} {1, 1, 1}	Expected Value  1 0 0 0 0 2 1 4 4 500 0 4 1 0 0 1 (711, 172, 173) (0, 0, 0, 0, 0) {1, 1, 1, 1}	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_NotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_Status(Param_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 0 0 2 1 4 4 500 0 4 1 0 0 4 2 0 3 0 0 1 {171, 172, 173} {1, 1, 1} {1, 1, 1} 500	Expected Value  1 0 0 0 0 2 1 4 4 500 0 4 1 0 0 1 (711, 172, 173) (0, 0, 0, 0, 0) {1, 1, 1} 500 ± 0.01	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqIdptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaSt_State_M_enum  LoaMgr_LoaScaleFctr_UIs_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotOurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_Igc  LoaMgr_TloaDi_Cnt_M_Igc  LoaMgr_TloaDi_Cnt_M_Igc  LoaMgr_TloaDi_Cnt_M_Igc  LoaMgr_VitgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UIspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 0 0 2 1 4 4 500 0 4 1 0 0 3 0 1 {171, 172, 173} {1, 1, 1} {1, 1, 1} 500 0	Expected Value  1 0 0 0 0 2 1 4 4 500 0 4 1 0 0 1 (171, 172, 173) {0, 0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqIdptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_LOASt_State_M_enum  LoaMgr_LOaSt_State_M_enum  LoaMgr_LoaScaleFctr_UIs_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgHotSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrIcoaMtgtnEn_Cnt_M_Igc  LoaMgr_TloaAvail_Cnt_M_Igc  LoaMgr_TloaDi_Cnt_M_Igc  LoaMgr_TloaDi_Cnt_M_Igc  LoaMgr_TloaDi_Cnt_M_Igc  LoaMgr_TloaDi_Cnt_M_Igc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UIspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 0 0 0 2 1 4 4 500 0 4 1 0 0 4 2 0 3 0 0 1 {171, 172, 173} {1, 1, 1} {1, 1, 1} 500 0 0 0	Expected Value  1 0 0 0 0 2 1 4 4 500 0 4 1 0 0 1 (171, 172, 173) (0, 0, 0, 0, 0) {1, 1, 1} 500 ± 0.01 0 ± 0.001 0	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 0 0 2 1 4 4 500 0 4 1 0 0 1 (171, 172, 173) (1, 1, 1) {1, 1, 1} 500 0 0 1	Expected Value  1 0 0 0 0 2 1 4 4 500 0 4 1 0 0 1 (171, 172, 173) (0, 0, 0, 0, 0) {1, 1, 1} 500 ± 0.01 0 1 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqIdptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrAeaMtgtnEn_Cnt_M_Igc  LoaMgr_LOASt_State_M_enum  LoaMgr_LOaSt_State_M_enum  LoaMgr_LoaScaleFctr_UIs_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAglcoaMtgtnEn_Cnt_M_Igc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_TloaAvail_Cnt_M_Igc  LoaMgr_TloaDi_Cnt_M_Igc  LoaMgr_TloaDi_Cnt_M_Igc  LoaMgr_VtgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UIspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value  target_LoaMgr_Per1_LOASt_State_enum.value	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 0 0 2 1 4 4 500 0 4 1 0 0 1 {171, 172, 173} {1, 1, 1} {1, 1, 1} 500 0 0 1 4 1 4 4 4	Expected Value  1 0 0 0 0 2 1 4 4 500 0 4 1 0 0 1 (171, 172, 173) (0, 0, 0, 0, 0) {1, 1, 1} 500 ± 0.01 0 ± 0.001 0 1 4	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1 0 0 0 2 1 4 4 500 0 4 1 0 0 1 (171, 172, 173) (1, 1, 1) {1, 1, 1} 500 0 0 1	Expected Value  1 0 0 0 0 2 1 4 4 500 0 4 1 0 0 1 (171, 172, 173) (0, 0, 0, 0, 0) {1, 1, 1} 500 ± 0.01 0 1 1	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	<b>✓</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.73 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr MotAgldptSigSV Cnt M u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	500
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C	:nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_l	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	~
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	_
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value		0	<b>✓</b>
M. D. 4 LOAGE GET	0		
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target_LoaMgr_Per1_LOASt_State_enum.value target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value			~ ~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~



Test Step 4.74 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1.
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	target_Rte_Inst_Ap_LoaMgr
k LoaMgr CurrivtrMtgtnKate_0isps_isz	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	f.
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32 k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	1 0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k LoaMgr MotAgldptSig3Resp Cnt u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	f.
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k LoaMgr RedcdStRate UlspS f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMigr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
2 2 2 1 2 1 3 1 3 2 1 2 1 2 1 2 1 2 1 2	, , , , , , , , , , , , , , , , , , , ,



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Name	Input Value			
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum			
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc			
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	1	1	~	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	<b>✓</b>	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~	
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	•	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>	
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>	
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~	
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•	
LoaMgr_TloaDi_Cnt_M_lgc	1	1	•	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~	
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>	

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.75 (Repeat Count = 1)	<b>12</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	250.478897		
k_LoaMgr_HwTqMtgtnSca_UIs_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	1		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32 k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k LoaMgr MotAgldptSig1NoSmaResp Cnt u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target LoaMgr Per1 TloaAvail Cnt Igc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target LoaMgr Per1 DiagcStslvtr1Inactv C	nt lac	
target Rte Inst Ap LoaMgr.LoaMgr Per1 DiagcStslvtr2Inactv Cnt Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_k		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	<b>-</b>
LoaMgr_HwTqldptSigSV_Cnt_M_u08		0	
	0		
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	1 4	1 4	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08	1 4 0	1 4 0	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1 4 0 1	1 4 0 1	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08	1 4 0 1 5	1 4 0 1 5	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	1 4 0 1 5 5 5	1 4 0 1 5	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	1 4 0 1 5 5 5	1 4 0 1 5 5 5	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	1 4 0 0 1 5 5 5 5 500 0 0	1 4 0 1 5 5 5 500	
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 4 0 0 1 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5	1 4 0 1 5 5 5 500 0 5	0
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	1 4 0 0 1 5 5 5 5 500 0 0	1 4 0 1 5 5 5 500	

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Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.76 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k LoaMgr MotAgMtgtnSca UIs f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSigFltThd Cnt u08	0		
	1		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32			
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target LoaMgr Per1 TloaAvail Cnt Igc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1	N=4 1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	_,	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqIdptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u	u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1		
LoaMgr_lvtrRespSV_Cnt_M_u08		1	✓
		2	<b>*</b>
LUGIVIUI LUASI SIGIE IVI EIIUIII	2	2	\ \ \ \ \ \
LoaMgr_LOASt_State_M_enum	2 5	2 5	· ·
LoaMgr_LoaRateLimit_UlspS_M_f32	2 5 500	2 5 500	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	2 5 500 1	2 5 500 1	~
LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	2 5 500 1 5	2 5 500 1 5	~
LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	2 5 500 1 5	2 5 500 1 5	· ·
LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	2 5 500 1 5 1 0	2 5 500 1 5 1	~
LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	2 5 500 1 5 1 0 5	2 5 500 1 5 1 0 5	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	2 5 500 1 5 1 0 5	2 5 500 1 5 1 0 5	· ·
LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_igc LoaMgr_MotAgRespSV_Cnt_M_u08	2 5 500 1 5 1 0 5	2 5 500 1 5 1 0 5	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2 5 500 1 5 1 0 5	2 5 500 1 5 1 0 5	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	2 5 500 1 5 1 0 5 2	2 5 500 1 5 1 0 5 2	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	2 5 500 1 5 1 0 5 2 0 3	2 5 500 1 5 1 0 5 2 0 3	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc	2 5 500 1 5 1 0 5 2 0 3 1	2 5 500 1 5 1 0 5 2 0 3	***************************************
LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	2 5 500 1 5 1 0 5 2 0 3 1 1 1	2 5 500 1 5 1 0 5 2 0 3 1 1	· · · · · · · · · · · · · · · · · · ·
LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	2 5 500 1 5 1 0 5 2 0 3 1 1 1 0 {170, 172, 173}	2 5 500 1 5 1 0 5 2 0 3 1 1 1 0 ( ( ( ( ( ( ( ( ( ( ( ( ( ( ( (	· · · · · · · · · · · · · · · · · · ·
LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2 5 500 1 5 1 0 5 2 0 3 1 1 1 0 {170, 172, 173}	2 5 500 1 5 1 0 5 2 0 3 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0}	
LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	2 5 500 1 5 1 0 5 2 0 3 1 1 1 0 {170, 172, 173} {1, 1, 1} {1, 1, 1}	2 5 500 1 5 1 0 5 2 0 3 1 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0, 0} {1, 1, 1}	
LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	2 5 500 1 5 1 0 5 2 0 3 1 1 1 0 {170, 172, 173} {1, 1, 1} {1, 1, 1} 500	2 5 500 1 5 1 0 5 2 0 3 1 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01	
LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UIspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	2 5 500 1 5 1 0 5 2 0 3 1 1 1 0 {170, 172, 173} {1, 1, 1} {1, 1, 1} 500 1	2 5 500 1 5 1 0 5 2 0 3 1 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001	
LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rto_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	2 5 500 1 5 1 0 5 2 0 3 1 1 0 {170, 172, 173} {1, 1, 1} {1, 1, 1} 500 1	2 5 500 1 5 1 0 5 2 0 3 1 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001 1	
LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	2 5 500 1 5 1 0 5 2 0 3 1 1 0 {170, 172, 173} {1, 1, 1} {1, 1, 1} 500 1 1	2 5 500 1 5 1 0 5 2 0 3 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001 1	
LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	2 5 500 1 5 1 0 5 2 0 3 1 1 1 0 {170, 172, 173} {1, 1, 1} {1, 1, 1} 500 1	2 5 500 1 5 1 0 5 2 0 3 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001 1 1 5	
LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	2 5 500 1 5 1 0 5 2 0 3 1 1 0 {170, 172, 173} {1, 1, 1} {1, 1, 1} 500 1 1	2 5 500 1 5 1 0 5 2 0 3 1 1 0 {170, 172, 173} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001 1	



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.77 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	ıt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	1	1	~
LoaMgr HwTqldptSigSV Cnt M u08	0	0	<b>~</b>
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	<b>~</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>~</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	
LoaMgr_LOASt_State_M_enum	5	5	<b>~</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	<b>~</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	<b>~</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>V</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>~</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	-
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>V</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	-
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 172, 173}	{170, 172, 173}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 4.78 (Repeat Count = 1)	
Name LoaMgr_AgSV_Cnt_M_lgc	Input Value
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	O
Rte_Inst_Ap_LoaMgr k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08 k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	0
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k LoaMgr MotCurrMtgtnRate UlspS f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
$target\_LoaMgr\_Per1\_DiagcStslvtr1Inactv\_Cnt\_lgc.value$	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•

Test Step 4.79 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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LoaMgr\_Per1



Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_IvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k LoaMgr MotAgldptSig3Resp Cnt u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08 k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt	:_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg	jc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	t lao	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cn target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u		
target Rte Inst Ap LoaMgr.LoaMgr Perl MotCurrLoaMtgtnEn Cnt Igc	target LoaMgr Peri_MotCurrloptSig_Cnt_u target LoaMgr Per1 MotCurrLoaMtgtnEn C		
target Rte Inst Ap LoaMgr.LoaMgr Per1 MtrPosldptSig Cnt u08			
target inte inst ap egaingleodividi peri ivitiposiditsia ent liux		·	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosidptSig_Cnt_uu8 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum	·	
	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	·	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum	·	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc	·	Result
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc	8	Result 🗸
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0	Expected Value 0 0	<b>*</b>
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_Igc target_LoaMgr_Per1_TloaDi_Cnt_Igc Actual Value 0 0 0	Expected Value 0 0 0	<b>*</b>
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_Igc target_LoaMgr_Per1_TloaDi_Cnt_Igc Actual Value 0 0 0 4	Expected Value 0 0 0 4	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_Igc target_LoaMgr_Per1_TloaDi_Cnt_Igc Actual Value 0 0 0 4	Expected Value  0 0 0 4 0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_Igc target_LoaMgr_Per1_TloaDi_Cnt_Igc Actual Value 0 0 0 0 0 0 0 0 0	Expected Value  0 0 0 4 0 0	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvaii_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 0 0 4 0 0 5	Expected Value  0 0 0 4 0 0 5	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IOASt_State_M_enum	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_Igc target_LoaMgr_Per1_TloaDi_Cnt_Igc Actual Value 0 0 0 0 5 5 5	Expected Value 0 0 0 4 0 0 5 5	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvaii_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value 0 0 0 4 0 0 5	Expected Value  0 0 0 4 0 0 5	***
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrAeAmtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvaii_Cnt_Igc target_LoaMgr_Per1_TloaDi_Cnt_Igc  Actual Value 0 0 0 4 0 0 5 5 5 5000	Expected Value 0 0 0 4 0 0 5 5 5	****
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LoaAtgtnEn_Cnt_M_lgc LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvaii_Cnt_Igc target_LoaMgr_Per1_TloaDi_Cnt_Igc  Actual Value 0 0 0 4 0 0 5 5 5 500 1	Expected Value 0 0 0 4 0 0 5 5 5 1	***************************************
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrQaAttgtnEn_Cnt_M_Igc LoaMgr_LoaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvaii_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 0 4 0 0 5 5 5 500 1 5	Expected Value 0 0 0 4 0 0 5 5 5 500 1 5	***************************************

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	<b>~</b>
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.80 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





	1		
Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	0.697300017		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	•		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lo	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	1=	1
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc	0	0	~
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	<b>-</b>
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	· ·
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Input Value
LoaMgr_AgSV_Cnt_M_lgc       1         LoaMgr_HwTqldptSigSV_Cnt_M_u08       2         LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc       0         LoaMgr_IvtrldptSigSV_Cnt_M_u08       0         LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc       1         LoaMgr_IvtrRespSV_Cnt_M_u08       4         LoaMgr_MotAgldptSigSV_Cnt_M_u08       1         LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc       0         LoaMgr_MotAgRespSV_Cnt_M_u08       4         LoaMgr_MotCurrIdptSigSV_Cnt_M_u08       4         LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc       0         LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc       0         LoaMgr_MotCurrRespSV_Cnt_M_u08       2         LoaMgr_MotCurrRespSV_Cnt_M_u08       3         LoaMgr_SnsrisMotAgAvail_Cnt_M_lgc       1
LoaMgr_HwTqldptSigSV_Cnt_M_u08       2         LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc       0         LoaMgr_HwTqRespSV_Cnt_M_u08       0         LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc       1         LoaMgr_IvtrRespSV_Cnt_M_u08       4         LoaMgr_MotAgldptSigSV_Cnt_M_u08       1         LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc       0         LoaMgr_MotAgRespSV_Cnt_M_u08       4         LoaMgr_MotCurrldptSigSV_Cnt_M_u08       4         LoaMgr_MotCurrldptSigSV_Cnt_M_u08       2         LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc       0         LoaMgr_MotCurrRespSV_Cnt_M_u08       2         LoaMgr_MotCurrRespSV_Cnt_M_u08       3         LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc       1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_HwTqRespSV_Cnt_M_u08         0           LoaMgr_IvtrldptSigSV_Cnt_M_u08         2           LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc         1           LoaMgr_MotAgldptSigSV_Cnt_M_u08         4           LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotAgRespSV_Cnt_M_u08         1           LoaMgr_MotQurrldptSigSV_Cnt_M_u08         4           LoaMgr_MotCurrldptSigSV_Cnt_M_u08         4           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotCurrRespSV_Cnt_M_u08         2           LoaMgr_MotCurrRespSV_Cnt_M_u08         3           LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc         1
LoaMgr_HwTqRespSV_Cnt_M_u08       0         LoaMgr_IvtrIdptSigSV_Cnt_M_u08       2         LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc       1         LoaMgr_MotAgldptSigSV_Cnt_M_u08       4         LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc       0         LoaMgr_MotAgRespSV_Cnt_M_u08       4         LoaMgr_MotCurrIdptSigSV_Cnt_M_u08       4         LoaMgr_MotCurrIdptSigSV_Cnt_M_u08       2         LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc       0         LoaMgr_MotCurrRespSV_Cnt_M_u08       2         LoaMgr_MotCurrRespSV_Cnt_M_u08       3         LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc       1
LoaMgr_IvtrIdptSigSV_Cnt_M_u08         2           LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc         1           LoaMgr_NotRespSV_Cnt_M_u08         4           LoaMgr_MotAgIdptSigSV_Cnt_M_u08         1           LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotQrrldptSigSV_Cnt_M_u08         4           LoaMgr_MotCurrldptSigSV_Cnt_M_u08         2           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotCurrRespSV_Cnt_M_u08         2           LoaMgr_MotCurrRespSV_Cnt_M_u08         3           LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc         1
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc       1         LoaMgr_IvtrRespSV_Cnt_M_u08       4         LoaMgr_MotAgldptSigSV_Cnt_M_u08       1         LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc       0         LoaMgr_MotQrrldptSigSV_Cnt_M_u08       4         LoaMgr_MotCurrldptSigSV_Cnt_M_u08       2         LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc       0         LoaMgr_MotCurrRespSV_Cnt_M_u08       3         LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc       1
LoaMgr_IvtrRespSV_Cnt_M_u08       4         LoaMgr_MotAgldptSigSV_Cnt_M_u08       1         LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc       0         LoaMgr_MotAgRespSV_Cnt_M_u08       4         LoaMgr_MotCurrldptSigSV_Cnt_M_u08       2         LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc       0         LoaMgr_MotCurrRespSV_Cnt_M_u08       3         LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc       1
LoaMgr_MotAgldptSigSV_Cnt_M_u08         1           LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotAgRespSV_Cnt_M_u08         4           LoaMgr_MotCurrldptSigSV_Cnt_M_u08         2           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotCurrRespSV_Cnt_M_u08         3           LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc         1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotAgRespSV_Cnt_M_u08         4           LoaMgr_MotCurrldptSigSV_Cnt_M_u08         2           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotCurrRespSV_Cnt_M_u08         3           LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc         1
LoaMgr_MotAgRespSV_Cnt_M_u08       4         LoaMgr_MotCurrIdptSigSV_Cnt_M_u08       2         LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc       0         LoaMgr_MotCurrRespSV_Cnt_M_u08       3         LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc       1
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 2 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 0 LoaMgr_MotCurrRespSV_Cnt_M_u08 3 LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc 1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc       0         LoaMgr_MotCurrRespSV_Cnt_M_u08       3         LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc       1
LoaMgr_MotCurrRespSV_Cnt_M_u08 3 LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc 1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc 1
V- V
LoaMgr_VltgMdSrcSV_Cnt_M_u08 1
Rte_Inst_Ap_LoaMgr target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtMtgtnRate_UlspS_f32 1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc 0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32 1
k LoaMgr FadeOutStRate UlspS f32 1
k LoaMgr HwTqldptSiq0TloaAvailResp Cnt u08 2
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08 4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08
k_LoaMgr_HwTqMtgtnRate_UlspS_f32 1
k_LoaMgr_HwTqMtgtnSca_Uls_f32
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08 0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08 0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08 0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32 1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc 0
k_LoaMgr_lvtrMtgtnSca_Uls_f32 1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 5
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc 0 k_LoaMgr_MotAgMtgtnSca_Uls_f32 1
2 4 32 4 3 3 4 4 2 2 4
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 2
k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0
k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0
k_LoaMgr_MotCurridptSigFltThd_Cnt_u08 0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc 1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32 1
k_LoaMgr_RedcdStRate_UlspS_f32 1
k_LoaMgr_RedcdStSca_Uls_f32 1
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value 0





LoaMgr_HNTQIqtPiSigSV_Cnt_M_u08 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Name	Input Value		
	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
Image   Loading   Per   Jimman   Jimm	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
Integral Loaking Pert   Toaking   Chip cavalue   0	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_Loadeg_Pert_Timosit_Cet_govalue	target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
	target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_Ris_Inst_A_LoaMgr_LoaMgr_Pert_Inst_A_LoaMgr_Dert_Inst_A_LoaMg	target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	Cnt_lgc	
target_Rel_Inst_Ap_LoaMgr_LoaMgr_Pert_N=T_LoaMgrEn_Cnt_Igc   target_LeaMgr_Pert_N=T_LoaMgr_LoaMgr_Pert_N=T_LoaMgr_LoaMgr_Pert_N=T_LoaMgr_LoaMgr_Pert_N=T_LoaMgr_N=T	target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_	Cnt_lgc	
	target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u(	08	
target_Rie_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASs_Diate_enum   target_LoaMgr_Per1_LoASs_Diate_enum   target_LoaMgr_Per1_LoASs_Diate_enum   target_LoaMgr_Per1_LoaSs_Diate_enum   target_LoaMgr_Per1_LoaSs_Diate_enum   target_LoaMgr_Per1_LoaMgr_LoaMgr_Per1_Mort_LoaMgr_Diate_enum   target_LoaMgr_Per1_Mort_LoaMgr_Diate_enum   target_LoaMgr_Per1_ToaAvail_Crit_lige   target_L	target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
Isrget_Rie_Inst_Ap_LoaMgr.LoaMgr_Perl _MokQucaMignEn_Cnt_lgc   Isrget_LoaMgr_Perl _MokQuridpsis_Cnt_u08   Isrget_Rie_Inst_Ap_LoaMgr.LoaMgr_Perl _MokQuridpsis_Cnt_u08   Isrget_Rie_Inst_Ap_LoaMgr.LoaMgr_Perl _MokQuridpsis_Cnt_u08   Isrget_Rie_Inst_Ap_LoaMgr.LoaMgr_Perl _MokQuridpsis_Cnt_u08   Isrget_Rie_Inst_Ap_LoaMgr.LoaMgr_Perl _MokQuridpsis_Cnt_u08   Isrget_Rie_Inst_Ap_LoaMgr.LoaMgr_Perl _MokQuridpsis_Cnt_u08   Isrget_Rie_Inst_Ap_LoaMgr.LoaMgr_Perl_MokQuridpsis_Cnt_u08   Isrget_Rie_Inst_Ap_LoaMgr.LoaMgr_Perl_SteenMod_Cnt_enum   Isrget_Rie_Inst_Ap_LoaMgr.LoaMgr_Perl_TinaAvall_Cnt_lgc   Isrget_Rie_Inst_Ap_LoaMgr.LoaMgr_Perl_TinaAvall_Cnt_lgc   Isrget_LoaMgr_Perl_TinaAvall_Cnt_lgc   Isrget_LoaMgr_Perl_TinaAvall_Cnt_Mgr_Perl_TinaAvall_Cnt_Mgr_Perl_TinaAvall_Cnt_Mgr_Perl_TinaAvall_Cnt_Mgr_Perl_TinaAvall_Cnt_Mgr_Perl_TinaAvall_Cnt_Mgr_Perl_TinaAvall_Cnt_Mgr_Perl_TinaAvall_Cnt_Mgr_Perl_TinaAvall_Cnt_Mgr_Perl_TinaAvall_Cnt_Mgr_Perl_TinaAvall_Cnt_Mgr_Perl_TinaAvall_Cnt_Mgr_Perl_TinaAvall_Cnt_Mgr_Perl_MokQraMgr_Perl_MokQraMgr_Perl_MokQraMgr_Perl_MokQraMgr_Perl_Mgr_Perl_MokQraMgr_Perl_Mgr_Perl_Mgr_Perl_MokQraMgr_Perl_Mgr_Per	target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_	lgc	
Isrget_Rie_Inst_Ap_LoaMgr.LoaMgr_Perl _MokQucaMignEn_Cnt_lgc   Isrget_LoaMgr_Perl _MokQuridpsis_Cnt_u08   Isrget_Rie_Inst_Ap_LoaMgr.LoaMgr_Perl _MokQuridpsis_Cnt_u08   Isrget_Rie_Inst_Ap_LoaMgr.LoaMgr_Perl _MokQuridpsis_Cnt_u08   Isrget_Rie_Inst_Ap_LoaMgr.LoaMgr_Perl _MokQuridpsis_Cnt_u08   Isrget_Rie_Inst_Ap_LoaMgr.LoaMgr_Perl _MokQuridpsis_Cnt_u08   Isrget_Rie_Inst_Ap_LoaMgr.LoaMgr_Perl _MokQuridpsis_Cnt_u08   Isrget_Rie_Inst_Ap_LoaMgr.LoaMgr_Perl_MokQuridpsis_Cnt_u08   Isrget_Rie_Inst_Ap_LoaMgr.LoaMgr_Perl_SteenMod_Cnt_enum   Isrget_Rie_Inst_Ap_LoaMgr.LoaMgr_Perl_TinaAvall_Cnt_lgc   Isrget_Rie_Inst_Ap_LoaMgr.LoaMgr_Perl_TinaAvall_Cnt_lgc   Isrget_LoaMgr_Perl_TinaAvall_Cnt_lgc   Isrget_LoaMgr_Perl_TinaAvall_Cnt_Mgr_Perl_TinaAvall_Cnt_Mgr_Perl_TinaAvall_Cnt_Mgr_Perl_TinaAvall_Cnt_Mgr_Perl_TinaAvall_Cnt_Mgr_Perl_TinaAvall_Cnt_Mgr_Perl_TinaAvall_Cnt_Mgr_Perl_TinaAvall_Cnt_Mgr_Perl_TinaAvall_Cnt_Mgr_Perl_TinaAvall_Cnt_Mgr_Perl_TinaAvall_Cnt_Mgr_Perl_TinaAvall_Cnt_Mgr_Perl_TinaAvall_Cnt_Mgr_Perl_MokQraMgr_Perl_MokQraMgr_Perl_MokQraMgr_Perl_MokQraMgr_Perl_Mgr_Perl_MokQraMgr_Perl_Mgr_Perl_Mgr_Perl_MokQraMgr_Perl_Mgr_Per			•	
target_Rie_Inst_Ap_Loaklyr_Loaklyr_Pert_MotCurridot\( Size_Pert_Rie_Inst_Ap_Loaklyr_Loaklyr_Pert_MotCurridot\( \text{Size_Pert_Rie_Inst_Ap_Loaklyr_Loaklyr_Pert_MotCurridot\( \text{Size_Pert_Rie_Inst_Ap_Loaklyr_Loaklyr_Pert_MotCurridot\( \text{Size_Pert_Rie_Inst_Ap_Loaklyr_Loaklyr_Pert_MotCurridot\( \text{Size_Pert_Rie_Inst_Ap_Loaklyr_Loaklyr_Pert_MotCurridot\( \text{Size_Pert_Rie_Inst_Ap_Loaklyr_Loaklyr_Pert_MotCurridot\( \text{Size_Pert_Rie_Inst_Ap_Loaklyr_Loaklyr_Pert_Indot\( \text{Circ_Pert_Rie_Inst_Ap_Loaklyr_Loaklyr_Pert_Indot\( \text{Circ_Pert_Rie_Inst_Ap_Loaklyr_Loaklyr_Pert_Indot\( \text{Circ_Pert_Rie_Inst_Ap_Loaklyr_Loaklyr_Pert_Indot\( \text{Circ_Pert_Rie_Inst_Ap_Loaklyr_Loaklyr_Pert_Indot\( \text{Circ_Pert_Rie_Inst_Ap_Loaklyr_Loaklyr_Pert_Indot\( \text{Circ_Pert_Rie_Inst_Ap_Loaklyr_Loaklyr_Pert_Indot\( \text{Circ_Pert_Rie_Inst_Ap_Loaklyr_Loaklyr_Pert_Indot\( \text{Circ_Pert_Rie_Inst_Ap_Loaklyr_Loaklyr_Pert_Indot\( \text{Circ_Pert_Rie_Inst_Ap_Loaklyr_Loaklyr_Loaklyr_Pert_Indot\( \text{Circ_Pert_Rie_Inst_Ap_Loaklyr_Loaklyr_Loaklyr_Loaklyr_Pert_Indot\( \text{Circ_Pert_Rie_Pert_Rie_Inst_Ap_Loaklyr_Loak				
			Ont lgc	
larget_Rie_Inst_Ap_LoaMgr_LoaMgr_Pert_Microurt.oaMtginEn_Cnt_lgc				
larget_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_SteerMod_Cnt_enum   target_LoaMgr_Pert_SteerMod_Cnt_enum   target_LoaMgr_Pert_SteerMod_Cnt_enum   target_LoaMgr_Pert_SteerMod_Cnt_enum   target_LoaMgr_Pert_SteerMod_Cnt_enum   target_LoaMgr_Pert_SteerMod_Cnt_enum   target_LoaMgr_Pert_ToaAvail_Cnt_Ugc   target_LoaMgr_LoaAvail_Cnt_Ugc   target_LoaMgr_NotCurrLoaAvail_Cnt_Ugc   target_LoaMgr_Not				
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum         target_LoaMgr_Per1_SteerMod_Cnt_enum           target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvall_Cnt_ligo         target_LoaMgr_Per1_TloaAvall_Cnt_ligo           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_ligo         target_LoaMgr_Per1_TloaDi_Cnt_ligo           Name         Actual Value         Expected Value         Result           LoaMgr_HorTqicphSigsV_Cnt_M_L08         1         1         4           LoaMgr_HorTqicphSigsV_Cnt_M_L08         0         0         0           LoaMgr_HorTqicaMighten_Cnt_M_L08         1         1         1           LoaMgr_HortLoaMighten_Cnt_M_L08         1         1         1           LoaMgr_HortLoaMighten_Cnt_M_L08         1         1         1           LoaMgr_HortLoaMighten_Cnt_M_L08         1         1         1           LoaMgr_HortLoaMighten_Cnt_M_L08         4         4         4           LoaMgr_LoaRateLimit_UlspS_M_132         500         500         0           LoaMgr_LoaRateLimit_UlspS_M_132         500         0         0           LoaMgr_MotAglosilsigsV_Cnt_M_L08         4         4         4           LoaMgr_MotAglosilsigsV_Cnt_M_L08         1         1         1           LoaMgr_MotAglosilsigsV_Cnt_M_L08         4         4				
target_Rte_inst_Ap_LoaMgr_LoaMgr_Peri_TloaAvail_Cnt_lgc   target_LoaMgr_Peri_TloaDi_Cnt_lgc   target_LoaMgr_Peri_TloaDi_Cnt_lgc   target_LoaMgr_Peri_TloaDi_Cnt_lgc				
target_Re_Inst_Ap_LoaMgr_LoaMgr_Perl_TloaDi_Cnt_Ugc				
Name				
LoaMgr_HNTqlqtplisjgSV_Cnt_M_u08 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1			Expected Value	Result
LoaMgr_HwTqLoaMtgInEn_Cnt_M_lgc			•	
LoaMgr HwTqLoaMtgtnEn_Cnt_M_lgc				
LoaMgr_HwTqRespSV_Cnt_M_u08	0_ 11 0			
LoaMgr_IvtrIoptSigSV_Cnt_M_u08				
LoaMgr_IvtrLoaMightEn_Cnt_M_lgc				
LoaMgr_VtrRespSV_Cnt_M_u08				
LoaMgr_LoaScaleFctr_Uls_M_f32				
LoaMgr_LoaRateLimit_UlspS_M_r32 500 500 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00				
LoaMgr_LoaScaleFctr_Uls_M_f32         0         0           LoaMgr_MaxMtgtnResp_Cnt_M_u08         4         4           LoaMgr_MotAgldptSigSV_Cnt_M_u08         1         1           LoaMgr_MotAglcasMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_MotCurrloptSigSV_Cnt_M_u08         4         4           LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_MotCurrloaMtgnEn_Cnt_M_lgc         0         0           LoaMgr_TloaDi_Cnt_M_lgc         0         0           LoaMgr_TloaDi_Cnt_M_lgc         0         0           LoaMgr_VltgMdSrcSV_Cnt_M_u08         1         1           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         (171, 172, 173)         (171, 172, 173)           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_Tu08)         (1, 1, 1)         (0, 0, 0, 0, 0)         3           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         (1, 1, 1)         (1, 1, 1)         (0, 0, 0, 0, 0)         3           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         (1, 1, 1)         (0, 0, 0, 0, 0)         3           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Cott_T_enum)         (1, 1, 1)         (0, 0, 0, 0, 0)         3 <td></td> <td></td> <td></td> <td></td>				
LoaMgr_MaxMtgtnResp_Cnt_M_u08				
LoaMgr_MotAgldptSigSV_Cnt_M_u08         1         1         V           LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc         0         0         V           LoaMgr_MotAgRespSV_Cnt_M_u08         4         4         4           LoaMgr_MotCurrtLoaMtgtnEn_Cnt_M_lgc         0         0         0           LoaMgr_MotCurrtRespSV_Cnt_M_u08         3         3         V           LoaMgr_TloaAvail_Cnt_M_lgc         0         0         0           LoaMgr_TloaD_Cnt_M_lgc         0         0         0           LoaMgr_TloaD_Cnt_M_lgc         0         0         0           LoaMgr_VltgMdSrcsV_Cnt_M_u08         1         1         V           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {171, 172, 173}         {171, 172, 173}         V           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {0, 0, 0, 0, 0}         X           Rte_Wite_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}         {1, 1, 1}           Rte_Wite_Ap_LoaMgr_LoaRateLimit_UlspS_732(data)         500         500 ± 0.01         X           Rte_Wite_Ap_LoaMgr_LoaRateLimit_UlspS_732(data)         0         0 ± 0.001         X           target_LoaMgr_Per1_HwTQLoaMtgtnEn_Cnt_lgc.value         0 <t< td=""><td></td><td></td><td></td><td></td></t<>				
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_MotAgRespSV_Cnt_M_u08         4         4           LoaMgr_MotCurrldptSigSV_Cnt_M_u08         2         2           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_MotCurrRespSV_Cnt_M_u08         3         3           LoaMgr_TloaAvail_Cnt_M_lgc         0         0           LoaMgr_TloaDi_Cnt_M_lgc         0         0           LoaMgr_TloaDi_Cnt_M_lgc         0         0           LoaMgr_VttgMdSrcsV_Cnt_M_u08         1         1           Rte_Call_Ap_LoaMgr_SetNTCStatus(NTC_Cnt_T_enum)         {171, 172, 173}         {171, 172, 173}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}           Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01         V           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         V           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         1         1         1           target_LoaMgr_Per1_MotAgLoaMtgtnEn_				
LoaMgr_MotAgRespSV_Cnt_M_u08         4         4           LoaMgr_MotCurrldptSigSV_Cnt_M_u08         2         2           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_MotCurrRespSV_Cnt_M_u08         3         3           LoaMgr_TloaAvail_Cnt_M_lgc         0         0           LoaMgr_TloaDi_Cnt_M_lgc         0         0           LoaMgr_VttgMdSrcSV_Cnt_M_u08         1         1           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {171, 172, 173}         {171, 172, 173}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}           Rte_Wite_Ap_LoaMgr_LoaRateLimit_Ulsps_f32(data)         500         500 ± 0.01         X           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         X           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         0         0         0           target_LoaMgr_Per1_HotAgLoaMtgtnEn_Cnt_lgc.value         1         1         4           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0 </td <td></td> <td></td> <td></td> <td></td>				
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08         2         2           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_MotCurrRespSV_Cnt_M_u08         3         3           LoaMgr_TloaAvail_Cnt_M_lgc         0         0           LoaMgr_VitgMdSrcSV_Cnt_M_u08         1         1           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {171, 172, 173}         {171, 172, 173}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001           Rte_Write_Ap_LoaMgr_LoaMtgtnEn_Cnt_lgc.value         0         0           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         1         1           target_LoaMgr_Per1_LOASt_State_enum.value         4         4           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0				
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_MotCurrRespSV_Cnt_M_u08         3         3           LoaMgr_TloaAvail_Cnt_M_lgc         0         0           LoaMgr_VitgMdSrcSV_Cnt_M_u08         1         1           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {171, 172, 173}         {171, 172, 173}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001           Rte_Write_Ap_LoaMgr_LoaMtgtnEn_Cnt_lgc.value         0         0           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         1         1           target_LoaMgr_Per1_LOASt_State_enum.value         4         4           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0				
LoaMgr_MotCurrRespSV_Cnt_M_u08       3       3         LoaMgr_TloaAvail_Cnt_M_lgc       0       0         LoaMgr_VitgMdSrcSV_Cnt_M_u08       1       1         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       {171, 172, 173}       {171, 172, 173}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       {1, 1, 1}       {0, 0, 0, 0, 0}       X         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       {1, 1, 1}       {1, 1, 1}       {1, 1, 1}       X         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01       X         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001       X         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001       X         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       0       0       X         target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value       1       1       1         target_LoaMgr_Per1_LOASt_State_enum.value       4       4       X         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0       X				
LoaMgr_TloaAvail_Cnt_M_lgc         0         0         0           LoaMgr_TloaDi_Cnt_M_lgc         0         0         0           LoaMgr_VitgMdSrcSV_Cnt_M_u08         1         1         4           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {171, 172, 173}         {171, 172, 173}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001           Rte_Write_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         0         0           target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value         1         1           target_LoaMgr_Per1_LOASt_State_enum.value         4         4           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0				
LoaMgr_TloaDi_Cnt_M_Igc       0       0         LoaMgr_VltgMdSrcSV_Cnt_M_u08       1       1         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       {171, 172, 173}       {171, 172, 173}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       {1, 1, 1}       {0, 0, 0, 0, 0}       X         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       {1, 1, 1}       {1, 1, 1}       {1, 1, 1}       X         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01       X         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001       X         rtarget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       0       0       X         target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value       1       1       X         target_LoaMgr_Per1_LOASt_State_enum.value       4       4       X         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0       X				
LoaMgr_VitgMdSrcSV_Cnt_M_u08       1       1         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       {171, 172, 173}       {171, 172, 173}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       {1, 1, 1}       {0, 0, 0, 0, 0}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       {1, 1, 1}       {1, 1, 1}         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001         rtarget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       0       0         target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value       1       1         target_LoaMgr_Per1_LOASt_State_enum.value       4       4         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0				
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       {171, 172, 173}       {171, 172, 173}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       {1, 1, 1}       {0, 0, 0, 0, 0}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       {1, 1, 1}       {1, 1, 1}         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001         xtarget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       0       0         target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value       1       1         target_LoaMgr_Per1_LOASt_State_enum.value       4       4         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0		0	0	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         0         0           target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value         1         1           target_LoaMgr_Per1_LOASt_State_enum.value         4         4           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0		1	1	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       {1, 1, 1}       {1, 1, 1}         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       0       0         target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value       1       1         target_LoaMgr_Per1_LOASt_State_enum.value       4       4         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0				
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       0       0         target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value       1       1         target_LoaMgr_Per1_LOASt_State_enum.value       4       4         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0				<b></b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)         0         0 ± 0.001         ✓           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value         0         0         ✓           target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value         1         1         ✓           target_LoaMgr_Per1_LOASt_State_enum.value         4         4         ✓           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc.value         0         0         ✓			1 -	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1				
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value				~
target_LoaMgr_Per1_LOASt_State_enum.value 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4				~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0				~
	target_LoaMgr_Per1_LOASt_State_enum.value			~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value 0	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

LoaMgr\_Per1



Test Step 4.82 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32 k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1 0
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	500
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32 k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_tvtLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_Loascabi_Cht_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
2 2 2 1 2 1 2 1 2 1 2 1 2 1 2 1 2 1 2	, , , , , , , , , , , , , , , , , , , ,





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.83 (Repeat Count = 1)		EX.
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_lvtrRespSV_Cnt_M_u08	1	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	0	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

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Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
	1		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08			
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k LoaMgr MotAgldptSig1NoSmaResp Cnt u08	5		
	1		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08			
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32			
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target LoaMgr Per1 SteerMod Cnt enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	ent lac	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lo	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target LoaMgr Per1 TloaDi Cnt Igc		
Name	Actual Value	Expected Value	Popult
		Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	✓
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	_

LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc LoaMgr\_MotAgRespSV\_Cnt\_M\_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	<b>~</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.84 (Repeat Count = 1)	N .
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	300.489197		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enu	m 	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	1	1
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	<b>Y</b>
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_IvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	<b>*</b>
LoaMgr_LOASt_State_M_enum	500	500	
LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	-
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	-
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr MotAgRespSV Cnt M u08	5	5	-
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr MotCurrRespSV Cnt M u08	2	2	
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	-
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
			~
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	{1, 1, 1}	{1, 1, 1}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	{1, 1, 1} 500	{1, 1, 1} 500 ± 0.01	•
			<b>*</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	500	500 ± 0.01 0 ± 0.001	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	500 0 1	500 ± 0.01 0 ± 0.001 1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	500 0 1 1	500 ± 0.01 0 ± 0.001 1	· ·
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	500 0 1 1 5	500 ± 0.01 0 ± 0.001 1 1 5	\(\frac{1}{2}\)



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	<b>~</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>~</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	<b>~</b>
AssignScale	1	AssignScale	1	<b>~</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>✓</b>

- 101 105 (D. 100 11)	
Test Step 4.85 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrlvtrMtgtnScaZeroEn Cnt Igc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_Igc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1
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Name	Input Value		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u(	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0	Ont Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn	Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enun		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	_
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	<b>~</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	-
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	2	2	
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	•
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>~</b>
target LoaMgr Per1 MotAgLoaMtgtnEn Cnt Igc.value	0	0	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	•

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>✓</b>



Test Step 4.86 (Repeat Count = 1)	12
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08 k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k LoaMgr IvtrMtgtnRate UlspS f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	0
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	0
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_InvrQLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_IvtrRespSV_Cnt_M_u08	4	4	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	<b>~</b>
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	<b>~</b>
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>~</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.87 (Repeat Count = 1)	<b>13</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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LoaMgr\_Per1



Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32 k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	1 0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	0		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target LoaMgr Per1 TloaAvail Cnt Igc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	_ ·	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lo	gc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target LoaMar Bord Mothal achitetaE- O-	at Ide	
	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr		
V =	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0	08 Cnt_lgc	
	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	08 Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0	08 Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u target_LoaMgr_Per1_MotCurrLoaMtgtnEn_c target_LoaMgr_Per1_MtrPosIdptSig_Cnt_uC target_LoaMgr_Per1_SteerMod_Cnt_enum	08 Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u target_LoaMgr_Per1_MotCurrLoaMtgtnEn_c target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc	08 Cnt_lgc	Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_MotCurrldptSig_Cnt_utarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0target_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgc	Dotalgo  Expected Value	Result
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_utarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0target_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgcActual Value  0 0	Dotal Control	<b>Y</b>
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvaii_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_utarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_ctarget_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0target_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 0 1	Dot_lgc  Expected Value  0 0 1	<b>*</b>
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvaii_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_utarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_ctarget_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0target_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 0 1 2	Expected Value  0 0 1 2	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvaii_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_utarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosIdptSig_Cnt_uCtarget_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_Igctarget_LoaMgr_Per1_TloaDi_Cnt_Igc  Actual Value  0 0 1 2 0	Expected Value  0 0 1 2 0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvaii_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_utarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosIdptSig_Cnt_uCtarget_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_Igctarget_LoaMgr_Per1_TloaDi_Cnt_Igc  Actual Value  0 0 1 2 0 1	Expected Value	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvaii_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_utarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosIdptSig_Cnt_uCtarget_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_Igctarget_LoaMgr_Per1_TloaDi_Cnt_Igc  Actual Value  0 0 1 2 0 1 5	Expected Value	***
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvaii_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIoaMtgtnEn_Cnt_M_U08 LoaMgr_IvtrIoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_utarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosIdptSig_Cnt_uCtarget_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_Igctarget_LoaMgr_Per1_TloaDi_Cnt_Igc  Actual Value  0 0 1 2 0 1 5 5	Expected Value	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvaii_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaSt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_utarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosIdptSig_Cnt_uCtarget_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_Igctarget_LoaMgr_Per1_TloaDi_Cnt_Igc  Actual Value  0 0 1 2 0 1 5	Expected Value	***
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvaii_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIoaMtgtnEn_Cnt_M_U08 LoaMgr_IvtrIoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_utarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosIdptSig_Cnt_utarget_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 0 1 2 0 1 5 5 5 500	Expected Value	***
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaState_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_utarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosIdptSig_Cnt_uCtarget_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_Igctarget_LoaMgr_Per1_TloaDi_Cnt_Igc  Actual Value  0 0 1 2 0 1 5 5 5 500	Expected Value	******
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LoaSt_state_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_utarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_uCtarget_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 0 1 2 0 1 5 5 5 500 0 5	Expected Value	******

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.88 (Repeat Count = 1)	N .
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSigFltThd Cnt u08	0		
k LoaMgr MotCurrMtgtnRate UlspS f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
	1		
k_LoaMgr_RedcdStSca_UIs_f32	0		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value			
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target LoaMgr Per1 DiagcStslvtr2Inactv C	Cnt Igc	
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqldptSig Cnt u08	target LoaMgr Per1 HwTqldptSig Cnt u0	В	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	90	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
		at lan	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
Name LoaMgr_AgSV_Cnt_M_lgc	Actual Value	Expected Value 0	Result
		•	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	0 0 0 4	0 0 0 4	* *
LoaMgr_Ag\$V_Cnt_M_lgc LoaMgr_HwTqldptSig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_lvtrldptSig\$V_Cnt_M_u08	0 0 0 4 0	0 0 0 4 0	* *
LoaMgr_Ag\$V_Cnt_M_lgc LoaMgr_HwTqldptSig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_lvtrldptSig\$V_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0 0 0 4 0	0 0 0 4 0	* *
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LoaMgr_Ag\$V_Cnt_M_lgc LoaMgr_HwTqldptSig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_IvtrldptSig\$V_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrResp\$V_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_Ulsp\$_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSig\$V_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_ToaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMd\$rc\$V_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTC\$tatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTC\$tatus(Param_Cnt_T_u08)	0 0 0 4 0 0 0 5 5 5 5 500 1 1 5 2 1 4 0 0 1 2 1 1 2 1	0 0 0 4 0 0 5 5 5 500 1 5 2 1 4 0 1 2 1 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0, 0}	
LoaMgr_Ag\$V_Cnt_M_lgc LoaMgr_HwTqldptSig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_IvtrldptSig\$V_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrResp\$V_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_Ulsp\$_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSig\$V_Cnt_M_u08 LoaMgr_MotAgldptSig\$V_Cnt_M_u08 LoaMgr_MotAgResp\$V_Cnt_M_u08 LoaMgr_MotCurrloptSig\$V_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrc\$V_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 0 0 4 0 0 0 5 5 5 5 500 1 1 5 2 1 4 0 0 1 2 1 1 2	0 0 0 4 0 0 5 5 5 500 1 5 2 1 4 0 1 2 1 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1, 1}	
LoaMgr_Ag\$V_Cnt_M_lgc LoaMgr_HwTqldptSig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_IvtrldptSig\$V_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrResp\$V_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSig\$V_Cnt_M_u08 LoaMgr_MotAgldptSig\$V_Cnt_M_u08 LoaMgr_MotAgResp\$V_Cnt_M_u08 LoaMgr_MotCurrldptSig\$V_Cnt_M_u08 LoaMgr_MotCurrldptSig\$V_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrc\$V_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 0 0 4 0 0 0 5 5 5 500 1 5 2 1 4 0 1 2 1 1 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1} 5 5 1 1 1 1 1 1 1 1 1 1 1 1 1	0 0 4 0 0 5 5 5 500 1 5 2 1 4 0 1 2 1 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1, 1} 500 ± 0.01	
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIcoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvaii_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UIspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	0 0 0 4 0 0 5 5 5 500 1 5 2 1 4 0 1 2 1 1 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1} 500 1	0 0 0 4 0 0 0 5 5 5 500 1 5 2 1 4 0 1 2 1 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001	
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_StyTcStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UIspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	0 0 0 4 0 0 0 5 5 5 5 500 1 5 2 1 4 0 1 2 1 1 0 {170, 172, 173, 174} {1, 1, 1, 1} 500 1	0 0 4 0 0 5 5 5 500 1 5 2 1 4 0 1 2 1 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 0	
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotOurrIdptSigSV_Cnt_M_u08 LoaMgr_MotOurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_VtlgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UIspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	0 0 0 4 0 0 0 5 5 5 500 1 5 2 1 4 0 1 2 1 1 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1} 500 1	0 0 4 0 0 5 5 5 500 1 5 2 1 4 0 1 2 1 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 0	
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_VttgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UIspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_LOASt_State_enum.value	0 0 0 4 0 0 0 5 5 5 5 500 1 5 2 1 4 0 1 2 1 1 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1, 1} 5 0 0 1 0 0 1 0 1 0 1 0 1 0 1 1 1 0 1 1 1 1 1 1 1 1 1 1 1 1 1	0 0 0 4 0 0 5 5 5 5 500 1 5 2 1 4 0 0 (170, 172, 173, 174) {0, 0, 0, 0, 0, 0} {1, 1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 0 0 5	
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotOurrIdptSigSV_Cnt_M_u08 LoaMgr_MotOurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_VtlgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UIspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	0 0 0 4 0 0 0 5 5 5 500 1 5 2 1 4 0 1 2 1 1 0 {170, 172, 173, 174} {1, 1, 1, 1} {1, 1, 1, 1} 500 1	0 0 4 0 0 5 5 5 500 1 5 2 1 4 0 1 2 1 1 0 {170, 172, 173, 174} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 0	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.89 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr MotAgldptSigSV Cnt M u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	0.671599984
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Ci	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	int Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target LoaMgr Per1 MtrPosldptSig Cnt u		
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target LoaMgr Per1 TloaAvail Cnt Igc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	0	0	✓ ×
LoaMgr HwTqldptSiqSV Cnt M u08	0	0	_
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	_
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	_
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	_
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	_
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_endin)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	- N
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1} {1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte Write Ap LoaMgr LoaRateLimit UlspS f32(data)	500	500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc.value	0	0	-
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
	1	1	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	-
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~



Injust Value	Test Step 4.90 (Repeat Count = 1)	13
Coating   Forticostage   Cort   Mile   Coating   Forticostage   Coating		
Leading Instructions Could by the Control of the Country of the Co		·
Leading Infortibresis Crit M, 198 Leadin		2
Leading   Johnson   Leading   Lead	LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
Leading Fundangering Core Mu 198 Leading Managedistigs Core Mu 198 Leading Leading Managedistigs Cor	LoaMgr_HwTqRespSV_Cnt_M_u08	0
Leading _ Markelgo SC _ Cet M _ Lottl   Lead	LoaMgr_lvtrldptSigSV_Cnt_M_u08	
Loading / MacAgadesia (SV Core, M. 100)		
Cooking Junding contingnies _Cort_U_lips		
Loudey MacCure Segretary Cost M, 198  Loudey M, 198  Register M, Loudey C, 198  Loudey M, Loudey M, 198  Loudey M,		
Loading   MacCardengling Cort   Mige		1
Looking - Michael Processor Compt. Mules		
Lookings	LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	
Loading / Lindhigh Searce	LoaMgr_MotCurrRespSV_Cnt_M_u08	3
	LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
L. Loadings / Landings/Saczenian   Circli ge	LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
	Rte_Inst_Ap_LoaMgr	V
L. Looking-  HardigotiSg0PTeackwalResp_Crt_U88		
L. Looking-In-WritingtisSg/Tellon Resp. Cnt. U08		
	k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	
Loakg_HwTqlqtpSightRep_Cnt_U8	k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	
		0
K.Loshig - Hv7qhtghRate   LispS-12	k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
K_LosMgr_MritghtsgrResp_Cnt_u08	k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
Loakgr_MridptSigPResp_Cnt_u08	k_LoaMgr_HwTqMtgtnRate_UlspS_f32	
K_LosMgr_IntridptSig2Pesp_Cnt_U08		
k_LoaMgr_IvridptSigPIRTid_Cnt_u08         0           k_LoaMgr_IvridptRate_UbpS_f32         1           k_LoaMgr_IvridptSicaZenoEn_Cnt_u08         1           k_LoaMgr_IvridptSicaZenoEn_Cnt_u08         5           k_LoaMgr_ModQipSisigNSimSimResp_Cnt_u08         1           k_LoaMgr_ModQipSisigRiRtin_Cnt_u08         2           k_LoaMgr_ModQipSisigRiRtin_Cnt_u08         2           k_LoaMgr_ModQipSisigRiRtin_Cnt_u08         2           k_LoaMgr_ModQipSisigRiRtin_Cnt_u08         2           k_LoaMgr_ModQipSisigRiRtin_Cnt_u08         2           k_LoaMgr_ModQipSisigRixes_Cnt_u08         2           k_LoaMgr_ModQipsiscaZenoEn_Cnt_u08         2           k_LoaMgr_ModQinginsCazenoEn_Cnt_u08         2           k_LoaMgr_ModCurridptSigResp_Cnt_u08         2           k_LoaMgr_ModCurridptSigResp_Cnt_u08         0           k_LoaMgr_ModCurridptSigResp_Cnt_u08		
K_Loakly_InvitMignRate_UlspS_132		
k, LoaMgr_IvriMiginSca_Uis_IS2         1           k, LoaMgr_ModupUsingIsNs_INSER_EXP_Cnt_U08         5           k, LoaMgr_ModupUsingIsNsER_EXP_Cnt_U08         1           k, LoaMgr_ModupUsingIsR_EXP_Cnt_U08         1           k, LoaMgr_ModupUsingIsR_EXP_Cnt_U08         2           k, LoaMgr_ModupUsingIsR_EXP_Cnt_U08         2           k, LoaMgr_ModupUsingIsR_EXP_Cnt_U08         2           k, LoaMgr_ModupMiginSca_EXP_Cnt_U09         0           k, LoaMgr_ModupMiginSca_EXP_Cnt_U08         2           k, LoaMgr_ModupMiginSca_EXP_Cnt_U08         2           k, LoaMgr_ModupUsinSex_EXP_Cnt_U08         2           k, LoaMgr_ModupUsinSex_EXP_Cnt_U08         2           k, LoaMgr_ModupUsinSex_EXP_Cnt_U08         0           k, LoaMgr_ModupUsinSex_EXP_Cnt_U08         0           k, LoaMgr_ModupHisSex_EXP_Cnt_U08         0           k, LoaMgr_ModupHisSex_EXP_Cnt_U08 <td></td> <td></td>		
k_LoaMgr_MorAgidpiSig/Resp_Cnt_U08         1           k_LoaMgr_MorAgidpiSig/Resp_Cnt_U08         0           k_LoaMgr_MorAgidpiSig/Resp_Cnt_U08         2           k_LoaMgr_MorAgidpiSig/Resp_Cnt_U08         2           k_LoaMgr_MorAgidpiSig/Resp_Cnt_U08         2           k_LoaMgr_MorAgidpiSig/Resp_Cnt_U08         0           k_LoaMgr_MorAgidpiSig/Resp_Cnt_U08         2           k_LoaMgr_MorAgidpiSig/Resp_Cnt_U08         2           k_LoaMgr_MorCurridpiSig/Resp_Cnt_U08         2           k_LoaMgr_MorCurridpiSig/Resp_Cnt_U08         0           k_LoaMgr_MorCurridpiSig/Resp_Cnt_U08         0           k_LoaMgr_MorCurridpiSig/Resp_Cnt_U08         0           k_LoaMgr_MorCurridpiSig/Resp_Cnt_U08         0           k_LoaMgr_MorCurridpiSig/Resp_Cnt_U08         0           k_LoaMgr_MorCurridpiSig/Resp_Cnt_U08         0           k_LoaMgr_MorCurridpiSig-Resp_Cnt_U08         0           k_LoaMgr_MorCurridpiSig-Resp_Cnt_U08         1           k_LoaMgr_MorCurridpiSig-Resp_Cnt_U08         1           k_LoaMgr_MorCurridpiSig-Resp_Cnt_U08         0           k_LoaMgr_MorCurridpiSig-Resp_Cnt_U08         0           k_LoaMgr_Pert_DiagosStivrtinactv_Cnt_Igc value         0           target_LoaMgr_Pert_DiagosStivrtinactv_Cnt_Igc value         0		
K_LoaMgr_MorAgidptSigSResp_Cnt_u08         0           K_LoaMgr_MorAgidptSigFiThd_Cnt_u08         2           K_LoaMgr_MorAgidptSigFiThd_Cnt_u08         1           K_LoaMgr_MorAgMighRefate_Uiss_F32         1           K_LoaMgr_MorAgMightSca_Uis_F32         1           K_LoaMgr_MorAgMightSca_Uis_F32         1           K_LoaMgr_MorAgSmaAvailresp_Cnt_u08         2           K_LoaMgr_MorCurridptSigResp_Cnt_u08         2           K_LoaMgr_MorCurridptSigResp_Cnt_u08         0           K_LoaMgr_MorCurridptSigResp_Cnt_u08         0           K_LoaMgr_MorCurridptSigResp_Cnt_u08         0           K_LoaMgr_MorCurridptSigResp_Cnt_u08         0           K_LoaMgr_MorCurridptSigResp_Cnt_u08         0           K_LoaMgr_MorCurridptSigResp_Cnt_u08         0           K_LoaMgr_MorCurridptSigResp_Cnt_u08         1           K_LoaMgr_MorCurridptSigResp_Cnt_u08         1           K_LoaMgr_MorCurridptSigr_Cnt_u08         1           K_LoaMgr_RedodSirea_Uisp_S_02         1           K_LoaMgr_RedodSirea_Uisp_S_02         1           K_LoaMgr_RedodSirea_Uisp_S_02         1           K_LoaMgr_RedodSirea_Uisp_S_02         1           K_LoaMgr_Pert_DiagcSistvrInactv_Cnt_lgc value         0           target_LoaMgr_Pert_LoaSc_02         0	k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSigFitTnd_Cnt_u08         2           k_LoaMgr_MotAghtglmScae_UlspS_f32         1           k_LoaMgr_MotAghtglmScae_Cnt_u08         0           k_LoaMgr_MotAghtgmScae_Uls_f32         1           k_LoaMgr_MotAghtgmScae_Cnt_u08         2           k_LoaMgr_MotCurridptSigGeRes_Cnt_u08         2           k_LoaMgr_MotCurridptSigGeRes_Cnt_u08         0           k_LoaMgr_MotCurridptSigFers_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           k_LoaMgr_MotCurridptSigSeRes_Cnt_u08         1           k_LoaMgr_MotCurridptSigSexEeroEn_Cnt_lgc         1           k_LoaMgr_MotCurridptSigSexEeroEn_Cnt_lgc         1           k_LoaMgr_MotCurridptSigSexEeroEn_Cnt_lgc         1           k_LoaMgr_RedcdSiRae_Ulsp S_f32         1           k_LoaMgr_RedcdSiRae_Ulsp S_f32         1           k_LoaMgr_Per1_DiagcStsivtrInactv_Cnt_lgc value         0           target_LoaMgr_Per1_DiagcStsivtrInactv_Cnt_lgc value         0           target_LoaMgr_Per1_MortCuridptSig_Cnt_u08 value         0           target_LoaMgr_Per1_ToaAval_Cnt_lgc value         0 <td>k_LoaMgr_MotAgldptSig2Resp_Cnt_u08</td> <td>1</td>	k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgMtgtnRate_UispS_f32         1           k_LoaMgr_MotAgMtgtnRate_UispS_f32         0           k_LoaMgr_MotAgMtgtnRate_UispS_f32         1           k_LoaMgr_MotAgMtgtnRate_UispS_f32         1           k_LoaMgr_MotCurridptSigtResp_Cnt_u08         2           k_LoaMgr_MotCurridptSigtResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigtResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigtResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigtResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigtResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigtResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigt_UispS_f32         1           k_LoaMgr_MotCurridptSigt_Sigt_UispS_f32         1           k_LoaMgr_ReddSiRate_UispS_f32         1           k_LoaMgr_ReddSiRate_UispS_f32         1           k_LoaMgr_Pert_DiagcSistritrinact_Cnt_lgc value         0           target_LoaMgr_Pert_DiagcSistritrinact_Cnt_lgc value         0           target_LoaMgr_Pert_MotCradptSig_Cnt_u08 value         0           target_LoaMgr_Pert_MotCradptSig_Cnt_u08 value         1           target_LoaMgr_Pert_MotCradptSig_Cnt_u08 value         2           target_LoaMgr_Pert_MotCradptSig_Cnt_u08 value         0           target_LoaMgr_Pert_MotCradptGr_Pert_MotCradptCradpt_Per	k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgMtgtnSca_ZeroEn_Cnt_lgc         0           k_LoaMgr_MotAgMtgtnSca_Uis_132         1           k_LoaMgr_MotAgmaAvailResp_Cnt_u08         2           k_LoaMgr_MotCurridptSigGResp_Cnt_u08         2           k_LoaMgr_MotCurridptSigGResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         1           k_LoaMgr_MotCurridptSig_Cnt_u08         1           k_LoaMgr_RedotSiRca_Uis_S122         1           k_LoaMgr_RedotSiRca_Uis_S122         1           t_LoaMgr_Pert_DiagcSistivtTinack_Cnt_lgc_value         0           target_LoaMgr_Pert_DiagcSistivtZinack_Cnt_lgc_value         0           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value         0           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value         1           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value         1           target_LoaMgr_Pert_TolaQual_Cnt_lgc_value         0           target_LoaMgr_Pert_TolaQual_Cnt_lgc_value         0           target_LoaMgr_Pert_TolaQual_Cnt_lgc_value	k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	
k_Loaklgr_MotAgSmaAvailResp_Cnt_u08         2           k_Loaklgr_MotAgSmaAvailResp_Cnt_u08         2           k_Loaklgr_MotCurridptSig1Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig2Resp_Cnt_u08         0           k_Loaklgr_MotCurridptSig2Aresp_Cnt_u08         1           k_Loaklgr_MotCurridptSig2Aresp_Cnt_u08         0           target_Loaklgr_Pert_DiageStsivfTlaactv_Cnt_lgc value         0           target_Loaklgr_Pert_MotCurridptSig_Cnt_u08 value         1           target_Loaklgr_Pert_ToaDi_Cnt_lgc value         0           target_Loaklgr_Pert_ToaDi_Cnt_lgc valu	k_LoaMgr_MotAgMtgtnRate_UlspS_f32	
k_LoaMgr_MotOuridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1FitThd_Cnt_u08         0           k_LoaMgr_MotCurridptSig1FitThd_Cnt_u08         0           k_LoaMgr_MotCurridptSig2FitThd_Cnt_u08         0           k_LoaMgr_MotCurridptSig2FitThd_Cnt_u08         1           k_LoaMgr_MotCurridptSig2FitThd_Cnt_u08         1           k_LoaMgr_MotCurridptSig2FitThd_Cnt_u08         1           k_LoaMgr_MotCurridptSig2FitThd_Cnt_u08         1           k_LoaMgr_MotCurridptSig_Cnt_u08_u02         1           k_LoaMgr_Pert_DiagcSistvirtInactv_Cnt_lgc_value         0           target_LoaMgr_Pert_DiagcSistvirtZinactv_Cnt_lgc_value         0           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08_value         0           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08_value         1           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08_value         2           target_LoaMgr_Pert_TioaDi_Cnt_lgc_value         0           target_LoaMgr_Pert_TioaDi_Cnt_lgc_value         0           target_LoaMgr_Pert_TioaDi_Cnt_lgc_value         1           target_LoaMgr_Pert_TioaDi_Cnt_lgc_value         0           target		1
k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptRigeTitnd_Cnt_u08         0           k_LoaMgr_MotCurridptRigeTitnd_Cnt_u08         0           k_LoaMgr_MotCurridptSigSca_Uis_52         1           k_LoaMgr_RedcdStRate_UispS_32         1           k_LoaMgr_RedcdStRate_UispS_32         1           k_LoaMgr_Pert_DiagcStslvtrInactv_Cnt_lgc.value         0           target_LoaMgr_Pert_DiagcStslvtr2inactv_Cnt_lgc.value         0           target_LoaMgr_Pert_LoaSca_Di_Cnt_lgc.value         0           target_LoaMgr_Pert_MotCurrldptSig_Cnt_u08 value         0           target_LoaMgr_Pert_MtrPosldptSig_Cnt_u08 value         1           target_LoaMgr_Pert_MtrPosldptSig_Cnt_u08 value         1           target_LoaMgr_Pert_TicaPert_Sca_Cnt_u08 value         1           target_LoaMgr_Pert_TicaDi_Cnt_lgc.value         0           target_LoaMgr_Pert_TicaDi_Cnt_lgc.value         0           target_LoaMgr_Pert_TicaDi_Cnt_lgc.value         0           target_Rel_inst_Ap_LoaMgr_LoaMgr_Pert_DiagcStslvtr2inactv_Cnt_lgc         target_LoaMgr_Pert_Mtr_DiagcStslvt		
k_LoaMgr_MolCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MolCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MolCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MolCurrMtgtnState_UlspS_132         0.00999999978           k_LoaMgr_MolCurrMtgtnSca_Uls_1532         1           k_LoaMgr_RedcdStRate_UlspS_132         1           k_LoaMgr_RedcdStSca_Uls_1532         1           k_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc_value         0           target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc_value         0           target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc_value         0           target_LoaMgr_Per1_MolCurridptSig_Cnt_u08 value         0           target_LoaMgr_Per1_MolCurridptSig_Cnt_u08 value         2           target_LoaMgr_Per1_MolCurridptSig_Cnt_u08 value         1           target_LoaMgr_Per1_SteerMod_Cnt_enun_value         2           target_LoaMgr_Per1_ToaDi_Cnt_lgc_value         0           target_LoaMgr_Per1_ToaDi_Cnt_lgc_value         0           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc		
k_LoaMgr_MolCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MolCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MolCurrMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_MolCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MolCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_Per1_DiagcStsIvtr1inactv_Cnt_lgc_value         0           target_LoaMgr_Per1_DiagcStsIvtr2inactv_Cnt_lgc_value         0           target_LoaMgr_Per1_MoldptSig_Cnt_u08_value         0           target_LoaMgr_Per1_MolCurridptSig_Cnt_u08_value         2           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08_value         2           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08_value         1           target_LoaMgr_Per1_TinaOAvail_Cnt_lgc_value         2           target_LoaMgr_Per1_TinaOAvail_Cnt_lgc_value         0           target_Re_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1inactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtr2inactv_Cnt_lgc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtr2inactv_Cnt_lgc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_InvTqLo		
k_LoaMgr_MotCurridptSigFItThd_Cnt_u08         0           k_LoaMgr_MotCurridptRate_UlspS_f32         0.00999999978           k_LoaMgr_MotCurrMigtmRate_UlspS_f32         1           k_LoaMgr_MotCurrMigtmSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_Per1_DiagcStsivtr1lnactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc.value         0           target_LoaMgr_Per1_HvTqldptSig_Cnt_u08.value         0           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         0           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         2           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 value         1           target_LoaMgr_Per1_SteerMod_Cnt_enum.value         2           target_LoaMgr_Per1_ToaAvail_Cnt_lgc.value         0           target_Rel_nst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsivtr1lnactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsivtr1lnactv_Cnt_lgc           target_Rel_nst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc           target_Rel_nst_Ap_LoaMgr_LoaMgr_Per1_HvTqldptSig_Cnt_u08         target_LoaMgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc           target_Rel_nst_Ap_LoaMgr_LoaMgr_Per1_HvTqldptSig_Cnt_u08         target_LoaMgr_Per1_HvTqldptSig_Cnt_u08           target_Rel_nst_Ap_LoaMgr_L		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_Perd_DiagcStsIvtr1Inactv_Cnt_lgc_value         0           target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc_value         0           target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc_value         0           target_LoaMgr_Per1_HvTqldptSig_Cnt_u08_value         0           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08_value         2           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08_value         1           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08_value         2           target_LoaMgr_Per1_ToaAvail_Cnt_lgc_value         0           target_LoaMgr_Per1_ToaAvail_Cnt_lgc_value         0           target_LoaMgr_Per1_ToaAvail_Cnt_lgc_value         0           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_LoaMgr_Per1_LoaScaDi_Cnt_lgc         target_LoaMgr		
k_LoaMgr_RedcdStRate_UlspS_f32	k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	0.0099999978
k_LoaMgr_RedcdStRate_UispS_32 1  k_LoaMgr_RedcdStSca_Uis_f32 1  target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value 0  target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value 0  target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value 0  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 0  target_LoaMgr_Per1_MortCurridptSig_Cnt_u08.value 2  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1  target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2  target_LoaMgr_Per1_SteerMod_Cnt_lgc.value 0  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 0  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1  target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc 1  target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc 1  target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc 1  target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc 1  target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc 1  target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_LvtrLoaMtgtnEn_Cnt_lgc 1  target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_LvtrLoaMtgtnEn_Cnt_lgc 1  target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_LvtrLoaMtgtnEn_Cnt_lgc 1  target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1  target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1  target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1  target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1  target_Rel_inst_Ap_LoaMgr_Lo	k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_RedcdStSca_Uls_f32 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc.value 0 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value 0 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_TloaAvail_Cnt_Igc.value 0 target_LoaMgr_Per1_TloaAvail_Cnt_Igc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value 1 target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc 1 target_Re_inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc 1 target_Re_inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc 1 target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc 1 target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc 1 target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtEn_Cnt_Igc 1 target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtEn_Cnt_Igc 1 target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc 1 target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc 1 target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc 1 target_LoaMgr_Per1_MotAgLoaMtgtEn_Cnt_Igc 1 target_LoaMgr_Per1_MotAgLoaMtgtEn_Cnt_Igc 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 1 target_L	k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value 0 target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value 0 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 0 target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_LoaMgr		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosIdptSig_Cnt_uo8.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMgtgnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMgtgnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMgtgnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMgtgnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08  target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08  target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08  target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 2 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr1DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMgr_Per1_Mo		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaSt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
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target_LoaMgr_Per1_MtrPosidptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
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target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_LoaSt_State_enum  target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_NotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_NotCurrldptSig_Cnt_u08	· · · · · · · · · · · · · · · · · · ·	
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target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	~
LoaMgr_LOASt_State_M_enum	4	4	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	4	4	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{171, 172, 173}	{171, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	4	4	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.91 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k LoaMgr HwTqldptSig1TloaAvailResp Cnt u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32 k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	1 0		
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1 0		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08 k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	500		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1	at Inc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Citarget_LoaMgr_Per1_DiagcStslvtr2Inact		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTgldptSig Cnt u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lg		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target LoaMgr Per1 MtrPosldptSig Cnt u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	<b>V</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	1 0	0	<b>*</b>
LoaMgr_IvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	<b>~</b>
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	1	<b>V</b>
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	

LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc LoaMgr\_MotAgRespSV\_Cnt\_M\_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>~</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc.value	0	0	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.92 (Repeat Count = 1)	N .
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Nama	Input Value		
Name k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k LoaMgr RedcdStRate UlspS f32	1		
k LoaMgr RedcdStSca Uls f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	90	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1 4	1	<b>*</b>
LoaMgr_HwTqRespSV_Cnt_M_u08	- ·	0	
LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	1	-
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	_
LoaMgr LOASt State M enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	<b>Y</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>*</b>
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	•
	-	5	-
target_LoaMgr_Per1_LOASt_State_enum.value	5		•
target_LoaMgr_Per1_LOASt_State_enum.value target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	-



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	<b>✓</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

<b>-</b> 101 100 <b>-</b> 10 10	
Test Step 4.93 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrlytrMtgtnScaZeroEn Cnt lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
	4
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k LoaMgr MotCurrldptSig2Resp Cnt u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	350.459595
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k LoaMgr MotCurrMtgtnSca Uls f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k LoaMgr RedodStSca Uls f32	1
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	1
a.gocoagr_r or r_biagootore midote_one_go.edido	<u>'</u>





Name	Input Value		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u(	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0	Ont Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn	Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enun		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaAvail Cnt Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	1	1	_
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	<b>~</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	-
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	_
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr MotCurrRespSV Cnt M u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr TloaDi Cnt M lgc	1	1	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	•
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>~</b>
target LoaMgr Per1 MotAgLoaMtgtnEn Cnt Igc.value	0	0	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 4.94 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnNcaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1 0
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k LoaMgr RedcdStRate UlspS f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LoaScaDi_Cnt_loc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>~</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	2	2	~
LoaMgr_LOASt_State_M_enum	5	5	<b>~</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	-

Test Step Call Trace			<b>✓</b>	
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.95 (Repeat Count = 1)		86
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_lvtrRespSV_Cnt_M_u08	4	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

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Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08 k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target LoaMgr Per1 MotCurrldptSig Cnt u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ci	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Ci		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg	jc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cn	t lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_ui		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqRospS\/ Cot M un8	4	4	~
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~

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Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	<b>~</b>
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	Ī	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.96 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	0
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





	l		
Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0 2		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k LoaMgr MotAgMtgtnSca Uls f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0 torget LeeMar Bort DiagoStelytrtIngs	the Cat Iga	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactarget_LoaMgr_Per1_DiagcStslvtr2Inactarget_LoaMgr_Per1_DiagcStslvtr2Inactarget_LoaMgr_Per1_DiagcStslvtr2Inactarget_LoaMgr_Per1_DiagcStslvtr2Inactarget_LoaMgr_Per1_DiagcStslvtr2Inactarget_LoaMgr_Per1_DiagcStslvtr2Inactarget_LoaMgr_Per1_DiagcStslvtr2Inactarget_LoaMgr_Per1_DiagcStslvtr1Inactarget_LoaMgr_Per1_DiagcStslvtr2Inactarget_LoaMgr_Per1_DiagcStslvt		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEr	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enu		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lg		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnE	n_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_C	Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtn	En_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Ci	nt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_er	num	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgd	C	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	<b>~</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	<b>•</b>
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	· ·
LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr LOASt State M enum	5	5	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	·
LoaMgr MaxMtgtnResp Cnt M u08	5	5	~
LoaMgr MotAgldptSigSV Cnt M u08	2	2	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1} {1, 1, 1, 1}	{0, 0, 0, 0, 0} {1, 1, 1, 1}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	{1, 1, 1, 1} {1, 1, 1, 1} 500	{0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	(1, 1, 1, 1) {1, 1, 1, 1} 500 0	{0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 0 ± 0.001	¥
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	(1, 1, 1, 1) {1, 1, 1, 1} 500 0 1	{0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 0 ± 0.001	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	(1, 1, 1, 1) {1, 1, 1, 1} 500 0 1	{0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 0 ± 0.001 1	¥
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	(1, 1, 1, 1) {1, 1, 1, 1} 500 0 1 1 5	{0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 0 ± 0.001 1 1 5	¥
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	(1, 1, 1, 1) {1, 1, 1, 1} 500 0 1	{0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 0 ± 0.001 1	¥



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.97 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrlvtrMtgtnScaZeroEn Cnt lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTgldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0
	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	1
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	:nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_le	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	~
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	•
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit An LoaMor FalloaScaleAndRate	1	Rte Exit An LoaMor FalloaScaleAndRate	1	<b>✓</b>





Test Step 4.98 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnNcaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1 0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08 k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_Igc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	0.587300003
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1 2
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2 2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k LoaMgr MotCurrMtgtnRate UlspS f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1 0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0 2
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
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Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.99 (Repeat Count = 1)	<b>2</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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LoaMgr\_Per1



Name	Innut Value		
Name	Input Value		
k_LoaMgr_CurrlytrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32 k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k LoaMgr HwTqMtgtnRate UlspS f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k LoaMgr lvtrldptSig1Resp Cnt u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	0.0099999978		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value		at lan	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lot target_LoaMgr_Per1_LOASt_State_enum	gC	
target Rte Inst Ap LoaMgr.LoaMgr Perl LoaScaDi Cnt Igc	target LoaMgr Peri_LOASt_State_enum target LoaMgr Peri LoaScaDi Cnt lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_MotAgLoaMtgtnEn_Cnt_lgc		nt lac	
target Rte Inst Ap LoaMgr.LoaMgr Perl MotCurrldptSig Cnt u08	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_(		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_Steenwou_Crit_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	- Coount
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	
LoaMgr_LOASt_State_M_enum	4	4	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	4	4	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~

LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

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LoaMgr\_Per1



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{171, 172, 173}	{171, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	4	4	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.100 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	500
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





M	In and Walter		
Name k LeeMar MetAaldetSia3Reen, Cet u09	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k LoaMgr MotAgMtgtnRate UlspS f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k LoaMgr MotAgMtgtnSca Uls f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1 2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target LoaMgr Per1 TloaDi Cnt lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr	at lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target LoaMgr Per1 DiagcStslvtr2Inactv Cr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	_,	
target_Rte_Inst_Ap_LoaMgr_Der1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt		
target_Rte_Inst_Ap_LoaMgr_Der1_lvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Ig		
target_Rte_Inst_Ap_LoaMgr_Der1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cn	t lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_ut		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C	cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_IvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_IvtrRespSV_Cnt_M_u08	4	4	~
LoaMgr_LOASt_State_M_enum	5	5	<b>~</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	· ·
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	Ž
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	1	4	-
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	·
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
toward Landson David Mathellan Mathellan Out Language	1	1	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	<b>✓</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.101 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_IvtrldptSigSV_Cnt_M_u08	0
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	
k LoaMgr MotCurrldptSig2Resp Cnt u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnNcaZeroEn_Cnt_lgc	1
	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	0
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	U





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_	u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum	1	
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr HwTqldptSigSV Cnt M u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	•
LoaMgr MotCurrLoaMtgtnEn Cnt M lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	•
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	•
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 173}	{170, 173}	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	•
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	•

Test Step Call Trace	est Step Call Trace			
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 4.102 (Repeat Count = 1)	TI CONTRACTOR OF THE CONTRACTO
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08 k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k LoaMgr lvtrMtgtnRate UlspS f32	367.258606
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	0
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	0
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3
target LoaMgr Per1 LoaScaDi Cnt Igc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_ivtrLoaMtgtnEn_Cnt_igc target_Rte_inst_Ap_LoaMgr.LoaMgr_Per1_ivtrLoaMtgtnEn_Cnt_igc	target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
toward Discharge Land April and Many David Mad Consider City Control of the Constant City Control of the Cont	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>~</b>

Test Step Call Trace	est Step Call Trace			<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.103 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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	1		
Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k LoaMgr MotCurrldptSig0Resp Cnt u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
	0		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	2		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value			
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_l	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ci		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc			×
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	2	2	<b>~</b>
<u> </u>			

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LoaMgr\_LoaRateLimit\_UlspS\_M\_f32 LoaMgr\_LoaScaleFctr\_Uls\_M\_f32

LoaMgr\_MaxMtgtnResp\_Cnt\_M\_u08

LoaMgr\_MotAgldptSigSV\_Cnt\_M\_u08

LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

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LoaMgr\_Per1



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	3	3	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1	1 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace	est Step Call Trace				
Actual Function	Count	Expected Function	Count	Result	
CalclvtrState	2	CalcivtrState	2	~	
LatchInputs	1	LatchInputs	1	~	
RqstRespConds	1	RqstRespConds	1	~	
RqstHwTqResp	1	RqstHwTqResp	1	<b>~</b>	
ArbResp	1	ArbResp	1	~	
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~	
AssignScale	1	AssignScale	1	<b>✓</b>	
SwBasMtgtn	1	SwBasMtgtn	1	~	
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>~</b>	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	<b>✓</b>	
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~	

Test Step 4.104 (Repeat Count = 1)	8
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr HwTqldptSigSV Cnt M u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr HwTqRespSV Cnt M u08	1
LoaMgr lvtrldptSigSV Cnt M u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr MotAgldptSigSV Cnt M u08	1
LoaMgr MotAgLoaMtgtnEn Cnt M Igc	0
LoaMgr MotAgRespSV Cnt M u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr MotCurrLoaMtgtnEn Cnt M Igc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1

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Name	Input Value		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	1		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k LoaMgr MotAgMtgtnScaZeroEn Cnt lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k LoaMgr MotCurrldptSig1Resp Cnt u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target LoaMgr Per1 MtrPosldptSig Cnt u08.value	1		
target LoaMgr Per1 SteerMod Cnt enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt	_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc		
torget Dte Inst An LeaMar LeaMar Dorf MtrDeeldetSig Cet u09	torget LeaMar Dorf MtrDeeldatCia Cat	00	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enur		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_SteerMod_Cnt_enur target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enur target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc	n	Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name	target_LoaMgr_Per1_SteerMod_Cnt_enur target_LoaMgr_Per1_TloaAvail_Cnt_lgc		Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc	target_LoaMgr_Per1_SteerMod_Cnt_enur target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	Expected Value	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name	target_LoaMgr_Per1_SteerMod_Cnt_enur target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	Expected Value	×
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgsV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enur target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	Expected Value  1 1	*
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_SteerMod_Cnt_enur target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0	Expected Value  1 1 0	*
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enur target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1	Expected Value  1 1 0 1	• • • • • • • • • • • • • • • • • • •
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lwtrlqtSigSV_Cnt_M_u08 LoaMgr_lwtrlqtSigSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enur target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 0 1	Expected Value  1 1 0 1 1 1 0 1 0 0 1 1 0	**************************************
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgsV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	target_LoaMgr_Per1_SteerMod_Cnt_enur target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 0 1 1 2	Expected Value  1 1 0 1 1 0 2	33 V V V
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgsV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrAgespSV_Cnt_M_u08 LoaMgr_LoaRateLimit_UlspS_M_f32	target_LoaMgr_Per1_SteerMod_Cnt_enur target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 0 2 500	Expected Value  1 1 0 1 1 0 2 500	**************************************
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgsV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaAst_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	target_LoaMgr_Per1_SteerMod_Cnt_enur target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 1 2 500 1	Expected Value  1 1 0 1 1 1 0 2 500 1	33 V V V
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgsV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaSt_State_M_enum LoaMgr_LoaState_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enur target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 0 2 500 1 2	Expected Value  1 1 0 1 1 1 0 2 500 1 2	**************************************
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgsV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrIdptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LoaSt_state_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enur target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 1 2 1	Expected Value  1 1 0 1 1 1 0 2 500 1 2 1	12   V
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgsV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_Igc	target_LoaMgr_Per1_SteerMod_Cnt_enur target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 1 1 2 500 1 2 1 0	Expected Value  1 1 0 1 1 0 2 500 1 2 1 0	12   V
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgsV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LoaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAglcoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enur target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 1 1 2 500 1 2 1 0 0 0	Expected Value	12   V
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enur target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 1 1 2 500 1 2 1 0	Expected Value  1 1 0 1 1 0 2 500 1 2 1 0	12   V
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgsV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LoaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAglcoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enur target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 0 2 500 1 2 1 0 0 0 0	Expected Value  1 1 0 1 1 0 1 1 1 2 500 1 2 1 0 0 0 0	12   V
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_SteerMod_Cnt_enur target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 0 2 500 1 2 1 0 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1	Expected Value  1 1 0 1 1 0 2 500 1 2 1 0 0 0 1 1	12   V
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	target_LoaMgr_Per1_SteerMod_Cnt_enur target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 0 2 500 1 2 1 0 0 0 1 2 1 2 1 0 0 0 1 2	Expected Value  1 1 0 1 1 0 2 500 1 2 1 0 0 0 1 2 1 2 1 0 0 0 1 2	1
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrlotSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgloptSigSV_Cnt_M_u08 LoaMgr_MotAglotSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc	target_LoaMgr_Per1_SteerMod_Cnt_enur target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 0 2 500 1 2 1 0 0 0 1 2 1 0 0 0 0 1	Expected Value  1 1 0 1 1 0 2 500 1 2 1 0 0 0 1 2 1 0 0 0 0 0 1 2 0 0	1
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgsV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_lwtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LoASt_State_M_enum LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_Ulsps_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrtdptSigSV_Cnt_M_u08 LoaMgr_MotCurrtdptSigSV_Cnt_M_u08 LoaMgr_MotCurrtLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrtRespSV_Cnt_M_u08 LoaMgr_MotCurrtRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	target_LoaMgr_Per1_SteerMod_Cnt_enur target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 0 2 500 1 2 1 0 0 0 1 2 1 0 0 1 2 1 0 0 1 1 1 1	Expected Value  1 1 0 1 1 0 1 1 0 2 500 1 2 1 0 0 0 1 2 1 1 0 0 1 1 2 1 1 0 0 0 1 1 1 1	3 V V V V V V V V V V V V V V V V V V V
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgsV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgloaMtgtnEn_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgCaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	target_LoaMgr_Per1_SteerMod_Cnt_enur target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 0 2 500 1 2 1 0 0 0 1 2 1 1 3 {173, 174}	Expected Value  1 1 0 1 1 0 1 1 1 2 500 1 2 1 0 0 0 1 2 1 3	III
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgsV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_lwtrloaMtgtnEn_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_Status(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_LoaMgr_Per1_SteerMod_Cnt_enur target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 0 2 500 1 2 1 0 0 0 1 2 1 3 {173, 174} {1, 1}	Expected Value  1 1 0 1 1 0 1 1 1 0 2 500 1 2 1 0 0 0 1 2 1 3 {173, 174} {0, 0, 0, 0, 0} {1, 1}	II
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LoASt_State_M_enum LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_Ulsps_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgCoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_StelTaDai_Cnt_M_lgc LoaMgr_StelTaDai_Cnt_M_lgc LoaMgr_NotCurrRespSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	target_LoaMgr_Per1_SteerMod_Cnt_enur target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 0 2 500 1 2 1 0 0 1 2 1 0 1 3 {173,174} {1, 1} 500	Expected Value  1 1 0 1 1 0 1 1 1 0 2 500 1 2 1 0 0 0 1 2 1 3 {173, 174} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01	H
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_Ulsps_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIcoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_cnt_M_Igc LoaMgr_TloaDi_cnt_M_Igc LoaMgr_StelnaDi_cnt_M_Igc LoaMgr_StelnaDi_cnt_StelnaDi_cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	target_LoaMgr_Per1_SteerMod_Cnt_enur target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 0 2 500 1 2 1 0 0 0 1 2 1 3 {173, 174} {1, 1} {1, 1} 500 1	Expected Value  1 1 0 1 1 0 1 1 1 0 2 500 1 2 1 0 0 0 1 2 1 2 1 0 0 1 2 1 2 1 1 0 0 1 1 2 1 1 1 1	II
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_Ulsps_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_StelnCoating_StelnCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	target_LoaMgr_Per1_SteerMod_Cnt_enur target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 0 2 500 1 2 1 0 0 0 1 2 1 3 {173, 174} {1, 1} {1, 1} 500 1 0	Expected Value  1 1 0 1 1 0 1 1 1 1 0 2 500 1 2 1 0 0 0 1 2 1 2 1 0 0 1 2 1 2 0 1 3 {173, 174} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0	II
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgsV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_StelnCoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	target_LoaMgr_Per1_SteerMod_Cnt_enur target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 0 2 500 1 2 1 0 0 0 1 2 1 0 1 3 {173, 174} {1, 1} 500 1 0 1 0 1	Expected Value  1 1 0 1 1 0 1 1 1 1 0 2 500 1 2 1 0 0 0 1 2 1 2 1 0 0 1 2 1 2 0 1 3 {173, 174} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0 1	II
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_Ulsps_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_StelnCoating_StelnCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	target_LoaMgr_Per1_SteerMod_Cnt_enur target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 1 1 0 2 500 1 2 1 0 0 0 1 2 1 3 {173, 174} {1, 1} {1, 1} 500 1 0	Expected Value  1 1 0 1 1 0 1 1 1 1 0 2 500 1 2 1 0 0 0 1 2 1 2 1 0 0 1 2 1 2 0 1 3 {173, 174} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0	II

 $target\_LoaMgr\_Per1\_MotAgLoaMtgtnEn\_Cnt\_lgc.value$ target\_LoaMgr\_Per1\_MotCurrLoaMtgtnEn\_Cnt\_lgc.value



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	-
AssignScale	1	AssignScale	1	•
SwBasMtgtn	1	SwBasMtgtn	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•

Test Step 4.105 (Repeat Count = 1)	Invest Males
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
_oaMgr_lvtrldptSigSV_Cnt_M_u08	2
_oaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
_oaMgr_lvtrRespSV_Cnt_M_u08	4
_oaMgr_MotAgldptSigSV_Cnt_M_u08	1
_oaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
_oaMgr_MotAgRespSV_Cnt_M_u08	4
.oaMgr_MotCurrldptSigSV_Cnt_M_u08	2
_oaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
.oaMgr_MotCurrRespSV_Cnt_M_u08	3
_oaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
_oaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
x_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
C_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	0
_LoaMgr_FadeOutStRate_UlspS_f32	1
_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
c_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
c_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
c_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
c_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
c_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
c_LoaMgr_HwTqMtgtnSca_Uls_f32	1
c_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
C_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
c_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_Igc	0
_LoaMgr_IvtrMtgtnSca_Uls_f32	1
c_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
c_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
C_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
c_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
c_LoaMgr_MotAgMtgtnSca_Uls_f32	1
LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
<pre>c_LoaMgr_MotCurrMtgtnRate_UlspS_f32</pre>	1
_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
<ul><li>LoaMgr_MotCurrMtgtnSca_Uls_f32</li></ul>	1
<_LoaMgr_RedcdStRate_UlspS_f32	1
C_LoaMgr_RedcdStSca_Uls_f32	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_le	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target Rte Inst Ap LoaMgr.LoaMgr Per1 LoaScaDi Cnt Igc	target LoaMgr Per1 LoaScaDi Cnt Igc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target LoaMgr Per1 MotCurrLoaMtgtnEn (		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaAvail Cnt Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	_
LoaMgr HwTqldptSigSV Cnt M u08	0	0	<b>~</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	_
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	2	_
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr lvtrRespSV Cnt M u08	4	4	
LoaMgr_LOASt_State_M_enum	4	4	<b>~</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	_
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr MaxMtgtnResp Cnt M u08	4	4	_
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	-
LoaMgr MotCurrLoaMtgtnEn Cnt M Igc	0	0	~
LoaMgr MotCurrRespSV Cnt M u08	3	3	
LoaMgr TloaAvail Cnt M lgc	0	0	·
LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	·
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{171, 172, 173}	{171, 172, 173}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	{1, 1, 1} {1, 1, 1}	{1, 1, 1}	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte Write Ap LoaMgr LoaScaleFctr Uls f32(data)	0	0 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	0	0	
target LoaMgr Per1 IvtrLoaMtgtnEn Cnt Igc.value	1	1	
target_LoaMgr_Per1_LOASt_State_enum.value	4	4	
	0	0	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	-
target_Loaivigi_F er i_iviotouriLoaivitgtriEri_Orit_igc.value	U	U	_

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	<b>✓</b>
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Institute   Instit	Test Step 4.106 (Repeat Count = 1)	13
Landery _ BATTON COMPUTED_COT_ALL_UPS  Landery _ BATTON C		
Leading Ju-Higlosphier, Dut. Mg 1  Leading Ju-Higlosphier, Dut. Mg 2  Leading Ju-Higlosphier, Dut. Mg 2  Leading Ju-Higlosphier, Leading Ju-Higlo		
Leading   Intelligency Cert   M. 100	LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
Loading   Juniferpings   Cort   M. 100	LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
Loading - Michagos (C. M. 1986)	LoaMgr_HwTqRespSV_Cnt_M_u08	4
Landary, Medigensigner, Cert. JU, 198  Landary, Madaplandsigner, Cert. JU, 198  Landary, Controllingsissa, Leve. 198  Landary, Controllingsissa, Leve. 198  Landary, Lan	LoaMgr_lvtrldptSigSV_Cnt_M_u08	
Loading   MacAppach(Perior Court   100	LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	
Loading   Amalgraphing   Cort   Ungris		
Looking MacContack Sign Cont. M. 198  Looking MacContack Sign Cont. M.		
Landary Macharandeligner, Cort, M. 196  Landary Anderson Registry, Cort, M., 196  Landary Senderson Landary, Cort, M., 196  Landary, Contributing Residue, 196, 197  Landary, L		
Loading / MacCure Responsible Cont. M. Spe Loading / Semination of American Cont. M. Spe Loading / Loading / Cont. M. Spe Loading / Control of the Special Cont. Spe Loading / Control of the Special Cont. Spe Loading / Loading / Control of the Special Cont. Spe Loading / Loa		
Loading - District Mode   District   Distr	LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	
Loading	LoaMgr_MotCurrRespSV_Cnt_M_u08	3
Rist   Incide   Control Major Reside   Links   St.	LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
Landing Controllingtonia (Ling 1922   1	LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Landing	Rte_Inst_Ap_LoaMgr	V =
Loading-FairColorState Users   12   Loading-FairColorState Users   12   Loading-FairColorState Users   12   Loading-FairColorState Users   12   Loading-FairColorState   13   Loading-Fa		
Loading / Info@pdisp() Toakvallistee, Ort. 108		
Landary Hardingthis (No Touris Cont. US)		
Lankgr   Mr Glopisg   No Tankers   Chr   U8		
L. Loads/g. Har-TopicSSQ-TEAN-BRANG_CM_UDB		
Landary   Interpolicy   Transport   Tran	k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	
L. Loading-In-Microphissylaterape, Cnt. U08   C. Loading-In-Microphissylateraped, Cnt. U08   C. Loading-In-	k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	
L. Loading-HwTqdNgriSpFITTING_Cnt_U08	k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
L. Loadkgr_HvfTqMtgginSap_UspS_123   L. Loadkgr_HvfTqMtgginSap_UspS_123   L. Loadkgr_HvfTqMtggisSap_Resp_Cnt_U08   0     L. Loadkgr_MvfTqgisSap_Resp_Cnt_U08   0     L. Loadkgr_MvfTqgisSap_Resp_Cnt_U08   0     L. Loadkgr_MvfTqgisSap_Resp_Cnt_U08   0     L. Loadkgr_MvfTqqisSap_Resp_Cnt_U08   0     L. Loadkgr_Mv	k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
K. Loaking/ Intridisting/Resp_Cnt_U08	k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
Loading   Nrtisplisig  Resp. Cnt   U08	k_LoaMgr_HwTqMtgtnRate_UlspS_f32	
LoaMgr   Mrt/dptSigPitThd   Cm   L08		
Loaklgr_InvinkignRate_UlspS_f32		
Loading   Intrinstripsica_Uis_132		
Loading   IntrinsignSca   Uis   132		
L. LoaMgr_MotAgdps[sig2Resp_Cnt_u08	k_LoaMgr_lvtrMtgtnSca_Uls_f32	
L. LoeMgr_MotAgloptSig9Resp_Cnt_u08	k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
L. LoaMgr_MotAgidpNisgFitThd_Cnt_u08	k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
LoaMgr_MotAgMtghRate_UlspS_f32	k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
LoaMgr_MotAgMtginSca_ZeroEn_Cnt_lgc	k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	
k_LoaMgr_MotAgSmaAvaiResp_Cnt_u08         2           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_Cnt_lgc         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_Cnt_lgc         1           k_LoaMgr_MotCurridptSig2Resp_Sig2         1           k_LoaMgr_MotCurridptSig2Resp_Sig2         1           k_LoaMgr_RedodSiRea_Uls_Sig2         1           k_LoaMgr_RedodSiRea_Uls_Sig2         1           k_LoaMgr_Pert_DiagcSistvirtlnactv_Cnt_lgc value         0           target_LoaMgr_Pert_DiagcSistvirtinactv_Cnt_lgc value         1           target_LoaMgr_Pert_AlloaCurridptSig_Cnt_u08.value         1           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08.value         1           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08.value         1           target_LoaMgr_Pert_ToaDi_Cnt_lgc.value         1           target_LoaMgr_Pert_ToaDi_Cnt_lgc.value         1           target_LoaMgr_Pert_ToaDi_Cnt_lgc.value         1           target_LoaMgr_Pert_ToaDi_Cnt_lgc.value         1           target_LoaMgr_Pert_ToaDi_Cnt_lgc.value		1
k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Scareofen_Cnt_lgc         1           k_LoaMgr_MotCurridptSig-Sca_Uis_f32         1           k_LoaMgr_RedcdSiRate_UispS_f32         1           k_LoaMgr_RedcdSiSca_Uis_f32         1           k_LoaMgr_Per1_DiagcStsivtr1Inactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc.value         1           target_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc.value         1           target_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc.value         1           target_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MirPosldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_TioaDi_Cnt_lgc.value         1           target_LoaMgr_Per1_TioaDi_Cnt_lgc.value         1           target_LoaMgr_Per1_TioaDi_Cnt_lgc.value         1           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc           target_Rel_Inst_Ap_LoaMgr_LoaMg		
k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2FitThd_Cnt_u08         0           k_LoaMgr_MotCurridptSig2FitThd_Cnt_u08         1           k_LoaMgr_MotCurridptSig2FitThd_Cnt_u08         1           k_LoaMgr_MotCurridptSig2FitThd LoaMgr_MotCurridptSig2_U1_si2         1           k_LoaMgr_MotCurridptSig2_U1_si32         1           k_LoaMgr_RedcdSiRate_U1spS_f32         1           k_LoaMgr_RedcdSiRate_U1spS_f32         1           k_LoaMgr_Per1_DiagcStsIvtTrlnactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStsIvtTv2Inactv_Cnt_lgc.value         1           target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value         1           target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value         1           target_LoaMgr_Per1_MctCurridptSig_Cnt_u08.value         2           target_LoaMgr_Per1_SteerMod_Cnt_enum.value         2           target_LoaMgr_Per1_TloaAval_Cnt_lgc.value         1           target_LoaMgr_Per1_TloaAval_Cnt_lgc.value         1           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc         target_LoaMgr_Per1_HwTqldplSig_Cnt_u08           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lg		
k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           k_LoaMgr_MotCurridtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurridtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurridtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_Perd_DiagcStstvf1Inactv_Cnt_lgc.value         0           target_LoaMgr_Perd_DiagcStstvf2Inactv_Cnt_lgc.value         1           target_LoaMgr_Perd_DiagcStstvf2Inactv_Cnt_lgc.value         1           target_LoaMgr_Perd_MotCurridptSig_Cnt_u08.value         1           target_LoaMgr_Perd_MotCurridptSig_Cnt_u08.value         2           target_LoaMgr_Perd_MotPosldptSig_Cnt_u08.value         1           target_LoaMgr_Perd_TincaAvail_Cnt_lgc.value         1 </td <td></td> <td></td>		
k_LoaMgr_MotCurrIdptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurrMtgtnRate_UlspS_f32         1           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdSiRate_UlspS_f32         1           k_LoaMgr_RedcdSiRate_UlspS_f32         1           k_LoaMgr_RedcdSiRate_UlspS_f32         1           k_LoaMgr_Per1_DiagcStstvtr1nactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStstvtr2nactv_Cnt_lgc.value         1           target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         2           target_LoaMgr_Per1_MotPosidptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MotPosidptSig_Cnt_u08.value         1           target_LoaMgr_Per1_SteerMod_Cnt_enum.value         2           target_LoaMgr_Per1_ToaAvail_Cnt_lgc.value         1           target_Rel_mst_Ap_LoaMgr_LoaMgr_Per1_DiagcStstvtr1nactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStstvtr2nactv_Cnt_lgc           target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStstvtr2nactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStstvtr2nactv_Cnt_lgc           target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc           target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1		
k_LoaMgr_MotCurrMigtnSca_Uis_f32	k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMigtnSca_Uls_f32	k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1.
k_LoaMgr_RedcdStRate_UlspS_f32	k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc	k_LoaMgr_MotCurrMtgtnSca_Uls_f32	
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc.value 1 target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value 1 target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08.value 1 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value 1 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value 2 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_TloaAvail_Cnt_Igc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_Igc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_Igc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_Igc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_Igc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc 1 target_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_Igc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc 1 target_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_Igc 1 target_LoaMgr_Per1_MotAgLoaMgr_Per1_M	k_LoaMgr_RedcdStRate_UlspS_f32	
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value  1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value  1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value  2 target_LoaMgr_Per1_SteerMod_Cnt_enum.value  1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value  2 target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value  1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_LoaMgr_Per1_HwTqldoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
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target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_uo8	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotOurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value			
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum			
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc			
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	0	0	~	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>	
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	~	
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~	
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	~	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~	
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~	
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•	
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~	
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.107 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	0.987299979		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k LoaMgr MotAgldptSig2Resp Cnt u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k LoaMgr MotAgMtgtnRate_0isp3_i32 k LoaMgr MotAgMtgtnScaZeroEn Cnt lgc	0		
k_LoaMgr_MotAgMtgtnSca2eroEn_Cnt_igc k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08			
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt	_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_	_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_	u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	→ Nobult
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	·
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
	0	0	-
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	4	4	
LoaMgr_IvtrRespSV_Cnt_M_u08			~
LoaMgr_LoaNst_State_M_enum	5	5	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	<b>✓</b>
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	V
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	<b>V</b>
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>V</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr MotAgRespSV Cnt M u08	4	4	<b>✓</b>

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	1	<b>~</b>
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.108 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	0.0099999978
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Landary Developation Computer	Name	Input Value		
Landary Monigensignation Company	Name			
Landary Mandagolishing Lings   12				
Landay Morighterise Use   12				
Licably_Moderates Use   1		1		
Looking MoCardistigNResp. CH LIGS   2	k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
Loading / McCorrispisign Resp. Crit L08   0	k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
Landary_MonComprisor(1978) PRIOR_COLLUBS	k_LoaMgr_MotAgSmaAvailResp_Cnt_u08			
Landing John-Carristop/Prince Cort Linds	k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08			
Landary McCardingSepTime Cort Lands Landary McCardingSepTime Cort Lands Landary McCardingSea Lands Cort Lands Landary McCardingSea Lands Cort Lands Landary Reductificate Usps 102 Landary Reductificate Usps 102 Landary Reductificate Usps 102 Landary Pert Disposition Front Cort Lips create Landary Pert Landary Pert Landary Cort Landary Cort Landary Pert La				
Lacety McCurringhreSazzerien   Leg   1				
Looking / Redough Re				
Loading-Red States   Use   D2		· ·		
Landary   Rescolations   Landary				
Image_Loady_Der_Loagestein/Tenetry_Cort_govalue   1				
Image   Loady Per   Mortargosis, Cet   USA value		0		
Langet_Loady_ Perf_LosSeaD_Cnt_go:auto   Langet_Loady_ Perf_Micropicing_Cnt_u08 value   Langet_Loady_ Perf_Micropicing_Cnt_u08 value   Langet_Loady_ Perf_Nocorrupting_Cnt_u08 value   Langet_Loady_ Perf_Loady_Cnt_go:auto   Langet_Loady_ Perf_Loady_Cnt_go:auto   Langet_Loady_ Perf_Loady_Cnt_go:auto   Langet_Loady_ Perf_Loady_Cnt_go:auto   Langet_Roady_ Perf_Loady_Cnt_go:auto   Langet_Roady_Cnt_go:auto   Langet_Roady_Cnt_go:auto   Langet_Roady_		1		
Image   Landing Pert   Medicineriplisis   Cert   Landing   Landing Pert   Medicineriplisis   Landing Pert   Landing   Landing Pert   Medicineriplisis   Landing Pert   Landing	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
Integred_Loading_PerT_TORDI_COL_EQUE value	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
Image   Canadar   Canada	target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
	V - V	1		
target, Rein, Rad, Loadlyr, Loadlyr, Perf. DisposSisht/Planck, Cnt. Igo target, Rein, Rad, Loadlyr, Loadlyr, Perf. Hw7qt, Coadlyr, Perf. Lync, Cnt. Igo target, Rein, Rad, Loadlyr, Loadlyr, Perf. Lync, Loadlyr, Perf. Lync, Coadlyr, Perf. Lync, Lync, Lync, Perf. Lync, Perf. Lync, Lync, Lync, Lync, Lync, Perf. Lync,	0 = 0 = = = = = = = = = = = = = = = = =			
single_Rinet_Ap_LoaMy_LoaMy_Per1_Hr7qipoRisig_Cnt_U8				
target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1   Nri*ToLoadkginEn_Cnt_lgc   target_Rel_inst_Ap_Loadkgr_Per1   Nri*ToLoadkginEn_Cnt_lgc   target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1   LOASS_State_enum   target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1   LOASS_State_enum   target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1   LOASS_Dtate_enum   target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1   LOASS_Dtate_enum   target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1   MoRourdostsis_Cnt_uos   target_Loadkgr_Per1   MoRourdostsis_Cnt_uos   target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1   MoRourdostsis_Cnt_uos   target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1   MoRourdostsis_Cnt_uos   target_Loadkgr_Per1   MoRourdostsis_Cnt_uos   target_Rel_inst_Ap_Loadkgr_Loadkgr_Per1   MoRourdostsis_Cnt_uos   target_Loadkgr_Per1   MoRourdostsis_Cnt_uos   target_Loadkgr_Dtate_Nath_Loadkgr_Loadkgr_Per1   MoRourdostsis_Cnt_uos   target_Loadkgr_Dtate_Nath_Loadkgr_Loadkgr_Per1   MoRourdostsis_Cnt_uos   target_Loadkgr_Dtate_Nath_Loadkgr_Loadkgr_Per1   MoRourdostsis_Cnt_uos   target_Loadkgr_Dtate_Nath_Loadkgr_Loadkgr_Per1   MoRourdostsis_Cnt_uos   target_Loadkgr_Per1   target_Loadkgr_Per1   target_Loadk				
large_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_unit_caltytinEn_Cnt_Upc			_	
target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_LOASL_State_enum arget_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_LoaScaDi_Cnt_lgc target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_MotOuridpiSig_Cnt_U08 target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_MotOuridpiSig_Cnt_U08 target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_MotOuridpiSig_Cnt_U08 target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_MotOuridpiSig_Cnt_U08 target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_MotOuridpiSig_Cnt_U08 target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_MotOuridpiSig_Cnt_U08 target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_MotOuridpiSig_Cnt_U08 target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_MotOuridpiSig_Cnt_U08 target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_lgc target_Rite_Inst_Ap_LoaMgr_LoaMg				
target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_LoaScaDi_Cnt_lgc   target_LoaMgr_Pert_MotOgLoaMignen_Cnt_lgc   target_Ret_Inst_Ap_LoaMigr_LoaMgr_Pert_SteemMod_Cnt_enum   target_LoaMgr_Pert_NoaMgr_LoaMgr_Pert_ToaAvail_Cnt_lgc   target_Ret_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_lgc   target_Ret_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_lgc   target_LoaMgr_Pert_ToaAvail_Cnt_lgc   target_LoaMgr_Pert_NoaMgr_DoaMgr_Pert_ToaAvail_Cnt_lgc   target_LoaMgr_Pert_NoaMgr_DoaMg				
target_Rie_Inst_Ap_Loaklyr_Loaklyr_Pert_MicNQLoaklytinEn_Cnt_Igo   target_Rie_Inst_Ap_Loaklyr_Loaklyr_Pert_MicNQLoaklytinEn_Cnt_Igo   target_Rie_Inst_Ap_Loaklyr_Loaklyr_Pert_MicNQLoaklyr_MicNQ				
Integret   Ref.   net   Ap   Loakingr   Loakingr   Pert   Microciantiquisting   Cnt   Loakingr   Pert   Thereologisting   Cnt   Loakingr   Pert   Thereologisting   Cnt   Loakingr   Pert   Thereologisting   Cnt   Cnt   Pert   Microciantiquisting   Pert   Thereologisting   Pert   Per				
larget_Rie_Inst_Ap_Loaklgr_Loaklgr_Perf_MirPosidptSig_Cnt_u08   target_Loaklgr_Perf_NewMod_Cnt_enum   target_Loaklgr_Perf_NewMod_Cnt_enum   target_Loaklgr_Perf_Toaklavalic_Reger_Vert_Perf_NewAvalic_				
target_Rie_Inst_Ap_Loak/gr_Loak/gr_Pert_TioaAvail_Cnt_lgc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtr	nEn_Cnt_lgc	
target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_TioaNc_Cnt_Igc   target_LoaMgr_Pert_TioaDi_Cnt_Igc   target_LoaMgr_Pert_TioaDi_Cnt_Igc   target_LoaMgr_Pert_TioaDi_Cnt_Igc   target_LoaMgr_Pert_TioaDi_Cnt_Igc   target_LoaMgr_Pert_TioaDi_Cnt_Igc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_C	nt_u08	
target_Rie_Inst_Ap_LoaMgr_LoaMgr_Pert_TloaDi_Cnt_lgc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_e	num	
Name         Actual Value         Expected Value         Result           LoaMgr_AgSV_Cnt_M_lgc         1         1         1           LoaMgr_HwTqLoaMignEn_Cnt_M_lgc         1         1         1           LoaMgr_HwTqLoaMignEn_Cnt_M_lgc         1         1         1           LoaMgr_HwTqLoaMignEn_Cnt_M_lgc         0         0         0           LoaMgr_LvirRespSV_Cnt_M_u08         0         0         0           LoaMgr_LvirRespSV_Cnt_M_lgc         1         1         1           LoaMgr_LvirRespSV_Cnt_M_u08         5         5         5           LoaMgr_LvirRespSV_Cnt_M_u08         5         5         5           LoaMgr_LoaRscale-furr_UIS_M_132         500         500         500           LoaMgr_LoaRscale-furr_UIS_M_132         0         0         0           LoaMgr_MotAglidpIsigSV_Cnt_M_u08         5         5         5           LoaMgr_MotAglidpIsigSV_Cnt_M_u08         1         1         1           LoaMgr_MotAglidpIsigSV_Cnt_M_u08         1         1         1           LoaMgr_MotCurrLoaMignEn_Cnt_M_lgc         0         0         0           LoaMgr_MotCurrLoaMignEn_Cnt_M_lgc         1         1         1           LoaMgr_TioaAvai_Cnt_M_lgc			С	
LoaMgr_HvTqlptSigSV_Cnt_M_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Loaklyr_HwTqlcpKisjqSV_Cnt_M_u08			·	Result
LoaMgr_HwTqLoaMtgInEn_Cnt_M_lgc				
LoaMgr_HwTqRespSV_Cnt_M_u08				
LoaMgr_IvitridptSigSV_Cnt_M_u08		· ·		
LoaMgr_IvtrLoaMtghEn_Cnt_M_log				
LoaMgr_IvtRespSV_Cnt_M_u08				
LoaMgr_LoaSt_State_Menum				
LoaMgr_LoaRateLimit_UispS_M_f32         500         500           LoaMgr_LoaScaleFctr_Uis_M_f32         0         0           LoaMgr_MaxMighrResp_Cnt_M_u08         5         5           LoaMgr_MotAgLoaMtighten_Cnt_M_lgc         0         0           LoaMgr_MotAgRespSV_Cnt_M_u08         5         5           LoaMgr_MotCurrldptSigSV_Cnt_M_u08         0         0           LoaMgr_MotCurrloaMtighten_Cnt_M_lgc         1         1           LoaMgr_MotCurrloaMtighten_Cnt_M_lgc         1         1           LoaMgr_MotQurrRespSV_Cnt_M_u08         2         2           LoaMgr_TloaAvail_Cnt_M_lgc         1         1           LoaMgr_TloaDi_Cnt_M_lgc         1         1           LoaMgr_VitgMSrcSV_Cnt_M_u08         1         1           LoaMgr_VitgMSrcSV_Cnt_M_u08         1         1           LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 172, 173}         {170, 172, 173}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0, 0, 0, 0}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1, 1}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_Se				<b>*</b>
LoaMgr_LoaScaleFct_Uls_M_f32         0         0           LoaMgr_MaxMtgtnResp_Cnt_M_u08         5         5           LoaMgr_MotAgldptSigSV_Cnt_M_u08         1         1           LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_MotQrudtptSigSV_Cnt_M_u08         5         5           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotOut_M_lgc         1         1           LoaMgr_TloaDi_Cnt_M_lgc         1         1           LoaMgr_TloaDi_Cnt_M_lgc         1         1           LoaMgr_VltgMSrcSV_Cnt_M_u08         1         1           Rte_Call_Ap_LoaMgr_NttrDiagMgr_SetNTCStatus(NTC_Cnt_Tenum)         (170, 172, 173)         (170, 172, 173)           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_Tu08)         (1, 1, 1)         (1, 1, 1)           Rte_Vite_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum)         (1, 1, 1)         (1, 1, 1)           Rte_Vite_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum)         (1, 1, 1)         (1, 1, 1)           Rte_Vite_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum)         (1, 1, 1)         (1, 1, 1)           Rte_Vite_A	·			_
LoaMgr_MotAgldptSigSV_Cnt_M_u08         1         1           LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_MotAgRespSV_Cnt_M_u08         5         5           LoaMgr_MotCurrIdptSigSV_Cnt_M_u08         0         0           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrRespSV_Cnt_M_u08         2         2           LoaMgr_TloaAvail_Cnt_M_lgc         1         1           LoaMgr_TloaDi_Cnt_M_lgc         1         1           LoaMgr_VitgMdSrcsV_Cnt_M_u08         1         1           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 172, 173}         {170, 172, 173}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}         **           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}         **           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}         **           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1, 1}         {1, 1, 1, 1}         **           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         500         500 ± 0.01         **           At arget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value				<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_MotAgRespSV_Cnt_M_u08         5         5           LoaMgr_MotCurrldptSigSV_Cnt_M_u08         0         0           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrRespSV_Cnt_M_u08         2         2           LoaMgr_TloaAvail_Cnt_M_lgc         1         1           LoaMgr_TloaDi_Cnt_M_lgc         1         1           LoaMgr_TloaDi_Cnt_M_lgc         1         1           LoaMgr_VttgMotScSV_Cnt_M_u08         1         1           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 172, 173}         {170, 172, 173}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}           Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Cont_T_enum)         {1, 1, 1}         {1, 1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         1         1           target_LoaMgr_Per1_LoASt_State_enum.value         5         5	LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotAgRespSV_Cnt_M_u08         5         5           LoaMgr_MotCurrIdptSigSV_Cnt_M_u08         0         0           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrRespSV_Cnt_M_u08         2         2           LoaMgr_TloaAvail_Cnt_M_lgc         1         1           LoaMgr_NtloaMgr_Sct_Cnt_M_u08         1         1           LoaMgr_VltgMdSrcSV_Cnt_M_u08         1         1           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 172, 173}         {170, 172, 173}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}         **           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}         **           Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}         **           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01         **           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         **           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         1         1         1           target_LoaMgr_Per1_LoASt_State_enum.value         5         5           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.v	LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08         0         0           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1           LoaMgr_MotCurrRespSV_Cnt_M_u08         2         2           LoaMgr_TloaAvail_Cnt_M_lgc         1         1           LoaMgr_TloaDi_Cnt_M_lgc         1         1           LoaMgr_VltgMdSrcSV_Cnt_M_u08         1         1           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 172, 173}         {170, 172, 173}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}         3           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}         {1, 1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01         4           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         4           rarget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         1         1         4           rarget_LoaMgr_Per1_LOASt_State_enum.value         5         5         4           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0         0	LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         1         1         2           LoaMgr_MotCurrRespSV_Cnt_M_u08         2         2         2           LoaMgr_TloaAvail_Cnt_M_lgc         1         1         1           LoaMgr_VltgMdSrcSV_Cnt_M_u08         1         1         1           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 172, 173}         {170, 172, 173}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}         X           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}         {1, 1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01         X           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         X           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001         X           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         1         1         1           target_LoaMgr_Per1_LOASt_State_enum.value         5         5         X           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0         X	LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrRespSV_Cnt_M_u08         2         2           LoaMgr_TloaAvail_Cnt_M_lgc         1         1           LoaMgr_VltgMdSrcSV_Cnt_M_u08         1         1           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {170, 172, 173}         {170, 172, 173}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         1         1           target_LoaMgr_Per1_LOASt_State_enum.value         5         5           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0	LoaMgr_MotCurrldptSigSV_Cnt_M_u08			~
LoaMgr_TloaAvail_Cnt_M_lgc       1       1       1         LoaMgr_TloaDi_Cnt_M_lgc       1       1       1         LoaMgr_VltgMdSrcSV_Cnt_M_u08       1       1       1         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       {170, 172, 173}       {170, 172, 173}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       {1, 1, 1}       {0, 0, 0, 0, 0}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       {1, 1, 1}       {1, 1, 1}         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       1       1         target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value       1       1         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       5       5         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0				~
LoaMgr_TloaDi_Cnt_M_lgc       1       1         LoaMgr_VltgMdSrcSV_Cnt_M_u08       1       1         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       {170, 172, 173}       {170, 172, 173}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       {1, 1, 1}       {0, 0, 0, 0, 0}       X         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       {1, 1, 1}       {1, 1, 1}       {1, 1, 1}       X         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01       X         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001       X         Rte_Write_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       1       1       X         target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value       1       1       X         target_LoaMgr_Per1_LOASt_State_enum.value       5       5       X         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0       X				~
LoaMgr_VltgMdSrcSV_Cnt_M_u08       1       1         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       {170, 172, 173}       {170, 172, 173}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       {1, 1, 1}       {0, 0, 0, 0, 0}       X         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       {1, 1, 1}       {1, 1, 1}       {1, 1, 1}       X         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01       X         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001       X         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       1       1       X         target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value       1       1       X         target_LoaMgr_Per1_LOASt_State_enum.value       5       5       X         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0       X				
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       {170, 172, 173}       {170, 172, 173}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       {1, 1, 1}       {0, 0, 0, 0, 0}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       {1, 1, 1}       {1, 1, 1}         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       1       1         target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value       1       1         target_LoaMgr_Per1_LOASt_State_enum.value       5       5         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0				<b>V</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1, 1}         {0, 0, 0, 0, 0}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1, 1}         {1, 1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         1         1           target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value         1         1           target_LoaMgr_Per1_LOASt_State_enum.value         5         5           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0				· ·
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_renum)       {1, 1, 1}       {1, 1, 1}         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       1       1         target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value       1       1         target_LoaMgr_Per1_LOASt_State_enum.value       5       5         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0		Ti i		
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         0         0 ± 0.001           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         1         1           target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value         1         1           target_LoaMgr_Per1_LOASt_State_enum.value         5         5           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0				
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       0       0 ± 0.001         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       1       1         target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value       1       1         target_LoaMgr_Per1_LOASt_State_enum.value       5       5         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0				
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1				
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value				~
target_LoaMgr_Per1_LOASt_State_enum.value 5 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0				~
0 = 0 = 0 = 0 = 0		5	5	<b>✓</b>
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value 1	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 4.109 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	500
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u(	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0	Ont Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn	Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enun		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	_
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	<b>~</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	-
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	2	2	
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	•
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>~</b>
target LoaMgr Per1 MotAgLoaMtgtnEn Cnt Igc.value	0	0	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	•

Test Step Call Trace	Fest Step Call Trace ✓				
Actual Function	Count	Expected Function	Count	Result	
CalcivtrState	2	CalclvtrState	2	~	
LatchInputs	1	LatchInputs	1	<b>✓</b>	
RqstRespConds	1	RqstRespConds	1	~	
RqstHwTqResp	1	RqstHwTqResp	1	~	
ArbResp	1	ArbResp	1	~	
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~	
AssignScale	1	AssignScale	1	~	
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~	
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>✓</b>	





Test Step 4.110 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32 k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1 0
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k LoaMgr MotAgldptSig3Resp Cnt u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32 k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_tvtLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaSt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_Loascabi_Cht_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
2 2 2 2 3 3 3 2 3 2 3 3 3 3 3 3 3 3 3 3	, , , , , , , , , , , , , , , , , , , ,





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	<b>~</b>
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>~</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Evit An LoaMor FalloaScaleAndRate	1	Rte Exit An LoaMor FalloaScaleAndRate	1	-

Test Step 4.111 (Repeat Count = 1)		×
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_lvtrRespSV_Cnt_M_u08	4	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	477.259705	

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LoaMgr\_Per1



Name	Input Value			
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1			
k_LoaMgr_FadeOutStRate_UlspS_f32	1			
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2			
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4			
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1			
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0			
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0			
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1			
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1			
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1			
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0			
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0			
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	1 0			
k_LoaMgr_lvtrMtgtnSca_UIs_f32	1			
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5			
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1			
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0			
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2			
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1			
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2			
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2			
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08 k LoaMgr MotCurrldptSig2Resp Cnt u08	0			
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1			
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1			
k_LoaMgr_RedcdStRate_UlspS_f32	1			
k_LoaMgr_RedcdStSca_Uls_f32	1			
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0			
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1			
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1			
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0			
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1			
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target LoaMgr Per1 DiagcStslytr2Inacty C			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc			
	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_l(	t_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_k target_LoaMgr_Per1_LOASt_State_enum	t_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	g t_lgc gc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	s t_lgc gc nt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	s t_lgc gc nt_lgc 08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrldptSig_Cnt_u	s t_lgc gc nt_lgc 08 Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lget_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lget_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Crtarget_LoaMgr_Per1_MotCurrldptSig_Cnt_u1 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C	s t_lgc gc nt_lgc 08 Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lget_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lget_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgetarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Crtarget_LoaMgr_Per1_MotCurrldptSig_Cnt_u01 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u01	s t_lgc gc nt_lgc 08 Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lget_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lget_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgetarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Crtarget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08capt_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cttarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cttarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08capt_loaMgr_Per1_SteerMod_Cnt_enum	s t_lgc gc nt_lgc 08 Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	t_lgc gc nt_lgc 08 Cnt_lgc 08 Expected Value	Result	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_uget_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgtarget_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Crtarget_LoaMgr_Per1_MotCurrldptSig_Cnt_u01 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u01 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	Expected Value	~	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1	Expected Value  1 0	<b>Y</b>	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgt target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1	Expected Value  1 0 1	<b>*</b>	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgt target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1	Expected Value  1 0 1 4	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgt target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1 4 1	Expected Value  1 0 1 4 1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIoaMtgtnEn_Cnt_M_Igc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgctarget_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrloaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value  1 0 1 4 1	Expected Value  1 0 1 4 1 0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgt target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1 4 1	Expected Value  1 0 1 4 1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIoaMtgtnEn_Cnt_M_Igc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotQurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1 4 1 0 4	Expected Value  1 0 1 4 1 0 4	***	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IOASt_State_M_enum	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotQurrldptSig_Cnt_u0 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1 4 1 0 4 5	Expected Value	***	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIopattgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 1 4 1 0 4 5 5 500	Expected Value  1 0 1 4 1 0 4 5 5 500	***	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIoptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_liget_LoaMgr_Per1_LoaScaDi_Cnt_ligetarget_LoaMgr_Per1_LoaScaDi_Cnt_ligetarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u0target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaDi_Cnt_ligetarget_LoaMgr_Per1_TloaDi_Cnt_lige  Actual Value  1 0 1 4 5 500 0 5 0	Expected Value 1 0 1 4 1 0 4 5 5 00 0 5 0	***************************************	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Ont_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_liget_LoaMgr_Per1_LoaScaDi_Cnt_ligetarget_LoaMgr_Per1_LoaScaDi_Cnt_ligetarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u0target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_ligetarget_LoaMgr_Per1_TloaDi_Cnt_lige  Actual Value  1 0 1 4 5 500 0 5	Expected Value  1 0 1 4 1 0 4 5 500 0 5	******	

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	<b>~</b>
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc.value	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~



```
Test Case 5: Path Test
 Specification
                                                                                                         Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:
                                                                                                     CPU Cycles:

TS 5.1 5012.00 Cycles
TS 5.2 5012.00 Cycles
TS 5.3 4974.00 Cycles
TS 5.4 5006.00 Cycles
TS 5.4 5006.00 Cycles
TS 5.6 4926.00 Cycles
TS 5.6 4926.00 Cycles
TS 5.7 4920.00 Cycles
TS 5.7 4920.00 Cycles
TS 5.9 4551.00 Cycles
TS 5.9 4551.00 Cycles
TS 5.11 4930.00 Cycles
TS 5.11 4930.00 Cycles
TS 5.12 4930.00 Cycles
TS 5.15 4994.00 Cycles
TS 5.16 4969.00 Cycles
TS 5.17 4934.00 Cycles
TS 5.18 4934.00 Cycles
TS 5.19 4987.00 Cycles
TS 5.19 4987.00 Cycles
TS 5.19 4987.00 Cycles
TS 5.20 4941.00 Cycles
TS 5.21 4925.00 Cycles
TS 5.21 4925.00 Cycles
TS 5.22 4908.00 Cycles
TS 5.23 4951.00 Cycles
TS 5.24 4961.00 Cycles
TS 5.24 4961.00 Cycles
                                                                                                         TS 5.24 4961.00 Cycles
                                                                                                      Vector Description:
Description
                                                                                                     TS5.1(HwTqldptSig_Cnt_T_u08 > D_MAXHWTQIDPTSIGALLWD_CNT_U08)==>True
TS5.2(MtrPosldptSig_Cnt_T_u08 > D_MAXMOTPOSNIDPTSIGALLWD_CNT_U08)==>True
TS5.3(MotCurrldptSig_Cnt_T_u08 > D_MAXMOTCURRIDPTSIGALLWD_CNT_U08)==>True
TS5.4(LoaMgr_LOASt_State_M_enum == LOAST_FADEOUT)==>True
TS5.5(LoaMgr_LOASt_State_M_enum == LOAST_SWBASDMTGTN)==>True
TS5.6(LoaMgr_LOASt_State_M_enum == LOAST_REDCD)==>True
TS5.7(LoaMgr_LOASt_State_M_enum == LOAST_NORM)==>True
                                                                                                       TS5.8(SteerMod_Cnt_T_enum == STEERMOD_FULLYATNMS)==>True
                                                                                                  TS5.9/default:

LoaMgr_LOASt_State_M_enum = LOAST_FADEOUT==>True"

TS5.10(SteerMod_Cnt_T_enum == STEERMOD_FULLYATNMS)==>True
TS5.11((NewValFlag_Cnt_T_lgc == TRUE) ==>True && (k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc == TRUE))==>True
TS5.12((NewValFlag_Cnt_T_lgc == TRUE) ==>False && (k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc == TRUE))
TS5.13((NewValFlag_Cnt_T_lgc == TRUE) ==>F && (k_LoaMgr_MotCurrlvtgtnScaZeroEn_Cnt_lgc == TRUE))
TS5.14((NewValFlag_Cnt_T_lgc == TRUE) ==>T && (k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc == TRUE))
TS5.15((NewValFlag_Cnt_T_lgc == TRUE) ==>T && (k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc == TRUE))==>T
TS5.16((NewValFlag_Cnt_T_lgc == TRUE)==>T && (k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc == TRUE)) ==>F
TS5.17((NewValFlag_Cnt_T_lgc == TRUE)==>T && (k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc == TRUE)) ==>F
TS5.18((NewValFlag_Cnt_T_lgc == TRUE)==>T && (k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc == TRUE)) ==>T
TS5.18((NewValFlag_Cnt_T_lgc == TRUE)==>F && (k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc == TRUE))
TS5.19(LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc == TRUE)==>T
TS5.20(LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc == TRUE)==>T
TS5.21((k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc == TRUE)==>T
TS5.22((k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc == TRUE)==>T
TS5.23 (LoaMgr_LoaRateLimit_UlspS_M_f32<=0.01)=>T
TS5.24(LoaMgr_LoaScaleFctr_Uls_M_f32<=0.01)=>T
                                                                                                       TS5.9"default:
```

Test Step 5.1 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	0.0099999978
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	0
k_LoaMgr_FadeOutStRate_UlspS_f32	0.0099999978
k_LoaMgr_HwTqIdptSig0TloaAvailResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	0
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	0.00999999978

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TS5.24(LoaMgr\_LoaScaleFctr\_Uls\_M\_f32<=0)=>F





Name	Input Value		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	0.0099999978		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	0		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	0		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	0.00999999978		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	0.00999999978		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	0		
k_LoaMgr_RedcdStRate_UlspS_f32	0.00999999978		
k_LoaMgr_RedcdStSca_Uls_f32	0		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	5		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inact		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inact		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cl		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enu		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgo		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotAgLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrIdptSig_Ci target_LoaMgr_Per1_MotCurrLoaMtgtnE	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_en	_	
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target LoaMgr Per1 TloaAvail Cnt Igc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M Igc	0	0	Result
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	-
LoaMgr lvtrLoaMtgtnEn Cnt M lgc	0	0	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	-
LoaMgr_LOASt_State_M_enum	5	5	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	•
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	•
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	0	0	•
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	[X
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	
HATE MARTIN ARE LOOKED LOOK OF LINE LINES #27/dota		500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	





Name	Actual Value	Expected Value	Result
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc.value	0	0	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>~</b>
RqstRespConds	1	RqstRespConds	1	<b>~</b>
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	<b>~</b>
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	-
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Name	Input Value
_oaMgr_AgSV_Cnt_M_lgc	0
	0
_oaMgr_HwTqldptSigSV_Cnt_M_u08	0
.oaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	·
_oaMgr_HwTqRespSV_Cnt_M_u08	0
.oaMgr_lvtrldptSigSV_Cnt_M_u08	0
_oaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
.oaMgr_lvtrRespSV_Cnt_M_u08	0
.oaMgr_MotAgldptSigSV_Cnt_M_u08	0
_oaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
_oaMgr_MotAgRespSV_Cnt_M_u08	0
_oaMgr_MotCurrldptSigSV_Cnt_M_u08	0
.oaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
.oaMgr_MotCurrRespSV_Cnt_M_u08	0
.oaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
.oaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	0.0099999978
_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	0
_LoaMgr_FadeOutStRate_UlspS_f32	0.0099999978
_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	0
_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	0
_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	0
_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	0.
_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
_LoaMgr_HwTqldptSigFltThd_Cnt_u08	0
LoaMgr HwTqMtgtnRate UlspS f32	0.0099999978
	0
_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
LoaMgr lvtrldptSig2Resp Cnt u08	0
LoaMgr lvtrldptSigFltThd Cnt u08	0
_LoaMgr_IvtrMtgtnRate_UlspS_f32	0.0099999978
LoaMgr IvtrMtgtnScaZeroEn Cnt Igc	0
_LoaMgr_lvtrMtgtnSca_Uls_f32	0
LoaMgr MotAgldptSig1NoSmaResp Cnt u08	0
coaMgr_MotAgldptSig2Resp_Cnt_u08	0
coamgr_motAgidptSig2Resp_Cnt_u06 : LoaMgr_MotAgidptSig3Resp_Cnt_u08	0
_LoaMgr_MotAgldptSig5Resp_Crit_u06 _LoaMgr_MotAgldptSigFltThd_Cnt_u08	0
	0.0099999978
_LoaMgr_MotAgMtgtnRate_UlspS_f32	0.0099999978
_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	·
_LoaMgr_MotAgMtgtnSca_Uls_f32	0
C_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0
x_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	0
x_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
<pre>c_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08</pre>	0
C_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0





Name	Input Value		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	0.0099999978		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	0		
k LoaMgr MotCurrMtgtnSca Uls f32	0		
k_LoaMgr_RedcdStRate_UlspS_f32	0.0099999978		
k LoaMgr RedcdStSca Uls f32	0		
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target LoaMgr Per1 LoaScaDi Cnt Igc.value	0		
target LoaMgr Per1 MotCurrldptSig Cnt u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target LoaMgr Per1 DiagcStslvtr1Inac	tv Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inac	tv Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enu		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lg		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnE		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_C		
target Rte Inst Ap LoaMgr.LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc	target LoaMgr Per1 MotCurrLoaMtgtn	_	
target Rte Inst Ap LoaMgr.LoaMgr Per1 MtrPosldptSig Cnt u08	target_LoaMgr_Per1_MtrPosIdptSig_Cr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_er	_	
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
			L 1/
Name	Actual Value	Expected Value	Result
Name LoaMgr_AgSV_Cnt_M_lgc	Actual Value	0 Expected Value	Result
		•	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	Result
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0 0 0	0 0 0	<i>y</i>
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	0 0 0 4	0 0 0 4	· ·
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08	0 0 0 4 0	0 0 0 4 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0 0 0 4 0	0 0 0 4 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08	0 0 0 4 0 0 5	0 0 0 4 0 0 5	***
LoaMgr_Ag\$V_Cnt_M_lgc LoaMgr_HwTqldptSig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_lvtrldptSig\$V_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrResp\$V_Cnt_M_u08 LoaMgr_lvtrResp\$V_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	0 0 0 4 0 0 5 5	0 0 0 4 0 0 5 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_Ag\$V_Cnt_M_Igc LoaMgr_HwTqldptSig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_IvtrldptSig\$V_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrResp\$V_Cnt_M_u08 LoaMgr_IvtrResp\$V_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_Ulsp\$_M_f32	0 0 0 4 0 0 5 5 5	0 0 0 4 0 0 5 5 5	**************************************
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	0 0 0 4 0 0 0 5 5 5 5 500	0 0 0 4 0 0 0 5 5 5 500	**************************************
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaState_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	0 0 0 4 0 0 0 5 5 5 5 500	0 0 0 4 0 0 0 5 5 5 500 0	**************************************
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaState_Mign_S_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	0 0 0 4 0 0 0 5 5 5 5 500 0	0 0 0 4 0 0 0 5 5 5 500 0	**************************************
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaState_Mign_S_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_Igc	0 0 0 4 0 0 0 5 5 5 5 500 0 0 5	0 0 0 4 0 0 0 5 5 5 5 500 0 0 5	**************************************
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaState_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08	0 0 0 4 0 0 0 5 5 5 5 500 0 0 5	0 0 0 4 0 0 0 5 5 5 5 500 0 0 5	**************************************
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LVtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaSt_ate_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08	0 0 0 4 0 0 0 5 5 5 5 500 0 0 5 0 0 5	0 0 0 4 0 0 0 5 5 5 5 500 0 0 5 0 0 5	> > > > > > > > > > > > > > > > > > >
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaSt_ate_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIoaMtgtnEn_Cnt_M_Igc	0 0 0 4 0 0 0 5 5 5 5 500 0 0 5 0 5 0 0	0 0 0 4 0 0 0 5 5 5 5 500 0 0 5 0 0 5	**************************************
LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrIdptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaSt_ate_M_enum  LoaMgr_LoaScaleFctr_UIs_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgCoaMtgtnEn_Cnt_M_Igc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrIcoaMtgtnEn_Cnt_M_Igc  LoaMgr_MotCurrRespSV_Cnt_M_u08	0 0 0 4 0 0 0 5 5 5 5 500 0 0 5 5 0 0 0 0	0 0 0 4 0 0 0 5 5 5 500 0 0 5 0 0 5 0 0	**************************************
LoaMgr_Ag\$V_Cnt_M_lgc LoaMgr_HwTqldptSig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_IvtrldptSig\$V_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrResp\$V_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_Ulsp\$S_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSig\$V_Cnt_M_u08 LoaMgr_MotAgldptSig\$V_Cnt_M_u08 LoaMgr_MotAgResp\$V_Cnt_M_u08 LoaMgr_MotCurrldptSig\$V_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrResp\$V_Cnt_M_u08 LoaMgr_MotCurrResp\$V_Cnt_M_u08 LoaMgr_MotCurrResp\$V_Cnt_M_u08 LoaMgr_MotCurrResp\$V_Cnt_M_u08 LoaMgr_MotCurrResp\$V_Cnt_M_u08	0 0 0 4 0 0 0 5 5 5 5 500 0 0 5 5 0 0 0	0 0 0 4 0 0 5 5 5 500 0 5 0 0 5 0 0	**************************************
LoaMgr_Ag\$V_Cnt_M_lgc LoaMgr_HwTqldptSig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_IvtrldptSig\$V_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MoxAgldptSig\$V_Cnt_M_u08 LoaMgr_MoxAgldptSig\$V_Cnt_M_u08 LoaMgr_MotAgldptSig\$V_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	0 0 0 4 0 0 0 5 5 5 5 500 0 0 5 5 0 0 0 0	0 0 0 4 0 0 0 5 5 5 500 0 0 5 0 0 5 0 0	**************************************
LoaMgr_Ag\$V_Cnt_M_lgc LoaMgr_HwTqldptSig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_IvtrldptSig\$V_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrResp\$V_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_Ulsp\$_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSig\$V_Cnt_M_u08 LoaMgr_MotAgdLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSig\$V_Cnt_M_u08 LoaMgr_MotCurrldptSig\$V_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrResp\$V_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMd\$rc\$V_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTC\$tatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTC\$tatus(Param_Cnt_T_u08)	0 0 0 4 0 0 0 5 5 5 500 0 0 5 0 0 0 0 0	0 0 0 4 0 0 0 5 5 5 500 0 0 5 0 0 0 5 0 0 0 0	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 0 0 4 0 0 0 5 5 5 500 0 0 5 0 0 0 0 0	0 0 0 4 0 0 5 5 5 500 0 5 0 0 0 0 0 0 0 0 0 0 0 0 0	
LoaMgr_Ag\$V_Cnt_M_Igc LoaMgr_HwTqldptSig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_IvtrldptSig\$V_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrResp\$V_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UIsp\$_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSig\$V_Cnt_M_u08 LoaMgr_MotAgUtSig\$V_Cnt_M_u08 LoaMgr_MotAgResp\$V_Cnt_M_u08 LoaMgr_MotOurrIdptSig\$V_Cnt_M_u08 LoaMgr_MotCurrIdptSig\$V_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrResp\$V_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_VltgMdSrc\$V_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 0 0 4 0 0 0 5 5 5 5 500 0 0 5 0 0 0 0	0 0 0 4 0 0 0 5 5 5 500 0 0 5 0 0 0 0 0 0 0 0 0 0 0 0 0	
LoaMgr_Ag\$V_Cnt_M_Igc  LoaMgr_HwTqldptSig\$V_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_HwTqResp\$V_Cnt_M_u08  LoaMgr_IvtrldptSig\$V_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrResp\$V_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UIsp\$_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSig\$V_Cnt_M_u08  LoaMgr_MotAglcaMtgtnEn_Cnt_M_Igc  LoaMgr_MotOcurrIdptSig\$V_Cnt_M_u08  LoaMgr_MotCurrIdptSig\$V_Cnt_M_u08  LoaMgr_MotCurrIcoaMtgtnEn_Cnt_M_Igc  LoaMgr_MotCurrResp\$V_Cnt_M_u08  LoaMgr_MotCurrResp\$V_Cnt_M_u08  LoaMgr_MotCurrResp\$V_Cnt_M_u08  LoaMgr_TloaAvaii_Cnt_M_Igc  LoaMgr_TloaDi_Cnt_M_Igc  LoaMgr_TloaDi_Cnt_M_Igc  LoaMgr_VltgMd\$rc\$V_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTC\$tatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTC\$tatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UIsp\$_f32(data)	0 0 0 4 0 0 0 5 5 5 5 500 0 0 5 0 0 0 0	0 0 0 4 0 0 5 5 5 500 0 5 0 0 0 5 0 0 0 0	
LoaMgr_Ag\$V_Cnt_M_Igc  LoaMgr_HwTqldptSig\$V_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_HwTqResp\$V_Cnt_M_u08  LoaMgr_IvtrldptSig\$V_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrResp\$V_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UIsp\$_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSig\$V_Cnt_M_u08  LoaMgr_MotAgldptSig\$V_Cnt_M_u08  LoaMgr_MotQurrIdptSig\$V_Cnt_M_u08  LoaMgr_MotCurrIdptSig\$V_Cnt_M_u08  LoaMgr_MotCurrResp\$V_Cnt_M_u08  LoaMgr_MotCurrResp\$V_Cnt_M_u08  LoaMgr_MotCurrResp\$V_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_Igc  LoaMgr_TloaDi_Cnt_M_Igc  LoaMgr_TloaDi_Cnt_M_Igc  LoaMgr_TloaDi_Cnt_M_Igc  LoaMgr_VltgMd\$rc\$V_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTC\$tatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTC\$tatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UIsp\$_f32(data)  ttarget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	0 0 0 4 0 0 0 5 5 5 5 500 0 0 5 0 0 0 0	0 0 0 4 0 0 5 5 5 500 0 5 0 0 0 5 0 0 0 0	V V V V V V V V V V V V V V V V V V V
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgHotSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0 0 0 4 0 0 5 5 5 5 500 0 0 5 0 0 0 0 0	0 0 0 4 0 0 5 5 5 500 0 0 5 0 0 0 0 0 0	V V V V V V V V V V V V V V V V V V V
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgHotSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_loASt_State_enum.value	0 0 0 4 0 0 5 5 5 5 500 0 0 5 0 0 0 0 0	0 0 0 4 0 0 5 5 5 500 0 0 5 0 0 0 0 0 0	V V V V V V V V V V V V V V V V V V V
LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgHotSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0 0 0 4 0 0 5 5 5 5 500 0 0 5 0 0 0 0 0	0 0 0 4 0 0 5 5 5 500 0 0 5 0 0 0 0 0 0	V V V V V V V V V V V V V V V V V V V



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	•
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 5.3 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	0.0099999978
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k LoaMgr CurrivtrMtgtnSca Uls f32	0
k LoaMgr FadeOutStRate UlspS f32	0.0099999978
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	0
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	0
	0
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	0
	0
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	0
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	0.0099999978
k_LoaMgr_HwTqMtgtnSca_UIs_f32	0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	0.0099999978
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	0
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	0
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	0.0099999978
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	0
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	0.0099999978
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	0
k_LoaMgr_RedcdStRate_UlspS_f32	0.0099999978
k_LoaMgr_RedcdStSca_Uls_f32	0
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	-	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	0	0	- Ttoouit
LoaMgr HwTqldptSigSV Cnt M u08	0	0	
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	-
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	-
	500	500	
LoaMgr_LoaRateLimit_UlspS_M_f32	0	0	-
LoaMgr_LoaScaleFctr_Uls_M_f32	5	5	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	0	0	-
LoaMgr_MotAglaptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	5	5	-
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08			-
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	-
LoaMgr_TloaAvail_Cnt_M_lgc			
LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>V</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	•
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>Y</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>Y</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Name	Test Step 5.4 (Repeat Count = 1)	<b>3</b>
Looking, Levingonisty, Core, A. Lyo  Looking, Terforposity, Core, M. Lyo  Looking, Levingonisty, Core, Lyo  Looking, Levingonisty, Lyo  Looking, Levingonisty, Lyo  Levingonisty, Lyo  Looking, Levingonisty, Lyo  Looking, Levingonisty, Lyo  Levingonisty, Lyo  Looking		
Looking   Jen's Capitage   Looking		·
Loading Justification Cort M. ge  Loadin		
Landery - Minispressy Cort M. 2008  Landery - Minispressy Minispre		
Loading   Methodipside   Cort   Mu   100		
Landary Environmental Communication of the Communic	LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
Loading / MASQ-place(SSV_COT_M_USB   1   1   1   1   1   1   1   1   1	LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	t.
Coading - Manage analoge   Comp   Log	LoaMgr_lvtrRespSV_Cnt_M_u08	0
Coading	LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
Landary MacContradignisty Cort JU 268 Loading AMSCONTROSSY JOT M, 1008 Loading AMSCONTROSS JOT M, 1008 Loading AMSCONTROSSY JOT M, 1008 Loading Loading AMSCONTROSSY JOT M, 1008 Loading Loading AMSCONTROSSY JOT M, 1008 Loading Loading AMS	LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
Loading MoChartecky Cort M. 198  Loading And Chartecky Cort M. 198  Loading Chartechy Cort M. 198  1 Loading Chartechy Chartechy Cort M. 198  1 Loading Chartechy Chart	LoaMgr_MotAgRespSV_Cnt_M_u08	
Coading   Sender Anglewan   Cort   M.   196	LoaMgr_MotCurrldptSigSV_Cnt_M_u08	
Lookings		
Loading / Jiny Margines   Lings   129		
Insert   I	v =	
L_analog_Currientingsroad_relian_Currientingsroad_re		
L. Landarg. Juristy Mingriss Care (Text.)		V
Loading-Info@displane-lusinss-Info Loading-Info@displane-lusinss-Info Loading-Info@displane-lusinss-Info Loading-Info@displane-lusinss-Info Loading-Info@displane-lusinss-Info Loading-Info@displane-lusinss-Info Loading-Info@displane-lusins-Info Loading-Info@displane-lusins-Inf		
L. Loadby_InVolagingStyTeachwallResp_Cott_U08	k LoaMgr FadeOutStRate UlspS f32	
Lackleg   Mrd cipicsig   No Time	k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	
Landsrg-Mindigstg2TeaDRess_Cot_U08	k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	
L. Loadby / M-Vigolosis/QarToon/Resp. Cnt. U08	k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	
Loaking-Hw7qtqlSig4Rep_Cnt_U8	k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
Loaking-HwTqNtgRispRTMC ort, UGB	k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
L. Loading-International Lights   132	k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
L. Loading _ Invitrightispic Resp_ Cnt_ u08	k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
Loaking_NritighSigResp_Cnt_u08	k_LoaMgr_HwTqMtgtnRate_UlspS_f32	
Loading		
Loaking		
L_LoaMgr_InvirMignRate_UlspS_132		
LoaMgr_MoNAglophSig1NoSmaResp_Cnt_U08		
LoaMgr_MotAgidptSig1NsSmaResp_Cnt_u08		
LoaMgr_MotAgldpiSig2Resp_Cnt_u08		
k_LoaMgr_MotAgidptSigSResp_Cnt_u08         0           k_LoaMgr_MotAgidptSigFirthd_Cnt_u08         2           k_LoaMgr_MotAgMignRate_Ulbsp5_f32         1           k_LoaMgr_MotAgMignRate_Ulbsp5_f32         1           k_LoaMgr_MotAgMignSca_Uls_f32         0           k_LoaMgr_MotAgMignSca_Dis_f32         1           k_LoaMgr_MotAgSmaAvailresp_Cnt_u08         0           k_LoaMgr_MotCurridptSigTesp_Cnt_u08         1           k_LoaMgr_MotCurridptSigTesp_Cnt_u08         1           k_LoaMgr_MotCurridptSigTesp_Cnt_u08         1           k_LoaMgr_RedodSitea_Ulsp_S_d2         1           k_LoaMgr_RedodSitea_Ulsp_S_d2         1           k_LoaMgr_RedodSitea_Ulsp_S_d2         1           k_LoaMgr_RedodSitea_Ulsp_S_d2         1           k_LoaMgr_RedodSitea_Ulsp_Cnt_u08 value         0           target_LoaMgr_Perl_DiagcStstvtTinactv_Cnt_lgc value         0           target_LoaMgr_Perl_MotCurdptSig_Cnt_u08 valu		
LoaMgr_MotAgMtghrRate_UlspS_f32	k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgMtginSca_ZeroEn_Cnt_lgc         0           k_LoaMgr_MotAgMtginSca_Uis_132         1           k_LoaMgr_MotAgMtginSca_Uis_132         1           k_LoaMgr_MotCurridptSigGResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigGResp_Cnt_u08         0           k_LoaMgr_MotCurridptSigFilThd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFilThd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFilThd_Cnt_u08         0           k_LoaMgr_MotCurrMgthRate_UispS_132         1           k_LoaMgr_MotCurridptSigFilThd_Cnt_u08         1           k_LoaMgr_MotCurridptSigSa_Uis_132         1           k_LoaMgr_MotCurridptSig_Cnt_u08         1           k_LoaMgr_RedcdSiRca_Uis_132         1           k_LoaMgr_RedcdSiRca_Uis_132         1           k_LoaMgr_Perl_DiagcSistintrInack_Cnt_lgc_value         0           target_LoaMgr_Perl_DiagcSistintrInack_Cnt_lgc_value         0           target_LoaMgr_Perl_MotCurridptSig_Cnt_u08 value         1           target_LoaMgr_Perl_MotCurridptSig_Cnt_u08 value         1           target_LoaMgr_Perl_MotCurridptSig_Cnt_u08 value         0           target_LoaMgr_Perl_MotCurridptSig_Cnt_u08 value         1           target_LoaMgr_Perl_Mpr_Cavalic_Cnt_lgc_value         0           target_LoaMgr_Perl_Tolaqcvalic_Cnt_lgc_value	k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
LoaMgr_MotAgMtginSca_Uls_f32	k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
K_LoaMgr_MotOurridptSig0Resp_Cnt_u08         2           K_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           K_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           K_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           K_LoaMgr_MotCurridptSig1FIThd_Cnt_u08         0           K_LoaMgr_MotCurridptSig1FIThd_Cnt_u08         0           K_LoaMgr_MotCurridptSig2FIThd_Cnt_u08         1           K_LoaMgr_MotCurridptSig2FIThd_Cnt_u08         1           K_LoaMgr_MotCurridptSig2Fithd_Cnt_u08         1           K_LoaMgr_MotCurridptSig2Fithd_Cnt_u08         1           K_LoaMgr_MotCurridptSig2D_Us_32         1           K_LoaMgr_MotCurridptSig_Us_32         1           K_LoaMgr_Pert_DiagcStsivtrilnactv_Cnt_lgc value         0           Larget_LoaMgr_Pert_DiagcStsivtrilnactv_Cnt_lgc value         0           Larget_LoaMgr_Pert_LoaScaD_Cnt_lgc_value         1           Larget_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value         1           Larget_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value         0           Larget_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value         1           Larget_LoaMgr_Pert_ToaAvail_Cnt_lgc_value         0           Larget_LoaMgr_Pert_ToaAvail_Cnt_lgc_value         0           Larget_LoaMgr_Pert_ToaAvail_Cnt_lgc_value         1           Larget_LoaMgr_Pert	k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         2           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         1           k_LoaMgr_MotCurridptSignEscaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurridptSigcaLus_Sca_Uis_f32         1           k_LoaMgr_RedcdSiRate_UispS_532         1           k_LoaMgr_RedcdSiSca_Uis_f32         1           target_LoaMgr_Per1_DiagcSistvtr2Inactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcSistvtr2Inactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcSistvtr2Inactv_Cnt_lgc.value         1           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 value         1           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 value         1           target_LoaMgr_Per1_MotPosldptSig_Cnt_u08 value         1           target_LoaMgr_Per1_TloaDi_Cnt_lgc.value         0           target_LoaMgr_Per1_TloaDi_Cnt_lgc.value         0           target_LoaMgr_Per1_TloaDi_Cnt_lgc.value         1           target_LoaMgr_Per1_TloaDi_Cnt_lgc.value         1           target_LoaMgr_Per1_TloaDi_Cnt_lgc.value         1           <	k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
K_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           K_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           K_LoaMgr_MotCurridptSig1FitThd_Cnt_u08         0           K_LoaMgr_MotCurrMtgtnRate_UlspS_102         1           K_LoaMgr_MotCurrMtgtnSca_Uls_52         1           K_LoaMgr_MotCurrMtgtnSca_Uls_52         1           K_LoaMgr_MotCurrMtgtnSca_Uls_52         1           K_LoaMgr_RedcdStRate_UlspS_162         1           K_LoaMgr_RedcdStRate_UlspS_162         1           K_LoaMgr_Pert_DiagcStslvtrInactv_Cnt_lgc.value         0           Larget_LoaMgr_Pert_DiagcStslvtr2lnactv_Cnt_lgc.value         0           target_LoaMgr_Pert_HwTqldptSig_Cnt_u08 value         2           target_LoaMgr_Pert_MotCurrldptSig_Cnt_u08 value         0           target_LoaMgr_Pert_MotCurrldptSig_Cnt_u08 value         0           target_LoaMgr_Pert_SteerMod_Cnt_enum value         0           target_LoaMgr_Pert_TigoaAvail_Cnt_lgc.value         1           target_LoaMgr_Pert_TigoaDi_Cnt_lgc.value         0           target_Rel_inst_Ap_LoaMgr_Pert_DiagcStslvtr2lnactv_Cnt_lgc         target_LoaMgr_Pert_DiagcStslvtr2lnactv_Cnt_lgc           target_Rel_inst_Ap_LoaMgr_Pert_DiagcStslvtr2lnactv_Cnt_lgc         target_LoaMgr_Pert_HwTqldptSig_Cnt_u08           target_Rel_inst_Ap_LoaMgr_LoaMgr_Pert_HwTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Pert_HwTqL	k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	•
k_LoaMgr_MolCurrldptSig2Resp_Cnt_u08         0           k_LoaMgr_MolCurrldptSigFitThd_Cnt_u08         0           k_LoaMgr_MolCurrMtgtnRete_UlspS_f32         1           k_LoaMgr_MolCurrMtgtnScaZencEn_Cnt_lgc         1           k_LoaMgr_MolCurrMtgtnScaZencEn_Cnt_lgc         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_Perd_DiagcStslvtr1nactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStslvtr2inactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStslvtr2inactv_Cnt_lgc.value         1           target_LoaMgr_Per1_MolCurrldptSig_Cnt_u08.value         2           target_LoaMgr_Per1_MolCurrldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MrPosldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_TinaOaVail_Cnt_lgc.value         1           target_LoaMgr_Per1_TinaOaVail_Cnt_lgc.value         1           target_LoaMgr_Per1_TinaOaVail_Cnt_lgc.value         1           target_Re_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1nactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStslvtr1nactv_Cnt_lgc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_DiagcStslvtr2nactv_Cnt_lgc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_		
k_LoaMgr_MotCurrIdptSigFilTh_Cn_u08         0           k_LoaMgr_MotCurrIdptSigFilTh_Cn_u08         1           k_LoaMgr_MotCurrMtgtnScazeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdSiRate_UlspS_f32         1           k_LoaMgr_RedcdSiRate_UlspS_f32         1           k_LoaMgr_Perd_DiagcStslvtr1lnactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc.value         0           target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value         2           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         0           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 value         1           target_LoaMgr_Per1_SteerMod_Cnt_enum.value         0           target_LoaMgr_Per1_ToaAvail_Cnt_lgc.value         0           target_LoaMgr_Per1_ToaDi_Cnt_lgc.value         0           target_LoaMgr_Per1_ToaDi_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStslvtr1lnactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc           target_Rie_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc           target_Rie_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMgtnEn_Cnt_lgc         target_LoaMgr_Per1_HwTqLoaMgtnEn_Cnt_lgc           target_LoaMgr_Per		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_Perd_DiagcStsIvtr1Inactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value         0           target_LoaMgr_Per1_HvTqldptSig_Cnt_u08.value         2           target_LoaMgr_Per1_MtrCurrldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value         0           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_SteerMod_Cnt_enum.value         0           target_LoaMgr_Per1_ToaAvail_Cnt_lgc.value         0           target_LoaMgr_Per1_ToaAvail_Cnt_lgc.value         1           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaScaDi_Cnt_lgc         target_LoaMgr_Per1_LoaMgr_Der1_LoaScaDi_Cnt_lgc <td< td=""><td></td><td></td></td<>		
k_LoaMgr_MotCurrMigtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMigtnSca_Uls_f32         1           k_LoaMgr_RedcdSiRate_UlspS_f32         1           k_LoaMgr_RedcdSiSca_Uls_f32         1           target_LoaMgr_Per1_DiagcStslvtr1nactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc.value         0           target_LoaMgr_Per1_HvTqldptSig_Cnt_u08.value         2           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         0           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_SteerMod_Cnt_enum.value         0           target_LoaMgr_Per1_TioaDvail_Cnt_lgc.value         1           target_LoaMgr_Per1_ToaDvail_Cnt_lgc.value         0           target_Rel_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1nactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStslvtr1nactv_Cnt_lgc           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2inactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStslvtr2inactv_Cnt_lgc           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc           target_LoaMgr_Per1_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc         target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
k_LoaMgr_MotCurrMtginSca_Uls_132		
k_LoaMgr_RedcdStRate_UlspS_32		
k_LoaMgr_RedcdStSca_Uls_f32  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value  2  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value  target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value  target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosidptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosidptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_TloaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Re_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Re_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Re_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Re_Inst_Ap_LoaMgr.LoaMgr_Per1_Intr_OaMtgtnEn_Cnt_lgc  target_Re_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value 0 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value 2 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScstip_Cnt_lgc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScstip_Cnt_lgc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoAScstate_enum 1 target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_LoaMgr_Per1_LoaMgr_DoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_LoaMgr_Per1_LoaMgr_Doa		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value 2 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_TloaAvai_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 1 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_VvtLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoASt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IndextgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_LoaMgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaMgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Rt_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Rt_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Rt_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Rt_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_Rt_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Rt_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Rt_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_SteerMod_Cnt_enum.value	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		1 - 1 - 1 - 1
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	
target_kte_inst_ap_toamgr.toamgr_peri_motourrtoamtgtnen_ont_igc   target_toamgr_peri_motourrtoamtgtnen_Cnt_igc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc



Name	Input Value			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc			
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	1	1	~	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	<b>✓</b>	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	<b>✓</b>	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>	
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~	
LoaMgr_LOASt_State_M_enum	3	3	~	
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	~	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>✓</b>	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	3	3	~	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>✓</b>	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~	
LoaMgr_MotAgRespSV_Cnt_M_u08	3	3	<b>✓</b>	
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>	
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~	
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•	
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1	1 ± 0.01	<b>✓</b>	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>	
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~	
target_LoaMgr_Per1_LOASt_State_enum.value	3	3	~	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	<b>~</b>
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 5.5 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Name	Input Value		
k_LoaMgr_CurrlytrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlytrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32 k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08 k_LoaMgr_IvtrMtgtnRate_UlspS_f32	0		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_IvtrMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1 torrest LeaMan Part DisasCtaluted Inserts	Ont Inc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_target_LoaMgr_Per1_DiagcStsIvtr2Inactv_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Peri_DiagcStsivtr2inactv_Cnt_igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Peri_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_itarget_LoaMgr_Per1_HwTqldptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_		
target Rte Inst Ap LoaMgr.LoaMgr Per1 LOASt State enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Der1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum	1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	1	×
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	_
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	Ž
LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	-
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	
LoaMgr LOASt State M enum	2	2	
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMar MaxMtatnResp Cnt M u08	2	2	

2

1

0

2

1

0

LoaMgr\_MaxMtgtnResp\_Cnt\_M\_u08

LoaMgr\_MotAgldptSigSV\_Cnt\_M\_u08

LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc

LoaMgr\_MotAgRespSV\_Cnt\_M\_u08

2018-05-18, 17:54:37+0530



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	3	3	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1	1 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
SwBasMtgtn	1	SwBasMtgtn	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 5.6 (Repeat Count = 1)	<b>13</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1





Name	Input Value		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
	1		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32			
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	1		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
	•		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target LoaMgr Per1 MtrPosldptSig Cnt u08.value	1		
target LoaMgr Per1 SteerMod Cnt enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Ina	ictv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Ina	ictv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cr	nt u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnE	_	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_er		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_l	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnl	En_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_	Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgt	nEn Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_C		
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_e	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lg	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	1	1	<b>✓</b>
LoaMgr HwTqldptSigSV Cnt M u08	1	1	-
	0	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0		
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	✓
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_LOASt_State_M_enum	1	1	•
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	<u> </u>
		0	•
LoaMgr_TloaAvail_Cnt_M_lgc	0		
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	0	1	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	-		
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	1 1 {173, 174}	1 1 {173, 174}	
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1 1 {173, 174} {1, 1}	1 1 {173, 174} {0, 0, 0, 0, 0}	₩
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1 1 {173, 174} {1, 1} {1, 1}	1 1 {173, 174} {0, 0, 0, 0, 0} {1, 1}	9 9
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1 1 {173, 174} {1, 1}	1 1 {173, 174} {0, 0, 0, 0, 0}	9 9
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1 1 {173, 174} {1, 1} {1, 1}	1 1 {173, 174} {0, 0, 0, 0, 0} {1, 1}	i i
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1 1 {173, 174} {1, 1} {1, 1}	1 1 {173, 174} {0, 0, 0, 0, 0} {1, 1} 1 ± 0.01	H
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1 1 {173, 174} {1, 1} {1, 1} 1 0	1 1 {173, 174} {0, 0, 0, 0, 0} {1, 1} 1 ± 0.01 1 ± 0.001 0	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1 1 {173, 174} {1, 1} {1, 1} 1 0	1 1 {173, 174} {0, 0, 0, 0, 0} {1, 1} 1 ± 0.01 1 ± 0.001 0	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	1 1 {173, 174} {1, 1} {1, 1} 1 0 1	1 1 {173, 174} {0, 0, 0, 0, 0} {1, 1} 1 ± 0.01 1 ± 0.001 0 1	1
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1 1 {173, 174} {1, 1} {1, 1} 1 0	1 1 {173, 174} {0, 0, 0, 0, 0} {1, 1} 1 ± 0.01 1 ± 0.001 0	# H V V V V V V V V V V V V V V V V V V



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	-
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 5.7 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k LoaMgr MotCurrldptSig2Resp Cnt u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k LoaMgr RedcdStSca Uls f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)         1         1 ± 0.001         ✓           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         0         0         ✓           target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value         1         1         ✓           target_LoaMgr_Per1_LOASt_State_enum.value         0         0         ✓           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0         ✓	Name	Input Value		
	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	0		
	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
Bargel_Loakley_Per1_Toakley_Eo_Crit_go:value	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
Integral Loading Pert   Trackvall   Cett   Excitation   Description	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LouNgr_Pert_Tools_Cnt_tgc-value	target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
Image File   Inst. Ap.   Loading Loading   Pert   DiagoState/strinace   Cert   Section   Secti	target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
Image   Re   Inst   Ap   LoaMyr LoaMyr   Pert   DiagoStativtZinact   Cent   Up	target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
	target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
	target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
	target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
	target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	nt_lgc	
larget Rie_Inst Ap_Loaklyr_LoaMgr_Pert   LoaScaDi_Cot_lgc   target   LoaMgr_Pert   LoaScaDi_Cot   gc   target   LoaMgr_Pert   LoaScaDi_Cot   gc   target   LoaMgr_Pert   MotAgloaklighten   Cnt_lgc   target   LoaMgr_Pert   MotAgloaklighten   Cnt_lgc   target   Rie_Inst Ap_LoaMgr_LoaMgr_Pert   MotCurt cakhighten   Cnt_lgc   target   Rie_Inst Ap_LoaMgr_LoaMgr_Pert   MotCurt cakhighten   Cnt_lgc   target   Rie_Inst Ap_LoaMgr_LoaMgr_Pert   MotCurt cakhighten   Cnt_lgc   target   Rie_Inst Ap_LoaMgr_LoaMgr_Pert   Serentod   Cnt_num   target   Rie_Inst Ap_LoaMgr_LoaMgr_Pert   Serentod   Cnt_num   target   Rie_Inst Ap_LoaMgr_LoaMgr_Pert   TioaAvail   Cnt_lgc   target   LoaMgr_Pert   TioaAvail   Cnt_lgc   target   Rie_Inst Ap_LoaMgr_LoaMgr_Pert   TioaAvail   Cnt_lgc   target   LoaMgr_HvTqLoaMgr_Pert   TioaAvail   Cnt_lgc   target   LoaMgr_HvTqLoaMgr_HvT	target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_l	lgc	
target, Rie, Inst, Ap, Loakingr, Loakingr, Pert, MickQuakinghising, Cnt, Ugc   target, Rie, Inst, Ap, Loakingr, Loakingr, Pert, MickCurridoptising, Cnt, Ugc   target, Rie, Inst, Ap, Loakingr, Loakingr, Pert, MickCurridoptising, Cnt, Ugc   target, Rie, Inst, Ap, Loakingr, Loakingr, Pert, MickCurridoptising, Cnt, Ugc   target, Rie, Inst, Ap, Loakingr, Loakingr, Pert, Simphold, Cnt, enum   target, Rie, Inst, Ap, Loakingr, Loakingr, Pert, TioaAvail, Cnt, Ingc   target, Loakingr, Pert, MickCurridoptising, Cnt, Ugc   target, Rie, Inst, Ap, Loakingr, Loakingr, Pert, TioaAvail, Cnt, Ingc   target, Loakingr, Pert, MickCuridopkingr, Cnt, Ingc   target, Loakingr, Pert, MickCuridopkingr, Cnt, Ingc   target, Loakingr, Pert, MickCuridopkingr, Sentricoskangr, Cnt, Ingc   target, Loakingr, NotaAvail, Cnt, Ingc   tar	target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
	target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
larget_Rie_Inst_Ap_LoaMgr_LoaMgr_Per1_MirPositgNsig_Cnt_U08   larget_LoaMgr_Per1_MirPositgNsig_Cnt_U08   larget_LoaMgr_Per1_MirPositgNsig_Cnt_U08   larget_LoaMgr_Per1_SteeMod_Cnt_enum   larget_Rie_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc   larget_Rie_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc   larget_Rie_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc   larget_Rie_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc   larget_LoaMgr_Per1_TloaAvail_Cnt_Igc   larget_LoaMgr_Per1_TloaAvai	target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
larget_Rie_Inst_Ap_LoaMgr_LoaMgr_Pert_SteerMod_Cnt_enum	target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u	.08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr.Pert_SteerMod_Cnt_enum   target_LoaMgr.Pert_SteerMod_Cnt_enum   target_Rte_Inst_Ap_LoaMgr.LoaMgr.Pert_TloaAvail_Cnt_Igo   target_LoaMgr.Pert_TloaAvail_Cnt_Igo   target_LoaMgr.Pert_TloaAvail_Cnt_Igo   target_LoaMgr.Pert_TloaAvail_Cnt_Igo   target_LoaMgr.Pert_TloaAvail_Cnt_Igo   target_LoaMgr.Pert_TloaAvail_Cnt_Igo   target_LoaMgr.Pert_TloaAvail_Cnt_Igo   target_LoaMgr.Pert_TloaAvail_Cnt_Igo   Result   target_LoaMgr.Pert_TloaAvail_Cnt_Igo   target_LoaMgr.Pert_TloaAvail_Cnt_TloaAvail_Cnt_Igo   target_LoaMgr.Pert_TloaAvail_Cnt_TloaAv	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
larget_Rte_Inst_Ap_LoaMgr_LoaMgr_Pert_TloaAvail_Cnt_Igc   target_LoaMgr_Pert_TloaDi_Cnt_Igc   target_LoaMgr_Pert_TloaDi_Cnt_Igc   target_LoaMgr_Pert_TloaDi_Cnt_Igc	target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u	08	
Rarget_Rel_Inst_Ap_LoaMgr_LoaMgr_Peri_TloaDi_Cnt_lgc	target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
Name	target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
LoaMgr_HvTqidptSigSV_Cnt_M_u08	target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
LoaMgr_HwTqLoaMtgInEn_Cnt_M_lgc	Name	Actual Value	Expected Value	Result
LoaMgr_HwTqLoaMtgnEn_Cnt_M_lgc	LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_IvrtLoaMightEn_Cnt_M_Igc	LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MrtLoaMightEn_Cht_M_lgc	LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_IvtRespSV_Cnt_M_u08 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_LoaSt_State_M_enum	LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_LoaRateLimit_UispS_M_f32 500 500 ↓  LoaMgr_LoaScaleFctr_Uis_M_f32 1 1 1 1	LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LoaScaleFctr_Uls_M_f32	LoaMgr_LOASt_State_M_enum	0	0	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08         0         0         V           LoaMgr_MotAgldptSigSV_Cnt_M_u08         1         1         1           LoaMgr_MotAgLoaMtgtnEn_Cnt_M_u0s         0         0         0           LoaMgr_MotCurrldptSigSV_Cnt_M_u08         0         0         0           LoaMgr_MotCurrloaMtgtnEn_Cnt_M_u08         0         0         0           LoaMgr_MotCurrRespSV_Cnt_M_u08         0         0         0           LoaMgr_TloaAvail_Cnt_M_u0s         0         0         0           LoaMgr_TloaDi_Cnt_M_lgc         1         1         1           LoaMgr_NtxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {173, 174}         {173, 174}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {1, 1}         {0, 0, 0, 0, 0}         3           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1}         {1, 1}         {1, 1}         **           Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1}         {1, 1}         **           Rte_Write_Ap_LoaMgr_LoaSaleEt_Limit_UlspS_f32(data)         500         500 ± 0.01         **           Rte_Write_Ap_LoaMgr_Per1_HvTLoaMtgtnEn_Cnt_lgc.value         0         0         **           target_LoaMgr_Per1_LOASt_State_enum.value <t< td=""><td>LoaMgr_LoaRateLimit_UlspS_M_f32</td><td>500</td><td>500</td><td>~</td></t<>	LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08       1       1         LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc       0       0         LoaMgr_MotAgRespSV_Cnt_M_u08       0       0         LoaMgr_MotCurrIdptSigSV_Cnt_M_u08       0       0         LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc       0       0         LoaMgr_MotCurrRespSV_Cnt_M_u08       0       0         LoaMgr_TloaAvail_Cnt_M_lgc       0       0         LoaMgr_TloaDi_Cnt_M_lgc       0       0         LoaMgr_TloaDi_Cnt_M_lgc       1       1         LoaMgr_VtlgMdSrcsV_Cnt_M_u08       1       1         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       {173, 174}       {173, 174}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       {1, 1}       {0, 0, 0, 0, 0}       **         Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       {1, 1}       {1, 1}       {1, 1}       *         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       500       500 ± 0.01       **         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       1       1 ± 0.001       **         target_LoaMgr_Per1_HwTQLoaMtgtnEn_Cnt_lgc.value       0       0       **         target_LoaMgr_Per1_LotAs_State_enum.value       0       0       **	LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>✓</b>
LoaMgr_MotAgLoaMigtnEn_Cnt_M_lgc         0         0         ✓           LoaMgr_MotAgRespSV_Cnt_M_u08         0         0         ✓           LoaMgr_MotCurrldptSigSV_Cnt_M_u08         0         0         ✓           LoaMgr_MotCurrLoaMigtnEn_Cnt_M_lgc         0         0         ✓           LoaMgr_MotCurrRespSV_Cnt_M_u08         0         0         ✓           LoaMgr_TloaAvail_Cnt_M_lgc         0         0         0           LoaMgr_TloaDi_Cnt_M_lgc         1         1         1           LoaMgr_VitgMdSrcSV_Cnt_M_u08         1         1         1           Rte_Call_Ap_LoaMgr_SetNTCStatus(NTC_Cnt_T_enum)         {173, 174}         {173, 174}         ✓           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1}         {1, 1}         ✓           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1}         {1, 1}         ✓           Rte_Wirte_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1}         {1, 1}         ✓           Rte_Wirte_Ap_LoaMgr_LoaRateLimit_Ulsps_f32(data)         500         500 ± 0.01         ✓           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         1         1 ± 0.001         ✓           target_LoaMgr_Per1_twtrLoaMtgtnEn_Cnt_lgc.value         0	LoaMgr_MaxMtgtnResp_Cnt_M_u08	0	0	-
LoaMgr_MotAgRespSV_Cnt_M_u08         0         0         ✓           LoaMgr_MotCurrIdptSigSV_Cnt_M_u08         0         0         ✓           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         0         0         ✓           LoaMgr_MotCurrRespSV_Cnt_M_u08         0         0         ✓           LoaMgr_TloaAvail_Cnt_M_lgc         0         0         ✓           LoaMgr_TloaDi_Cnt_M_lgc         1         1         1           LoaMgr_VttgMdSrcSV_Cnt_M_u08         1         1         1           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {173, 174}         {173, 174}         ✓           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1}         {0, 0, 0, 0, 0}         ✓           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1}         {1, 1}         ✓           Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1}         √            Rte_Write_Ap_LoaMgr_LoaRateLimit_Ulsps_f32(data)         500         500 ± 0.01         ✓           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         1         1 ± 0.001         ✓           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         0         0         ✓           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value<	LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08         0         0         ✓           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         0         0         ✓           LoaMgr_MotCurrRespSV_Cnt_M_u08         0         0         ✓           LoaMgr_TloaAvail_Cnt_M_lgc         0         0         ✓           LoaMgr_VitgMdSrcSV_Cnt_M_u08         1         1         1           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {173, 174}         {173, 174}         ✓           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1}         {0, 0, 0, 0, 0}         ✓           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1}         {1, 1}         ✓           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01         ✓           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         1         1 ± 0.001         ✓           rtarget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         0         0         ✓           target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value         1         1         1           target_LoaMgr_Per1_LOASt_State_enum.value         0         0         ✓           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0         ✓	LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         0         0           LoaMgr_MotCurrRespSV_Cnt_M_u08         0         0           LoaMgr_TloaAvail_Cnt_M_lgc         0         0           LoaMgr_TloaDi_Cnt_M_lgc         1         1           LoaMgr_VitgMdSrcSV_Cnt_M_u08         1         1           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {173, 174}         {173, 174}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1}         {0, 0, 0, 0, 0}         X           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1}         {1, 1}         {1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01         X           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         1         1 ± 0.001         X           Rte_Write_Ap_LoaMgr_LoaNdtgtnEn_Cnt_lgc.value         0         0         X           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         1         1         1           target_LoaMgr_Per1_LOASt_State_enum.value         0         0         X           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0         X	LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08         0         0         ✓           LoaMgr_TloaAvail_Cnt_M_lgc         0         0         ✓           LoaMgr_VitgMdSrcSV_Cnt_M_u08         1         1         1           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {173, 174}         {173, 174}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1}         {0, 0, 0, 0, 0}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1}         {1, 1}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1}         {1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01           xte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         1         1 ± 0.001           xtarget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         0         0           xtarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value         1         1           xtarget_LoaMgr_Per1_LOASt_State_enum.value         0         0           xtarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0	LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_TloaAvail_Cnt_M_lgc         0         0           LoaMgr_TloaDi_Cnt_M_lgc         1         1           LoaMgr_VitgMdSrcSV_Cnt_M_u08         1         1           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)         {173, 174}         {173, 174}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1}         {0, 0, 0, 0, 0}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1}         {1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         1         1 ± 0.001           Rte_gt_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         0         0           target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value         1         1           target_LoaMgr_Per1_LOASt_State_enum.value         0         0           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0	LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc       1       1         LoaMgr_VltgMdSrcSV_Cnt_M_u08       1       1         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       {173, 174}       {173, 174}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       {1, 1}       {0, 0, 0, 0, 0}       X         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       {1, 1}       {1, 1}       {1, 1}       **         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01       **         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       1       1 ± 0.001       **         Rte_get_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       0       0       **         target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value       1       1       **         target_LoaMgr_Per1_LOASt_State_enum.value       0       0       **         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0       **	LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08       1       1         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       {173, 174}       {173, 174}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       {1, 1}       {0, 0, 0, 0, 0}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       {1, 1}       {1, 1}         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       1       1 ± 0.001         xtarget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       0       0         target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value       1       1         target_LoaMgr_Per1_LOASt_State_enum.value       0       0         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0	LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)       {173, 174}       {173, 174}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)       {1, 1}       {0, 0, 0, 0, 0}         Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       {1, 1}       {1, 1}         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       1       1 ± 0.001         xtarget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       0       0         target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value       1       1         target_LoaMgr_Per1_LOASt_State_enum.value       0       0         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0	LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)         {1, 1}         {0, 0, 0, 0, 0}           Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)         {1, 1}         {1, 1}           Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         1         1 ± 0.001           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         0         0           target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value         1         1           target_LoaMgr_Per1_LOASt_State_enum.value         0         0           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0	LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)       {1, 1}       {1, 1}         Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)       500       500 ± 0.01         Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)       1       1 ± 0.001         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value       0       0         target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value       1       1         target_LoaMgr_Per1_LOASt_State_enum.value       0       0         target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value       0       0	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)         500         500 ± 0.01           Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         1         1 ± 0.001           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         0         0           target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value         1         1           target_LoaMgr_Per1_LOASt_State_enum.value         0         0           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)         1         1 ± 0.001         ✓           target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value         0         0         ✓           target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value         1         1         ✓           target_LoaMgr_Per1_LOASt_State_enum.value         0         0         ✓           target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value         0         0         ✓	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0 vtarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value 1 1 1 vtarget_LoaMgr_Per1_LOASt_State_enum.value 0 0 0 vtarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0 0 vtarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0 vtarget_LoaMgr_Per1_MotAgLoa	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_LOASt_State_enum.value 0 0 0 varget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0 v	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
	target_LoaMgr_Per1_LOASt_State_enum.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value 0	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 5.8 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1.
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Inst_Ap_LoaMgr	target Rte Inst Ap LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08 k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k LoaMgr MotCurrldptSig2Resp Cnt u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k LoaMgr MotCurrMtgtnRate UlspS f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value	1
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0 1
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurridptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_IvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	1	1	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	1	1	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	•
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	<b>~</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	1	1	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	•

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 5.9 (Repeat Count = 1)	<b>8</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Input Value



Name



Name	Input value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08			
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	1		
	0		
k_LoaMgr_MotCurridptSig1Resp_Cnt_u08			
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
	2		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value			
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target LoaMgr Per1 DiagcStslvtr1Inactv C	nt lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqldptSig Cnt u08			
· · · · · · · · · · · · · · · · · · ·	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum			
	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc	Expected Value	Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc	Expected Value	Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name LoaMgr_AgSV_Cnt_M_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value		Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1	1	Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1	1 1 0	Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 0 1	1 1 0 1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value  1 1 0 1	1 1 0 1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value  1  1  1  1  1  1  1	1 1 0 1 1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1  1  0  1  1  0  1  0	1 1 0 1 1 1	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value  1  1  1  1  1  1  1	1 1 0 1 1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1  1  0  1  1  0  1  0	1 1 0 1 1 1	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrlOaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  1  1  0  1  1  0  5	1 1 0 1 1 1 1 0 5	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	target_LoaMgr_Per1_TioaAvail_Cnt_lgc target_LoaMgr_Per1_TioaDi_Cnt_lgc  Actual Value  1  1  0  1  1  5  500  1	1 1 0 1 1 1 1 0 5 500	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaState_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	target_LoaMgr_Per1_TioaAvail_Cnt_lgc target_LoaMgr_Per1_TioaDi_Cnt_lgc  Actual Value  1  1  0  1  1  5  500  1  5	1 1 0 1 1 1 1 0 5 500	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaState_Minds LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TioaAvail_Cnt_lgc target_LoaMgr_Per1_TioaDi_Cnt_lgc  Actual Value  1  1  0  1  1  5  5  1  1  1  1  1  1  1  1  1	1 1 0 1 1 1 1 0 5 5 500	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc  Name  LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaScaleFctr_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	target_LoaMgr_Per1_TioaAvail_Cnt_lgc target_LoaMgr_Per1_TioaDi_Cnt_lgc  Actual Value  1  1  0  1  1  5  500  1  5	1 1 0 1 1 1 1 0 5 500	· · · · · · · · · · · · · · · · · · ·

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Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>~</b>

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 5.10 (Repeat Count = 1)	<b>3</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Inmus Value		
Name k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	Input Value		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 k LoaMgr MotCurrMtgtnScaZeroEn Cnt lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_	Cat las	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_		
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt	_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enur	n	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	Francisco d Volum	Danulé
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	Expected Value	Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0	1	*
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1	1	×
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0	1	*
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1 0	1 1 0	×
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 0	1 1 0 0	×
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  0  1	1 1 0 0 1	×
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  0  1  1  1  1	1 1 0 0 1 1	×
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrlOaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  0  1  0  1  0  0  1  1  0  0	1 1 0 0 1 1 1	×
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrlOaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  0  1  1  0  2  500  1	1 1 0 0 1 1 1 0 2 500	×
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaScaleFctr_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  0  1  1  0  2  500  1  2	1 1 0 0 1 1 1 0 2 500 1 2	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaScaleFctr_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  0  1  1  0  2  500  1  2  1	1 1 0 0 0 1 1 1 0 2 500 1 2	×
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_ItvrldptSigSV_Cnt_M_u08  LoaMgr_lvtrldptSigSV_Cnt_M_u08  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_lvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  0  1  1  0  2  500  1  2  1  0	1 1 0 0 0 1 1 1 0 2 500 1 2 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaState_Limit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 0 1 1 1 0 2 500 1 2 1 0 0 0	1 1 0 0 0 1 1 1 0 2 500 1 2 1 0 0	
target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_ItvrldptSigSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaState_Limit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 0 1 1 1 0 2 500 1 2 1 0 0 0 0 0 0 0 0 0 0 0 0 0	1 1 0 0 0 1 1 1 0 2 500 1 2 1 0 0	
target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LVtrRespSV_Cnt_M_u08  LoaMgr_LOaSt_State_M_enum  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 0 1 1 1 0 2 500 1 2 1 0 0 0 1 1 1 1 0 1 1 1 1 0 1 1 1 1	1 1 0 0 0 1 1 1 0 2 500 1 2 1 0 0	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LoaSt_state_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgldoaMtgtnEn_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0 1 0 0 1 1 1 0 2 500 1 2 1 0 0 0 0 0 0 0 0 0 0 0 0 0	1 1 0 0 0 1 1 1 0 2 500 1 2 1 0 0	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrlcoaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1 0 0 1 1 0 2 500 1 2 1 0 0 0 1 2 1 2 1 0 0 0 1 2 1 2 1	1 1 0 0 0 1 1 1 0 2 500 1 2 1 0 0 0	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LoaSt_state_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgldoaMtgtnEn_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1 0 0 1 1 0 2 500 1 2 1 0 0 0 1 2 1 2 1 0 0 0 0 1	1 1 0 0 0 1 1 1 0 2 500 1 1 2 1 0 0 0	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_Igc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc  LoaMgr_IwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgIdptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSv_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1 0 0 1 1 0 2 500 1 2 1 0 0 0 1 2 1 2 1 0 0 0 1 1 2 1 0 0 0 1 1 1 1	1 1 0 0 0 1 1 1 0 2 500 1 1 2 1 0 0 0 0 1 2	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrlOaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MoxAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAglcaMtgnEn_Cnt_M_lgc  LoaMgr_MotCurrloptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1 0 0 1 1 0 2 500 1 2 1 0 0 0 1 2 1 0 0 1 2 1 3	1 1 0 0 0 1 1 1 0 2 500 1 1 2 1 0 0 0 0 1 2 1 2	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  0  0  1  1  0  2  500  1  2  1  0  0  1  2  1  1  1  1  1  1  1  1  1  1  1	1 1 0 0 0 1 1 1 0 2 500 1 2 1 0 0 0 0 1 2 1 2 1 0 0 1 2 1 1 2 0 1 1 2 0 1 1 1 2 0 0 1 1 1 1	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_LoASt_State_M_enum  LoaMgr_LoaState_Imit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgCoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  0  0  1  1  0  2  500  1  2  1  0  0  1  2  1  3  {173, 174}  {1, 1}	1 1 0 0 0 1 1 1 0 2 500 1 2 1 0 0 0 1 2 1 2 1 1 0 0 0 1 2 1 2	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrlOaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrloaMtgtnEn_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  0  0  1  1  0  2  500  1  2  1  0  0  1  2  1  1  0  1  1  1  1  1  1  1  1  1  1	1 1 0 0 0 1 1 1 1 0 2 500 1 2 1 0 0 0 1 2 1 3 {173, 174} {0, 0, 0, 0, 0} {1, 1}	
target_Rte_Inst_Ap_LoaMgr_Ner1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgCont_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrlcoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvaii_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  0  0  1  1  0  2  500  1  2  1  0  0  1  2  1  3  {173, 174}  {1, 1}  {1, 1}  500	1 1 0 0 0 1 1 1 1 0 2 500 1 2 1 0 0 0 1 2 1 2 1 0 0 1 2 0 1 3 {173, 174} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0	
target_Rte_Inst_Ap_LoaMgr_Ner1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgCont_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRsteLimit_UlspS_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  0  0  1  1  0  2  500  1  2  1  0  0  1  2  1  1  0  0  1  2  1  0  1  1  0  0  1  2  1  0  1  1  1  0  0  1  2  1  0  1  1  2  1  0  1  2  1  1  0  1  1  2  0  1  1  2  0  1  1  3  {173, 174}  {1, 1}  {1, 1}  500  1  0  1	1 1 0 0 0 1 1 1 1 0 2 500 1 2 500 1 2 1 0 0 0 1 2 1 3 {173,174} {0,0,0,0,0} {1,1} 500±0.01 1±0.001 0 1	
target_Rte_Inst_Ap_LoaMgr_Ner1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrlQaSigSV_Cnt_M_u08  LoaMgr_IvtrlQaSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrlQptSigSV_Cnt_M_u08  LoaMgr_MotCurrlQptSigSV_Cnt_M_u08  LoaMgr_MotCurrlCoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value  target_LoaMgr_Per1_LOASt_State_enum.value	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  0  0  1  1  0  2  500  1  2  1  0  0  1  2  1  1  0  0  1  2  1  0  1  1  0  1  2  1  0  1  2  1  0  1  2  1  0  1  2  1  0  1  2  1  1  2  0  1  1  2  0  1  1  2  0  1  1  2  0  1  1  2  0  1  1  2  0  1  1  2  0  1  1  2  1  1  1  1  1  1  1  1  1  1	1 1 0 0 0 1 1 1 1 0 2 500 1 2 500 1 2 1 0 0 0 1 1 2 1 3 {173, 174} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0 1 2	
target_Rte_Inst_Ap_LoaMgr_Ner1_TloaDi_Cnt_lgc  Name  LoaMgr_AgSV_Cnt_M_lgc  LoaMgr_HwTqldptSigSV_Cnt_M_u08  LoaMgr_HwTqRespSV_Cnt_M_u08  LoaMgr_IwTqRespSV_Cnt_M_u08  LoaMgr_IvtrldptSigSV_Cnt_M_u08  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_IvtrRespSV_Cnt_M_u08  LoaMgr_LOASt_State_M_enum  LoaMgr_LoaRateLimit_UlspS_M_f32  LoaMgr_LoaScaleFctr_Uls_M_f32  LoaMgr_MaxMtgtnResp_Cnt_M_u08  LoaMgr_MotAgldptSigSV_Cnt_M_u08  LoaMgr_MotAgCont_M_u08  LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRsteLimit_UlspS_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	target_LoaMgr_Per1_TloaDi_Cnt_lgc  Actual Value  0  1  0  0  1  1  0  2  500  1  2  1  0  0  1  2  1  1  0  0  1  2  1  0  1  1  0  0  1  2  1  0  1  1  1  0  0  1  2  1  0  1  1  2  1  0  1  2  1  1  0  1  1  2  0  1  1  2  0  1  1  3  {173, 174}  {1, 1}  {1, 1}  500  1  0  1	1 1 0 0 0 1 1 1 1 0 2 500 1 2 500 1 2 1 0 0 0 1 2 1 3 {173,174} {0,0,0,0,0} {1,1} 500±0.01 1±0.001 0 1	



Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	-
AssignScale	1	AssignScale	1	•
SwBasMtgtn	1	SwBasMtgtn	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•

Test Step 5.11 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgIdptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
<ul><li>LoaMgr_CurrlvtrMtgtnRate_UlspS_f32</li></ul>	1
<ul><li>LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc</li></ul>	1
<ul><li>LoaMgr_CurrlvtrMtgtnSca_Uls_f32</li></ul>	1
<pre>c_LoaMgr_FadeOutStRate_UlspS_f32</pre>	1
c_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
c_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1
c_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
<_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
<_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
<ul><li>LoaMgr_HwTqldptSig4Resp_Cnt_u08</li></ul>	0
<_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
<ul><li>_LoaMgr_lvtrldptSig2Resp_Cnt_u08</li></ul>	0
<pre>c_LoaMgr_lvtrldptSigFltThd_Cnt_u08</pre>	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
<ul><li>_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc</li></ul>	0
c_LoaMgr_IvtrMtgtnSca_Uls_f32	1
<_LoaMgr_MotAgIdptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgIdptSig2Resp_Cnt_u08	1
c_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
<pre>c_LoaMgr_MotAgldptSigFltThd_Cnt_u08</pre>	2
<pre>c_LoaMgr_MotAgMtgtnRate_UlspS_f32</pre>	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
c_LoaMgr_MotAgMtgtnSca_Uls_f32	1
<ul><li>_LoaMgr_MotAgSmaAvailResp_Cnt_u08</li></ul>	0
<pre>k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08</pre>	2
<_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
<_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
<_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1





Name	Input Value			
target LoaMgr Per1 DiagcStslvtr1Inactv Cnt lgc.value	0			
target LoaMgr Per1 DiagcStslvtr2Inactv Cnt Igc.value	0			
target LoaMgr Per1 HwTqldptSig Cnt u08.value	2			
target LoaMgr Per1 LoaScaDi Cnt Igc.value	1			
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0			
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0			
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1			
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv			
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inact			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc		target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc		target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn	_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cr	t_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnE	n_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt	_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_en	ım		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc			
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc			×	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	<b>✓</b>	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	<b>✓</b>	
1 14 14 16 07 07 07 14 00				

target_rtte_inet_rtp_coatingr.coatingr. int_neabl_ent_ige	target_Loanigi_i cri_rioa	BI_GIN_I90	
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc			×
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	✓
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	2	2	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	✓
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	✓
LoaMgr_VltgMdSrcSV_Cnt_M_u08	3	3	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	✓
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	✓
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	✓
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	<b>✓</b>

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	<b>✓</b>
SwBasMtgtn	1	SwBasMtgtn	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit An LoaMor FalloaScaleAndRate	1	Rte Exit An LoaMor FalloaScaleAndRate	1	-



Sample   John Control   1	Test Step 5.12 (Repeat Count = 1)	13
anday purity control (pr. 1)	Name	
analog in infrastration for, at it get analog in infrastration for, at it get analog in infrastration for, at it get analog infrastration for, and it and infrastration for, at it get analog infrastration for, a	LoaMgr_AgSV_Cnt_M_lgc	·
andag Junifelpes Cort M. 1988  codes Junifelpes Cort M. 1989	LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
sodies principality Cost M. 100  obody principality Cost M. 10	LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
analog principality Crit M. selection of the control of the contro	LoaMgr_HwTqRespSV_Cnt_M_u08	
andle_MekapesNy_Cont_M_Usis  andle_MekapesNy_Cont_MUsis  andle_MekapesNy_C	LoaMgr_lvtrldptSigSV_Cnt_M_u08	
Contagn   Institute   Instit		
Cooling Josephigen Prop. Opt. Up. 19  Cooling Josephigen Prop. Up. 19  Cooling		-
analog MacAllerago C of M. 108  analog MacContracting C of M. 108  analog M. 108		
Conding Modification (Conting Conting Contin		1
analey_Mortinerson_Com_Muse  analey_Mortinerson_Com_Muse  analey_Mortinerson_Com_Muse  analey_Mortinerson_Com_Muse  ite_lear_May_Low/Mortinerson_Com_Muse  ite_lear_May_Low/Mortinerson_Com_Muse  ite_lear_May_Low/Mortinerson_Com_Muse  Lear/May_Low/Mortinerson_Com_Muse  Lear/May_Low/May_Low/May_Muse  Lear/May_Low/Mortinerson_Com_Muse  Lear/May_Low/May_Low/May_Muse  Lear/May_Low/May_Low/May_Muse  Lear/May_Low/May_Low/May_Muse  Lear/May_Low/May_Low/May_Muse  Lear/May_Low/May_Low/May_Muse  Lear/May_Low/May_Mortinerson_Com_Muse  Lear/May_Low/M		
	LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
	LoaMgr_MotCurrRespSV_Cnt_M_u08	0
	LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
Landary Controlodings Casterion Cont. 196	LoaMgr_VltgMdSrcSV_Cnt_M_u08	
Clanding Control Migristics (2016)   20	Rte_Inst_Ap_LoaMgr	V
Clanding Fairbooks Principal Princ		
Leading   Friority (1975)   Transport   Lead		
Laahgr HorfqdipSipTrotanAvaileap_Cnt_U8		
Loading   Mindignity   Trackwallers   Cnt. U8		
Loading   Mind page   Stratum   St		
Loading	k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	
Claaking   Min   Taling   Ta	k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
CLashig	k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
Luankg	k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	
CLoaking   MintightSign   List   32	k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
Loaking   Invitigising   Reap		
CLoaking		
Cloaking   Writispisigniff   Crit   U08   Cloaking   Writispisigniff   Crit   U08   Cloaking   Writispisicazeroein   Crit   Crit		
Cloaking		
Cloaking		
Loaklgr_Morkgliptsig1NoSmaResp_Cnt_u08	k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	
Loadkgr_MotAgloptSig2Resp_Cnt_u08	k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
Cloaking MorkajidpiSig3Resp	k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
LoaMgr_MorkQhtglnStepTitThd_Cnt_u08	k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
Loadkgr_MorkAgMtghrRate_UlspS_r32	k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	
CloaMgr_MotAgMightScazZeroEn_Cnt_lgc	k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	
LoaMgr_MotAgMtginSca_Uls_f32		
LloaMgr MotCurridptSig1Resp_Cnt_u08		1
LoaMgr_MotCurridptSig1Resp_Cnt_u08		
L_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           L_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           L_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           L_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           L_LoaMgr_MotCurridptSig2Resp_S_32         1           L_LoaMgr_MotCurridptSigAca_Uls_932         1           L_LoaMgr_RedcdSitRate_Ulsp5_932         1           L_LoaMgr_RedcdSitRate_Ulsp5_932         1           L_LoaMgr_ReddSitSac_Uls_932         1           L_LoaMgr_Per1_DiagcStstvirTinactv_Cnt_lgc.value         0           arget_LoaMgr_Per1_DiagcStstvirTlactv_Cnt_lgc.value         0           arget_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value         1           arget_LoaMgr_Per1_McCurridptSig_Cnt_u08 value         2           arget_LoaMgr_Per1_McCurridptSig_Cnt_u08 value         0           arget_LoaMgr_Per1_McCurridptSig_Cnt_u08 value         1           arget_LoaMgr_Per1_ToaAvait_Cnt_lgc.value         0           arget_LoaMgr_Per1_ToaAvait_Cnt_lgc.value         0           arget_LoaMgr_Per1_ToaAvait_Cnt_lgc.value         0           arget_LoaMgr_Per1_ToaAvait_Cnt_lgc.value         0           arget_LoaMgr_Per1_ToaAvait_Cnt_lgc.value         1           arget_LoaMgr_Per1_ToaAvait_Cnt_lgc.value         0           arget_LoaMgr_Per1_ToaAvait_Cnt_lgc.		
CLOAMGr_MotCurridptSig2Resp_Cnt_u08		
\LoaMgr_MotCurrMtgtnRate_UlspS_132 \\ \LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc \\ \LoaMgr_MotCurrMtgtnSca_Uls_132 \\ \LoaMgr_RedcdStRate_UlspS_132 \\ \LoaMgr_RedcdStRate_UlspS_132 \\ \LoaMgr_Pert_DiagcStslvtr1nactv_Cnt_lgc.value \\ \arget_LoaMgr_Pert_DiagcStslvtr2nactv_Cnt_lgc.value \\ \arget_LoaMgr_Pert_DiagcStslvtr2nactv_Cnt_lgc.value \\ \arget_LoaMgr_Pert_DiagcStslvtr2nactv_Cnt_lgc.value \\ \arget_LoaMgr_Pert_DiagcStslvtr2nactv_Cnt_lgc.value \\ \arget_LoaMgr_Pert_DiagcStslvtr2nactv_Cnt_lgc.value \\ \arget_LoaMgr_Pert_MotCurrldptSig_Cnt_u08.value \\ \arget_LoaMgr_Pert_MotCurrldptSig_Cnt_u08.value \\ \arget_LoaMgr_Pert_MotCurrldptSig_Cnt_u08.value \\ \arget_LoaMgr_Pert_MotCurrldptSig_Cnt_u08.value \\ \arget_LoaMgr_Pert_SigerMod_Cnt_enum_value \\ \arget_LoaMgr_Pert_TioaDi_Cnt_lgc.value \\ \arget_LoaMgr_Pert_DiagcStslvtr1nactv_Cnt_lgc \\ \arget_LoaMgr_Pert_TioaDi_Cnt_lgc.value \\ \arget_LoaMgr_Pert_DiagcStslvtr2nactv_Cnt_lgc \\ \arget_LoaMgr_Pert_LoaMgr_LoaMgr_Pert_LoaMgr_Pert_LoaMgr_Pert_LoaMgr_Pert_LoaMgr_Pert_LoaMgr_Pert_LoaMgr_Pert_LoaMgr_Pert_LoaMgr_Pert_LoaMgr_Pert_LoaMgr_Pert_LoaMgr_Pert	k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
LoaMgr_MolCurrMigtnScaZeroEn_Cnt_lgc	k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
LoaMgr_RedcdStRate_UlspS_f32	k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
LoaMgr_RedcdStRate_UtspS_f32 LoaMgr_Per1_DiagcStstvtr1Inactv_Cnt_lgc_value LoaMgr_Per1_DiagcStstvtr2Inactv_Cnt_lgc_value LoaMgr_Per1_DiagcStstvtr2Inactv_Cnt_lgc_value LoaMgr_Per1_DiagcStstvtr2Inactv_Cnt_lgc_value LoaMgr_Per1_LoaScaDi_Cnt_u08_value LoaMgr_Per1_LoaScaDi_Cnt_u08_value LoaMgr_Per1_MotCurridptSig_Cnt_u08_value LoaMgr_Per1_MotCurridptSig_Cnt_u08_value LoaMgr_Per1_MtrPosldptSig_Cnt_u08_value LoaMgr_Per1_SteerMod_Cnt_enum_value LoaMgr_Per1_ToaAvail_Cnt_lgc_value LoaMgr_Per1_ToaAvail_Cnt_lgc_value LoaMgr_Per1_ToaAvail_Cnt_lgc_value LoaMgr_Per1_ToaAvail_Cnt_lgc_value LoaMgr_Per1_ToaAvail_Cnt_lgc_value LoaMgr_Per1_ToaAvail_Cnt_lgc_value LoaMgr_Per1_DiagcStstvtr1Inactv_Cnt_lgc LoaMgr_Per1_DiagcStstvtr2Inactv_Cnt_lgc Loamgr_Per1_Loamgr_DiagcStstvtr2Inactv_Cnt_lgc Loamgr_Per1_Loamgr_DiagcStstvtr2Inactv_Cnt_lgc Loamgr_Per1_Loamgr_DiagcStstvtr2Inactv_Cnt_lgc Loamgr_Per1_Loamgr_DiagcStstvtr2Inactv_Cnt_lgc Loamgr_Per1_Loamgr_DiagcStstvtr2Inactv_Cnt_lgc Loamgr_Per1_Loamgr_DiagcStstvtr2Inactv_Cnt_lgc Loamgr_Per1_Loamgr_DiagcStstvtr2Inactv_Cnt_lgc Loamgr_Per1_Loamgr_DiagcStstvtr2Inactv_Cnt_lgc Loamgr_Per1_Loamgr_DiagcStstvtr2Inactv_Cnt_lgc Loamgr_DiagcStstvtr2Inactv_DiagcStstvtr2Inactv_Diagc	k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	
LoaMgr_RedodStSca_Uls_132 1 arget_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value 0 arget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value 0 arget_LoaMgr_Per1_HwTqldptSig_Cnt_u08 value 2 arget_LoaMgr_Per1_HwTqldptSig_Cnt_u08 value 1 arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 value 0 arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 arget_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 arget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 arget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 arget_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc 1 arget_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 arget_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 arget_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 arget_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc 1 arget_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_LvtTLoaMtgtnEn_Cnt_lgc 1 arget_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_LvtTLoaMtgtnEn_Cnt_lgc 1 arget_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 arget_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 arget_Ret_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 arget_Ret_Inst_Ap_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc 1 arget_Ret_Inst_Ap_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_Igc 1 arget_Ret_Inst_Ap_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_Igc 1 arget_Ret_Inst_Ap_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_Igc	k_LoaMgr_MotCurrMtgtnSca_Uls_f32	
arget_LoaMgr_Per1_DiagcStsivtr1Inactv_Cnt_lgc.value 0 arget_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc.value 0 arget_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 2 arget_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1 arget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 1 arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 arget_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 arget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc 1 arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvTLoaMtgtnEn_Cnt_lgc 1 arget_Rte_Inst_Ap_LoaMgr_Per1_VrtLoaMtgtnEn_Cnt_lgc 1 arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum 1 arget_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 arget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 arget_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 arget_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 arget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 arget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 1 arget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 1 arget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 1 arget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 1 arget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	k_LoaMgr_RedcdStRate_UlspS_f32	
arget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value		
arget_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 1 arget_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1 arget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 0 arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 arget_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 arget_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 arget_LoaMgr_Per1_DiapCstslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagCstslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagCstslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagCstslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagCstslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagCstslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
arget_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  arget_LoaMgr_Per1_SteerMod_Cnt_enum.value  arget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value  arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value  arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value  arget_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  arget_Rte_inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  arget_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  arget_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  arget_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  arget_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc  arget_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc  arget_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum  arget_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  arget_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  arget_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08  arget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
arget_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value 0 arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 arget_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 arget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 arget_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 arget_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 arget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 arget_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	
arget_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 arget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 arget_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_Inst_Ap_LoaMgr_Per1_Inst_Ap_LoaMgr_Per1_Inst_Ap_LoaMgr_Per1_Inst_Ap_LoaMgr_Per1_Inst_Ap_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	
arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value  arget_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08  arget_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum  arget_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  arget_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	
arget_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc     target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc       arget_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc     target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc       arget_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum     target_LoaMgr_Per1_LOASt_State_enum       arget_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc     target_LoaMgr_Per1_LoaScaDi_Cnt_lgc       arget_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc     target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc       arget_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08     target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
arget_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc     target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc       arget_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum     target_LoaMgr_Per1_LOASt_State_enum       arget_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc     target_LoaMgr_Per1_LoaScaDi_Cnt_Igc       arget_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc     target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc       arget_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08     target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
arget_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum     target_LoaMgr_Per1_LOASt_State_enum       arget_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc     target_LoaMgr_Per1_LoaScaDi_Cnt_lgc       arget_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc     target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc       arget_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08     target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
arget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc       target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc         arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08       target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
arget_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	
arget_Rte_Inst_Ap_LoaMgr_Der1_MotCurrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u(	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc			
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc			×	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	<b>✓</b>	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	<b>✓</b>	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•	
LoaMgr_IvtrRespSV_Cnt_M_u08	0	0	~	
LoaMgr_LOASt_State_M_enum	2	2	•	
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	~	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>✓</b>	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	~	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>✓</b>	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~	
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	•	
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•	
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~	
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•	
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	3	3	<b>✓</b>	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1	1 ± 0.01	•	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•	
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~	
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	•	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~	

To ad Odam On II Turana				
Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	•
AssignScale	1	AssignScale	1	•
SwBasMtgtn	1	SwBasMtgtn	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Evit An LoaMor FalloaScaleAndRate	1	Rte Exit An LoaMar FalloaScaleAndRate	1	

Test Step 5.13 (Repeat Count = 1)	<b>□</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr





Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1		
k LoaMgr CurrivtrMtgtnScaZeroEn Cnt lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_Igc	0		
k_LoaMgr_IvtrMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0 2		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	1		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k LoaMgr MotAgMtgtnScaZeroEn Cnt lgc	0		
k LoaMgr MotAgMtgtnSca Uls f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_Diagc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_Diagc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTq		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTq	·	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoa		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOAS		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaSc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAg		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCu		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCu		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_MtrPo target_LoaMgr_Per1_SteerN		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaA		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaD		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	1	Nesult
LoaMgr HwTqldptSigSV Cnt M u08	1	1	

target_rtte_inst_Ap_Edatingr.Edatingr_r err_rtdabr_drit_ige	target_Loamigi_i cri_noabi	target_Loamigi_i et i_noabi_ont_ige			
Name	Actual Value	Expected Value	Result		
LoaMgr_AgSV_Cnt_M_lgc			×		
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	✓		
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	✓		
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	<b>✓</b>		
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	<b>✓</b>		
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	✓		
LoaMgr_IvtrRespSV_Cnt_M_u08	0	0	<b>✓</b>		
LoaMgr_LOASt_State_M_enum	2	2	<b>✓</b>		
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	<b>✓</b>		
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>✓</b>		
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	✓		
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>✓</b>		
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>		

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Name	Actual Value	Expected Value	Result
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	<b>✓</b>
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1	1 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	<b>✓</b>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	·
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	<b>~</b>
SwBasMtgtn	1	SwBasMtgtn	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	<b>~</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 5.14 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	2
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_Igc	0





Name	Input Value		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08 k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtg		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnl		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSi		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_l	lgc	
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc			×
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	<b>~</b>
LoaMgr_LOASt_State_M_enum	2	2	<b>V</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	· · ·
LoaMgr_LoaScaleFctr_Uls_M_f32	1 2	2	
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr MotAgRespSV Cnt M u08	0	0	<b>~</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	_
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	<b>✓</b>
9			<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	
		1 {173, 174}	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	1 {173, 174} {1, 1}		<b>✓</b>
LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1 {173, 174} {1, 1} {1, 1}	{173, 174} {0, 0, 0, 0, 0} {1, 1}	V
LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1 {173, 174} {1, 1} {1, 1} 500	{173, 174} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01	**************************************
LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1 {173, 174} {1, 1} {1, 1} 500 1	{173, 174} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001	**************************************
LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1 {173, 174} {1, 1} {1, 1} 500 1	{173, 174} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0	**************************************
LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value  target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1 {173, 174} (1, 1) {1, 1} 500 1 0 0	{173, 174} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0	× × ×
LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value  target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value  target_LoaMgr_Per1_LOASt_State_enum.value	1 {173, 174} {1, 1} {1, 1} 500 1 0 0 2	{173, 174} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0 0	**************************************
LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value  target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1 {173, 174} (1, 1) {1, 1} 500 1 0 0	{173, 174} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0	9



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	•
SwBasMtgtn	1	SwBasMtgtn	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•

Input Value	**
LoaMgr_AgSV_Cnt_M_lgc	
LoaMgr_HwTqiptSigSV_Cnt_M_u08	
LoaMgr_HwTqLoaMtghEn_Cnt_M_u08	
LoaMgr_HwTqRespSV_Cnt_M_u08	
LoaMgr_IvtrlosNigSV_Cnt_M_u08	
LoaMgr_IvfreepSV_Cnt_M_u08	
LoaMgr_IvtrRespSV_Cnt_M_u08	
LoaMgr_MotAglaphSigSV_Cn_M_u08	
LoaMgr_MotAgLoaMtgnEn_Cnt_M_lgc	
LoaMgr_MotCurrloptSigSV_Cnt_M_u08   2	
LoaMgr_MotCurrLoaMtghEn_Cnt_M_log	
LoaMgr_MotCurrLoaMightnEn_Cnt_M_lgc         0           LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc         1           LoaMgr_VitgMdSrcSV_Cnt_M_u08         2           Rte_Inst_Ap_LoaMgr         target_Rte_Inst_Ap_LoaMgr           k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32         1           k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_GurdvtrMtgtnSca_Uls_f32         1           k_LoaMgr_FadeOutStRate_UlspS_f32         1           k_LoaMgr_HwTqldptSigOTloaAvailResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig1TloaDaVailResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig1TloaDiResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig1Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSig1Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSig1Resp_Cnt_u08         1           k_LoaMgr_HwTqldptSig1Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSig1Resp_Cnt_u08         1           k_LoaMgr_HwTqldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_	
LoaMgr_MotCurrRespSV_Cnt_M_u08         0           LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc         1           LoaMgr_VitgMdSrcSV_Cnt_M_u08         2           Rte_Inst_Ap_LoaMgr         target_Rte_Inst_Ap_LoaMgr           k_LoaMgr_CurrivrIndtgnRate_UlspS_f32         1           k_LoaMgr_CurrivrMtgtnSca_vis_f32         1           k_LoaMgr_FadeOutSiRate_UlspS_f32         1           k_LoaMgr_HavTqldptSigOTloaAvaiiResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig1NoTloaAvaiiResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig2TloaDraAvaiiResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig2TloaDraResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08         0           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSig4FltTnd_Cnt_u08         1           k_LoaMgr_HwTqldptSigFltTnd_Cnt_u08         1           k_LoaMgr_HwTqldptSig1Resp_Cnt_u08         0           k_LoaMgr_IwtrldptSig1Resp_Cnt_u08         1           k_LoaMgr_IwtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IwtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IwtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IwtrldptSig1Resp_Cnt_u08         0           k_LoaMgr_IwtrldptSig2Resp_Cnt_u08         0           k_LoaMgr_Iwtr	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	
LoaMgr_VitgMdSrcSV_Cht_M_u08   2	
Rte_Inst_Ap_LoaMgr         target_Rte_Inst_Ap_LoaMgr           k_LoaMgr_CurrIvtrMtgtnRate_UlspS_f32         1           k_LoaMgr_CurrIvtrMtgtnSca_Uls_f32         0           k_LoaMgr_FadeOutStRate_UlspS_f32         1           k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08         2           k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08         1           k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08         0           k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08         0           k_LoaMgr_HwTqldptSig4Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSigFitThd_Cnt_u08         1           k_LoaMgr_HwTqldptSig1Resp_Cnt_u08         0           k_LoaMgr_HwTqldptSig1Resp_Cnt_u08         1           k_LoaMgr_IvtrIdptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrIdptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrIdptSig1Resp_Cnt_u08         0           k_LoaMgr_IvtrIdptSigFitThd_Cnt_u08         0           k_LoaMgr_IvtrIdptSigFitThd_Cnt_u08         0           k_LoaMgr_IvtrIdptSigFitThd_Cnt_u08         0           k_LoaMgr_IvtrIdptSigFitThd_Cnt_u08         0           k_LoaMgr_IvtrIdptSigFitThd_Cnt_u08         0           k_LoaM	
k_LoaMgr_CurrIvtrMtgtnRate_UIspS_f32       1         k_LoaMgr_CurrIvtrMtgtnSca_UIs_f32       1         k_LoaMgr_FadeOutStRate_UIspS_f32       1         k_LoaMgr_HwTqldptSigOTloaAvailResp_Cnt_u08       2         k_LoaMgr_HwTqldptSig1NoTloaAvailResp_Cnt_u08       1         k_LoaMgr_HwTqldptSig2TloaAvailResp_Cnt_u08       2         k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08       1         k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08       0         k_LoaMgr_HwTqldptSig4Resp_Cnt_u08       0         k_LoaMgr_HwTqldptSigFltThd_Cnt_u08       1         k_LoaMgr_HwTqldptSigFltThd_Cnt_u08       1         k_LoaMgr_HwTqMtgtnRate_UIspS_f32       1         k_LoaMgr_IvtrldptSig1Resp_Cnt_u08       0         k_LoaMgr_IvtrldptSig2Resp_Cnt_u08       0         k_LoaMgr_IvtrldptSig1Resp_Cnt_u08       0         k_LoaMgr_IvtrldptSig2Resp_Cnt_u08       0         k_LoaMgr_Ivtrld	
k_LoaMgr_CurrIvtrMtgtnSca_Uls_f32       0         k_LoaMgr_FadeOutStRate_UlspS_f32       1         k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08       2         k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08       1         k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08       2         k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08       2         k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08       1         k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08       0         k_LoaMgr_HwTqldptSig4Resp_Cnt_u08       0         k_LoaMgr_HwTqldptSig1TlnA_Cnt_u08       1         k_LoaMgr_HwTqMtgtnSca_Uls_f32       1         k_LoaMgr_IvtrldptSig1Resp_Cnt_u08       0         k_LoaMgr_IvtrldptSig1Resp_Cnt_u08       0         k_LoaMgr_IvtrldptSig2Resp_Cnt_u08       0         k_LoaMgr_IvtrldptSigFltThd_Cnt_u08       0         k_LoaMgr_IvtrldptSigFltThd_Cnt_u08       0         k_LoaMgr_IvtrldptSigFltThd_Cnt_u08       0         k_LoaMgr_IvtrldptSigFltThd_Cnt_u08       0         k_LoaMgr_IvtrldptSigFltThd_Cnt_u08       0         k_LoaMgr_IvtrlMtgtnScaZeroEn_Cnt_lgc       0	
k_LoaMgr_CurrIvtrMtgtnSca_Uls_f32       1         k_LoaMgr_FadeOutStRate_UlspS_f32       1         k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08       2         k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08       1         k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08       2         k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08       1         k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08       0         k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08       0         k_LoaMgr_HwTqldptSig1FltThd_Cnt_u08       1         k_LoaMgr_HwTqldptSig1FltThd_Cnt_u08       1         k_LoaMgr_HwTqMtgtnSca_UlspS_f32       1         k_LoaMgr_IvtrldptSig1Resp_Cnt_u08       0         k_LoaMgr_IvtrldptSig2Resp_Cnt_u08       0         k_LoaMgr_IvtrldptSig1FltThd_Cnt_u08       0         k_LoaMgr_IvtrldptSig1FltThd_Cnt_u08       0         k_LoaMgr_IvtrMtgtnRate_UlspS_f32       1         k_LoaMgr_IvtrMtgtnRate_UlspS_f32       1         k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc       0	
k_LoaMgr_FadeOutStRate_UlspS_f32       1         k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08       2         k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08       1         k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08       2         k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08       1         k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08       0         k_LoaMgr_HwTqldptSig4Resp_Cnt_u08       0         k_LoaMgr_HwTqldptSigFltThd_Cnt_u08       1         k_LoaMgr_HwTqMtgtnRate_UlspS_f32       1         k_LoaMgr_IvtrldptSig1Resp_Cnt_u08       0         k_LoaMgr_IvtrldptSig1Resp_Cnt_u08       0         k_LoaMgr_IvtrldptSig2Resp_Cnt_u08       0         k_LoaMgr_IvtrldptSigFltThd_Cnt_u08       0         k_LoaMgr_IvtrldptSigFltThd_Cnt_u08       0         k_LoaMgr_IvtrMtgtnRate_UlspS_f32       1         k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc       0	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08       2         k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08       1         k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08       2         k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08       1         k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08       0         k_LoaMgr_HwTqldptSig4Resp_Cnt_u08       0         k_LoaMgr_HwTqldptSigFltThd_Cnt_u08       1         k_LoaMgr_HwTqMtgtnRate_UlspS_f32       1         k_LoaMgr_HwTqMtgtnSca_Uls_f32       1         k_LoaMgr_IvtrldptSig1Resp_Cnt_u08       0         k_LoaMgr_IvtrldptSig2Resp_Cnt_u08       0         k_LoaMgr_IvtrldptSigFitThd_Cnt_u08       0         k_LoaMgr_IvtrldptSigFitThd_Cnt_u08       0         k_LoaMgr_IvtrMtgtnRate_UlspS_f32       1         k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc       0	
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08       1         k_LoaMgr_HwTqldptSig2TloaAvailResp_Cnt_u08       2         k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08       1         k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08       0         k_LoaMgr_HwTqldptSig4Resp_Cnt_u08       0         k_LoaMgr_HwTqldptSigFltThd_Cnt_u08       1         k_LoaMgr_HwTqMtgtnRate_UlspS_f32       1         k_LoaMgr_HwTqMtgtnSca_Uls_f32       1         k_LoaMgr_IvtrldptSig1Resp_Cnt_u08       0         k_LoaMgr_IvtrldptSig2Resp_Cnt_u08       0         k_LoaMgr_IvtrldptSigFitThd_Cnt_u08       0         k_LoaMgr_IvtrMtgtnRate_UlspS_f32       1         k_LoaMgr_IvtrMtgtnRate_UlspS_f32       1         k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc       0	
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08       2         k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08       1         k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08       0         k_LoaMgr_HwTqldptSig4Resp_Cnt_u08       0         k_LoaMgr_HwTqldptSigFltThd_Cnt_u08       1         k_LoaMgr_HwTqMtgtnRate_UlspS_f32       1         k_LoaMgr_HwTqMtgtnSca_Uls_f32       1         k_LoaMgr_lvtrldptSig1Resp_Cnt_u08       0         k_LoaMgr_lvtrldptSig2Resp_Cnt_u08       0         k_LoaMgr_lvtrldptSigFitThd_Cnt_u08       0         k_LoaMgr_lvtrMtgtnRate_UlspS_f32       1         k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc       0	
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08       1         k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08       0         k_LoaMgr_HwTqldptSig4Resp_Cnt_u08       0         k_LoaMgr_HwTqldptSigFltThd_Cnt_u08       1         k_LoaMgr_HwTqMtgtnRate_UlspS_f32       1         k_LoaMgr_HwTqMtgtnSca_Uls_f32       1         k_LoaMgr_lvtrldptSig1Resp_Cnt_u08       0         k_LoaMgr_lvtrldptSig2Resp_Cnt_u08       0         k_LoaMgr_lvtrldptSigFitThd_Cnt_u08       0         k_LoaMgr_lvtrMtgtnRate_UlspS_f32       1         k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc       0	
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08       0         k_LoaMgr_HwTqldptSig4Resp_Cnt_u08       0         k_LoaMgr_HwTqldptSigFltThd_Cnt_u08       1         k_LoaMgr_HwTqMtgtnRate_UlspS_f32       1         k_LoaMgr_HwTqMtgtnSca_Uls_f32       1         k_LoaMgr_lvtrldptSig1Resp_Cnt_u08       0         k_LoaMgr_lvtrldptSig2Resp_Cnt_u08       0         k_LoaMgr_lvtrldptSigFitThd_Cnt_u08       0         k_LoaMgr_lvtrMtgtnRate_UlspS_f32       1         k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc       0	
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08       0         k_LoaMgr_HwTqldptSigFitThd_Cnt_u08       1         k_LoaMgr_HwTqMtgtnRate_UlspS_f32       1         k_LoaMgr_HwTqMtgtnSca_Uls_f32       1         k_LoaMgr_lvtrldptSig1Resp_Cnt_u08       0         k_LoaMgr_lvtrldptSig2Resp_Cnt_u08       0         k_LoaMgr_lvtrldptSigFitThd_Cnt_u08       0         k_LoaMgr_lvtrMtgtnRate_UlspS_f32       1         k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc       0	
k_LoaMgr_HwTqldptSigFitThd_Cnt_u08       1         k_LoaMgr_HwTqMtgtnRate_UlspS_f32       1         k_LoaMgr_HwTqMtgtnSca_Uls_f32       1         k_LoaMgr_lvtrldptSig1Resp_Cnt_u08       0         k_LoaMgr_lvtrldptSig2Resp_Cnt_u08       0         k_LoaMgr_lvtrldptSigFitThd_Cnt_u08       0         k_LoaMgr_lvtrMtgtnRate_UlspS_f32       1         k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc       0	
k_LoaMgr_HwTqMtgtnRate_UlspS_f32       1         k_LoaMgr_HwTqMtgtnSca_Uls_f32       1         k_LoaMgr_lvtrldptSig1Resp_Cnt_u08       0         k_LoaMgr_lvtrldptSig2Resp_Cnt_u08       0         k_LoaMgr_lvtrldptSigFitThd_Cnt_u08       0         k_LoaMgr_lvtrMtgtnRate_UlspS_f32       1         k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc       0	
k_LoaMgr_HwTqMtgtnSca_Uls_f32       1         k_LoaMgr_lvtrldptSig1Resp_Cnt_u08       0         k_LoaMgr_lvtrldptSig2Resp_Cnt_u08       0         k_LoaMgr_lvtrldptSigFltThd_Cnt_u08       0         k_LoaMgr_lvtrMtgtnRate_UlspS_f32       1         k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc       0	
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08       0         k_LoaMgr_IvtrldptSig2Resp_Cnt_u08       0         k_LoaMgr_IvtrldptSigFitThd_Cnt_u08       0         k_LoaMgr_IvtrMtgtnRate_UlspS_f32       1         k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc       0	
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08         0           k_LoaMgr_IvtrldptSigFitThd_Cnt_u08         0           k_LoaMgr_IvtrMtgtnRate_UlspS_f32         1           k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc         0	
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08     0       k_LoaMgr_lvtrMtgtnRate_UlspS_f32     1       k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc     0	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc 0	
k LooMar LytrMtatnCoo Lllo f22	
k_LoaMgr_IvtrMtgtnSca_UIs_f32	
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 5	
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 1	
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 0	
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 2	
k_LoaMgr_MotAgMtgtnRate_UlspS_f32 1	
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc 0	
k_LoaMgr_MotAgMtgtnSca_Uls_f32	
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 0	
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 2	
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08 0	
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08 0	
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08 0	
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 1	
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc 0	
k_LoaMgr_MotCurrMtgtnSca_Uls_f32 1	
k_LoaMgr_RedcdStRate_UlspS_f32 1	
k_LoaMgr_RedcdStSca_Uls_f32	





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Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcSts	lvtr1Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcSts	lvtr2Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldp	tSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMt	gtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_S	tate_enum	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaD	i_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLo	aMtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrL	oaMtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosId	ptSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod	I_Cnt_enum	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_C	nt_lgc	
Name	Actual Value	Expected Value	Resul
LoaMgr_AgSV_Cnt_M_lgc			2
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	•
LoaMgr_LOASt_State_M_enum	2	2	•
LoaMor LoaRateLimit UlsoS M f32	1	1	

		The state of the s	
LoaMgr_AgSV_Cnt_M_lgc	0		×
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	2	2	~
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1	1 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
SwBasMtgtn	1	SwBasMtgtn	1	-
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr FaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	_



Test Step 5.16 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlytrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08 k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k LoaMgr IvtrMtgtnRate UlspS f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	0
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedodStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	0
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2
target LoaMgr Per1 LoaScaDi Cnt Igc.value	1
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_inst_Ap_LoaMgr.LoaMgr_Per1_ivirLoaMtgtnEn_Cnt_igc target_Rte_inst_Ap_LoaMgr.LoaMgr_Per1_ivirLoaMtgtnEn_Cnt_igc	target_LoaMgr_Perl_IntrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc			×
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_IvtrRespSV_Cnt_M_u08	2	2	~
LoaMgr_LOASt_State_M_enum	2	2	~
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	2	2	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1	1 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
SwBasMtgtn	1	SwBasMtgtn	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	-
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	<b>✓</b>

Test Step 5.17 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr





Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08 k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k LoaMgr HwTqMtgtnSca Uls f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k LoaMgr lvtrldptSig2Resp Cnt u08	0		
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	1		
k LoaMgr lvtrMtgtnSca Uls f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k LoaMgr MotAgldptSig2Resp Cnt u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k LoaMgr MotAgMtgtnRate UlspS f32	1		
k LoaMgr MotAgMtgtnScaZeroEn Cnt lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k LoaMgr MotCurrldptSig0Resp Cnt u08	0		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_Diago		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_Diago		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTo		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTo		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLo		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOAS		
target_Rte_Inst_Ap_LoaMgr_Der1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaS		
target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotA		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotC		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotC		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPc	· •	
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_Steer		
target_Rte_Inst_Ap_LoaMgr_Der1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaA		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaD		
Name	Actual Value	Expected Value	Resul
LoaMgr_AgSV_Cnt_M_lgc	0	1	2
LoaMgr HwTqldptSigSV Cnt M u08	1	1	•

target_Inte_inst_Ap_coalwigt.coalwigt_Fert_Indabt_crit_ige	target_Loawigi_Feri_floabi_	target_Loaiwgr_Ferr_floabi_Crit_ige			
Name	Actual Value	Expected Value	Result		
LoaMgr_AgSV_Cnt_M_lgc			*		
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	✓		
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	✓		
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	✓		
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	✓		
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	✓		
LoaMgr_IvtrRespSV_Cnt_M_u08	2	2	✓		
LoaMgr_LOASt_State_M_enum	2	2	✓		
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	✓		
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	✓		
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	✓		
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	✓		
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	✓		





Name	Actual Value	Expected Value	Result
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	•
LoaMgr_VltgMdSrcSV_Cnt_M_u08	2	2	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	•
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	•
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	<b>✓</b>
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	•
SwBasMtgtn	1	SwBasMtgtn	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 5.18 (Repeat Count = 1)		
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	1	
LoaMgr HwTqldptSigSV Cnt M u08	1	
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_lvtrRespSV_Cnt_M_u08	2	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	0	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	2	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0	
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1	
k_LoaMgr_FadeOutStRate_UlspS_f32	1	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2	
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1	
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2	
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1	
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0	
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0	
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1	
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1	
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1	
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0	
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0	
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0	
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1	
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0	

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Loanigi_1 or i		(	100
Name	Input Value		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgIdptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
<_LoaMgr_RedcdStRate_UlspS_f32	1		
<pre>c_LoaMgr_RedcdStSca_Uls_f32</pre>	1		
arget_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc.value	0		
arget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value	0		
arget_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
arget_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
arget_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
arget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
rarget_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
arget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
arget_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1	0.1.1	
rarget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_		
arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_		
arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u		
arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_0		
arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt	_igc	
arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	Ont Inc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_i		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enur	II	
arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc arget_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc		
	Actual Value	Expected Value	Resi
Name	0	Expected Value	Rest
_oaMgr_AgSV_Cnt_M_lgc	1	1	
_oaMgr_HwTqldptSigSV_Cnt_M_u08			
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
_oaMgr_HwTqRespSV_Cnt_M_u08			
_oaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
_oaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc _oaMgr_lvtrRespSV_Cnt_M_u08	2	2	
LoaMgr_LOASt_State_M_enum	2	2	
_oaMgr_LoaRateLimit_UlspS_M_f32	1	1	
.oaMgr_LoaScaleFctr_Uls_M_f32	1	1	
LoaMgr MaxMtgtnResp Cnt M u08	2	2	
_oaMgr_MotAgldptSigSV Cnt_M_u08	1	1	
_oaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc		0	
	0		
	0		
	0	0	
_oaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0 0	
_oaMgr_MotCurrIdptSigSV_Cnt_M_u08 _oaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0 0 0	0 0 0	
.oaMgr_MotCurrIdptSigSV_Cnt_M_u08 .oaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc .oaMgr_MotCurrRespSV_Cnt_M_u08	0 0 0 0	0 0 0 0	
.oaMgr_MotCurrldptSigSV_Cnt_M_u08 .oaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc .oaMgr_MotCurrRespSV_Cnt_M_u08 .oaMgr_TloaAvail_Cnt_M_lgc	0 0 0 0 0	0 0 0 0 0	
.oaMgr_MotCurrIdptSigSV_Cnt_M_u08 .oaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc .oaMgr_MotCurrRespSV_Cnt_M_u08 .oaMgr_TloaAvail_Cnt_M_lgc .oaMgr_TloaDi_Cnt_M_lgc	0 0 0 0 0 0	0 0 0 0 0 0	
_oaMgr_MotCurrIdptSigSV_Cnt_M_u08 _oaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc _oaMgr_MotCurrRespSV_Cnt_M_u08 _oaMgr_TloaAvail_Cnt_M_lgc _oaMgr_TloaDi_Cnt_M_lgc _oaMgr_VltgMdSrcSV_Cnt_M_u08	0 0 0 0 0 1 2	0 0 0 0 0 0 1 2	
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0 0 0 0 0 1 2 {173, 174}	0 0 0 0 0 1 2 {173, 174}	
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0 0 0 0 0 1 2 {173, 174}	0 0 0 0 0 1 1 2 {173, 174} {0, 0, 0, 0, 0}	
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 0 0 0 0 1 2 {173, 174} {1, 1}	0 0 0 0 0 1 2 {173, 174} {0, 0, 0, 0, 0} {1, 1}	
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	0 0 0 0 0 1 2 {173, 174} {1, 1} {1, 1}	0 0 0 0 0 1 2 {173, 174} {0, 0, 0, 0, 0} {1, 1} 1 ± 0.01	
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0 0 0 0 0 1 2 {173, 174} {1, 1} {1, 1}	0 0 0 0 0 1 2 {173, 174} {0, 0, 0, 0, 0} {1, 1} 1 ± 0.01 1 ± 0.001	
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VitgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0 0 0 0 0 1 2 {173, 174} {1, 1} {1, 1}	0 0 0 0 1 2 {173, 174} {0, 0, 0, 0, 0} {1, 1} 1 ± 0.01 1 ± 0.001 0	
LoaMgr_MotAgRespSV_Cnt_M_u08  LoaMgr_MotCurrldptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VltgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0 0 0 0 0 1 2 {173, 174} {1, 1} {1, 1} 1 0	0 0 0 0 1 2 {173, 174} {0, 0, 0, 0, 0} {1, 1} 1 ± 0.01 1 ± 0.001 0	
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08  LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc  LoaMgr_MotCurrRespSV_Cnt_M_u08  LoaMgr_TloaAvail_Cnt_M_lgc  LoaMgr_TloaDi_Cnt_M_lgc  LoaMgr_VitgMdSrcSV_Cnt_M_u08  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)  Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)  Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)  Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0 0 0 0 0 1 2 {173, 174} {1, 1} {1, 1}	0 0 0 0 1 2 {173, 174} {0, 0, 0, 0, 0} {1, 1} 1 ± 0.01 1 ± 0.001 0	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	-
AssignScale	1	AssignScale	1	•
SwBasMtgtn	1	SwBasMtgtn	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•

Test Step 5.19 (Repeat Count = 1)	
Name	Input Value
LoaMgr AgSV Cnt M Igc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	target_Rte_inst_Ap_coalingi
k LoaMgr CurrivtrMtgtnScaZeroEn Cnt lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	2
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
	1
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	0
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_IvtrMtgtnSca_UIs_f32	5
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	1
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k LoaMgr MotAgldptSigFltThd Cnt u08	2
	1
k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k LoaMgr MotAgMtgtnScaZeroEn Cnt lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
	0
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	·
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	1
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsl	vtr1Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsl	vtr2Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldpt	Sig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoa	MtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMt	gtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_S	tate_enum	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaD	i_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrL	oaMtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldp	otSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod	I_Cnt_enum	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_C	nt_lgc	
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0		×
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	<b>✓</b>
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	2	2	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	~

LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	2	2	•
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>~</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1	1 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
SwBasMtgtn	1	SwBasMtgtn	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	-





Insulate Junificación Cer M. 100  Lander Junificación Cer M. 1	Test Step 5.20 (Repeat Count = 1)	8
Leading Fund Count   Jick Leading Fund Count		
Leading - In-in-Incident - During - Leading - In-In-Incident - During - In-Incident - During - Du		·
Looking   Look	LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
Leading   Juniferpings   Cort   M. 100	LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
Loading - Michagos (C. M. 1986)  Loading - Michagos (M. 1	LoaMgr_HwTqRespSV_Cnt_M_u08	
Loading, Medigensigner, Cert. JU, ges Loading, Medicansigner, Loading, Loadi	LoaMgr_lvtrldptSigSV_Cnt_M_u08	
Loading   Montgage State   Loading   Loading   Montgage State   Loading   Montgage State   Loading   Loading   Montgage State   Loading   Loading   Montgage State   Loading   Montgage State   Loading   Loading   Montgage State   Loading   Loading   Montgage State   Loading   Loading   Loading   Montgage State   Loading   Loading   Montgage State   Loading   Loading   Loading   Montgage State   Loading   Loading   Montgage State   Loading   Loading   Montgage State   Loading		
Loading   Amalgraphing   Cort   Ungris   Loading   Loading   Loading   Ungris		1
Looking MacContack Style Co. M. 1.08  Looking MacContack Style Co. M. 1.09  Looking MacContack Style MacContack Style Co. M. 1.09  Looking MacContack Style MacContack Style Co. M. 1.09  Looking MacContack Style MacContack		
Looking Modurant Southern Look In Jose		
Loading / MacCure Service Cort M. Go Loading / Loading / Cort M. Go Ris, Incid. A. Loading / Loadin		
Looking   Free Prince   Looking	LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
Loading	LoaMgr_MotCurrRespSV_Cnt_M_u08	0
Item   Intel   Apr   Loady	LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
Landing Controllingtonia (Ling 1922)	LoaMgr_VltgMdSrcSV_Cnt_M_u08	
Landing	Rte_Inst_Ap_LoaMgr	V V
Loading-InterfoStRina-Ullies   Diz   Loading-I		
Loading / Info@pdisy0 Toakvailless		
Landary Hardingthis (No Touris Cont. 1988   2		
Lankgr   Mr-Topics   Transverses   Crt   Lankgr   Lankg		
Loadby_M-TraingsignTealBases_Crt_U08	k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	
Laakgr   Mr   Galgis   Grant	k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	
Loaking-Hw7dqbsSp4ftep_Cnt_U8	k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
L. Loading-HwTqdNgriSpFITTING_Cnt_U08	k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
L. Loadkgr_HvfTqMtgginSap_UspS_1232	k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	
K. Loaking/ Intridisting/Resp_Cnt_U08		
Loading   Nrtigistig   Resp. Cnt   U08		
LoaMgr   Mrt/dptSigPitThd   Cm   L08		
L. Loakigr   MvfMighrSace_Ulsp S   12		
Loading   Intrinstripsica_Uis_132		
	k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	
	k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
L. LoeMgr_MotAgloptSig9Resp_Cnt_u08	k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
LoaMgr_MotAgidplSigFitThd_Cnt_u08	k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
LoaMgr_MotAgMtghRate_UlspS_f32	k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	
LoaMgr_MotAgMtginSca_Uis_72	k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	
k_LoaMgr_MotAgSmaAvaiResp_Cnt_u08         0           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_U08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_Cnt_lgc         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_Cnt_lgc         1           k_LoaMgr_MotCurridptSig2Resp_Sig2         1           k_LoaMgr_MotCurridptSig2Sig1Sig2         1           k_LoaMgr_RedGSiRea_Uisp5_32         1           k_LoaMgr_RedGSiRea_Uisp5_32         1           k_LoaMgr_Pert_DiagcSistvirtinactv_Cnt_lgc value         0           target_LoaMgr_Pert_DiagcSistvirtinactv_Cnt_lgc value         0           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08.value         1           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08.value         1           target_LoaMgr_Pert_MotCurridptSig_Cnt_u08.value         1           target_LoaMgr_Pert_ToaAvail_Cnt_lgc.value         1           target_LoaMgr_Pert_ToaAvail_Cnt_lgc.value         1           target_LoaMgr_Pert_ToaAvail_Cnt_lgc.value         1           target_LoaMgr_Pert_ToaAvail_Cnt_lgc.		
k_LoaMgr_MotCurridptSigResp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSigFiThd_Cnt_u08         0           k_LoaMgr_MotCurridptSigFiThd_Cnt_u08         0           k_LoaMgr_MotCurridptSigScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurridptSigSca_Uis_f32         1           k_LoaMgr_RedcdSiSrate_Uis_f32         1           k_LoaMgr_RedcdSiSrate_Uis_f32         1           k_LoaMgr_Per1_DiagcStsivtr1Inactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStsivtr2Inactv_Cnt_lgc.value         0           target_LoaMgr_Per1_MotCurridptSig_Cnt_u08 value         1           target_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value         1           target_LoaMgr_Per1_TioaDl_Cnt_lgc.value         0           target_LoaMgr_Per1_TioaDl_Cnt_lgc.value         1           target_LoaMgr_Per1_TioaDl_Cnt_lgc.value         1           target_LoaMgr_Per1_TioaDl_Cnt_lgc.value         1           target_LoaMgr_Per1_TioaDl_Cnt_lgc.value         1           target_LoaMgr		
k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2FitThd_Cnt_u08         0           k_LoaMgr_MotCurridptSig2FitThd_Cnt_u08         1           k_LoaMgr_MotCurridptSig2FitThd_Cnt_u08         1           k_LoaMgr_MotCurridptSig2FitThd LoaMgr_MotCurridptSig2_U1_si2         1           k_LoaMgr_MotCurridptSig2_U1_si32         1           k_LoaMgr_RedcdSiRate_U1spS_f32         1           k_LoaMgr_RedcdSiRate_U1spS_f32         1           k_LoaMgr_Per1_DiagcStsIvtTrlnactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStsIvtTv2Inactv_Cnt_lgc.value         0           target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value         2           target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value         1           target_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value         0           target_LoaMgr_Per1_SteerMod_Cnt_enum.value         2           target_LoaMgr_Per1_SteerMod_Cnt_enum.value         2           target_LoaMgr_Per1_TloaAval_Cnt_lgc.value         1           target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc         target_LoaMgr_Per1_HwTqldplSig_Cnt_u08           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc		•
k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           k_LoaMgr_MotCurridtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurridtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurridtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_Perd_DiagcStstvf1Inactv_Cnt_lgc.value         0           target_LoaMgr_Perd_DiagcStstvf2Inactv_Cnt_lgc.value         0           target_LoaMgr_Perd_DiagcStstvf2Inactv_Cnt_lgc.value         1           target_LoaMgr_Perd_MortCurridptSig_Cnt_u08.value         2           target_LoaMgr_Perd_MortPosldptSig_Cnt_u08.value         1           target_LoaMgr_Perd_MortPosldptSig_Cnt_u08.value         1           target_LoaMgr_Perd_TincaAvail_Cnt_lgc.value         1           target_LoaMgr_Perd_TincaAvail_Cnt_lgc.value         0           target_LoaMgr_Perd_TincaAvail_Cnt_lgc.value         1           target_LoaMgr_Perd_TincaAvail_Cnt_lgc.value         1           target_LoaMgr_Perd_TincaAvail_Cnt_lgc.value         1           target_LoaMgr_Perd_TincaAvail_Cnt_lgc.value         1           target_LoaMgr_Perd_TincaAvail_Cnt_lgc.value         1           target_LoaMgr_Perd_TincaAvail_Cnt_lgc.value         1		
k_LoaMgr_MotCurrIdptSigFitThd_Cnt_u08         0           k_LoaMgr_MotCurrMtgtnRate_UlspS_f32         1           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdSiRate_UlspS_f32         1           k_LoaMgr_RedcdSiRate_UlspS_f32         1           k_LoaMgr_RedcdSiRate_UlspS_f32         1           k_LoaMgr_Per1_DiagcStstvtr1nactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStstvtr2nactv_Cnt_lgc.value         0           target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value         2           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         0           target_LoaMgr_Per1_MotPosidptSig_Cnt_u08.value         1           target_LoaMgr_Per1_SteerMod_Cnt_enum.value         2           target_LoaMgr_Per1_SteerMod_Cnt_enum.value         2           target_LoaMgr_Per1_ToaAvail_Cnt_lgc.value         0           target_Rel_nst_Ap_LoaMgr_LoaMgr_Per1_DiagcStstvtr1nactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStstvtr2nactv_Cnt_lgc           target_Rel_nst_Ap_LoaMgr_LoaMgr_Per1_DiagcStstvtr2nactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStstvtr2nactv_Cnt_lgc           target_Rel_nst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc           target_Rel_nst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_WortLo		
k_LoaMgr_MotCurrMigtnSca_Uis_f32	k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMigtnSca_Uls_f32	k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	
k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MctCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotQLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotQCurrldptSig_Cnt_u08	k_LoaMgr_MotCurrMtgtnSca_Uls_f32	
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc.value 0 target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value 0 target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08.value 2 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value 1 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_TloaAvail_Cnt_Igc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value 1 target_NoaMgr_Per1_TloaDi_Cnt_Igc.value 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_InvTqLoaMtgtnEn_Cnt_Igc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LwTqLoaMtgtnEn_Cnt_Igc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LwTqLoaMtgtnEn_Cnt_Igc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_Igc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_Igc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc 1 target_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_Igc 1 target_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_Igc 1 target_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_Igc 1 target_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_Igc 1 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 1 target_Lo	k_LoaMgr_RedcdStRate_UlspS_f32	
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value 2 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MitPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2 target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoASt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  1 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value  1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value  2 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_InacMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value 2  target_LoaMgr_Per1_TloaAvaii_Cnt_lgc.value 0  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc 1  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 1  target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc 1  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc 1  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc 1  target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc 1  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc 1  target_LoaMgr_Per1_LoaSt_State_enum 1  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1  target_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc 1  target_LoaMgr_Per1_NotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotOurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_		1 - 1 - 1 - 1 - 1 - 1
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>~</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	2	2	<b>~</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
SwBasMtgtn	1	SwBasMtgtn	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	<b>~</b>

Test Step 5.21 (Repeat Count = 1)	<b>13</b>
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr

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Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ci	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Ci	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt	_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lg	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr HwTqldptSigSV Cnt M u08	1	1	<b>V</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc			
	0	0	<b>✓</b>
		0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0		-
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0 0 1	0	<b>V</b>
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08	0 0 1 0	0 1 0	<b>*</b>
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	0 0 1 0 0 2	0 1 0 0 2	<b>*</b>
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08	0 0 1 0	0 1 0 0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \

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LoaMgr\_MaxMtgtnResp\_Cnt\_M\_u08 LoaMgr\_MotAgldptSigSV\_Cnt\_M\_u08

 $LoaMgr\_MotAgLoaMtgtnEn\_Cnt\_M\_lgc$ 

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Name	Actual Value	Expected Value	Result
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	2	2	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
SwBasMtgtn	1	SwBasMtgtn	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 5.22 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_Igc	0





Nome	Innut Value		
Name	Input Value		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	5		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08			
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target LoaMgr Per1 MotCurrldptSig Cnt u08.value	0		
	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt	_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt	_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn	Cnt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enur		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaAvail Cnt Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
			Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	<b>V</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	2	2	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	•
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	2	2	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	•
LoaMgr MotCurrldptSigSV Cnt M u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	
	0	0	-
LoaMgr_TloaAvail_Cnt_M_lgc			
LoaMgr_TloaDi_Cnt_M_lgc	1	1	-
LoaMgr_VitgMdSrcSV_Cnt_M_u08	1 (472, 474)	1 (472, 474)	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
	111 11	{1, 1}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}		<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	
	500	1 ± 0.001	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500		·
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	500	1 ± 0.001	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	500 1 0	1 ± 0.001 0	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	500 1 0 0	1 ± 0.001 0 0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	<b>~</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	•
AssignScale	1	AssignScale	1	-
SwBasMtgtn	1	SwBasMtgtn	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

LoaMgr_AgSV_Cnt_M_lgc         0           LoaMgr_HwTqldptSigSV_Cnt_M_u08         1           LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_IwtrldptSigSV_Cnt_M_u08         1           LoaMgr_IvtrlOaMtgtnEn_Cnt_M_lgc         0           LoaMgr_IvtrRespSV_Cnt_M_u08         0           LoaMgr_MotAgldptSigSV_Cnt_M_u08         1           LoaMgr_MotAgldptSigSV_Cnt_M_u08         1           LoaMgr_MotAgRespSV_Cnt_M_u08         2           LoaMgr_MotCurrldptSigSV_Cnt_M_u08         2           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotCurrRespSV_Cnt_M_u08         3           LoaMgr_MotCurrRespSV_Cnt_M_u08         3           LoaMgr_MotCurrRespSV_Cnt_M_u08         3           LoaMgr_SnsrisMotAgAvaii_Cnt_M_lgc         1           LoaMgr_VltgMdSrcSV_Cnt_M_u08         1           Rte_Inst_Ap_LoaMgr         tar           k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32         1           k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32         1           k_LoaMgr_FadeOutStRate_UlspS_f32         0	
LoaMgr_AgSV_Cnt_M_lgc         0           LoaMgr_HwTqldptSigSV_Cnt_M_u08         1           LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_HwTqRespSV_Cnt_M_u08         1           LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc         0           LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotAgldptSigSV_Cnt_M_u08         1           LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotAgRespSV_Cnt_M_u08         1           LoaMgr_MotCurrldptSigSV_Cnt_M_u08         2           LoaMgr_MotCurrldptSigSV_Cnt_M_u08         2           LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc         3           LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc         1           LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc         1           LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc         1           LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc         1           LoaMgr_VitgMdSrcsV_Cnt_M_u08         1           Rte_Inst_Ap_LoaMgr         tar           k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32         1           k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32         1           k_LoaMgr_FadeOutStRate_UlspS_f32         0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08         1           LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_HwTqRespSV_Cnt_M_u08         1           LoaMgr_IvtrldptSigSV_Cnt_M_u08         1           LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotAgldptSigSV_Cnt_M_u08         0           LoaMgr_MotAgldptSigSV_Cnt_M_u08         1           LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotAgrespSV_Cnt_M_u08         2           LoaMgr_MotCurrldptSigSV_Cnt_M_u08         2           LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotCurrRespSV_Cnt_M_u08         3           LoaMgr_MotCurrRespSV_Cnt_M_u08         3           LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc         1           LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc         1           LoaMgr_StlgMdSrcSV_Cnt_M_u08         1           Rte_Inst_Ap_LoaMgr         tar           k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32         1           k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32         1           k_LoaMgr_FadeOutStRate_UlspS_f32         0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_HwTqRespSV_Cnt_M_u08         1           LoaMgr_IvtrldptSigSV_Cnt_M_u08         1           LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotAgldptSigSV_Cnt_M_u08         1           LoaMgr_MotAgldptSigSV_Cnt_M_u08         1           LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotAgrespSV_Cnt_M_u08         2           LoaMgr_MotCurrldptSigSV_Cnt_M_u08         2           LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotCurrRespSV_Cnt_M_u08         3           LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc         1           LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc         1           LoaMgr_VtlgMdSrcSV_Cnt_M_u08         1           Re_Inst_Ap_LoaMgr         tar           k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32         1           k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32         1           k_LoaMgr_FadeOutStRate_UlspS_f32         0	
LoaMgr_HwTqRespSV_Cnt_M_u08         1           LoaMgr_IvtrldptSigSV_Cnt_M_u08         1           LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotAgldptSigSV_Cnt_M_u08         0           LoaMgr_MotAgldptSigSV_Cnt_M_u08         1           LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotCurrldptSigSV_Cnt_M_u08         2           LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotCurrRespSV_Cnt_M_u08         3           LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc         1           LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc         1           LoaMgr_VitgMdSrcSV_Cnt_M_u08         1           Rte_inst_Ap_LoaMgr         tar           k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32         1           k_LoaMgr_CurrivtrMtgtnSca_Uls_f32         1           k_LoaMgr_FadeOutStRate_UlspS_f32         0	
LoaMgr_IvtrIdptSigSV_Cnt_M_u08         1           LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_IvtrRespSV_Cnt_M_u08         0           LoaMgr_MotAgIdptSigSV_Cnt_M_u08         1           LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotCurrIdptSigSV_Cnt_M_u08         2           LoaMgr_MotCurrIdptSigSV_Cnt_M_u08         2           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotCurrRespSV_Cnt_M_u08         3           LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc         1           LoaMgr_VitgMdSrcSV_Cnt_M_u08         1           Rte_inst_Ap_LoaMgr         tar           k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32         1           k_LoaMgr_CurrivtrMtgtnSca_Uls_f32         1           k_LoaMgr_FadeOutStRate_UlspS_f32         0           k_LoaMgr_FadeOutStRate_UlspS_f32         0	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_IvtrRespSV_Cnt_M_u08         0           LoaMgr_MotAgldptSigSV_Cnt_M_u08         1           LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotCurrldptSigSV_Cnt_M_u08         2           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotCurrRespSV_Cnt_M_u08         3           LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc         1           LoaMgr_VitgMdSrcSV_Cnt_M_u08         1           Rte_inst_Ap_LoaMgr         tar           k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32         1           k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32         1           k_LoaMgr_FadeOutStRate_UlspS_f32         0           k_LoaMgr_FadeOutStRate_UlspS_f32         0	
LoaMgr_IvtrRespSV_Cnt_M_u08         0           LoaMgr_MotAgldptSigSV_Cnt_M_u08         1           LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotAgRespSV_Cnt_M_u08         2           LoaMgr_MotCurrldptSigSV_Cnt_M_u08         2           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotCurrRespSV_Cnt_M_u08         3           LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc         1           LoaMgr_VitgMdSrcSV_Cnt_M_u08         1           Rte_Inst_Ap_LoaMgr         tar           k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32         1           k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32         1           k_LoaMgr_FadeOutStRate_UlspS_f32         0	
LoaMgr_MotAgldptSigSV_Cnt_M_u08         1           LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotAgRespSV_Cnt_M_u08         2           LoaMgr_MotCurrldptSigSV_Cnt_M_u08         2           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotCurrRespSV_Cnt_M_u08         3           LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc         1           LoaMgr_VltgMdSrcSV_Cnt_M_u08         1           Rte_Inst_Ap_LoaMgr         tar           k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32         1           k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32         1           k_LoaMgr_FadeOutStRate_UlspS_f32         0	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotAgRespSV_Cnt_M_u08         2           LoaMgr_MotCurrloptSigSV_Cnt_M_u08         2           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotCurrRespSV_Cnt_M_u08         3           LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc         1           LoaMgr_VitgMdSrcSV_Cnt_M_u08         1           Rte_Inst_Ap_LoaMgr         tar           k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32         1           k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32         0           k_LoaMgr_FadeOutStRate_UlspS_f32         0	
LoaMgr_MotAgRespSV_Cnt_M_u08         2           LoaMgr_MotCurridptSigSV_Cnt_M_u08         2           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotCurrRespSV_Cnt_M_u08         3           LoaMgr_SnsrisMotAgAvail_Cnt_M_lgc         1           LoaMgr_VitgMdSrcSV_Cnt_M_u08         1           Rte_Inst_Ap_LoaMgr         tar           k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32         1           k_LoaMgr_CurrivtrMtgtnSca_ZeroEn_Cnt_lgc         0           k_LoaMgr_CurrivtrMtgtnSca_Uls_f32         1           k_LoaMgr_FadeOutStRate_UlspS_f32         0	
LoaMgr_MotCurridptSigSV_Cnt_M_u08         2           LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc         0           LoaMgr_MotCurrRespSV_Cnt_M_u08         3           LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc         1           LoaMgr_VitgMdSrcSV_Cnt_M_u08         1           Rte_Inst_Ap_LoaMgr         tar           k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32         1           k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_CurrivtrMtgtnSca_Uls_f32         1           k_LoaMgr_FadeOutStRate_UlspS_f32         0	
LoaMgr_MotCurrLoaMigtnEn_Cnt_M_lgc         0           LoaMgr_MotCurrRespSV_Cnt_M_u08         3           LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc         1           LoaMgr_VitgMdSrcSV_Cnt_M_u08         1           Rte_Inst_Ap_LoaMgr         tar           k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32         1           k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32         1           k_LoaMgr_FadeOutStRate_UlspS_f32         0	
LoaMgr_MotCurrRespSV_Cnt_M_u08         3           LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc         1           LoaMgr_VitgMdSrcSV_Cnt_M_u08         1           Rte_Inst_Ap_LoaMgr         tar           k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32         1           k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32         1           k_LoaMgr_FadeOutStRate_UlspS_f32         0	
LoaMgr_SnsrlsMotAgAvaii_Cnt_M_lgc         1           LoaMgr_VltgMdSrcSV_Cnt_M_u08         1           Rte_Inst_Ap_LoaMgr         tar           k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32         1           k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32         1           k_LoaMgr_FadeOutStRate_UlspS_f32         0	
LoaMgr_VitgMdSrcSV_Cnt_M_u08         1           Rte_Inst_Ap_LoaMgr         tar           k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32         1           k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_CurrivtrMtgtnSca_Uls_f32         1           k_LoaMgr_FadeOutStRate_UlspS_f32         0	rget_Rte_Inst_Ap_LoaMgr
Rte_Inst_Ap_LoaMgr         tar           k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32         1           k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_CurrivtrMtgtnSca_Uls_f32         1           k_LoaMgr_FadeOutStRate_UlspS_f32         0	rget_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32         1           k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_CurrivtrMtgtnSca_Uls_f32         1           k_LoaMgr_FadeOutStRate_UlspS_f32         0	3
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc         0           k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32         1           k_LoaMgr_FadeOutStRate_UlspS_f32         0	
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	
k_LoaMgr_FadeOutStRate_UlspS_f32 0	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08 2	
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08 2	
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 2	
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 1	
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 0	
k LoaMgr HwTqldptSig4Resp Cnt u08	
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	
k_LoaMgr_HwTqMtgtnSca_Uls_f32	
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08 0	
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc 0	
k_LoaMgr_lvtrMtgtnSca_Uls_f32	
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 5	
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc 1	
k_LoaMgr_MotAgMtgtnSca_Uls_f32	
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc 1	
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	
k_LoaMgr_RedcdStRate_UlspS_f32	
k_LoaMgr_RedcdStSca_Uls_f32	





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Ina	ctv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Ina	ctv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cr	t_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnE	n_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_er	ium	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_l	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnl	En_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_	Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgt	nEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_0	cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_e	enum	
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lg	ıc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Resul
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	•
LoaMgr_LOASt_State_M_enum	3	3	•
LoaMgr_LoaRateLimit_UlspS_M_f32	0.00999999978	0.00999999978	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	3	3	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	•
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	1
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	0.00999999978	0.00999999978 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	•
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_LOASt_State_enum.value	3	3	
target LoaMgr Per1 MotAgLoaMtgtnEn Cnt Igc.value	1	1	
target_barrigi_r er i_wotAgbarrigtiibii_ont_ige.value	1.	l ·	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Injust Value   Injus Value	Test Step 5.24 (Repeat Count = 1)	13
Coating   Forticostage   Cost   Mode   Cost   Cos		
Leading Instructions Cot III jug 0  Leading Instructions Leading Leading Cot III jug 0  Leading Instructions Leading Leading Cot III jug 0  Leading Instructions Leading Leading Cot III jug 1  Leading Instructions Leading Leading Cot III jug 1  Leading Instructions Leading Leadi		·
Loading - Info-General Cord   Muse		1
Leading   Medicipality   Cort   M.   108	LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
Leading Fundangsing Core M. 198  Leading Mangadistics Or Core M. 199  Leading Mangadistics Or Core M. 198  Leading Leading Mangadistics Or M. 199  Leading Mangadistics Or Core M. 198  Leading Leading Mangadistics Or M. 199  Leading Leading Mangadistics Or M. 199  Leading Leading Mangadistics Or M. 199  Leading Leading Mangadistics Upus 192  Leading Mangadi	LoaMgr_HwTqRespSV_Cnt_M_u08	0
Loading_Ambidges/Sept Cost M_Loss	LoaMgr_lvtrldptSigSV_Cnt_M_u08	
Loading   Managlaphing   Cort   Mg   10		
Contagn_Managenesing_Cont_M_Size   Contagn_Manage		•
Louding MacCurt (1985) (**O. IM. 198   0   1   1   1   1   1   1   1   1   1		
Loading   MacCardengling Cort   Mile		
Looking - Michael Personnel Cont. Mulge		
Lookings	LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	
Loading / Lindhoffseta, Using 1/12   Lindhoffseta   Lingh 1/12   Lindhoffseta   Lindhoffseta   Lingh 1/12   Lindhoffseta   Lingh 1/12   Lindhoffseta   Lingh 1/12   Lindhoffseta   L	LoaMgr_MotCurrRespSV_Cnt_M_u08	1
	LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
L. Landing/Lamfordingnissazzenich (mil. go.)		1
Landing-Landing-Resolution   Landing-Resolution	Rte_Inst_Ap_LoaMgr	V
Landary Information Style Transcripting Cott (Jul 8)		
L.Dodrigs   Marciago   Not Total Resp. Cert. USB		
	k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	
Loaking-HwiTqldsSigNRtsp_Cnt_U8	k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	
		0
K_Loshig_HwTqhtghRate_UspS_122	k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
K. Loosing-/ InvirightignResp_Cnt_u08	k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
Loadig_NvfdptSigPResp_Cnt_u08	k_LoaMgr_HwTqMtgtnRate_UlspS_f32	
K_LosMgr_InvirightSig2Pesp_Cnt_U08		
k_LoaMgr_IvridqsSigPRTRd_Cnt_u08         0           k_LoaMgr_IvridqsSigPRTRd_Cnt_u08         1           k_LoaMgr_IvridqsSig2RecEn_Cnt_u08         5           k_LoaMgr_MoNdqdptSig1NsSamResp_Cnt_u08         5           k_LoaMgr_MoNdqdptSig2Resp_Cnt_u08         1           k_LoaMgr_MoNdqdptSig1Resp_Cnt_u08         2           k_LoaMgr_MoNdqdptSig1Resp_Cnt_u08         2           k_LoaMgr_MoNdqdptSig1Resp_Cnt_u08         2           k_LoaMgr_MoNdqdptSig1Resp_Cnt_u08         2           k_LoaMgr_MoNdqdptSig1Resp_Cnt_u08         1           k_LoaMgr_MoNdqdptsSig2Resp_Cnt_u08         0           k_LoaMgr_MoNdqdtgmsAcareCnt_u08         0           k_LoaMgr_MoNdqdtgmsAcareCnt_u08         0           k_LoaMgr_MoCuridqtSig1Resp_Cnt_u08         0 <td></td> <td></td>		
K_Loakly_InvitMignRate_UlspS_132		
k_LoaMgr_IvriMiginSca_Uis_IS2         1           k_LoaMgr_Morty (brighs)ca_Uis_IS2         1           k_LoaMgr_Morty(bris)ca_Uis_IS2Resp_Cnt_U08         5           k_LoaMgr_Morty(bris)ca_Uis_IS2Resp_Cnt_U08         1           k_LoaMgr_Morty(bris)ca_Uis_IS2Resp_Cnt_U08         2           k_LoaMgr_Morty(bris)ca_Uis_IS2Resp_Cnt_U08         2           k_LoaMgr_Morty(bris)ca_Uis_IS2         1           k_LoaMgr_Morty(bris)ca_Uis_IS2         0           k_LoaMgr_Morty(bris)ca_Uis_IS2         0           k_LoaMgr_Morty(bris)ca_Uis_IS2         0           k_LoaMgr_Morty(bris)ca_Uis_IS2         1		
k_LoaMgr_MorAgidpiSig/Resp_Cnt_U08         1           k_LoaMgr_MorAgidpiSig/Resp_Cnt_U08         0           k_LoaMgr_MorAgidpiSig/Resp_Cnt_U08         2           k_LoaMgr_MorAgidpiSig/Resp_Cnt_U08         2           k_LoaMgr_MorAgidpiSig/Resp_Cnt_U08         1           k_LoaMgr_MorAgidpiSig/Resp_Cnt_U08         1           k_LoaMgr_MorAgidpiSig/Resp_Cnt_U08         0           k_LoaMgr_MorAgidpiSig/Resp_Cnt_U08         0           k_LoaMgr_MorCurridpiSig/Resp_Cnt_U08         0           k_LoaMgr_MorCurridpiSig-Resp_Cnt_U08         0           k_LoaMgr_MorCurridpiSig-Resp_Cnt_U08         0           k_LoaMgr_MorCurridpiSig-Resp_Cnt_U08         0           k_LoaMgr_MorCurridpiSig-Resp_Cnt_U08         0           k_LoaMgr_MorCurridpiSig-Resp_Cnt_U08         0           k_LoaMgr_Pert_DiagosSistr/Tinactv_Cnt_Igc value         0           target_LoaMgr_Pert_Inscapable_Cnt_U08 value         0           <		
K_LoaMgr_MorAgidptSigSResp_Cnt_u08         0           K_LoaMgr_MorAgidptSigFiThd_Cnt_u08         2           K_LoaMgr_MorAgidptSigFiThd_Cnt_u08         1           K_LoaMgr_MorAgMighrRate_Uibs_F32         1           K_LoaMgr_MorAgMightSca_Uis_F32         1           K_LoaMgr_MorAgMightSca_Uis_F32         1           K_LoaMgr_MorAgSmaAvailresp_Cnt_u08         0           K_LoaMgr_MorCurridptSigResp_Cnt_u08         1           K_LoaMgr_MorCurridptSigResp_Cnt_u08         1           K_LoaMgr_MorCurridptSigr_Cnt_u08         1           K_LoaMgr_MorCurridptSigr_Cnt_u08         1           K_LoaMgr_RedodSisca_Uis_32         1           K_LoaMgr_RedodSisca_Uis_32         1           K_LoaMgr_RedodSisca_Uis_32         0.123400003           Larget_LoaMgr_Pert_DiagcSistvrTianctv_Cnt_lgc value         0           Larget_LoaMgr_Pert_LoaSca_Dic_Dic_Uig_value </td <td>k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08</td> <td>5</td>	k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
K_LoaMgr_MotAgldptSigFitTnd_Cnt_u08         2           K_LoaMgr_MotAghtglmScate_UlspS_f32         1           K_LoaMgr_MotAghtglmScate_Cnt_u0g         1           K_LoaMgr_MotAghtglmScate_Uls_f32         1           K_LoaMgr_MotAghtgmScate_Uls_f32         1           K_LoaMgr_MotCurridptSigGeRsp_Cnt_u08         0           K_LoaMgr_MotCurridptSigGeRsp_Cnt_u08         0           K_LoaMgr_MotCurridptSigFeRsp_Cnt_u08         0           K_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           K_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           K_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           K_LoaMgr_MotCurridptSigFitTnd_Cnt_u08         0           K_LoaMgr_MotCurridptSigSeZeroEn_Cnt_log         1           K_LoaMgr_MotCurridptSigSeZeroEn_Cnt_log         1           K_LoaMgr_RodoSiRaL_uls_f32         1           K_LoaMgr_RodoSiRaL_uls_f32         1           K_LoaMgr_RodoSiRaL_uls_f32         1           K_LoaMgr_Port_DiagcSistvirInactv_Cnt_lgc value         0           Larget_LoaMgr_Port_DiagcSistvirInactv_Cnt_lgc value         0           Larget_LoaMgr_Port_LoaScaDi_Cnt_lgc value         0           Larget_LoaMgr_Port_Sig_Cnt_u08 value         1           Larget_LoaMgr_Port_Toalval_Cnt_lgc value         0           L	k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgMtgtnRate_UispS_f32         1           k_LoaMgr_MotAgMtgtnSacZeroEn_Cn_Ltgc         1           k_LoaMgr_MotAgMtgtnSacZeroEn_Cn_Uo8         0           k_LoaMgr_MotAgMtgtnSac_Us_Us_f32         1           k_LoaMgr_MotCurridptSigtGResp_Cnt_uo8         0           k_LoaMgr_MotCurridptSigtGResp_Cnt_uo8         0           k_LoaMgr_MotCurridptSigtGResp_Cnt_uo8         0           k_LoaMgr_MotCurridptSigtGResp_Cnt_uo8         0           k_LoaMgr_MotCurridptSigtGResp_Cnt_uo8         0           k_LoaMgr_MotCurridptSigtGResp_Cnt_uo8         0           k_LoaMgr_MotCurridptSigt_UspS_f32         1           k_LoaMgr_MotCurridptSigt_UspS_f32         1           k_LoaMgr_RedcdSiRate_UispS_f32         1           k_LoaMgr_RedcdSiRate_UispS_f32         1           k_LoaMgr_Pert_DiagcSistritrinact_Cnt_lgc value         0           target_LoaMgr_Pert_DiagcSistritrinact_Cnt_lgc value         0           target_LoaMgr_Pert_Bridger_Cnt_uo8 value         0           target_LoaMgr_Pert_MotCrottpSig_Cnt_uo8 value         0           target_LoaMgr_Pert_MotCrottpSig_Cnt_uo8 value         0           target_LoaMgr_Pert_MotGrader_Cnt_enum_value         0           target_LoaMgr_Pert_Tinach_Cnt_lgc value         0           target_LoaMgr_Pert_Tinach_Cnt_lgc         tar	k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgMtgtnSca_ZeroEn_Cnt_lgc         1           k_LoaMgr_MotAgMtgtnSca_Uis_132         1           k_LoaMgr_MotAgmaAvailResp_Cnt_u08         0           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_RedotSiRca_Uis_S122         1           k_LoaMgr_RedotSiRca_Uis_S122         1           k_LoaMgr_RedotSiRca_Uis_S122         1           k_LoaMgr_Pert_DiagcSistivt1riact_Cnt_lgc value         0           larget_LoaMgr_Pert_DiagcSistivt2lnact_Cnt_lgc value         0           larget_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value         2           larget_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value         1           larget_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value         0           larget_LoaMgr_Pert_TolaQual_Cnt_lgc_value         0           larget_LoaMgr_Pert_TolaQual_Cnt_lgc_value         0	k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	
k_LoaMgr_MotAgMtginSca_Uls_f32         1           k_LoaMgr_MotAgSmaAvailResp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         1           k_LoaMgr_MotCurridptSig2AcroEn_Cnt_lgc         1           k_LoaMgr_ReddSiSca_Uls_f32         1           k_LoaMgr_ReddSiSca_Uls_f32         1           k_LoaMgr_Per_DiagcStsivf2lnactv_Cnt_lgc value         0           target_LoaMgr_Per_LioaScaDi_Cnt_lgc value         0           target_LoaMgr_Per_TioaDi_Cnt_lgc value         0           target_LoaMgr_Per_TioaDi_Cnt_lgc value         0	k_LoaMgr_MotAgMtgtnRate_UlspS_f32	
k_LoaMgr_MotOurridptSig0Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1FitThd_Cnt_u08         0           k_LoaMgr_MotCurridptSig1FitThd_Cnt_u08         0           k_LoaMgr_MotCurridptSig2FitThd_Cnt_u08         1           k_LoaMgr_Mgr_MotCurridptSig2FitThd_Cnt_u08         0           target_LoaMgr_Per1_LoaScaDi_Cnt_lgc_value         0           target_LoaMgr_Per1_MotCurridptSig_Cnt_u08 value         0           target_LoaMgr_Per1_MotCurridptSig_Cnt_u08 value         1           target_LoaMgr_Per1_ToaOl_Cnt_lgc_value         0           target_LoaMgr_Per1_ToaOl_Cnt_lgc_value         0           target_LoaMgr_Per1_ToaOl_Cnt_lgc_value         0           target_LoaMgr_Per1_ToaOl_Cnt_lgc		
k_LoaMgr_MotCurridptSig0Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MotCurridptRigeTitnd_Cnt_u08         0           k_LoaMgr_MotCurridptRigeTitnd_Cnt_u08         1           k_LoaMgr_MotCurridptSig_Ca_Uis_52         1           k_LoaMgr_RedcdStRate_UispS_32         1           k_LoaMgr_RedcdStRate_UispS_32         1           k_LoaMgr_Pert_DiagcStsivtrInactv_Cnt_lgc_value         0           target_LoaMgr_Pert_DiagcStsivtr2inactv_Cnt_lgc_value         0           target_LoaMgr_Pert_LoaScaDi_Cnt_lgc_value         0           target_LoaMgr_Pert_Mr_LoaGcaDi_Cnt_lgc_value         0           target_LoaMgr_Pert_MrProsidptSig_Cnt_u08 value         0           target_LoaMgr_Pert_MrProsidptSig_Cnt_u08 value         1           target_LoaMgr_Pert_TicaScaDi_Cnt_lgc_value         0           target_LoaMgr_Pert_TicaDi_Cnt_lgc_value         0           target_LoaMgr_Pert_TicaDi_Cnt_lgc_value         1           target_Re_Inst_Ap_LoaMgr_Pert_DiagcStsivtrInactv_Cnt_lgc         target_LoaMgr_Pert_DiagcStsivtrInactv_Cnt_lgc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_HivTqLoaMtgrEn_Cnt_lgc <td>_ 0_ 0</td> <td></td>	_ 0_ 0	
k_LoaMgr_MolCurridptSig1Resp_Cnt_u08         0           k_LoaMgr_MolCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MolCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MolCurrMtgtnRate_UlspS_132         1           k_LoaMgr_MolCurrMtgtnSca_Uls_1532         1           k_LoaMgr_RedcdStRate_UlspS_132         1           k_LoaMgr_RedcdStSca_Uls_1532         1           k_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc_value         0           target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc_value         0           target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc_value         0           target_LoaMgr_Per1_MolCurridptSig_Cnt_u08 value         2           target_LoaMgr_Per1_MolCurridptSig_Cnt_u08 value         0           target_LoaMgr_Per1_MolCurridptSig_Cnt_u08 value         0           target_LoaMgr_Per1_SteerMod_Cnt_enun_value         0           target_LoaMgr_Per1_ToaDi_Cnt_lgc_value         0           target_LoaMgr_Per1_ToaDi_Cnt_lgc_value         0           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc           target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc           targ		
k_LoaMgr_MolCurridptSig2Resp_Cnt_u08         0           k_LoaMgr_MolCurridptSigFitThd_Cnt_u08         0           k_LoaMgr_MolCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MolCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MolCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_Perd_DiagcStsIvtr1inactv_Cnt_lgc_value         0           target_LoaMgr_Per1_DiagcStsIvtr2inactv_Cnt_lgc_value         0           target_LoaMgr_Per1_MoldptSig_Cnt_u08 value         2           target_LoaMgr_Per1_MolCurridptSig_Cnt_u08.value         0           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value         0           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value         1           target_LoaMgr_Per1_ToaAvaii_Cnt_lgc_value         0           target_LoaMgr_Per1_ToaAvaii_Cnt_lgc_value         1           target_Rel_inst_Ap_LoaMgr_Per1_DiagcStsIvtr1inactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtr2inactv_Cnt_lgc           target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMigtnEn_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtr2inactv_Cnt_lgc           target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMigtnEn_Cnt_lgc         target_LoaMgr_Per1_HwTqLoaMigtnEn_Cnt_lgc           target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMigtnEn_Cnt_lgc         target_LoaMgr_Per1_LoaXgcD		
k_LoaMgr_MotCurridptSigFltTh_Cn_u08         0           k_LoaMgr_MotCurridptMgthRate_UlspS_f32         1           k_LoaMgr_MotCurrMtgthScaz_Uls_f32         1           k_LoaMgr_MotCurrMtgthSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_Perd_DiagcStsIvtr1lnactv_Cnt_lgc.value         0           target_LoaMgr_Per1_DiagcStsIvtr2lnactv_Cnt_lgc.value         0           target_LoaMgr_Per1_HvTqldptSig_Cnt_u08.value         2           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         0           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         0           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value         0           target_LoaMgr_Per1_SteerMod_Cnt_enum.value         0           target_DoaMgr_Per1_TloaAvail_Cnt_lgc.value         0           target_DoaMgr_Per1_TloaDi_Cnt_lgc.value         0           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1lnactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtr2lnactv_Cnt_lgc           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2lnactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtr2lnactv_Cnt_lgc           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc           target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc         target_LoaMg		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc         1           k_LoaMgr_MotCurrMtgtnSca_Uls_f32         1           k_LoaMgr_RedcdStRate_UlspS_f32         1           k_LoaMgr_Perd_DiagcStsIvtr1Inactv_Cnt_lgc_value         0           target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc_value         0           target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc_value         0           target_LoaMgr_Per1_HvTqldptSig_Cnt_u08_value         2           target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08_value         0           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08_value         0           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08_value         1           target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08_value         0           target_LoaMgr_Per1_ToaAvail_Cnt_lgc_value         0           target_LoaMgr_Per1_ToaAvail_Cnt_lgc_value         0           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc         target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc           target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc         target_LoaMgr_Per1_LoaMgr_Per1_LoaScaDi_Cnt_lgc         target_L		
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_Per1_DiagcStslvtr1nactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 value 2 target_LoaMgr_Per1_MrPosldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MrPosldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SterMod_Cnt_enum.value 0 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1inactv_Cnt_lgc target_Re_inst_Ap_LoaMgr_Per1_DiagcStslvtr2inactv_Cnt_lgc target_Re_inst_Ap_LoaMgr_Per1_DiagcStslvtr2inactv_Cnt_lgc target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_L	k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
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k_LoaMgr_RedcdStSca_Uls_f32	k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value 0 target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value 2 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 0 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MotSudptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MotSudptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_Mo		
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target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value  target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvai_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaSt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value  target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_LoaMgr_Per1_MtrPosidptSig_Cnt_u08.value  target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value  target_LoaMgr_Per1_TloaAvai_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_MotCurrldptSig_Cnt_uo8	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	
target_LoaMgr_Per1_TloaAvain_Cnt_lgc.value  target_LoaMgr_Per1_TloaDi_Cnt_lgc.value  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08  target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_LoaSt_State_enum  target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc  target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_NotAgLoaMtgtnEn_Cnt_lgc  target_LoaMgr_Per1_NotCurrldptSig_Cnt_uo8	· · · · · · · · · · · · · · · · · · ·	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u(	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	1	1	<b>✓</b>
LoaMgr_LoaRateLimit_UlspS_M_f32	1	1	•
LoaMgr_LoaScaleFctr_Uls_M_f32	0.123400003	0.123400003	<b>✓</b>
LoaMgr_MaxMtgtnResp_Cnt_M_u08	1	1	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	1	1	<b>✓</b>
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	<b>✓</b>
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1	1 ± 0.01	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0.123400003	0.123400003 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	<b>✓</b>
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_LOASt_State_enum.value	1	1	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	<b>✓</b>
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	<b>✓</b>
ArbResp	1	ArbResp	1	<b>✓</b>
ChooseFinalResp	1	ChooseFinalResp	1	<b>✓</b>
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	<b>✓</b>
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	<b>✓</b>

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LoaMgr\_Init1

 Project
 SF049A\_LoaMg

 Module
 LoaMgr

 Test Object
 LoaMgr\_Init1

#### Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

#### **Statistics**

Total Testcases	1	
Successful	1	✓
Failed	0	
Not Executed	0	

#### **Module Properties**

Project Root Directory	D:\Synergy_Work_Area\LoaMgr_SF049A_4.3.0_0_NoUTP
Configuration File	D:\Synergy_Work_Area\LoaMgr_SF049A_4.3.0_0_NoUTP\tessy\config\configuration.xml
Target Environment	GNU GCC Eclipse CDT (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\LoaMgr\src\Ap_LoaMgr.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\LoaMgr\tools\contract -I\$(PROJECTROOT)\LoaMgr\tools\contract\Ap_LoaMgr -I\$ (PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(ProgramW6432)\Texas Instruments\ccsv4\tools\compiler \tms470 4.9 \Sinclude

Comments/Description/Spe	cification
Name	Text

LoaMgr\_Init1

Module 'LoaMgr



Name of Tester:Pradnya Mhetre
Code File(s) Under Test:Ap\_LoaMgr.c
Code File(s) Version:1
Module Design Version:9
Requirement Document Version:NA
Data Dictionary Version:8
Unit Test Plan Version:8
Unit Test Plan Version:5
Optimization Level:Ogeneral
Compiler (CodeGen) Version:TMS470\_4.9.5
Model Type:None
Model Version:Nexteer EPS Unit Test Tool 1.0
Total FLASH Used (Bytes):274
Total RAM Used (Bytes):277
Total CALS Used (Bytes):76
Special Test Requirements:NA
Test Date:5/18/2018
Comments:
Note1:Inline functions defined in 'globalmacro.h' are not unit tested.
Note2:The Expected output calculated from MATLAB model.
Note3:Local functions "LatchInputs()"", ""RgstRespConds()"", ""ArbResp()"", ""AssignScale()"", ""RgstHwTqResp()"", ""ChooseFinalResp()"" and ""SwBasMtgtn()"" are not tested individually as they are tested in "LoaMgr\_Per1" function.
Please refer snapshot "LoaMgr\_Per1" function.
Please refer snapshot "LoaMgr\_Per1" footoneap "for the same.
Note4:In CSV\_9 and CSV\_10, value for boolean variable ""DiagcStsIvtr1Inactv"" given in "Simdata" is in float.
Hence input values for "DiagcStsIvtr1Inactv" are captured from Matlab design and given in Tessy for those test steps.
Note5: Out of range value is given to the variable ""HwTqldptSig\_Cnt\_u08=5"" over the DD range {0,4}, tested the path at the line new Order. The Version of the variable ""HwTqldptSig\_Cnt\_u08=5"" over the DD range {0,4}, tested the path at the line new Order. The Version of the variable ""HwTqldptSig\_Cnt\_u08=5"" over the DD range {0,4}, tested the path at the line new Order. The Version of the variable ""HwTqldptSig\_Cnt\_u08=5"" over the DD range {0,4}, tested the path at the line new Order. The Version of the variable ""HwTqldptSig\_Cnt\_u08=5"" over the DD range {0,4}, tested the path at the line new Order. The Version of the variable ""HwTqldptSig\_Cnt\_u08=5"" over the DD range {0,4}, tested the path at the line new Order. The Version of the variable ""HwTqldptSig\_Cnt\_u08=5"" over the DD range {0,4}, tested the variable ""DiagcStsIvtr1 line to "The V

and given in Tessy for those test steps.

Note5: Out of range value is given to the variable ""HwTqldptSig\_Cnt\_u08=5"" over the DD range {0,4}, to cover the path at src line no.908 in TS5.1

Note6: Out of range value is given to the variable ""MtrPosldptSig\_Cnt\_u08=4"" over the DD range {0,3}, to cover the path at src line no.918 in TS5.2

Note7: Out of range value is given to the variable ""MotCurrldptSig\_Cnt\_u08=3"" over the DD range {0,2}, to cover the path at src line no.928 in TS5.3

Note8:Output variable "LoaMgr\_AgSV\_Cnt\_M\_lgc" is failing because of Source Model Mismatch at src line no.1503

Anomaly EA3#20083 is logged for the same.

Mote9:Parameter of "Rte\_Call\_NxtrDiagMgr\_SetNTCStatus" "Param\_Cnt\_T\_u08" is failing because of Source Model Mismatch.

Anomaly EA3#20083 is logged for the same.

Note10:Output variable "LoaMgr\_VtlgMdSrcSV\_Cnt\_M\_u08" is going out of range as [0 3] over DD range [0 1] in PRM test vector TS4.103

Anomaly EA3#20083 is logged for the same.

Note11:""CBD\_Sandbox\_dbg.map"" map file is embedded for reference.

Attributes	
Name	Value
Compiler Install Path	\$(ProgramW6432)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5\include
Float Precision	9



#### Test Case 1: PRM Range Test

Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles: Specification

TS 1.1 2849.00 Cycles TS 1.2 2849.00 Cycles

Description Vector Description

TS1.1 k\_LoaMgr\_SnsrlsMotAgAvail\_Cnt\_lgc=>Min/Default TS1.2 k\_LoaMgr\_SnsrlsMotAgAvail\_Cnt\_lgc=>Max

Name	Input Value		
	o o		
k_LoaMgr_SnsrlsMotAgAvail_Cnt_lgc	U		
Name	Actual Value	Expected Value	Result
LoaMgr_HwTqldptSigSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3	3	~
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2	2	~
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 171, 172, 173, 174, 175}	{170, 171, 172, 173, 174, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{0, 0, 0, 0, 0, 0}	{0, 0, 0, 0, 0, 0}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{0, 0, 0, 0, 0, 0}	{0, 0, 0, 0, 0, 0}	<b>✓</b>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	6	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	6	~

Test Step 1.2 (Repeat Count = 1)			<b>✓</b>
Name	Input Value		
k_LoaMgr_SnsrlsMotAgAvail_Cnt_lgc	1		
Name	Actual Value	Expected Value	Result
LoaMgr_HwTqldptSigSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3	3	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	<b>✓</b>
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 171, 172, 173, 174, 175}	{170, 171, 172, 173, 174, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{0, 0, 0, 0, 0, 0}	{0, 0, 0, 0, 0, 0}	~
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	{0, 0, 0, 0, 0, 0}	{0, 0, 0, 0, 0, 0}	<b>✓</b>

Test Step Call Trace				<b>✓</b>
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	6	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	6	~