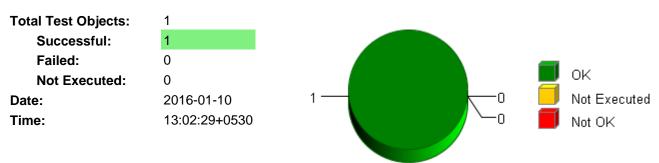


Summary

Overall Test Object Results (including Coverage)



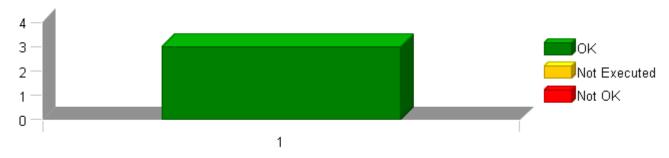
Selected Project Items

Test Object "CBD_UnitTest/MtrCtrl_CM_QuadDet/QuadDet_Per1"

Used Test Environments

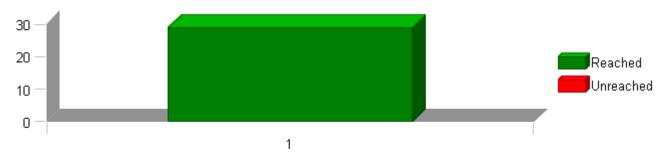
TI TMS 570 PLS UDE (Default)

Test Case Results for Each Test Object (without Coverage)



The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

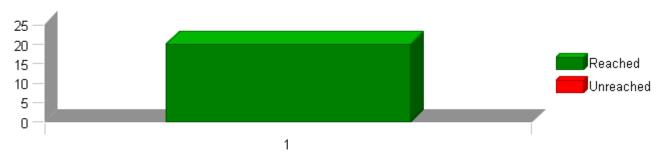
Statement (C0) Coverage: Total Statements for Each Test Object



The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.



Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

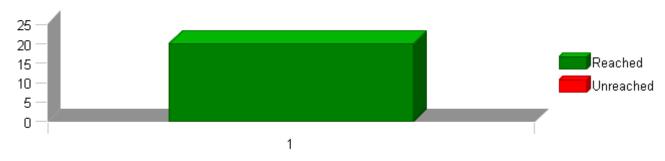
Decision Coverage: Total Decision Outcomes for Each Test Object



The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.

MC/DC Coverage: Total Condition Combinations for Each Test Object

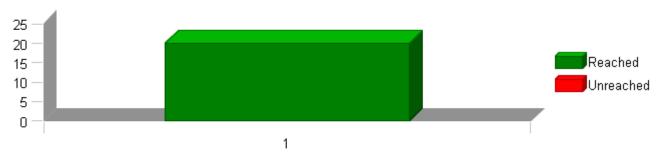


The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.



MCC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MCC coverage, each decision requires all contained atomic conditions to evaluate to all possible combinations of true and false values. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

TEST OVERVIEW REPORT

2016-01-10, 13:02:29+0530



Test Object List

Project MtrCtrl

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	DC	MC/DC	MCC	Test Cases	Result
	MtrCtrl	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	~
	CBD_UnitTest	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	~
	MtrCtrl_CM_QuadDet	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	~
1	QuadDet Per1	100 %	100 %	100 %	100 %	100 %	3 of 3 passed	•

© Report created by TESSY V3.1.12, report template V2.0

2016-01-10, 13:02:02+0530





Project MtrCtrl

Module MtrCtrl_CM_QuadDet

Test Object QuadDet_Per1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Decision Coverage	100 %
Branch (C1) Coverage	100 %
MCC Coverage	100 %
MC/DC Coverage	100 %

Statistics

Total Testcases	3	
Successful	3	✓
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\MtrCtrl_CM		
Configuration File	D:\Synergy_Work_Area\MtrCtrl_CM\UnitTestEnv\config\TMS570_GCC_UDE_CCS4_Config.xml		
Target Environment	TI TMS 570 PLS UDE (Default)		
Kind of Test	Unit Test		
Linker Options			
Source File(s)			
File	\$(PROJECTROOT)\MtrCtrl_CM\src\Ap_QuadDet.c		
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\NxtrLib\include -I\$(PROJECTROOT)\StdDef\include -I\$(PROJECTROOT)\MtrCtrl_CM \include -I\$(PROJECTROOT)\MtrCtrl_CM\utp\contract\Ap_QuadDet -I\$(Compiler Install Path) \include		

Name	Text
Module 'MtrCtrl_CM_QuadDet'	Name of Tester: Priti Mangalekar Code File(s) Under Test: Ap_QuadDet.c Code File(s) Version: 9 Module Design Document: Quadrant_Detection_MDD.docx Module Design Document Version: 2 Data Dictionary Version: 13 Unit Test Plan Version: 1 Optimization Level: Level 2 Compiler (CodeGen) Version: TMS470_4.9.5 Model Type: Excel Macro Model Version: Nexteer EPS Unit Test Tool 2.7d/EPS Library 1.32 Total FLASH Used (Bytes):376 Total RAM Used (Bytes):6 Total CALS Used (Bytes):1026 Special Test Requirements: Test Date:01/10/2016 Comments: Note1: Inline function defined in Global Macro.h are not unit tested. Note2:"CBD_Sandbox_dbg.map" map file is embedded for reference.

Attributes				
Name	Value			
Compiler Install Path	<pre>\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5</pre>			
Float Precision	9			
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj			
nitSrcDir \$(PROJECTROOT)\UnitTestEnv\static_build_files\src				
inker File \$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd				
Makefile Template \$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl				
Target Install Path \$(ProgramFiles)\pls\UDE 3.2				
Time Unit	Cycles			
Timer Enabled	false			
Timer Prescale	0			
Timer Resolution	1			

QuadDet_Per1

2016-01-10, 13:02:02+0530



Attributes					
Name	Value				
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg				
Workspace File	D:\Synergy_Work_Area\MtrCtrl_CM\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP				



Test Case 1: Metrics Test

Specification

Performance Metrics: (With "None" Instrumentation and "WithPS" Environment)

TS 1.1 585.00 Cycles Shortest Execution Path TS 1.2 611.00 Cycles Longest Execution Path Shortest Execution Path

Vector Description: Description

TS 1.1 "Shortest Execution Path:

TS 1.1 "Shortest Execution Path:
if(DeltaMotPos_Deg_T_f32 > (CumMtrPosLmt_Deg_T_f32/2.0F))=>True
SignDeltaMotPos_Cnt_T_s08 = (sint8)Sign_f32_m(DeltaMotPos_Deg_T_f32);=>False
if(PrevInstMtrDir_Cnt_M_s08 == SignDeltaMotPos_Cnt_T_s08)=>True
if(Abs_f32_m(MtrTrqCmdScl_MtrNm_T_f32) > D_MTRTRQCMDTOL_MTRNM_F32)=>False
if(MtrTrqCmdSign_Cnt_D_s08 == PrevInstMtrDir_Cnt_M_s08)=>True

if(1 == MtrTrqCmdSign_Cnt_D_s08)=>False"

if(1 == MtrTrqCmdSign_Cnt_D_su8)=>Faise"

TS 1.2 "Longest Execution Path:
if(DeltaMotPos_Deg_T_f32 > (CumMtrPosLmt_Deg_T_f32/2.0F))=>False
else if(DeltaMotPos_Deg_T_f32 > (CumMtrPosLmt_Deg_T_f32/2.0F))=>True
SignDeltaMotPos_Cnt_T_s08 = (Sint8)Sign_f32_m(DeltaMotPos_Deg_T_f32)=>True
if(PrevInstMtrDir_Cnt_M_s08 == SignDeltaMotPos_Cnt_T_s08)=>False
else if(AbsDeltaMotPos_Deg_T_f32 >= k_InstMtrDirHyst_Deg_f32)=>True
if(Abs_f32_m(MtrTrqCmdScl_MtrNm_T_f32) > D_MTRTRQCMDTOL_MTRNM_F32)=>True
MtrTrqCmdSign_Cnt_D_s08 = (sint8)Sign_f32_m(MtrTrqCmdScl_MtrNm_T_f32)=>True
if(MtrTrqCmdSign_Cnt_D_s08 == PrevInstMtrDir_Cnt_M_s08)=>True
if(1 == MtrTrqCmdSign_Cnt_D_s08 => PrevInstMtrDir_Cnt_M_s08)=>True

if(1 == MtrTrqCmdSign_Cnt_D_s08)=>True"

Test Step 1.1 (Repeat Count = 1)			✓		
Name	Input Value				
MtrTrqCmdSign_Cnt_D_s08	-1				
PrevCumMtrPos_Deg_M_f32	32				
PrevInstMtrDir_Cnt_M_s08	-1				
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet				
k_InstMtrDirHyst_Deg_f32	5				
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	11797200				
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	0.0001	0.0001			
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08 tgt_QuadDet_Per1_InstMtrDir_Cnt_s08				
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumM	/trPos_Deg_f32			
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTr	qCmdScl_MtrNm_f32			
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_0	Cnt_u08			
Name	Actual Value	Expected Value	Result		
MtrTrqCmdSign_Cnt_D_s08	-1	-1	~		
PrevCumMtrPos_Deg_M_f32	11797200	11797200	✓		
PrevInstMtrDir_Cnt_M_s08	-1	-1	~		
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✓		
tgt QuadDet Per1 MtrQuad Cnt u08.value	3	3	✓		

Test Step Call Trace ✓				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✓

Name	Innut Value				
	Input Value				
MtrTrqCmdSign_Cnt_D_s08	-1				
PrevCumMtrPos_Deg_M_f32	20				
PrevInstMtrDir_Cnt_M_s08	-1				
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet				
k_InstMtrDirHyst_Deg_f32	0				
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	-11797200				
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	1				
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08			
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32				
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmd	IScI_MtrNm_f32			
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u	08			
Name	Actual Value	Expected Value	Result		
MtrTrqCmdSign_Cnt_D_s08	1	1	✓		
PrevCumMtrPos_Deg_M_f32	-11797200	-11797200	•		
PrevInstMtrDir_Cnt_M_s08	1	1	•		
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	•		
tgt QuadDet Per1 MtrQuad Cnt u08.value	1	1			





Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	~

Test Case 2: Boundary Test

Specification

Performance Metrics:
(With "None" Instrumentation and "WithPS" Environment)

CPU Cycles:

TS 2.1 595.00 Cycles 607.00 Cycles 598.00 Cycles 598.00 Cycles 592.00 Cycles 606.00 Cycles 605.00 Cycles 607.00 Cycles 607.00 Cycles 608.00 Cycles 593.00 Cycles 606.00 Cycles 607.00 Cycles 607.00 Cycles 607.00 Cycles 607.00 Cycles 607.00 Cycles 606.00 Cycles 606.00 Cycles 606.00 Cycles 595.00 Cycles TS 2.2
TS 2.3
TS 2.3
TS 2.5
TS 2.5
TS 2.7
TS 2.8
TS 2.9
TS 2.10
TS 2.11
TS 2.12
TS 2.13
TS 2.14
TS 2.15
TS 2.16
TS 2.17
TS 2.15
TS 2.16
TS 2.17
TS 2.15
TS 2.16
TS 2.17
TS 2.17
TS 2.21
TS 2.21
TS 2.22
TS 2.23
TS 2.24
TS 2.24
TS 2.25
TS 2.22
TS 2.22
TS 2.24
TS 2.25
TS 2.25
TS 2.27
TS 2.28
TS 2.27
TS 2.28
TS 2.29 592.00 Cycles 606.00 Cycles 605.00 Cycles 605.00 Cycles 607.00 Cycles 607.00 Cycles 607.00 Cycles 606.00 Cycles 605.00 Cycles 605.00 Cycles 605.00 Cycles 605.00 Cycles 605.00 Cycles 606.00 Cycles 608.00 Cycles 609.00 Cycles 609.00 Cycles 609.00 Cycles 609.00 Cycles 592.00 Cycles TS 2.30 TS 2.31

Description

Vector Description:

TS 2.2MRFMtrTrqCmdScl_MtrNm_f32=Max TS 2.3MRFMtrTrqCmdScl_MtrNm_f32=Zero TS 2.4MRFMtrTrqCmdScl_MtrNm_f32=Pos TS 2.5MRFMtrTrqCmdScl_MtrNm_f32=Neg TS 2.6 PrevCumMtrPos=Min TS 2.7PrevCumMtrPos=Max TS 2.8 PrevCumMtrPos=Zero TS 2.9 PrevCumMtrPos=Neg
TS 2.10PrevCumMtrPos=Pos TS 2.11MRFCumMtrPos=Min TS 2.12MRFCumMtrPos=Max TS 2.13 MRFCumMtrPos=Zero TS 2.14MRFCumMtrPos=Neg TS 2.15MRFCumMtrPos=Pos TS 2.16MtrTrqCmdSign_Cnt_D_s08=Min TS 2.17MtrTrqCmdSign_Cnt_D_s08=Max TS 2.18MtrTrqCmdSign_Cnt_D_s08=Zero TS 2.19PrevInstMtrDir_Cnt_M_s08=Min TS 2.20PrevInstMtrDir_Cnt_M_s08=Max TS 2.21PrevInstMtrDir_Cnt_M_s08= Zero TS 2.22k_InstMtrDirHyst_Deg_f32 =min TS 2.23k_InstMtrDirHyst_Deg_132 = max
TS 2.23k_InstMtrDirHyst_Deg_f32 = max
TS 2.24k_InstMtrDirHyst_Deg_f32 = middle/Default
TS 2.25MRFMtrVel_MtrRadpS_f32=min
TS 2.26MRFMtrVel_MtrRadpS_f32=max
TS 2.27MRFMtrVel_MtrRadpS_f32=zero

TS 2.28MRFMtrVel_MtrRadpS_f32=pos TS 2.29MRFMtrVel_MtrRadpS_f32=neg

TS 2.1MRFMtrTrqCmdScl_MtrNm_f32=Min

Test Step 2.1 (Repeat Count = 1)		
Name	Input Value	
MtrTrqCmdSign_Cnt_D_s08	1	
PrevCumMtrPos_Deg_M_f32	5556.02	
PrevInstMtrDir_Cnt_M_s08	1	
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet	
k_InstMtrDirHyst_Deg_f32	2	
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	34876.3	
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	-8.8	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32	

TS 2.30All Min TS 2.31All Max

2016-01-10, 13:02:02+0530



Name	Input Value			
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32			
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cr	nt_u08		
Name	Actual Value	Expected Value	Result	
MtrTrqCmdSign_Cnt_D_s08	-1	-1	✓	
PrevCumMtrPos_Deg_M_f32	34876.3008	34876.3	✓	
PrevInstMtrDir_Cnt_M_s08	1	1	✓	
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✓	
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	4	4	✓	

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~	
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	•	

Test Step 2.2 (Repeat Count = 1)			✓
Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	0		
PrevCumMtrPos_Deg_M_f32	26372.3		
PrevInstMtrDir_Cnt_M_s08	1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	1		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	2347.4		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	8.8		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPe	os_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCm	dScl_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_	u08	
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	✓
PrevCumMtrPos_Deg_M_f32	2347.3999	2347.4	✓
PrevInstMtrDir_Cnt_M_s08	-1	-1	✓
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✓
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	2	2	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	•

Name	Input Value			
MtrTrqCmdSign_Cnt_D_s08	-1			
PrevCumMtrPos_Deg_M_f32	4377.4			
PrevInstMtrDir_Cnt_M_s08	1			
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet			
k_InstMtrDirHyst_Deg_f32	3			
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	3828.21			
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	0			
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cn	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrP	os_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCn	ndScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_	_u08		
Name	Actual Value	Expected Value	Result	
MtrTrqCmdSign_Cnt_D_s08	-1	-1	•	
PrevCumMtrPos_Deg_M_f32	3828.20996	3828.21	•	
PrevInstMtrDir_Cnt_M_s08	-1	-1	•	
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	•	
tgt QuadDet Per1 MtrQuad Cnt u08.value	3	3		

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	~



Test Step 2.4 (Repeat Count = 1)			✓
Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	-1		
PrevCumMtrPos_Deg_M_f32	11828.1		
PrevInstMtrDir_Cnt_M_s08	-1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	4		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	8547.1		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	5.6		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_	<u>f</u> 32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_Mt	rNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	✓
PrevCumMtrPos_Deg_M_f32	8547.09961	8547.1	✓
PrevInstMtrDir_Cnt_M_s08	-1	-1	~
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✓
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	2	2	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	•

Test Step 2.5 (Repeat Count = 1)			✓
Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	-1		
PrevCumMtrPos_Deg_M_f32	3848.2		
PrevInstMtrDir_Cnt_M_s08	-1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	2		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	28382.02		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	-6.3		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_0	Cnt_s08	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumM	trPos_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrq	CmdScl_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_C	nt_u08	
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	-1	-1	•
PrevCumMtrPos_Deg_M_f32	28382.0195	28382.02	✓
PrevInstMtrDir_Cnt_M_s08	1	1	~
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✓
tgt QuadDet Per1 MtrQuad Cnt u08.value	4	4	_

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	~

Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	-1		
PrevCumMtrPos_Deg_M_f32	-11797200		
PrevInstMtrDir_Cnt_M_s08	-1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	2.3		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	1000.3		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	2.3		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_C	nt_s08	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMt	rPos_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrq0	CmdScl_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cr	nt_u08	
Name	Actual Value	Expected Value	Resul
MtrTrqCmdSign_Cnt_D_s08	1	1	•
PrevCumMtrPos Deg M f32	1000.29999	1000.3	

2016-01-10, 13:02:02+0530



Name	Actual Value	Expected Value	Result
PrevInstMtrDir_Cnt_M_s08	-1	-1	✓
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✓
tgt QuadDet Per1 MtrQuad Cnt u08.value	2	2	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	~

Test Step 2.7 (Repeat Count = 1)			✓
Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	0		
PrevCumMtrPos_Deg_M_f32	11797200		
PrevInstMtrDir_Cnt_M_s08	0		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	5		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	10000.14		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	5		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_C	nt_s08	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtr	Pos_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqC	CmdScl_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cn	ıt_u08	
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	~
PrevCumMtrPos_Deg_M_f32	10000.1396	10000.14	✓
PrevInstMtrDir_Cnt_M_s08	-1	-1	~
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✓
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	2	2	✓

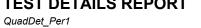
Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~	
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✓	

Test Step 2.8 (Repeat Count = 1)			✓
Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	0		
PrevCumMtrPos_Deg_M_f32	0		
PrevInstMtrDir_Cnt_M_s08	0		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	6		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	6565.32		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	6		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_st	08	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_	_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdS	ScI_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u0	8	
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	~
PrevCumMtrPos_Deg_M_f32	6565.31982	6565.32	✓
PrevInstMtrDir_Cnt_M_s08	1	1	~
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✓
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	1	1	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte Call QuadDet Per1 CP1 CheckpointReached	1	Rte Call QuadDet Per1 CP1 CheckpointReached	1	✓

Test Step 2.9 (Repeat Count = 1)	✓
Name	Input Value
MtrTrqCmdSign_Cnt_D_s08	0
PrevCumMtrPos_Deg_M_f32	-3728.21

2016-01-10, 13:02:02+0530





Name	Input Value			
PrevInstMtrDir_Cnt_M_s08	0			
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet			
k_InstMtrDirHyst_Deg_f32	4			
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	4336.41			
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	4			
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08			
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_De	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_	MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08			
Name	Actual Value	Expected Value	Result	
MtrTrqCmdSign_Cnt_D_s08	1	1	~	
PrevCumMtrPos_Deg_M_f32	4336.41016	4336.41	~	
PrevInstMtrDir_Cnt_M_s08	1	1	~	
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	~	
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	1	1	✓	

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	~

Test Step 2.10 (Repeat Count = 1)			✓
Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	1		
PrevCumMtrPos_Deg_M_f32	4837.32		
PrevInstMtrDir_Cnt_M_s08	1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	2.4		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	4326.3		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	7.4		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cn	t_s08	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrF	Pos_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCr	ndScl_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt	_u08	
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	✓
PrevCumMtrPos_Deg_M_f32	4326.2998	4326.3	✓
PrevInstMtrDir_Cnt_M_s08	-1	-1	✓
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✓
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	2	2	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	~

Test Step 2.11 (Repeat Count = 1)			✓
Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	0		
PrevCumMtrPos_Deg_M_f32	12000.14		
PrevInstMtrDir_Cnt_M_s08	0		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	1.2		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	-11797200		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	1.2		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_0	Cnt_s08	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMt	rPos_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrq0	CmdScl_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cr	nt_u08	
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	~
PrevCumMtrPos_Deg_M_f32	-11797200	-11797200	✓
PrevInstMtrDir_Cnt_M_s08	1	1	✓
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✓
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	1	1	~





Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~	
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✓	

Test Step 2.12 (Repeat Count = 1)			✓
Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	-1		
PrevCumMtrPos_Deg_M_f32	-12000.21		
PrevInstMtrDir_Cnt_M_s08	-1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	3.5		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	11797200		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	-6.5		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMt	rPos_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrq0	CmdScl_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cr	nt_u08	
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	-1	-1	~
PrevCumMtrPos_Deg_M_f32	11797200	11797200	~
PrevInstMtrDir_Cnt_M_s08	-1	-1	~
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✓
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	3	3	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte Call QuadDet Per1 CP1 CheckpointReached	1	Rte Call QuadDet Per1 CP1 CheckpointReached	1	✓

Test Step 2.13 (Repeat Count = 1)			✓
Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	1		
PrevCumMtrPos_Deg_M_f32	3728.3		
PrevInstMtrDir_Cnt_M_s08	1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	4.2		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	0		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	-4.2		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s0	8	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_	Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdSe	cl_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08	3	
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	-1	-1	~
PrevCumMtrPos_Deg_M_f32	0	0	✓
PrevInstMtrDir_Cnt_M_s08	-1	-1	✓
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✓
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	3	3	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✓

Test Step 2.14 (Repeat Count = 1)		✓
Name	Input Value	
MtrTrqCmdSign_Cnt_D_s08	0	
PrevCumMtrPos_Deg_M_f32	9938.7	
PrevInstMtrDir_Cnt_M_s08	0	
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet	
k_InstMtrDirHyst_Deg_f32	3	
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	-27382.21	

2016-01-10, 13:02:02+0530



Name	Input Value		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	3		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f	32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	~
PrevCumMtrPos_Deg_M_f32	-27382.2109	-27382.21	•
PrevInstMtrDir_Cnt_M_s08	-1	-1	~
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	~
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	2	2	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	~

Test Step 2.15 (Repeat Count = 1)			✓
Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	-1		
PrevCumMtrPos_Deg_M_f32	2837.08		
PrevInstMtrDir_Cnt_M_s08	-1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	4		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	37478.1		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	7		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_	_Cnt_s08	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCuml	MtrPos_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTr	qCmdScl_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_	Cnt_u08	
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	~
PrevCumMtrPos_Deg_M_f32	37478.1016	37478.1	✓
PrevInstMtrDir_Cnt_M_s08	1	1	~
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✓
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	1	1	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	~

Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	-1		
PrevCumMtrPos_Deg_M_f32	3848.09		
PrevInstMtrDir_Cnt_M_s08	1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	4.2		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	38283.3		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	4.2		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt	_s08	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPd	os_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCm	dScl_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_	u08	
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	•
PrevCumMtrPos_Deg_M_f32	38283.3008	38283.3	•
PrevInstMtrDir_Cnt_M_s08	1	1	•
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	•
tgt QuadDet Per1 MtrQuad Cnt u08.value	1	1	•



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✓

Test Step 2.17 (Repeat Count = 1)			✓
Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	1		
PrevCumMtrPos_Deg_M_f32	9858.21		
PrevInstMtrDir_Cnt_M_s08	1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	1.5		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	5483.21		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	1.5		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cn	ıt_s08	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrF	Pos_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCr	mdScl_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt	_u08	
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	~
PrevCumMtrPos_Deg_M_f32	5483.20996	5483.21	✓
PrevInstMtrDir_Cnt_M_s08	-1	-1	~
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✓
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	2	2	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte Call QuadDet Per1 CP1 CheckpointReached	1	Rte Call QuadDet Per1 CP1 CheckpointReached	1	✓

Test Step 2.18 (Repeat Count = 1)			✓
Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	0		
PrevCumMtrPos_Deg_M_f32	258.32		
PrevInstMtrDir_Cnt_M_s08	0		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	6.2		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	293.32		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	6.2		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s	808	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos	s_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmd	Scl_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u	08	
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	~
PrevCumMtrPos_Deg_M_f32	293.320007	293.32	✓
PrevInstMtrDir_Cnt_M_s08	1	1	✓
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✓
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	1	1	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~	
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✓	

Test Step 2.19 (Repeat Count = 1)		✓
Name	Input Value	
MtrTrqCmdSign_Cnt_D_s08	0	
PrevCumMtrPos_Deg_M_f32	2932.12	
PrevInstMtrDir_Cnt_M_s08	-1	
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet	
k_InstMtrDirHyst_Deg_f32	3.3	
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	3929.32	

2016-01-10, 13:02:02+0530



Name	Input Value		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	3.3		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f	32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	~
PrevCumMtrPos_Deg_M_f32	3929.32007	3929.32	✓
PrevInstMtrDir_Cnt_M_s08	1	1	~
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✓
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✓

Test Step 2.20 (Repeat Count = 1)			✓
Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	0		
PrevCumMtrPos_Deg_M_f32	5848.03		
PrevInstMtrDir_Cnt_M_s08	1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	4.2		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	4358.5		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	4.2		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_	_Cnt_s08	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumN	/trPos_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTro	qCmdScl_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_0	Cnt_u08	
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	~
PrevCumMtrPos_Deg_M_f32	4358.5	4358.5	✓
PrevInstMtrDir_Cnt_M_s08	-1	-1	~
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	~
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	2	2	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	~

Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	1		
PrevCumMtrPos_Deg_M_f32	14587.21		
PrevInstMtrDir_Cnt_M_s08	0		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	2		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	14587.6		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	5.4		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt	_s08	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrP	os_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCm	ndScl_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_	u08	
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	•
PrevCumMtrPos_Deg_M_f32	14587.21	14587.21	•
PrevInstMtrDir_Cnt_M_s08	0	0	•
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	0	0	•
tgt QuadDet Per1 MtrQuad Cnt u08.value	2	2	•





Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~		
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✓		

Test Step 2.22 (Repeat Count = 1)			✓
Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	0		
PrevCumMtrPos_Deg_M_f32	9858.32		
PrevInstMtrDir_Cnt_M_s08	1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	0		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	5483.1		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	1.5		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_0	nt_s08	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMt	rPos_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrq0	CmdScl_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cr	nt_u08	
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	~
PrevCumMtrPos_Deg_M_f32	5483.1001	5483.1	✓
PrevInstMtrDir_Cnt_M_s08	-1	-1	✓
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✓
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	2	2	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte Call QuadDet Per1 CP1 CheckpointReached	1	Rte Call QuadDet Per1 CP1 CheckpointReached	1	✓

Test Step 2.23 (Repeat Count = 1)			✓
Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	-1		
PrevCumMtrPos_Deg_M_f32	258.12		
PrevInstMtrDir_Cnt_M_s08	0		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	6.25		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	293.2		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	6.2		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos	s_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmd	Scl_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u	08	
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	~
PrevCumMtrPos_Deg_M_f32	293.200012	293.2	✓
PrevInstMtrDir_Cnt_M_s08	1	1	✓
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✓
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	1	1	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✓

Test Step 2.24 (Repeat Count = 1)		✓
Name	Input Value	
MtrTrqCmdSign_Cnt_D_s08	1	
PrevCumMtrPos_Deg_M_f32	2932.2	
PrevInstMtrDir_Cnt_M_s08	-1	
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet	
k_InstMtrDirHyst_Deg_f32	2.5	
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	3929.03	

2016-01-10, 13:02:02+0530



Name	Input Value		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	3.3		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_t	f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	~
PrevCumMtrPos_Deg_M_f32	3929.03003	3929.03	•
PrevInstMtrDir_Cnt_M_s08	1	1	~
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	~
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	1	1	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✓

Test Step 2.25 (Repeat Count = 1)			✓
Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	0		
PrevCumMtrPos_Deg_M_f32	9858.02		
PrevInstMtrDir_Cnt_M_s08	-1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	1.2		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	3929.8		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	4		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_	_Cnt_s08	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumN	/trPos_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTre	qCmdScl_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_0	Cnt_u08	
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	~
PrevCumMtrPos_Deg_M_f32	3929.80005	3929.8	✓
PrevInstMtrDir_Cnt_M_s08	-1	-1	~
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✓
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	2	2	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	~

Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	-1		
PrevCumMtrPos_Deg_M_f32	258.3		
PrevInstMtrDir_Cnt_M_s08	-1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	3.5		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	4358.9		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	7.4		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt	_s08	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPd	os_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCm	dScl_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_	u08	
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	•
PrevCumMtrPos_Deg_M_f32	4358.8999 4358.9		
PrevInstMtrDir_Cnt_M_s08	1	1	•
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	•
tgt QuadDet Per1 MtrQuad Cnt u08.value	1	1	•





Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte Call QuadDet Per1 CP1 CheckpointReached	1	Rte Call QuadDet Per1 CP1 CheckpointReached	1	✓

Test Step 2.27 (Repeat Count = 1)			✓
Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	1		
PrevCumMtrPos_Deg_M_f32	2932.4		
PrevInstMtrDir_Cnt_M_s08	-1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	4.2		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	14587.4		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	1.2		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt	_s08	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPd	os_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCm	dScl_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_	u08	
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	~
PrevCumMtrPos_Deg_M_f32	14587.4004	14587.4	✓
PrevInstMtrDir_Cnt_M_s08	1	1	✓
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✓
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	1	1	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte Call QuadDet Per1 CP1 CheckpointReached	1	Rte Call QuadDet Per1 CP1 CheckpointReached	1	✓

Test Step 2.28 (Repeat Count = 1)			✓
Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	1		
PrevCumMtrPos_Deg_M_f32	5848		
PrevInstMtrDir_Cnt_M_s08	0		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	3		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	5483.03		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	-6.5		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_[Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdSc	I_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	-1	-1	~
PrevCumMtrPos_Deg_M_f32	5483.02979	5483.03	✓
PrevInstMtrDir_Cnt_M_s08	-1	-1	✓
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✓
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	3	3	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~	
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✓	

Test Step 2.29 (Repeat Count = 1)		✓
Name	Input Value	
MtrTrqCmdSign_Cnt_D_s08	0	
PrevCumMtrPos_Deg_M_f32	14587.04	
PrevInstMtrDir_Cnt_M_s08	0	
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet	
k_InstMtrDirHyst_Deg_f32	4	
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	293	

2016-01-10, 13:02:02+0530



Name	Input Value		
	input value		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	-4.2		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f3	32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_Mtrl	Nm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	-1	-1	~
PrevCumMtrPos_Deg_M_f32	293	293	~
PrevInstMtrDir_Cnt_M_s08	-1	-1	~
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	~
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	3	3	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	•

Test Step 2.30 (Repeat Count = 1)			Ť
Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	-1		
PrevCumMtrPos_Deg_M_f32	-11797200		
PrevInstMtrDir_Cnt_M_s08	-1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	0		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	-11797200		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	-8.8		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_	_Cnt_s08	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumM	/trPos_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTr	qCmdScl_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_0	Cnt_u08	
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	-1	-1	✓
PrevCumMtrPos_Deg_M_f32	-11797200	-11797200	•
PrevInstMtrDir_Cnt_M_s08	1	1	→
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✓
tgt QuadDet Per1 MtrQuad Cnt u08.value	4	4	<u> </u>

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	~

Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	1		
PrevCumMtrPos_Deg_M_f32	11797200		
PrevInstMtrDir_Cnt_M_s08	1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	6.25		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	11797200		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	8.8		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt	_s08	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPd	os_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCm	dScl_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_	u08	
Name	Actual Value	Expected Value	Resu
MtrTrqCmdSign_Cnt_D_s08	1	1	
PrevCumMtrPos_Deg_M_f32	11797200	11797200	
PrevInstMtrDir_Cnt_M_s08	1	1	
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	
tgt QuadDet Per1 MtrQuad Cnt u08.value	1	1	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✓

Test Step 3.1 (Repeat Count = 1)			✓
Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	1		
PrevCumMtrPos_Deg_M_f32	32		
PrevInstMtrDir_Cnt_M_s08	1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	0		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	4000		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	1		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_	Cnt_s08	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumM	ftrPos_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTro	qCmdScl_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_0	Cnt_u08	
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	~
PrevCumMtrPos_Deg_M_f32	4000	4000	✓
PrevInstMtrDir_Cnt_M_s08	1	1	~
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✓
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	1	1	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte Call QuadDet Per1 CP1 CheckpointReached	1	Rte Call QuadDet Per1 CP1 CheckpointReached	1	✓

Test Step 3.2 (Repeat Count = 1)	
Name	Input Value
MtrTrqCmdSign_Cnt_D_s08	-1
PrevCumMtrPos_Deg_M_f32	75
PrevInstMtrDir_Cnt_M_s08	-1
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet

2016-01-10, 13:02:02+0530



Name	Input Value		
k_InstMtrDirHyst_Deg_f32	0		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	87		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	0		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f	32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_Mtrl	Nm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	-1	-1	~
PrevCumMtrPos_Deg_M_f32	87	87	~
PrevInstMtrDir_Cnt_M_s08	1	1	~
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	~
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	4	4	~

Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~		
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	~		

Test Step 3.3 (Repeat Count = 1)			~
Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	0		
PrevCumMtrPos_Deg_M_f32	10		
PrevInstMtrDir_Cnt_M_s08	0		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	0		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	2		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	1		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_	_Cnt_s08	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumM	/trPos_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTr	qCmdScl_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_0	Cnt_u08	
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	~
PrevCumMtrPos_Deg_M_f32	2	2	✓
PrevInstMtrDir_Cnt_M_s08	-1	-1	•
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	•
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	2	2	·

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~	
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✓	

Test Step 3.4 (Repeat Count = 1)			✓
Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	-1		
PrevCumMtrPos_Deg_M_f32	47		
PrevInstMtrDir_Cnt_M_s08	-1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	0		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	38		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	0		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_C	nt_s08	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtr	Pos_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqC	cmdScl_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cn	t_u08	
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	-1	-1	✓
PrevCumMtrPos_Deg_M_f32	38	38	•
PrevInstMtrDir_Cnt_M_s08	-1	-1	•
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	•
tgt QuadDet Per1 MtrQuad Cnt u08.value	3	3	·



Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~		
Rte Call QuadDet Per1 CP1 CheckpointReached	1	Rte Call QuadDet Per1 CP1 CheckpointReached	1	✓		

Test Step 3.5 (Repeat Count = 1)			✓
Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	1		
PrevCumMtrPos_Deg_M_f32	19		
PrevInstMtrDir_Cnt_M_s08	1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	0		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	19		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	1		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_0	Cnt_s08	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumM	trPos_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrq	CmdScl_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_C	nt_u08	
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	~
PrevCumMtrPos_Deg_M_f32	19	19	~
PrevInstMtrDir_Cnt_M_s08	1	1	~
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✓
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	1	1	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte Call QuadDet Per1 CP1 CheckpointReached	1	Rte Call QuadDet Per1 CP1 CheckpointReached	1	✓

Test Step 3.6 (Repeat Count = 1)			✓
Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	0		
PrevCumMtrPos_Deg_M_f32	22		
PrevInstMtrDir_Cnt_M_s08	0		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	0		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	43		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	0		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s	s08	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos	s_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmd	IScI_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u	08	
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	0	0	✓
PrevCumMtrPos_Deg_M_f32	43	43	✓
PrevInstMtrDir_Cnt_M_s08	1	1	✓
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✓
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	4	4	✓

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~	
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✓	

Test Step 3.7 (Repeat Count = 1)		✓
Name	Input Value	
MtrTrqCmdSign_Cnt_D_s08	1	
PrevCumMtrPos_Deg_M_f32	3	
PrevInstMtrDir_Cnt_M_s08	1	
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet	
k_InstMtrDirHyst_Deg_f32	2	
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	4	

2016-01-10, 13:02:02+0530



Name	Input Value		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	1		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_	f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_Mtr	Nm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	~
PrevCumMtrPos_Deg_M_f32	4	4	•
PrevInstMtrDir_Cnt_M_s08	1	1	-
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	•
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	1	1	~

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~	
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✓	

Test Step 3.8 (Repeat Count = 1)			✓
Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	0		
PrevCumMtrPos_Deg_M_f32	32		
PrevInstMtrDir_Cnt_M_s08	0		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	0		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	4000		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	1		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_	_Cnt_s08	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumI	MtrPos_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTr	qCmdScl_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_	Cnt_u08	
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	~
PrevCumMtrPos_Deg_M_f32	4000	4000	✓
PrevInstMtrDir_Cnt_M_s08	1	1	~
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✓
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	1	1	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte Call QuadDet Per1 CP1 CheckpointReached	1	Rte Call QuadDet Per1 CP1 CheckpointReached	1	✓

Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	1		
PrevCumMtrPos_Deg_M_f32	-20		
PrevInstMtrDir_Cnt_M_s08	1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	0		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	11797200		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	1		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrP	os_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCn	ndScl_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_	_u08	
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	•
PrevCumMtrPos_Deg_M_f32	11797200	11797200	•
PrevInstMtrDir_Cnt_M_s08	-1	-1	•
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	•
tgt QuadDet Per1 MtrQuad Cnt u08.value	2	2	



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	~

Test Step 3.10 (Repeat Count = 1)			✓
Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	1		
PrevCumMtrPos_Deg_M_f32	20		
PrevInstMtrDir_Cnt_M_s08	1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	0		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	-11797200		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	1		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtr	Pos_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrq0	CmdScl_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cn	ıt_u08	
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	~
PrevCumMtrPos_Deg_M_f32	-11797200	-11797200	✓
PrevInstMtrDir_Cnt_M_s08	1	1	•
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	✓
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	1	1	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte Call QuadDet Per1 CP1 CheckpointReached	1	Rte Call QuadDet Per1 CP1 CheckpointReached	1	✓

Test Step 3.11 (Repeat Count = 1)			✓
Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	-1		
PrevCumMtrPos_Deg_M_f32	2		
PrevInstMtrDir_Cnt_M_s08	-1		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	2		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	2		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	1		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPc	s_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCme	dScl_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u	108	
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	1	1	~
PrevCumMtrPos_Deg_M_f32	2	2	✓
PrevInstMtrDir_Cnt_M_s08	-1	-1	✓
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	✓
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	2	2	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	~

Test Step 3.12 (Repeat Count = 1)		✓
Name	Input Value	
MtrTrqCmdSign_Cnt_D_s08	-1	
PrevCumMtrPos_Deg_M_f32	32	
PrevInstMtrDir_Cnt_M_s08	-1	
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet	
k_InstMtrDirHyst_Deg_f32	0	
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	20	

2016-01-10, 13:02:02+0530



Name	Input Value		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	-0.5		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f	32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08		
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	-1	-1	~
PrevCumMtrPos_Deg_M_f32	20	20	✓
PrevInstMtrDir_Cnt_M_s08	-1	-1	~
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	~
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	3	3	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	✓

Test Step 3.13 (Repeat Count = 1)			✓
Name	Input Value		
MtrTrqCmdSign_Cnt_D_s08	0		
PrevCumMtrPos_Deg_M_f32	14587.04		
PrevInstMtrDir_Cnt_M_s08	0		
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet		
k_InstMtrDirHyst_Deg_f32	4		
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	293		
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	-4.2		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08		
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumM	ltrPos_Deg_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTro	CmdScl_MtrNm_f32	
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_0	Cnt_u08	
Name	Actual Value	Expected Value	Result
MtrTrqCmdSign_Cnt_D_s08	-1	-1	~
PrevCumMtrPos_Deg_M_f32	293	293	~
PrevInstMtrDir_Cnt_M_s08	-1	-1	~
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	-1	-1	~
tgt_QuadDet_Per1_MtrQuad_Cnt_u08.value	3	3	✓

Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	~		
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	~		

Name	Input Value			
MtrTrqCmdSign_Cnt_D_s08	1			
PrevCumMtrPos_Deg_M_f32	5556.02			
PrevInstMtrDir_Cnt_M_s08	1			
Rte_Inst_Ap_QuadDet	tgt_Rte_Inst_Ap_QuadDet			
k_InstMtrDirHyst_Deg_f32	2			
tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32.value	34876.3			
tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32.value	-8.8			
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_InstMtrDir_Cnt_s08	tgt_QuadDet_Per1_InstMtrDir_Cnt_s08			
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFCumMtrPos_Deg_f32	tgt_QuadDet_Per1_MRFCumMtrPos_Deg_f32			
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32	tgt_QuadDet_Per1_MRFMtrTrqCmdScl_MtrNm_f32			
tgt_Rte_Inst_Ap_QuadDet.QuadDet_Per1_MtrQuad_Cnt_u08	tgt_QuadDet_Per1_MtrQuad_Cnt_u08			
Name	Actual Value	Expected Value	Result	
MtrTrqCmdSign_Cnt_D_s08	-1	-1	•	
PrevCumMtrPos_Deg_M_f32	34876.3008	34876.3	•	
PrevInstMtrDir_Cnt_M_s08	1	1	•	
tgt_QuadDet_Per1_InstMtrDir_Cnt_s08.value	1	1	•	
tgt QuadDet Per1 MtrQuad Cnt u08.value	4	4	•	

2016-01-10, 13:02:02+0530



Test Step Call Trace						
Actual Function	Count	Expected Function	Count	Result		
Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP0_CheckpointReached	1	•		
Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	Rte_Call_QuadDet_Per1_CP1_CheckpointReached	1	•		