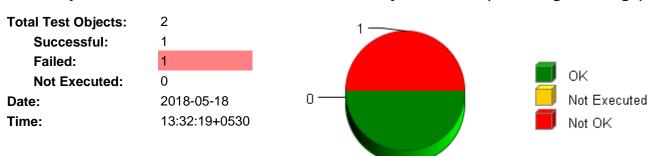


Summary

Overall Test Object Results (including Coverage)



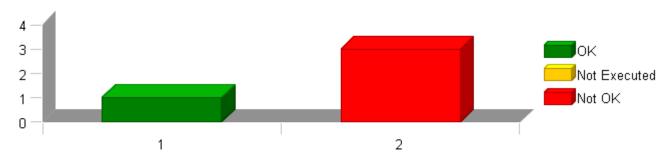
Selected Project Items

Test Object "CBD_Unittest/LoaMgr/LoaMgr_Init1" Test Object "CBD_Unittest/LoaMgr/LoaMgr_Per1"

Used Test Environments

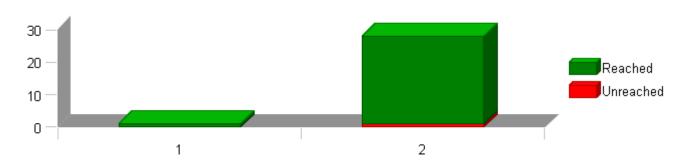
TI TMS 570 PLS UDE (Default)

Test Case Results for Each Test Object (without Coverage)



The table above shows each test object on the x axis and the number of test cases of the respective test object on the y axis. Each bar is divided into passed, not executed and failed test cases. The test case results do not take into account any coverage result (i.e. if all test cases of a test object are passed in this table but the coverage is failed, the overall test object result will be failed).

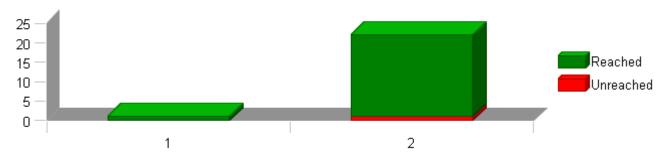
Statement (C0) Coverage: Total Statements for Each Test Object





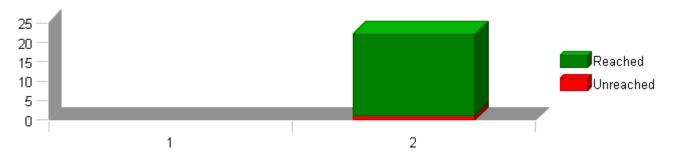
The table above shows each test object on the x axis and the number of statements of the respective test object on the y axis. Each bar is divided into reached statements (i.e. statements that have been executed during the test) and unreached statements.

Branch (C1) Coverage: Total Branches for Each Test Object



The table above shows each test object on the x axis and the number of branches of the respective test object on the y axis. Each bar is divided into reached branches (i.e. branches that have been executed during the test) and unreached branches.

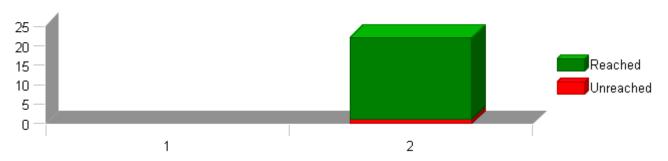
Decision Coverage: Total Decision Outcomes for Each Test Object



The table above shows test objects on the x axis and the number of possible outcomes of all decisions of the respective test object on the y axis. To achieve full DC coverage, each decision must evaluate to both true and false.

Each bar is divided into reached and unreached decision outcomes.

MC/DC Coverage: Total Condition Combinations for Each Test Object



The table above shows test objects on the x axis and the number of condition combinations of all decisions of the respective test object on the y axis. The number of condition combinations is based on the number of boolean conditions within each decision of the test object. To achieve full MC/DC coverage, each decision requires all contained atomic conditions to evaluate to both true and false independently of all other conditions. The cumulated number of rows within such tables of condition combinations is what is displayed in this table.

Each bar is divided into reached condition combinations (i.e. combinations of boolean condition values that have been executed during the test) and unreached condition combinations.

TEST OVERVIEW REPORT

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Test Object List

Project LoaMgr

The following table lists all test objects with their test case and coverage results. The cumulated results for modules, folders and test collections are also displayed, the indentation within the name column indicates the parent relationship of the elements.

Please note that only test objects are numbered within the first column. This number is referenced on the x axis within the overview charts for test case and coverage results available on previous pages (if included into the report).

No.	Name	C0	C1	DC	MC/DC	Test Cases	Result
	LoaMgr	96.55 %	95.65 %	95.45 %	95.45 %	3 of 4 failed	*
	CBD_Unittest	96.55 %	95.65 %	95.45 %	95.45 %	3 of 4 failed	*
	LoaMgr	96.55 %	95.65 %	95.45 %	95.45 %	3 of 4 failed	*
1	LoaMgr Init1	100 %	100 %	-	-	1 of 1 passed	•
2	<u>LoaMgr_Per1</u>	96.42 %	95.45 %	95.45 %	95.45 %	3 of 3 failed	*

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LoaMgr_Init1

 Project
 LoaMgr

 Module
 LoaMgr

 Test Object
 LoaMgr_Init1

Instrumentation: Test Object Only

Statement (C0) Coverage	100 %
Branch (C1) Coverage	100 %

Statistics

Total Testcases	1	
Successful	1	✓
Failed	0	
Not Executed	0	

Module Properties

Project Root Directory	D:\Synergy_Work_Area\LoaMgr_SF049A_4.3.0_0_NoUTP
Configuration File	D:\Synergy_Work_Area\LoaMgr_SF049A_4.3.0_0_NoUTP\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\LoaMgr\src\Ap_LoaMgr.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\LoaMgr\tools\contract -I\$(PROJECTROOT)\LoaMgr\tools\contract\Ap_LoaMgr -I\$ (PROJECTROOT)\StdDef\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(Compiler Install Path)\include

Comments/Description/Spe	ecification
Name	Text



Module 'LoaMgr'

Name of Tester:Pradnya Mhetre
Code File(s) Under Test:Ap_LoaMgr.c
Code File(s) Under Test:Ap_LoaMgr.c
Code File(s) Under Test:Ap_LoaMgr.c
Code File(s) Version:11
Module Design Version:9
Requirement Document Version:NA
Data Dictionary Version:8
Unit Test Plan Version:5
Optimization Level:Ogeneral
Compiler (CodeGen) Version:TMS470_4.9.5
Model Type:None
Model Version:Nexteer EPS Unit Test Tool 1.0
Total FLASH Used (Bytes):227
Total CALS Used (Bytes):272
Total CALS Used (Bytes):273
Total CALS Used (Bytes):76
Special Test Requirements:NA
Test Date:5/18/2018
Comments:
Note1:Inline functions defined in 'globalmacro.h' are not unit tested.
Note2:The Expected output calculated from MATLAB model.
Note3:Local functions ""Latchinputs(p", ""RqstRespConds(p", ""ArbResp()", ""AssignScale()"", ""RgstHwTqResp()", ""ChooseFinalResp()" and ""SwBasMtgtn()"" are not tested individually as they are tested in "LoaMgr Per1 Coverage" for the same.
Note4:In CSV_9 and CSV_10,value for boolean variable ""DiagcStsivtr1Inactv" given in "Simdata" is in float.
Hence input values for "DiagcStsIvtr1Inactv" are captured from Matlab design and given in Tessy for those test steps.
Note5: Out of range value is given to the variable ""HwTqldptSig_Cnt_u08=5"" over the DD range {0.4}, to cover the path at src line no.918 in TSS.1
Note6: Out of range value is given to the variable ""MtrPosldptSig_Cnt_u08=3"" over the DD range {0.2}, to cover the path at src line no.918 in TSS.2
Note7: Out of range value is given to the variable ""MtrPosldptSig_Cnt_u08=3"" over the DD range {0.2}, to cover the path at src line no.918 in TSS.2
Note8:Output variable "LoaMgr_AgSV_Cnt_M_Igc" is failing because of Source Model Mismatch at src line no.1503

Attributes				
Name	Value			
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5			
Float Precision	9			
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj			
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src			
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd			
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl			
Target Install Path	<pre>\$(ProgramFiles)\pls\UDE 4.4</pre>			
Time Unit	cycles			
Timer Enabled	false			
Timer Prescale	0			
Timer Resolution	1			
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg			
Workspace File	D:\Synergy_Work_Area\LoaMgr_SF049A_4.3.0_0_NOUTP\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP			

because of Source Model Mismatch at src line no.1503
Anomaly EA3#20083 is logged for the same.
Mote9:Parameter of "Rte_Call_NxtrDiagMgr_SetNTCStatus" "Param_Cnt_T_u08" is failing because of Source Model Mismatch.
Anomaly EA3#20083 is logged for the same.
Note10:Output variable "LoaMgr_VltgMdSrcSV_Cnt_M_u08" is going out of range as [0 3] over DD range [0 1] in PRM test vector TS4.103
Anomaly EA3#20083 is logged for the same.
Note11:""CBD_Sandbox_dbg.map"" map file is embedded for reference.



Test Case 1: PRM Range Test

Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles: Specification

TS1.1 2849.00 Cycles TS1.2 2849.00 Cycles

Description Vector Description:

TS1.1k_LoaMgr_SnsrlsMotAgAvail_Cnt_lgc=>Min/Default TS1.2k_LoaMgr_SnsrlsMotAgAvail_Cnt_lgc=>Max

Test Step 1.1 (Repeat Count = 1) ✓			
Name	Input Value		
k_LoaMgr_SnsrlsMotAgAvail_Cnt_lgc	0		
Name	Actual Value	Expected Value	Result
LoaMgr_HwTqldptSigSV_Cnt_M_u08	4	4	✓
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	2	✓
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3	3	✓
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2	2	✓
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0	0	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	6	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	6	~

Test Step 1.2 (Repeat Count = 1)			✓
Name	Input Value		
k_LoaMgr_SnsrlsMotAgAvail_Cnt_lgc	1		
Name	Actual Value	Expected Value	Result
LoaMgr_HwTqldptSigSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	2	✓
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3	3	~
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2	2	✓
LoaMgr SnsrlsMotAgAvail Cnt M lgc	1	1	✓

Test Step Call Trace				•
Actual Function	Count	Expected Function	Count	Result
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	6	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	6	~

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LoaMgr_Per1

Project	LoaMgr
Module	LoaMgr
Test Object	LoaMgr_Per1

Instrumentation: Test Object Only

Statement (C0) Coverage	96.42 %
Decision Coverage	95.45 %
Branch (C1) Coverage	95.45 %
MC/DC Coverage	95.45 %

Statistics

Total Testcases	3
Successful	0
Failed	3
Not Executed	0



Module Properties

Project Root Directory	D:\Synergy_Work_Area\LoaMgr_SF049A_4.3.0_0_NoUTP
Configuration File	D:\Synergy_Work_Area\LoaMgr_SF049A_4.3.0_0_NoUTP\UnitTestEnv\config \TMS570_GCC_UDE_CCS4_Config.xml
Target Environment	TI TMS 570 PLS UDE (Default)
Kind of Test	Unit Test
Linker Options	
Source File(s)	
File	\$(PROJECTROOT)\LoaMgr\src\Ap_LoaMgr.c
Compiler Options	-D_DATA_ACCESS= -Dconst= -I\$(PROJECTROOT)\LoaMgr\tools\contract -I\$(PROJECTROOT)\LoaMgr\tools\contract\Ap_LoaMgr -I\$ (PROJECTROOT)\StdDef\include -I\$(PROJECTROOT)\NxtrLib\include -I\$(Compiler Install Path)\include

lame	Text
lodule 'LoaMgr'	**************************************
-	Name of Tester:Pradnya Mhetre
	Code File(s) Under Test:Ap_LoaMgr.c
	Code File(s) Version:11
	Module Design Version:9
	Requirement Document Version:NA
	Data Dictionary Version:8
	Unit Test Plan Version:5 Optimization Level:Ogeneral
	Compiler (CodeGen) Version:TMS470 4.9.5
	Model Type:None
	Model Version: Nexteer EPS Unit Test Tool 1.0
	Total FLASH Used (Bytes):2244
	Total RAM Used (Bytes):27
	Total CALS Used (Bytes):76
	Special Test Requirements:NA
	Test Date:5/18/2018
	Comments:
	Note1:Inline functions defined in 'globalmacro.h' are not unit tested.
	Note2:The Expected output calculated from MATLAB model.
	Note3:Local functions ""LatchInputs()"", ""RqstRespConds()"", ""ArbResp()"", ""AssignScale()"", ""AssignScale()", ""Ass
	""RqstHwTqResp()"", ""ChooseFinalResp()"" and ""SwBasMtgtn()"" are not tested individually as they are tested in "LoaMqr Per1" function.
	as they are tested in Loading Fer in Indicati. Please refer snapshot "Loading Per 1 Coverage" for the same.
	Note4:In CSV 9 and CSV 10,value for boolean variable ""DiagcSts/tr1Inactv""
	given in "Simdata" is in float.
	Hence input values for "DiacSts/vtr1Inactv" are captured from Matlab design
	and given in Tessy for those test steps.
	Note5: Out of range value is given to the variable ""HwTqldptSig_Cnt_u08=5"" over the DD range {0,4},
	to cover the path at src line no.908 in TS5.1
	Note6: Out of range value is given to the variable ""MtrPosldptSig_Cnt_u08=4"" over the DD range {0,3},
	to cover the path at src line no.918 in TS5.2
	Note7: Out of range value is given to the variable ""MotCurrldptSig_Cnt_u08=3"" over the DD range {0,2},
	to cover the path at src line no.928 in TS5.3
	Note8:Output variable "LoaMgr_AgSV_Cnt_M_lgc" is failing because of Source Model Mismatch at src line no.1503
	Anomaly EA3#20083 is logged for the same.
	Mote9:Parameter of "Rite Call NxtrDiagMgr SetNTCStatus" "Param Cnt T u08" is failing
	because of Source Model Mismatch.
	Anomaly EA3#20083 is logged for the same.
	Note10:Output variable "LoaMgr_VitgMdSrcSV_Cnt_M_u08" is going out of range as [0 3]
	over DD range [0 1] in PRM test vector TS4.103
	Anomaly EA3#20083 is logged for the same.
	Note11:""CBD Sandbox dbg.map"" map file is embedded for reference.

Attributes	
Name	Value
Compiler Install Path	\$(ProgramFiles)\Texas Instruments\ccsv4\tools\compiler\tms470_4.9.5
Float Precision	9
InitObjDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\obj
InitSrcDir	\$(PROJECTROOT)\UnitTestEnv\static_build_files\src
Linker File	\$(PROJECTROOT)\UnitTestEnv\static_build_files\sys_link.cmd
Makefile Template	\$(PROJECTROOT)\UnitTestEnv\config\Nexteer_ts_make_ude_ti_tms570_ps.tpl
Target Install Path	\$(ProgramFiles)\pls\UDE 4.4
Time Unit	cycles
Timer Enabled	false
Timer Prescale	0
Timer Resolution	1
UDE Config File	\$(PROJECTROOT)\UnitTestEnv\config\TMS570_UDE_12PIN_JTAG.cfg
Workspace File	D:\Synergy_Work_Area\LoaMgr_SF049A_4.3.0_0_NoUTP\UnitTestEnv\config\UDE_TMS570_DEBUG.WSP



```
Test Case 1: Metric Test
Specification
                                                                               Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:
                                                                               TS1.1 2849.00 Cycles
TS1.2 5486.00 Cycles
Description
                                                                              Vector Description:
                                                                        Vector Description:

TS1.1Shortest Execution Path

if(LoaMgr_LOASt_State_M_enum == LOAST_RPDSHTDWNREQD)=>T

if(LoaMgr_LOASt_State_M_enum == LOAST_RPDSHTDWNREQD)=>T

if(LoaMgr_LOASt_State_M_enum == LOAST_SPDSHTDWNREQD)=>T

(LoaMgr_LOASt_State_M_enum == LOAST_SWMTGTN_CNT_U08)=>F||(LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc == TRUE)=>F)

if((MtvTqResp_Cnt_T_u08 == D_LOASTSWMTGTN_CNT_U08)=>F||(LoaMgr_MotAqLoaMtgtnEn_Cnt_M_lgc == TRUE)=>F)

if((MtvTqResp_Cnt_T_u08 == D_LOASTSWMTGTN_CNT_U08)=>F||(LoaMgr_MotAqLoaMtgtnEn_Cnt_M_lgc == TRUE)=>F)

if((NtvTqSp_Cnt_T_u08 == D_LOASTSWMTGTN_CNT_U08)=>F||(LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc == TRUE)=>F)

if((NtpTqSp_Cnt_T_u08 == D_LOASTSWMTGTN_CNT_U08)=>F||(LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc == TRUE)=>F)

if((LoaMgr_MotAqLoaMtgtnEn_Cnt_M_lgc == TRUE)=>F)

if((MtgtnCount_Cnt_T_u08 == D_LOASTSWMTGTN_CNT_U08)=>F||(LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc == TRUE)=>F)

if((MtgtnCount_Cnt_T_u08 == D_LOASTSWMTGTN_CNT_U08)=>F

else if(LoaMgr_LOASt_State_M_enum == LOAST_RORM)=>F

else if(LoaMgr_LOASt_State_M_enum == LOAST_REDCD)=>F

else if(LoaMgr_LOASt_State_M_enum == LOAST_REDCD)=>F

else if(LoaMgr_LOASt_State_M_enum == LOAST_CTRLDSHTDWNREQD)=>F

if(Ivtrlnactv_Cnt_T_logl == TRUE)=>F

Max_m(MultiMtgtnResp_Cnt_T_u08_Max_m(MtvTqResp_Cnt_T_u08, Max_m(MtvTqSp_Cnt_T_u08, MtvTqSp_Cnt_T_u08, Max_m(MtvTqSp_Cnt_T_u08, MtvTqSp_Cnt_T_u08, MtvTqSp_Cnt_T
                                                                              TS1.1Shortest Execution Path
                                                                              TS1.2 Largest Execution Path
                                                                         if(LoaMgr_LOASt_State_M_enum == LOAST_RPDSHTDWNREQD)>F
```

Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_lvtrRespSV_Cnt_M_u08	1	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	

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Name	Input Value		
LoaMgr_MotAgRespSV_Cnt_M_u08	5		
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1		
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0		
LoaMgr_MotCurrRespSV_Cnt_M_u08	3		
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0		
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0		
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr		
k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	0 1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0 0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08 k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	1 2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DlagcStsivtr2inactv_Cnt_gc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_nw1qluptsig_Cnt_uoo.value	1		
target LoaMgr Per1 MotCurrldptSig Cnt u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cn	ıt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	lee.	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igraget_LoaMgr_Per1_LOASt_State_enum		
target_Rie_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt	: Igc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	-

Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	✓
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	✓
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	2	✓
LoaMgr lvtrLoaMtgtnEn Cnt M lgc	0	0	✓

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LoaMgr_Per1

Name	Actual Value	Expected Value	Result
LoaMgr lvtrRespSV Cnt M u08	1	1	~
LoaMgr LOASt State M enum	5	5	✓
LoaMgr LoaRateLimit UlspS M f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	✓
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	•
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170}	{170}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1}	{1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	✓
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	✓
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	V

Test Step 1.2 (Repeat Count = 1)	13
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	0
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1





Name	Input Value		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgMgtbSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32 k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	0.123400003		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1 target LeaMar Port DiagoStelytr1Ingety (Ont Igo	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0 target_LoaMgr_Per1_DiagcStslvtr2Inactv_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_BlagcotsWt2Inactv_Ont_igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IntriLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target LoaMgr Per1 LoaScaDi Cnt Igc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_	u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	*
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	*
LoaMgr_LOASt_State_M_enum LoaMgr LoaRateLimit UlspS M f32	1	1	
LoaMgr_LoaScaleFctr_Uls_M_f32	0.123400003	0.123400003	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	1	1	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	1	1	-
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{173, 174}	{173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	1	1 ± 0.01	*
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0.123400003	0.123400003 ± 0.001	

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Name	Actual Value	Expected Value	Result
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	1	1	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	✓
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Case 2: Range Test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles:

TS2.1 5287.00 Cycles
TS2.2 5486.00 Cycles
TS2.2 5486.00 Cycles
TS2.3 5054.00 Cycles
TS2.3 5054.00 Cycles
TS2.4 5021.00 Cycles
TS2.5 5005.00 Cycles
TS2.6 5005.00 Cycles
TS2.7 5005.00 Cycles
TS2.9 5005.00 Cycles
TS2.9 5005.00 Cycles
TS2.10 5005.00 Cycles
TS2.11 5005.00 Cycles
TS2.12 5005.00 Cycles
TS2.13 5005.00 Cycles
TS2.14 5005.00 Cycles
TS2.15 5005.00 Cycles
TS2.15 5005.00 Cycles
TS2.16 5005.00 Cycles
TS2.17 5005.00 Cycles
TS2.18 5005.00 Cycles
TS2.19 5005.00 Cycles
TS2.19 5005.00 Cycles
TS2.19 5005.00 Cycles
TS2.20 5005.00 Cycles
TS2.22 5005.00 Cycles
TS2.22 5005.00 Cycles
TS2.22 5005.00 Cycles
TS2.23 5005.00 Cycles TS2.23 5005.00 Cycles TS2.24 5005.00 Cycles

Description

Vector Description:

TS2.1All Min

TS2.2Ali Mini
TS2.2Ali Max
TS2.3DiagcStslvtr1Inactv_Cnt_lgc==>Min
TS2.4DiagcStslvtr1Inactv_Cnt_lgc==>Min
TS2.5DiagcStslvtr2Inactv_Cnt_lgc==>Min
TS2.6DiagcStslvtr2Inactv_Cnt_lgc==>Min
TS2.6DiagcStslvtr2Inactv_Cnt_lgc==>Max
TS2.7HwTqldptSig_Cnt_u08==>Max
TS2.7HwTqldptSig_Cnt_u08==>Max
TS2.9HwTqldptSig_Cnt_u08==>Min
TS2.10MotCurrldptSig_Cnt_u08==>Min
TS2.11MotCurrldptSig_Cnt_u08==>Max
TS2.12MotCurrldptSig_Cnt_u08==>Max
TS2.13MtrPosldptSig_Cnt_u08==>Min
TS2.14MtrPosldptSig_Cnt_u08==>Max
TS2.15MtrPosldptSig_Cnt_u08==>Pos
TS2.16TloaAvail_Cnt_lgc==>Min
TS2.16TloaAvail_Cnt_lgc==>Max TS2.2All Max TS2.17TloaAvail_Cnt_lgc==>Max TS2.17TloaAvall_Cnt_lgc=>Max
TS2.18TloaDi_Cnt_lgc=>Min
TS2.19TloaDi_Cnt_lgc=>Max
TS2.20LoaScaDi_Cnt_lgc=>Min
TS2.21LoaScaDi_Cnt_lgc=>Max
TS2.22SteerMod_Cnt_enum==>STEERMOD_BASEPS
TS2.23SteerMod_Cnt_enum==>STEERMOD_SEMIATNMS
TS2.24SteerMod_Cnt_enum==>STEERMOD_FULLYATNMS

Test Step 2.1 (Repeat Count = 1) Input Value Name LoaMgr_AgSV_Cnt_M_lgc 0 $LoaMgr_HwTqIdptSigSV_Cnt_M_u08$ 0 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc 0 LoaMgr_HwTqRespSV_Cnt_M_u08 0





Name LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrResptSV_Cnt_M_u08 LoaMgr_MotCurrResptSV_Cnt_M_u08	Input Value 0 0 0 0 0 0
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	0 0 0 0
LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	0 0 0
LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	0 0
LoaMgr_MotAgLoaMtgtnEn_cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
	0
LooMar SportoMotAgAyoil Cot M Inc	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	0.0099999978
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	0
k_LoaMgr_FadeOutStRate_UlspS_f32	0.0099999978
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	0
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	0.0099999978
k_LoaMgr_HwTqMtgtnSca_UIs_f32	0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	0.0099999978
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	0
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	0
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	0.0099999978
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	0
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	0.00999999978
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	0
k_LoaMgr_RedcdStRate_UlspS_f32	0.0099999978
k_LoaMgr_RedcdStSca_Uls_f32	0
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc.value	0
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqloptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LoASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc
Name LoaMgr_AgSV_Cnt_M_lgc	Actual Value Expected Value Re

LoaMgr_Per1





Name	Actual Value	Expected Value	Result
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	✓
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	✓
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	✓
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	✓

Test Step Call Trace				~
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	✓
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte Exit An LoaMor FalloaScaleAndRate	1	Rte Exit An LoaMor FalloaScaleAndRate	1	V

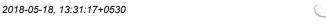
Test Step 2.2 (Repeat Count = 1)		×
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	4	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	5	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotCurrRespSV_Cnt_M_u08	5	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	500	
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	1	
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1	
k_LoaMgr_FadeOutStRate_UlspS_f32	500	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	5	
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	5	
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	5	

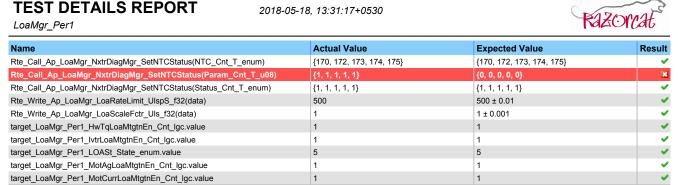




Name	Input Value		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	5		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	5		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	5		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	4		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	500		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	5		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	5		
k LoaMgr lvtrldptSigFltThd Cnt u08	2		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	500		
k LoaMgr lvtrMtgtnScaZeroEn Cnt lgc	1		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	5		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	3		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	500		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	5		
	5		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k LoaMgr MotCurrldptSig1Resp Cnt u08	5		
k LoaMgr MotCurrldptSig2Resp Cnt u08	5		
	2		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	500		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc			
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	500		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u(08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_	_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u	108	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enun	1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	1	1	~

target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	4	4	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3	3	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	5	5	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~





Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	-
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.3 (Repeat Count = 1)		Ε
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc	0	
LoaMgr_IvtrRespSV_Cnt_M_u08	0	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	0	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0	
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1	
k_LoaMgr_FadeOutStRate_UlspS_f32	1	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2	
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4	
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2	
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1	
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0	
k LoaMgr HwTqldptSig4Resp Cnt u08	0	
k LoaMgr HwTqldptSigFltThd Cnt u08	1	
k LoaMgr HwTqMtgtnRate UlspS f32	1	
k LoaMgr HwTqMtgtnSca Uls f32	1	
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0	
k LoaMgr lvtrldptSig2Resp Cnt u08	0	
k LoaMgr lvtrldptSigFltThd Cnt u08	0	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1	
k LoaMgr lvtrMtgtnScaZeroEn Cnt lgc	0	
k LoaMgr lvtrMtgtnSca Uls f32	1	
k LoaMgr MotAgldptSig1NoSmaResp Cnt u08	5	
k LoaMgr MotAgldptSig2Resp Cnt u08	1	
k LoaMgr MotAgldptSig3Resp Cnt u08	0	
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2	
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1	
k LoaMgr MotAgMtgtnScaZeroEn Cnt Igc	0	
k LoaMgr MotAgMtgtnSca UIs f32	1	





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1 1 0	1 1 0	· ·
1 1 0 {170, 172}	1 1 0 {170, 172}	*
1 1 0 {170, 172} {1, 1}	1 1 0 {170, 172} {0, 0, 0, 0, 0}	\rightarrow \frac{1}{2}
1 1 0 {170, 172} {1, 1} {1, 1}	1 1 0 {170, 172} {0, 0, 0, 0, 0} {1, 1}	y y y
1 1 0 {170, 172} {1, 1} {1, 1} 500	1 1 0 {170, 172} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01	**************************************
1 1 0 {170, 172} {1, 1} {1, 1} 500	1 1 0 {170, 172} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001	**************************************
1 1 0 {170, 172} {1, 1} {1, 1} 500 1	1 1 0 {170, 172} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0	
1 1 0 {170, 172} {1, 1} {1, 1} 500 1 0	1 1 0 {170, 172} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0	**************************************
1 1 0 {170, 172} {1, 1} {1, 1} 500 1	1 1 0 {170, 172} {0, 0, 0, 0, 0} {1, 1} 500 ± 0.01 1 ± 0.001 0	**************************************
	2 2 0 0 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1	2 2 0 0 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.4 (Repeat Count = 1)	
	3
Name Input Value	
LoaMgr_AgSV_Cnt_M_lgc *none*	
LoaMgr_HwTqldptSigSV_Cnt_M_u08 *none*	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc *none*	
LoaMgr_HwTqRespSV_Cnt_M_u08 *none*	
LoaMgr_lvtrldptSigSV_Cnt_M_u08 *none*	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc *none*	
LoaMgr_lvtrRespSV_Cnt_M_u08 *none*	
LoaMgr_MotAgldptSigSV_Cnt_M_u08 *none*	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc *none*	
LoaMgr_MotAgRespSV_Cnt_M_u08 *none*	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08 *none*	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc *none*	
LoaMgr_MotCurrRespSV_Cnt_M_u08 *none*	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc *none*	
LoaMgr_VltgMdSrcSV_Cnt_M_u08 *none*	
Rte_Inst_Ap_LoaMgr target_Rte_Inst	_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32 1	
k LoaMgr CurrlytrMtgtnScaZeroEn Cnt lgc 0	
k LoaMgr CurrlvtrMtgtnSca Uls f32	
k_LoaMgr_FadeOutStRate_UlspS_f32	
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08 2	
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 4	
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 2	
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08 0	
k LoaMgr HwTgldptSig4Resp Cnt u08 0	
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
k_LoaMgr_HwTqMtgtnRate_UlspS_f32 1	
k_LoaMgr_HwTqMtgtnSca_Uls_f32	
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08 0	
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08 0	
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08 0	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc 0	
k_LoaMgr_lvtrMtgtnSca_Uls_f32	
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 5	
1	
_ 0_ 0 1 0 1	
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 0 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 2	
_ 0_ 0 0 _ 1 1 _	
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_Igc 0 k_LoaMgr_MotAgMtgtnSca_Uls_f32 1	
2 11 32 11 3 101 11121	
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 2	
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 2	
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08 0	
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08 0	
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08 0	
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc 1	
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	
k_LoaMgr_RedcdStRate_UlspS_f32 1	
k_LoaMgr_RedcdStSca_Uls_f32	
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value 1	





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Ci	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	- 100011
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	-
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	-
LoaMgr MotCurrRespSV Cnt M u08	2	2	
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	
LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174, 175}	{170, 172, 173, 174, 175}	
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC_CII_1_endin) Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(Param Cnt T u08)	{1, 1, 1, 1, 1}	{0, 0, 0, 0, 0}	X
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1, 1} {1, 1, 1, 1, 1}	{1, 1, 1, 1, 1}	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	
	5	5	
target_LoaMgr_Per1_LOASt_State_enum.value	0	0	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	*

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	✓
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	5	~
AssignScale	1	AssignScale	1	✓
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 2.5 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV Cnt M u08	*none* *none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4 2
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k LoaMgr HwTgldptSigFltThd Cnt u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1 0
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k LoaMgr MotAgMtgtnRate UlspS f32	1
k LoaMgr MotAgMtgtnScaZeroEn Cnt Igc	0
k LoaMgr MotAgMtgtnSca Uls f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_UIs_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Crit_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
0	, , , , , , , , , , , , , , , , , , , ,





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172}	{170, 172}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	-
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.6 (Repeat Count = 1)		×
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	*none*	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*	
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*	
LoaMgr_IvtrRespSV_Cnt_M_u08	*none*	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*	
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*	
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

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Input Value



Name



k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
	2		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
	0		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08			
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
larger coalvigr Peri Diagcossivir imaciv uni inc vaine	U		
	a		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	2		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1 1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	2 1 1 1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2 1 1 1 1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2 1 1 1 1 0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2 1 1 1 1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	2 1 1 1 1 0	nt_lgc	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TioaAvail_Cnt_Igc.value target_LoaMgr_Per1_TioaDi_Cnt_Igc.value	2 1 1 1 1 0 0		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_Igc.value target_LoaMgr_Per1_TloaDi_Cnt_Igc.value target_LoaMgr_Per1_TloaDi_Cnt_Igc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	2 1 1 1 1 0 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C	nt_lgc	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_Igc.value target_LoaMgr_Per1_TloaDi_Cnt_Igc.value target_LoaMgr_Per1_TloaDi_Cnt_Igc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	2 1 1 1 1 0 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Ctarget_LoaMgr_Per1_DiagcStsIvtr2Inactv_Ctarget_DiagcStsIvtr2Inactv_Ctarget_DiagcStsIvtr2Inactv_Ctarget_DiagcStsI	nt_lgc 3	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_Igc.value target_LoaMgr_Per1_TloaDi_Cnt_Igc.value target_LoaMgr_Per1_TloaDi_Cnt_Igc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqIdptSig_Cnt_u08	2 1 1 1 1 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ctarget_LoaMgr_Per1_DiagcStslvtr2Inactv_Ctarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08	nt_lgc 3 t_lgc	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_Igc.value target_LoaMgr_Per1_TloaDi_Cnt_Igc.value target_LoaMgr_Per1_TloaDi_Cnt_Igc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	2 1 1 1 1 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ctarget_LoaMgr_Per1_DiagcStslvtr2Inactv_Ctarget_LoaMgr_Per1_HwTqldptSig_Cnt_u0⌖_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	nt_lgc 3 t_lgc	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_Igc.value target_LoaMgr_Per1_TloaDi_Cnt_Igc.value target_LoaMgr_Per1_TloaDi_Cnt_Igc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	2 1 1 1 1 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lq target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lq	nt_lgc 3 t_lgc	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_Igc.value target_LoaMgr_Per1_TloaDi_Cnt_Igc.value target_LoaMgr_Per1_TloaDi_Cnt_Igc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	2 1 1 1 1 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lq target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lq target_LoaMgr_Per1_loASt_State_enum	nt_lgc 3 t_lgc gc	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_Igc.value target_LoaMgr_Per1_TloaDi_Cnt_Igc.value target_LoaMgr_Per1_TloaDi_Cnt_Igc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	2 1 1 1 1 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_letarget_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_letarget_LoaMgr_Per1_loASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	nt_igc 3 t_igc gc nt_igc	
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target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotOurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08	2 1 1 1 1 1 0 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgtarget_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 0 4 0 0 5	nt_lgc t_lgc t_lgc close Expected Value 0 0 0 4 0 0 5	***
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target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotOurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08	2 1 1 1 1 1 0 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgtarget_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 0 4 0 0 5	nt_lgc t_lgc t_lgc close Expected Value 0 0 0 4 0 0 5	***
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_log LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	2 1 1 1 1 1 0 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u0⌖_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_ugt_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoASt_Diagc_Cnt_u0⌖_LoaMgr_Per1_MotCurrloptSig_Cnt_u0⌖_LoaMgr_Per1_MotCurrloaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0⌖_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0⌖_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 0 0 1 4 0 0 5 5 5	nt_lgc 3 t_lgc gc nt_lgc nt_lgc 08 Expected Value 0 0 0 4 0 0 5 5	***
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TioaAvail_Cnt_lgc.value target_LoaMgr_Per1_TioaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotOurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotOurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotOurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	2 1 1 1 1 1 0 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u0⌖_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_loatraget_LoaMgr_Per1_IoaMtgtnEn_Cnt_loatraget_LoaMgr_Per1_LoaScaDi_Cnt_logctarget_LoaMgr_Per1_MotCurrloptSig_Cnt_u0⌖_LoaMgr_Per1_MotCurrloaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0⌖_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0⌖_LoaMgr_Per1_TloaAvail_Cnt_logctarget_LoaMgr_Per1_TloaDi_Cnt_logctarget_LoaMgr_Per1_TloaDi_Cnt_logctarget_LoaMgr_Per1_TloaDi_Cnt_logctarget_LoaMgr_Per1_TloaDi_Cnt_logctarget_LoaMgr_Per1_TloaDi_Cnt_logctarget_LoaMgr_Per1_TloaDi_Cnt_logctarget_LoaMgr_Per1_TloaDi_Cnt_logctarget_LoaMgr_Per1_TloaDi_Cnt_logctarget_DoaMgr_Per1_TloaDi_Cnt_DoaMgr_Per1_TloaDi_Cnt_DoaMgr_Per	nt_lgc 3 t_lgc gc nt_lgc nt_lgc 08 Expected Value 0 0 0 4 0 0 5 5 5	***
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrlogMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqlptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaRtelmit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	2 1 1 1 1 1 0 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_IncaMtgtnEn_Cnt_target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_MotAgLoaMtgtnEn_Crt_target_LoaMgr_Per1_MotCurrlotptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrloaMtgtnEn_Crt_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Crt_target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_LoaMgr_Per1_TioaAvail_Cnt_lgc target_LoaMgr_Per1_TioaDi_Cnt_lgc Actual Value 0 0 0 5 5 5 500 1	nt_lgc 3 t_lgc gc nt_lgc gc nt_lgc 08 Cnt_lgc 08 Expected Value 0 0 0 0 4 0 0 5 5 5 500 1	***************************************
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Ret_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaNtgtnEn_Cnt_M_lgc LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	2 1 1 1 1 1 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotQLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cr target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TioaAvail_Cnt_lgc target_LoaMgr_Per1_TioaDi_Cnt_lgc Actual Value 0 0 0 5 5 5 500 1 5	nt_lgc st_lgc t_lgc gc nt_lgc nt_	***************************************

LoaMgr_MotAgRespSV_Cnt_M_u08

2018-05-18, 13:31:17+0530

LoaMgr_Per1



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	✓
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.7 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqIdptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lo	gc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	- 100 U.I.
LoaMgr HwTqldptSigSV Cnt M u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr HwTqRespSV Cnt M u08	4	4	-
· · · ·		0	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0		
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc		0	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	_
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	✓
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	-
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	
target_LoaMgr_Per1_LOASt_State_enum.value target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	
	14	10	· •
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	•
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.8 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u	08	
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr HwTqldptSigSV Cnt M u08	0	0	_
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	_
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	✓
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	_
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	✓
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	_
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	_
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	-
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 173}	{170, 173}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	-
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	-
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~
v = =v			

Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
CalcivtrState	2	CalclvtrState	2	~	
LatchInputs	1	LatchInputs	1	~	
RqstRespConds	1	RqstRespConds	1	~	
RqstHwTqResp	1	RqstHwTqResp	1	•	
ArbResp	1	ArbResp	1	•	
ChooseFinalResp	1	ChooseFinalResp	1	•	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~	
AssignScale	1	AssignScale	1	~	
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~	
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~	



Test Step 2.9 (Repeat Count = 1)	
Name	Input Value
LoaMgr AgSV Cnt M Igc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32 k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	1 0
k LoaMgr CurrivtrMtgtnSca Uls f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k LoaMgr HwTqldptSig1NoTloaResp Cnt u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0 1
k_LoaMgr_lvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k LoaMgr MotAgMtgtnRate UlspS f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1.
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedodStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Ont_Igc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMigtnEn_Cnt_igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
goopcosmgcosmgi_r or i_motouricosmginicii_oni_igo	



LoaMgr_Per1

Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 174}	{170, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.10 (Repeat Count = 1)		2
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	*none*	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*	
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*	
LoaMgr_IvtrRespSV_Cnt_M_u08	*none*	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*	
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*	
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

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Input Value



Name



Name	Input value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k LoaMgr FadeOutStRate UlspS f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
	0		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08			
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32			
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target LoaMgr Per1 DiagcStslvtr1Inactv Ci	nt Igc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Ci		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqldptSig Cnt u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt		
	target LoaMgr Per1 lvtrLoaMtgtnEn Cnt lg		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	' - ' - ' '	lc .	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Der1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
	0	0	Jourt
LoaMgr_AgSV_Cnt_M_lgc	0	0	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08			
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	-
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
	5	5	~
LoaMgr_lvtrRespSV_Cnt_M_u08		5	✓
LoaMgr_LOASt_State_M_enum	5		
	5 500	500	~
LoaMgr_LOASt_State_M_enum			~
LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	500 0	500 0	~
LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	500 0 5	500 0 5	V





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	✓
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 174}	{170, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt lgc.value	1	1	✓

Test Step Call Trace	Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result	
CalclvtrState	2	CalcivtrState	2	~	
LatchInputs	1	LatchInputs	1	~	
RqstRespConds	1	RqstRespConds	1	~	
RqstHwTqResp	1	RqstHwTqResp	1	~	
ArbResp	1	ArbResp	1	~	
ChooseFinalResp	1	ChooseFinalResp	1	✓	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~	
AssignScale	1	AssignScale	1	~	
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~	
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~	

Test Step 2.11 (Repeat Count = 1)	13
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_IvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





N.	1		
Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2		
	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_loaning	дс	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr HwTqldptSigSV Cnt M u08	0	0	·
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr HwTqRespSV Cnt M u08	0	0	~
· · · ·			
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc		0	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	-
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	
LAIGUE LUAINUI FEIT LUAGE GIAITE CHUIT.VAIUE	5	J	
	0	0	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	V



Test Step Call Trace ✓				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.12 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_IvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	1





Image Loadley Field Degicial Selection Degi	Name	Input Value		
Barget_Loaking_Pert_Micromisplase_Cort_Micromispl	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
Barget_Loading_Pert_Tisenhot_Cf_Cn_cnum value 1	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
strage L. Loaking Pert Tokevala C (Toll govalue 1 1 1 1 1 1 1 1 1	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1		
	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
strayet_LoaMyr_Pert_Traba[/_Pert_Traba[/_Pert_Desposits/rt/nactv_Crt_lgc target_LoaMyr_Pert_Desposits/rt/nactv_Crt_lgc target_LoaMyr_Pert_Desposits/rt/nactv_Crt_lgc target_LoaMyr_Pert_Desposits/rt/nactv_Crt_lgc target_LoaMyr_Pert_Desposits/rt/nactv_Crt_lgc target_LoaMyr_Pert_Desposits/rt/nactv_Crt_lgc target_LoaMyr_Pert_Desposits/rt/nactv_Crt_lgc target_LoaMyr_Pert_Desposits/rt/nactv_Crt_lgc target_LoaMyr_Pert_Desposits/rt/nactv_Crt_lgc target_LoaMyr_Pert_Desposits/rt/nactv_Crt_lgc target_LoaMyr_Pert_Desposits_Crt_lgc target_LoaMyr_Pe	target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
Integral Ris., Int. Ap. Looking Looking Pert Disgositativi (Tinace), Crit. Local Integral Continger Looking Looking Looking Pert Looking Statistical Looking Looking Pert Having Statistical Looking Looking Pert Having Looking Pert Pert	target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
Isrget_Ris_Inst_Ap_LoaMyr_LoaMyr_Pert_DispoStsMvtZinacvt_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_	Cnt_lgc	
larget_Ris_Inst_Ap_LoaMgrLoaMgr_Pert_HVTqLoaMignEn_Cnt_igc target_Ris_Inst_Ap_LoaMgrLoaMgr_Pert_LOAS_State_num target_Ris_Inst_Ap_LoaMgrLoaMgr_Pert_LOAS_State_num target_Ris_Inst_Ap_LoaMgrLoaMgr_Pert_LOAS_State_num target_Ris_Inst_Ap_LoaMgrLoaMgr_Pert_LOAS_State_num target_Ris_Inst_Ap_LoaMgrLoaMgr_Pert_MolCurridoptis_Cnt_igc target_Ris_Inst_Ap_LoaMgrLoaMgr_Pert_MolCurridoptis_Cnt_igc target_Ris_Inst_Ap_LoaMgrLoaMgr_Pert_MolCurridoptis_Cnt_iu8 target_Ris_Inst_Ap_LoaMgrLoaMgr_Pert_MolCurridoptis_Cnt_iu8 target_Ris_Inst_Ap_LoaMgr_LoaMgr_Pert_MolCurridoptis_Cnt_iu8 target_Ris_Inst_Ap_LoaMgr_LoaMgr_Pert_MolCurridoptis_Cnt_iu8 target_LoaMgr_Pert_MolCurridoptis_Cnt_iu8 target_Ris_Inst_Ap_LoaMgr_LoaMgr_Pert_MolCurridoptis_Cnt_iu8 target_Ris_Inst_Ap_LoaMgr_LoaMgr_Pert_Ris_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaD_Cnt_iu8 target_Ris_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaD_Cnt_iu8 target_Ris_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaD_Cnt_iu8 target_Ris_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaD_Cnt_iu8 target_LoaMgr_Pert_NoaMgr_NoaMgr_NoaMgr_NoaMgr_Pert_NoaMgr	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_	Cnt_lgc	
larget_Ris_Inst_Ap_Loaklgr_Loaklgr_Pert_LOAS_State_enum target_Ris_Inst_Ap_Loaklgr_Loaklgr_Pert_LOAS_State_enum target_Ris_Inst_Ap_Loaklgr_Loaklgr_Pert_LOAS_State_enum target_Loaklgr_Pert_LOAS_State_enum target_Ris_Inst_Ap_Loaklgr_Loaklgr_Pert_Loaklgr_Per	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	08	
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	lgc	
		target LoaMgr Per1 LOASt State enum	•	
target_Rie_Inst_Ap_LoaMgr_LoaMgr_Pert_MolCourloip(Sig_Cnt_Lu08 target_LoaMgr_Pert_MolCourloip(Sig_Cnt_Lu08 target_LoaMgr_Pert_ToaDourloip(Sig_Cnt_Lu08 t				
larget_Rie_Inst_Ap_LoaMgr_LoaMgr_Pert_MolCurrLoaMignEn_Cnt_Ugs larget_LoaMgr_Pert_MolCurrLoaMignEn_Cnt_Ugs larget_Rie_Inst_Ap_LoaMgr_LoaMgr_Pert_MolCurrLoaMignEn_Cnt_Ugs larget_LoaMgr_Pert_MolCurrLoaMignEn_Cnt_Ugs larget_LoaMgr_Pert_MolCurrLoaMignEn_Cnt_Ugs larget_Rie_Inst_Ap_LoaMgr_Dert_SteerMod_Cnt_enum larget_Rie_Inst_Ap_LoaMgr_Pert_MolCurrLoaMgr_Pert_Mol			Ont lgc	
larget_Rie_Inst_Ap_LoaMgr_LoaMgr_Per1_MirrOsitghtSig_Cnt_U08 target_LoaMgr_Per1_MirrOsitghtSig_Cnt_U08 target_Rie_Inst_Ap_LoaMgr_LoaMgr_Per1_MirrOsitghtSig_Cnt_U08 target_LoaMgr_Per1_SteeMod_Cnt_enum target_Rie_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_LoaMgr_Per1_SteeMod_Cnt_enum target_Rie_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_LoaMgr_Per1_TloaAvail_Cnt_Igc target_LoaMgr_Per1_TloaAvail_Cnt_Igc target_LoaMgr_Per1_TloaAvail_Cnt_Igc				
larget_Rie_Inst_Ap_LoaMgr_LoaMgr_Pert_MirrPosiopISig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_SteerMod_Cnt_enum target_LoaMgr_Pert_ToaMavail_Cnt_Ige target_LoaMgr_Pert_ToaMavail_Cnt_Ige target_LoaMgr_Pert_ToaMavail_Cnt_Ige target_LoaMgr_Pert_ToaMavail_Cnt_Ige target_LoaMgr_Pert_ToaMavail_Cnt_Ige target_LoaMgr_Pert_ToaMavail_Cnt_Ige target_LoaMgr_Pert_ToaMavail_Cnt_Ige target_LoaMgr_Pert_ToaMgr_Pert_				
target Re inst Ap LoaMgr LoaMgr Pert SteerMod Cnt enum target LoaMgr Pert TloaAvail Cnt go target LoaMgr Pert TloaAvail Cnt go target LoaMgr Ap LoaMgr LoaMgr Pert TloaAvail Cnt go target LoaMgr Pert TloaAvail Cnt go target LoaMgr Pert TloaAvail Cnt go Go Cnt Go Go Go Go Go Go Go G				
target_Re_inst_Ap_LoaMgr_LoaMgr_Pert_TloaAvail_Cnt_lgc target_LoaMgr_Pert_TloaDi_Cnt_lgc target_LoaMgr_Pert_TloaDi_Cnt_lgc target_LoaMgr_Pert_TloaDi_Cnt_lgc target_LoaMgr_Pert_TloaDi_Cnt_lgc target_LoaMgr_Pert_TloaDi_Cnt_lgc Result Call Result				
target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1_TioaDi_Cnt_lgc				
Name				
LoaMgr_HwTqlqbfSigSV_Cnt_M_u08			Expected Value	Result
LoaMgr HwTqlcaNtglnEn_Cnt_M_lgc				
LoaMgr_HwTqLoaMtghEn_Cnt_M_lgc		•		
LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvfrldplSigSV_Cnt_M_u08 LoaMgr_IvfrldplSigSV_Cnt_M_u08 LoaMgr_LoaNdtghten_Cnt_M_u08 LoaMgr_LoaSt_State_M_enum 5 LoaMgr_LoaSt_State_M_enum 5 LoaMgr_LoaSt_State_M_enum 5 LoaMgr_LoaSt_SeleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMighrResp_Cnt_M_u08 5 LoaMgr_MaxMighrResp_Cnt_M_u08 LoaMgr_MotAgloptsigSV_Cnt_M_u08 LoaMgr_MotAgloptsigSV_Cnt_M_u08 LoaMgr_MotAgloaMighten_Cnt_M_ulge LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrlcaMgmEn_Cnt_M_u08 LoaMgr_MotCurrlcaMgmEn_Cnt_M_u08 LoaMgr_MotCurrlcaMgmEn_Cnt_M_u08 LoaMgr_MotCurrlcaMgmEn_Cnt_M_u08 LoaMgr_MotOurrRespSV_Cnt_M_u08 LoaMgr_MotOurrRespSV_Cnt_M_u08 LoaMgr_MotOurrRespSV_Cnt_M_u08 LoaMgr_MotOurrRespSV_Cnt_M_u08 LoaMgr_MotOurrRespSV_Cnt_M_u08 LoaMgr_MotOurrRespSV_Cnt_M_u08 LoaMgr_MotOurrRespSV_Cnt_M_u08 LoaMgr_MotOurrRespSV_Cnt_M_u08 LoaMgr_NotDiagMgr_SetNTCStatus(NTC_Cnt_Tenum) Re_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_Tenum) Re_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_Tenum) Re_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum) Re_Virte_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum) Re_Wirte_Ap_LoaMgr_LoaScaleFctr_Uls_132(data) LoaMgr_Pert_LwtLoaMgtnEn_Cnt_Igc_value 10 Loamgr_Pert_IwtLoaMighten_Cnt_Igc_value 10 Loamgr_Pert_MotAgLoaMighten_Cnt_Igc_value				
LoaMgr_IvrticaMtghtEn_Cnt_M_lgc 0 0 LoaMgr_IvrtroaMtghtEn_Cnt_M_lgc 0 0 LoaMgr_LoASt_State_M_enum 5 5 LoaMgr_LoaRateLimit_UlspS_M_f32 500 500 LoaMgr_LoaScaleFctr_Uls_M_f32 0 0 LoaMgr_MaxMtghrResp_Cnt_M_u08 5 5 LoaMgr_MotAgldptsigSV_Cnt_M_u08 0 0 LoaMgr_MotAglcaMtghten_Cnt_M_lgc 0 0 LoaMgr_MotQLoaMtghten_Cnt_M_lgc 0 0 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 5 5 LoaMgr_MotCurrLoaMtghten_Cnt_M_lgc 1 1 LoaMgr_MotCurrLoaMtghten_Cnt_M_u08 2 2 LoaMgr_MotCurrRespSV_Cnt_M_u08 2 2 LoaMgr_TloaDi_Cnt_M_lgc 0 0 LoaMgr_TloaDi_Cnt_M_lgc 0 0 LoaMgr_NtxtDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 172, 173} {170, 172, 173} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_32(data) 0 0 0 Rte_W		· ·		
LoaMgr_MrtLoaMigtnEn_Cnt_M_lgc				
LoaMgr_LorRespSV_Cnt_M_u08			1	
LoaMgr_LoaSt_State_M_enum	1			
LoaMgr_LoaRateLimit_UispS_M_f32				
LoaMgr_LoaScaleFctr_Uls_M_f32 0 0 LoaMgr_MaxMtgtnResp_Cnt_M_u08 5 5 LoaMgr_MotAgldptSigSV_Cnt_M_u08 0 0 LoaMgr_MotAgRespSV_Cnt_M_u08 5 5 LoaMgr_MotCurrloqhSigSV_Cnt_M_u08 5 5 LoaMgr_MotCurrLoaMignEn_Cnt_M_lgc 1 1 LoaMgr_MotCurrRespSV_Cnt_M_u08 2 2 LoaMgr_TloaD_Cnt_M_lgc 0 0 LoaMgr_TloaLyc_Cnt_M_lgc 1 1 LoaMgr_TloaD_Cnt_M_lgc 0 0 LoaMgr_VitgMdSrcSV_Cnt_M_u08 0 0 Rte_Call_Ap_LoaMgr_NtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) 4 1 Rte_Call_Ap_LoaMgr_NtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) 4 4 4 Rte_Call_Ap_LoaMgr_NtrDiagMgr_SetNTCStatus(Param_Cnt_Tu08) 4 4 4 4 4 4 Rte_Call_Ap_LoaMgr_NtrDiagMgr_SetNTCStatus(Param_Cnt_Tu08) 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4				
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 5 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 0 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 1 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 2 LoaMgr_MotCurrRespSV_Cnt_M_u08 2 LoaMgr_TloaAvail_Cnt_M_lgc 1 LoaMgr_TloaDi_Cnt_M_lgc 0 LoaMgr_TloaDi_Cnt_M_lgc 1 1 1 2 LoaMgr_VtlgMdSrcSV_Cnt_M_u08 0 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) 1 Rto_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum) 1 Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum) 1 Rte_Write_Ap_LoaMgr_LoaSaleFctr_Uls_f32(data) 0 0 0 1 arget_LoaMgr_Per1_HvrtLoaMtgtnEn_Cnt_lgc.value 1 0 1 atraget_LoaMgr_Per1_LOASt_State_enum.value 5 5 5 4 atraget_LoaMgr_Per1_LOASt_State_enum.value 5 5 5 6 6 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7				
LoaMgr_MotAgloptSigSV_Cnt_M_u08 0 0 ✓ LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc 0 0 ✓ LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 5 5 ✓ LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 1 1 ✓ LoaMgr_MotCurrRespSV_Cnt_M_u08 2 2 2 LoaMgr_TloaAvail_Cnt_M_lgc 0 0 0 LoaMgr_TloaDi_Cnt_M_lgc 1 1 1 LoaMgr_TloaDi_Cnt_M_lgc 1 1 1 LoaMgr_VltgMdSrcsV_Cnt_M_u08 0 0 0 Rte_Call_Ap_LoaMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 172, 173} {170, 172, 173} √ Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {0, 0, 0, 0, 0} × Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} √ Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 500 500 ± 0.01 ✓ Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 0 ± 0.001 target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc.value 0 0				
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc 0 0 LoaMgr_MotAgRespSV_Cnt_M_u08 5 5 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 0 0 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 1 1 LoaMgr_MotCurrRespSV_Cnt_M_u08 2 2 LoaMgr_TloaAvail_Cnt_M_lgc 0 0 LoaMgr_TloaDi_Cnt_M_lgc 1 1 LoaMgr_VltgMdSrcSV_Cnt_M_u08 0 0 Rte_Call_Ap_LoaMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 172, 173} {170, 172, 173} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0} ** Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} ** Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} ** Rte_Write_Ap_LoaMgr_LoaRateLimit_Ulsps_f32(data) 500 500 ± 0.01 ** Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 ** target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0 ** target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 <td></td> <td></td> <td></td> <td></td>				
LoaMgr_MotAgRespSV_Cnt_M_u08 5 5 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 0 0 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 1 1 LoaMgr_MotCurrRespSV_Cnt_M_u08 2 2 LoaMgr_TloaAvail_Cnt_M_lgc 0 0 LoaMgr_TloaDi_Cnt_M_lgc 1 1 LoaMgr_VltgMdSrcSV_Cnt_M_u08 0 0 Re_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 172, 173} {170, 172, 173} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0} ** Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} ** Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} ** Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} ** Rte_Call_Ap_LoaMgr_LoaRcaleLimit_UlspS_f32(data) 500 500 ± 0.01 ** Rte_Write_Ap_LoaMgr_LoaRcaleFctr_Uls_f32(data) 0 0 ± 0.001 ** target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0 0				
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 0 0 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 1 1 LoaMgr_MotCurrRespSV_Cnt_M_u08 2 2 LoaMgr_TloaAvail_Cnt_M_lgc 0 0 LoaMgr_VltgMdSrcSV_Cnt_M_u08 1 1 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 172, 173} {170, 172, 173} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 rtarget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0 target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value 0 0 target_LoaMgr_Per1_LOASt_State_enum.value 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0		-		
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 1 1 2 LoaMgr_MotCurrRespSV_Cnt_M_u08 2 2 2 LoaMgr_TloaAvail_Cnt_M_lgc 0 0 0 LoaMgr_VitgMdSrcSV_Cnt_M_u08 0 0 0 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 172, 173} {170, 172, 173} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 Rte_Write_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0 target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value 0 0 target_LoaMgr_Per1_LOASt_State_enum.value 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0				
LoaMgr_MotCurrRespSV_Cnt_M_u08 2 2 LoaMgr_TloaAvail_Cnt_M_lgc 0 0 LoaMgr_VltgMdSrcSV_Cnt_M_u08 0 0 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 172, 173} {170, 172, 173} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 xtarget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0 target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value 0 0 target_LoaMgr_Per1_LOASt_State_enum.value 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0				
LoaMgr_TloaAvail_Cnt_M_lgc 0 0 LoaMgr_TloaDi_Cnt_M_lgc 1 1 LoaMgr_VltgMdSrcSV_Cnt_M_u08 0 0 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 172, 173} {170, 172, 173} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 xtarget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0 target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value 0 0 target_LoaMgr_Per1_LOASt_State_enum.value 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0		•		
LoaMgr_TloaDi_Cnt_M_lgc 1 1 LoaMgr_VltgMdSrcSV_Cnt_M_u08 0 0 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 172, 173} {170, 172, 173} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0} X Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} {1, 1, 1} X Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 X Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 X rtarget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0 X target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value 0 0 X target_LoaMgr_Per1_LOASt_State_enum.value 5 5 X target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0 X				
LoaMgr_VitgMdSrcSV_Cnt_M_u08 0 0 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 172, 173} {170, 172, 173} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0} 3 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} {1, 1, 1} Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 500 ± 0.01 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 4 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0 4 target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value 0 0 4 target_LoaMgr_Per1_LOASt_State_enum.value 5 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0 4				
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 172, 173} {170, 172, 173} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 xtarget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0 target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value 0 0 xtarget_LoaMgr_Per1_LOASt_State_enum.value 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0				
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0 target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value 0 0 target_LoaMgr_Per1_LOASt_State_enum.value 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0				
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0 target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value 0 0 target_LoaMgr_Per1_LOASt_State_enum.value 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0	, _ ,			
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0 target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value 0 0 target_LoaMgr_Per1_LOASt_State_enum.value 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0				
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data) 0 0 ± 0.001 v target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0 v target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value 0 0 v target_LoaMgr_Per1_LOASt_State_enum.value 5 5 v target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0 v			1	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1				
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value 0 0 vtarget_LoaMgr_Per1_LOASt_State_enum.value 5 5 5 vtarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc.value 0 0 vt				~
target_LoaMgr_Per1_LOASt_State_enum.value 5 5 5 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1				~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0				~
	target_LoaMgr_Per1_LOASt_State_enum.value			~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value 1	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1.	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	✓
RqstHwTqResp	1	RqstHwTqResp	1	✓
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	-





Test Step 2.13 (Repeat Count = 1)	II and the second se
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV Cnt M u08	*none*
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2 4
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1 5
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0 1
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k LoaMgr MotCurrMtgtnSca Uls f32	1
k LoaMgr RedcdStRate UlspS f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0 1
target_coalwgr_Per1_noabl_Cnt_gc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtrImactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_ivtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.14 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	5		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	2		
k_LoaMgr_MotAgIdptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca2eroEn_Cnt_igc k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k LoaMgr MotCurrldptSig1Resp Cnt u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt	- ·	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enur	m 	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	Y
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	· ·
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	· ·
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	· ·
LoaMgr_LOASt_State_M_enum	5	5	V
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	· ·
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	V
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	✓
LoaMgr_MotAglopMattsEp_Cot_M_u08	0	0	
LoaMgr_MotAgRoapSV_Cot_M_ugc	0	0	
LoaMgr MotAgRespSV Cnt M u08	5	5	~

LoaMgr_MotAgRespSV_Cnt_M_u08

LoaMgr_Per1





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 174}	{170, 172, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.15 (Repeat Count = 1)	3
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





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Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08	S	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lg	ус	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	- TOO LITE
LoaMgr HwTqldptSigSV Cnt M u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	-
	4	4	-
LoaMgr_HwTqRespSV_Cnt_M_u08			
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc		0	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	_
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	Y
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	-
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174, 175}	{170, 173, 174, 175}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	
target_LoaMgr_Per1_LOASt_State_enum.value target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	
		11/	· •
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.16 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_IvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target Rte Inst Ap LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrlvtrMtgtnScaZeroEn Cnt lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1
<u> </u>	'





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Ci	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	-	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	0	0	/ /
LoaMgr HwTqldptSigSV Cnt M u08	0	0	
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	-
LoaMgr lytrldptSigSV Cnt M u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	·
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	
	500	500	
LoaMgr_LoaRateLimit_UlspS_M_f32	0	0	-
LoaMgr_LoaScaleFctr_Uls_M_f32	5	5	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	0	0	-
LoaMgr_MotAglaptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	5	5	•
LoaMgr_MotAgRespSV_Cnt_M_u08	0	0	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	2	2	
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	
LoaMgr_TloaAvail_Cnt_M_lgc			
LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	V
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{170, 172, 174}	{170, 172, 174}	✓
	{1, 1, 1}	{0, 0, 0, 0, 0}	· ·
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	V
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	V
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	0	0	
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	V
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	V
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	✓
RqstHwTqResp	1	RqstHwTqResp	1	✓
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 2.17 (Repeat Count = 1)	2
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_IvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none* *none*
Rte_Inst_Ap_LoaMgr	target Rte Inst Ap LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrivtrMtgtnScaZeroEn Cnt lgc	0
k LoaMgr CurrlytrMtgtnSca Uls f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1 0
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32	1
	0
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	0
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
	Harrier Licawigg Pert Motturn Cawitothen Cht IdC



LoaMgr_Per1

Name	Input Value			
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum			
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc			
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	0	0	~	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	•	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~	
LoaMgr_LOASt_State_M_enum	5	5	~	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	•	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	•	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~	
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~	
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~	
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~	
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•	
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	✓

Test Step 2.18 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target LoaMgr Per1 TloaAvail Cnt lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target Rte Inst Ap LoaMgr.LoaMgr Per1 DiagcStslvtr1Inactv Cnt Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv	Cnt lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target LoaMgr Per1 DiagcStslvtr2Inactv		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqLoaMtgtnEn Cnt Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target LoaMgr Per1 lvtrLoaMtgtnEn Cni		
target Rte Inst Ap LoaMgr.LoaMgr Per1 LOASt State enum	target_LoaMgr_Per1_LOASt_State_enum		
target Rte Inst Ap LoaMgr.LoaMgr Per1 LoaScaDi Cnt Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScabi_cit_gc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Citt_ige target_LoaMgr_Per1_MotAgLoaMtgtnEn_	Cnt lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEr		
target Rte Inst Ap LoaMgr.LoaMgr Per1 MtrPosldptSig Cnt u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enu		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_Steenwou_Cit_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc	"	
target Rte Inst Ap LoaMgr.LoaMgr Per ToaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
		Evnested Value	Decut
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	•
LoaMgr_LOASt_State_M_enum	5	5	·
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	•
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr MotAgRespSV Cnt M u08	5	5	✓

LoaMgr_MotAgRespSV_Cnt_M_u08





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	✓
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	✓
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.19 (Repeat Count = 1)	18
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Landing Makingdespitame, Circl, up	Name	Input Value		
Landary Montagnation Landary Montagnation				
Landary Jandary (January Carlos)				
Landary Johnsphalers Landary Debugs		2		
Loading Andonymanisman Units Color	k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
Landary MonCardistignResp. Crit LoSS 2 Landary MonCardistignResp. Crit LoSS 0 Landary MonCardistign Crit LoSS 0 Lan	k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
Labeling McControls(s)(Peeps Circl USS 0 0 0 0 0 0 0 0 0				
Lindardy_MONUTORISSIPRIESS_COLLUSE CLANDER CANTUME CLANDER CANTUME CLANDER CANTUME CANTUME CANTUME CLANDER CANTUME CLANDER CANTUME CLANDER CANTUME CLANDER CANTUME CLANDER CANTUME CANTUME CLANDER CANTUME CLANDER CANTUME CANTUME	k_LoaMgr_MotAgSmaAvailResp_Cnt_u08			
Landary Maintenning Part Landary Landary Maintenning Part Landary Landary Maintenning Part Landary Landary				
L cabely MoContralpsical Lines (2.32) L cabely MoContralpsical Lines (2.32) L cabely MoContralpsica (2.32) L cabely Recodificate Lines (2.32) L cabely Recodificate				
Leading / McConfignificacide (1985 152 1				
Loading / McCarding/Sca2_Mag. 202 1				
		· ·		
Landary Recodificials United 1				
		1		
Langer L. Landy Per L. Hardingtong, Crit L. 108 value		1		
	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value			
	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value			
target Rie, Inst, An, Loader, Loadery C. Derby, Carebra, Cort. Igo: target, Loadery, Deathy, Loadery C. Deathy, C. Prof. Diago-Sishird Frack, Crit. Igo: target, Rie, Inst, Ap, Loadery, Loadery, Pert J. Diago-Sishird Frack, Crit. Igo: target, Rie, Inst, Ap, Loadery, Loadery, Pert J. Hur TajdptSig, Crit. Ju8 target, Rie, Inst, Ap, Loadery, Loadery, Pert J. Hur TajdptSig, Crit. Ju8 target, Loadery, Pert J. Hur TajdptSig, Crit. Ju8 target, Loadery, Pert J. Loadery, Loadery, Pert J. Loadery, Loadery, Pert J. Mortal Signific, Crit. Igo: target, Loadery, Pert J. Loadery, Cont. Igo: target, Loadery, Pert J. Loadery, Cont. Igo: target, Loadery, Pert J. Mortal Size enum target, Loadery, Pert J. Loadery, Cont. Igo: target, Loadery, Pert J. Mortal Size enum target, Loadery, Pert J. Mortal Size, Crit. Igo: target, Loadery, Pert J. Indoor, Crit. Igo:				
target. Rel. nst. Ap. Loadlyr. Loadlyr. Pert. Disposition (Cnt. Upc target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. Private (Stories) Cnt. Upc target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. Invitation (Cnt. Upc target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. Invitation (Cnt. Upc target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. Invitation (Cnt. Upc target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. Loadlyr. En urun target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. Loadlyr. En urun target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. Loadlyr. En urun target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. Mod. Loadlyr. Enc. Upc target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. Mod. Loadlyr. Enc. Upc target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. Mod. Loadlyr. Enc. Upc target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. Mod. Loadlyr. Enc. Upc target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. Mod. Loadlyr. Enc. Upc target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. Mod. Loadlyr. Enc. Upc target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. Mod. Loadlyr. Enc. Upc target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. Mod. Loadlyr. Enc. Upc target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. Mod. Loadlyr. Enc. Upc target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. Inst. Pert. Mod. Loadlyr. Enc. Upc target. Rel. Inst. Ap. Loadlyr. Loadlyr. Pert. Inst. Pert. Mod. Loadlyr. Pert. Inst. Pert. Toadlyr. Pert. Inst. Pert.				
target_Rel_Inst_Ap_LoaMgr_LoaMgr_Per1 MirqlophSig_Cot_Lu8 target_LoaMgr_Per1 MirqlophSig_Cot_Lu8 target_LoaMgr_Per1 MirqloaMgr_Per1 MirqloaMgr				
target_Rel_Inst_Ap_Loadky_Loadkg_Per1_htrot_coatkginen_Cnt_lgc target_Loakkg_Per1_htrot_coatkginen_Cnt_lgc target_Rel_Inst_Ap_Loakkg_Loakkg_Per1_LOASS_State_enum target_Rel_Inst_Ap_Loakkg_Loakkg_Per1_LOASS_State_enum target_Rel_Inst_Ap_Loakkg_Loakkg_Per1_LOASS_State_enum target_Rel_Inst_Ap_Loakkg_Loakkg_Per1_LOASS_Coatk_Per1_LOASS_State_enum target_Rel_Inst_Ap_Loakkg_Loakkg_Per1_MoCurridgist_G_Cnt_U08 target_Rel_Inst_Ap_Loakkg_Loakkg_Per1_MoCurridgist_G_Cnt_U08 target_Rel_Inst_Ap_Loakkg_Loakkg_Per1_MoCurridgist_G_Cnt_U08 target_Rel_Inst_Ap_Loakkg_Loakkg_Per1_MoCurridgist_G_Cnt_U08 target_Rel_Inst_Ap_Loakkg_Loakkg_Per1_MoCurridgist_G_Cnt_U08 target_Rel_Inst_Ap_Loakkg_Loakkg_Per1_MoCurridgist_G_Cnt_U08 target_Loakkg_Per1_MoCurridgist_G_Cnt_U08 target_Rel_Inst_Ap_Loakkg_Loakkg_Per1_MoCurridgist_G_Cnt_U08 target_Loakkg_Per1_MoCurridgist_G_Cnt_U08 target_Loakkg_P				
target_Rel_inst_Ap_Loakly_Cloaklyg_Pert_LOASS_Istate_enum target_Rel_inst_Ap_Loaklyg_Cloaklyg_Pert_LoASS_Istate_enum target_Rel_inst_Ap_Loaklyg_Cloaklyg_Pert_LoASS_Istate_enum target_Rel_inst_Ap_Loaklyg_Cloaklyg_Pert_LoASS_Istate_enum target_Rel_inst_Ap_Loaklyg_Cloaklyg_Pert_LoASS_Istate_enum target_Rel_inst_Ap_Loaklyg_Cloaklyg_Pert_MortCurptSig_Cnt_u08 target_Rel_inst_Ap_Loaklyg_Cloaklyg_Pert_MortCurptSig_Cnt_u08 target_Rel_inst_Ap_Loaklyg_Cloaklyg_Pert_MortCurptSig_Cnt_u08 target_Rel_inst_Ap_Loaklyg_Cloaklyg_Pert_MortCurptSig_Cnt_u08 target_Rel_inst_Ap_Loaklyg_Cloaklyg_Pert_MortCurptSig_Cnt_u08 target_Rel_inst_Ap_Loaklyg_Cloaklyg_Pert_MortCurptSig_Cnt_u08 target_Rel_inst_Ap_Loaklyg_Cloaklyg_Pert_MortCurptSig_Cnt_u08 target_Rel_inst_Ap_Loaklyg_Cloaklyg_Pert_MortCurptSig_Cnt_u08 target_Rel_inst_Ap_Loaklyg_Cloaklyg_Pert_MortCurptSig_Cnt_u08 target_Rel_inst_Ap_Loaklyg_Cloaklyg_Pert_Tioaklyg_Cloaklyg_Pert_MortCurpt_DioAklyg_Pert_M				
target_Rel_Inst_Ap_LoaMgr_LoaMgr_Pert_LOASE_State_enum				
target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_LoaScaDi_Cnt_Igc target_LoaMgr_Pert_Mork_LoaMgr_LoaMgr_Pert_Mork_LoaMgr_Enc_Cnt_Igc target_LoaMgr_Pert_Mork_LoaMgr_Dert_Mork_LoaMgr_Enc_Cnt_Igc target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_Mork_LoaMgr_Enc_Cnt_Igc target_LoaMgr_Pert_Mork_LoaMgr_Pert_Mork_LoaMgr_Enc_Cnt_Igc target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_Mork_LoaMgr_Enc_Cnt_Igc target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_Mork_LoaMgr_Enc_Cnt_Igc target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_SteemMod_Cnt_enum target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_Igc target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_Igc target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_Igc target_Rite_Inst_Ap_LoaMgr_LoaMgr_Pert_ToaAvail_Cnt_Igc target_LoaMgr_Pert_ToaAvail_Cnt_Igc target_LoaMgr_P				
target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_MolGundMgthEn_Cnt_Igc target_LoaMgr_Per1_MolGundMgthEn_Cnt_Igc target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_MolGundMgthEn_Cnt_Igc target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_MirPosidgisig_Cnt_U08 target_LoaMgr_Per1_MirPosidgisig_Cnt_U08 target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_MirPosidgisig_Cnt_U08 target_LoaMgr_Per1_MirPosidgisig_Cnt_U08 target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_Steehod_Cnt_enum target_LoaMgr_Per1_Steehod_Cnt_enum target_LoaMgr_Per1_ToaAvail_Cnt_Igc target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_ToaDl_Cnt_Igc target_LoaMgr_Per1_ToaAvail_Cnt_Igc target_LoaMgr_Per1_MirLoaMgthEn_Cnt_Igc target_LoaMgr_Per1_MirLoaMgthEn_Cnt_Igc target_LoaMgr_Per1_MirLoaMgr_Per1_MirLoaMgr_Per1_MirLoaMgr_Per1_MirLoaMgr_Per1_Igcvalue target_LoaMgr_Per1_MirLoaMgr_Per1_MirLoaMgr_Per1_MirLoaMgr_Per1_MirLoaMgr_Per1_Igcvalue target_LoaMgr_Per1_MirLoaMgr_Per1_MirLoaMgr_Per1_MirLoaMgr_Per1_MirLoaMgr_Per1_MirLoaMgr_Per1_MirLoaMgr_Per1_MirLoaMgr_Per1_MirLoaMgr_Per1_MirLoaMgr_Per1_MirLoaMgr_Per1_MirLoaMgr_Per1_MirLoaMgr_Per1_MirLoaMgr_Per1_MirLoaMgr_Per1_MirLoaMgr_Per1_MirLoaMgr_Per1_MirLoaM				
target_Rite_Inst_Ap_LoaMgr_LoaMgr_Per1_Microdiptisig_Cnt_u08 target_LoaMgr_Per1_Microdiptisig_Cnt_u08 target_Rite_Inst_Ap_LoaMgr_LoaMgr_Per1_Microdiptisig_Cnt_u08 target_LoaMgr_Per1_Mirrosidiptisig_Cnt_u08 target_LoaMgr_Per1_Mirrosidiptisig_Cnt_u08 target_LoaMgr_Per1_Mirrosidiptisig_Cnt_u08 target_LoaMgr_Per1_Mirrosidiptisig_Cnt_u08 target_LoaMgr_Per1_Mirrosidiptisig_Cnt_u08 target_LoaMgr_Per1_Mirrosidiptisig_Cnt_u08 target_LoaMgr_Per1_Mirrosidiptisig_Cnt_u08 target_LoaMgr_Per1_Mirrosidiptisig_Cnt_u08 target_LoaMgr_Per1_ToaAvail_Cnt_lgc toaMgr_Mirrosidet_Cnt_Milgc toaMgr_				
larget_Rie_Inst_Ap_LoaMgr_LoaMgr_Pert_MirrPosidptSig_Cnt_u08 target_LoaMgr_Pert_Mexing_Cnt_u08 target_LoaMgr_Pert_NoaMgr				
larget Rie_Inst. Ap_Loaklgr.Loaklgr.Perl_TioakVail_Crt.l.go target_Loaklgr.Perl_TioakVail_Crt.l.go Instigut_Rie_Inst. Ap_Loaklgr.Loaklgr.Perl_Tioab\Crt.l.go target_Loaklgr.Perl_Tioab\Crt.l.go Name Actual Value Expected Value Result Loaklgr_AgSV_Crt.l.M.l.go 0 0 0 0 Loaklgr_HwTqLoaklignEnc_Crt.l.M.l.go 0 0 0 0 0 Loaklgr_HwTqCashSySV_Crt.l.M.u08 0	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnE	En_Cnt_lgc	
target_Rie_Inst_Ap_LoaMgr_LoaMgr_Pert_TioaN_Cnt_lgc target_LoaMgr_Pert_TioaD_Cnt_lgc Vame Actual Value Expected Value Result LoaMgr_AgsV_Cnt_M_lgc 0 0 0 0 LoaMgr_HwTqloptSigsV_Cnt_M_u08 0 0 0 0 LoaMgr_HwTqRespSV_Cnt_M_u08 0 0 0 0 LoaMgr_HwTqRespSV_Cnt_M_u08 0 0 0 0 LoaMgr_LvrloptSigsV_Cnt_M_u08 0 0 0 0 LoaMgr_LvrloptSigsV_Cnt_M_u08 0 0 0 0 LoaMgr_LvrloptSigsV_Cnt_M_u08 5 5 5 0 LoaMgr_LvrloptSigsV_Cnt_M_u08 5 5 5 0 0 LoaMgr_LoaScaleFort_Us_M_132 0 0 0 0 0 0 LoaMgr_MoxAgloptSigsV_Cnt_M_u08 5 5 5 5 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cn	t_u08	
target_Rie_Inst_Ap_LoaMgr_LoaMgr_Pert_TloaDi_Cnt_lgc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_en	um	
Name Actual Value Expected Value Result LoaMgr_AgSV_Cnt_M_Igc 0 0 0 LoaMgr_HwTqLoaMtgmEn_Cnt_M_Igc 0 0 0 LoaMgr_HwTqLoaMtgmEn_Cnt_M_Igc 0 0 0 LoaMgr_HwTqLoaMtgmEn_Cnt_M_Igc 0 0 0 LoaMgr_LytrRespSV_Cnt_M_Uo8 0 0 0 LoaMgr_LytrRespSV_Cnt_M_Uo8 5 5 5 LoaMgr_LytrRespSV_Cnt_M_Uo8 5 5 5 LoaMgr_LoaRstelLmit_UlspS_M_f32 500 500 0 LoaMgr_LoaRstelLmit_UlspS_M_f32 0 0 0 LoaMgr_MotAglightResp_Cnt_M_uo8 5 5 5 LoaMgr_MotAglightSgSV_Cnt_M_uo8 0 0 0 LoaMgr_MotAglightSgSV_Cnt_M_uo8 0 0 0 LoaMgr_MotCurrLoaMgrespSV_Cnt_M_uo8 0 0 0 LoaMgr_MotCurrLoaMgrespSV_Cnt_M_uo8 0 0 0 LoaMgr_MotCurrLoaMgrespSV_Cnt_M_uo8 2 2 2 LoaMgr_TloaAval_Cnt_M_lgc				
Loaklgr_AgSV_Cnt_M_lgc 0 0 Loaklgr_HwTqldpiSigSV_Cnt_M_u08 0 0 Loaklgr_HwTqlcaklighEn_Cnt_M_lgc 0 0 Loaklgr_HwTqRespSV_Cnt_M_u08 0 0 Loaklgr_HvtrQRespSV_Cnt_M_u08 0 0 Loaklgr_LvtrRespSV_Cnt_M_u08 5 5 Loaklgr_LoASL_State_Menum 5 5 Loaklgr_LoASL_State_Menum 5 5 Loaklgr_LoASL_State_Menum 5 5 Loaklgr_LoASL_State_Menum 5 5 Loaklgr_LoASL_State_Fotr_UIs_M_52 500 500 Loaklgr_LoASL_State_Fotr_UIS_M_52 0 0 Loaklgr_MaxMtgtnResp_Cnt_M_u08 5 5 Loaklgr_MotAlgdptSigSV_Cnt_M_u08 0 0 Loaklgr_MotAlgdcakltgnresp_Cnt_M_u08 0 0 Loaklgr_MotCurrLoakltgnresp_Cnt_M_u08 0 0 Loaklgr_MotCurrLoakltgnresp_Cnt_M_u08 0 0 Loaklgr_MotCurrLoakltgnresp_Cnt_M_u08 1 1 Loaklgr_MotCurrLoakltgnresp_Cnt_M_u08 1 1 Loaklgr_MotC	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Loakligr_HwTqlopkSigSV_Cnt_M_u08 0 0 Loakligr_HwTqLoaklightEn_Cnt_M_lgc 0 0 Loakligr_HwTqLoaklightEn_Cnt_M_u08 0 0 Loakligr_IvitropkSigSV_Cnt_M_u08 0 0 Loakligr_IvitropkSigSV_Cnt_M_u08 5 5 Loakligr_Loak_Sitate_M_enum 5 5 Loakligr_MotAll_Callag_Cite_Cite_M_u08 0 0 Loakligr_MotAll_Callag_MotCurrical_MignEn_Cit_M_u08 0 0 Loakligr_MotCurrical_MignEn_Cit_M_u08 2 2 Loakligr_MotCurrical_MignEn_Cit_M_u08 1 1 Loakligr_MidloaklighEn_Cit_M_u08 0 0	Name		·	Result
LoaMgr_HwTqLoaMighEn_Cnt_M_lgc 0 0 LoaMgr_HwTqRespSV_Cnt_M_u08 0 0 LoaMgr_WrldpSigSV_Cnt_M_u08 0 0 LoaMgr_WrldpMignEn_Cnt_M_lgc 0 0 LoaMgr_LwTRespSV_Cnt_M_u08 5 5 LoaMgr_LoASt_State_M_enum 5 5 LoaMgr_LoASt_State_M_enum 5 500 LoaMgr_LoAScaleFctr_Uls_M_f32 500 500 LoaMgr_MaxMignResp_Cnt_M_u08 5 5 LoaMgr_MaxMignResp_Cnt_M_u08 5 5 LoaMgr_MotAgloaMignEn_Cnt_M_lgc 0 0 LoaMgr_MotAgloaMignEn_Cnt_M_lgc 0 0 LoaMgr_MotCurrIdSigSV_Cnt_M_u08 5 5 LoaMgr_MotCurrIdSigSV_Cnt_M_u08 0 0 LoaMgr_MotCurrLoaMignEn_Cnt_M_lgc 1 1 LoaMgr_MotCurrLoaMignEn_Cnt_M_lgc 1 1 LoaMgr_TicaAvail_Cnt_M_lgc 1 1 LoaMgr_TicaAvail_Cnt_M_lgc 1 1 LoaMgr_VitgMGSrcSV_Cnt_M_u08 0 0 LoaMgr_NidGMGsrcSV_Cnt_M_u08 0 </td <td></td> <td></td> <td></td> <td>~</td>				~
LoaMigr_InviTigRespSV_Cnt_M_u08				
LoaMgr_WrtfqbtSigSV_Cnt_M_u08 0 0 0 LoaMgr_WrtRepSSV_Cnt_M_u08 5 5 LoaMgr_LoaRsteLimit_UlspS_M_f32 5 5 LoaMgr_LoaRsteLimit_UlspS_M_f32 500 500 LoaMgr_LoaScaleFctr_Uls_M_f32 0 0 LoaMgr_MaxMtgnResp_Cnt_M_u08 5 5 LoaMgr_MaxMtgtnResp_Cnt_M_u08 0 0 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc 0 0 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc 0 0 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 0 0 LoaMgr_MotCurrLoaMtgnEn_Cnt_M_u08 0 0 LoaMgr_MotCurrRespSV_Cnt_M_u08 0 0 LoaMgr_MotCurrRespSV_Cnt_M_u08 2 2 LoaMgr_TloaAvail_Cnt_M_lgc 1 1 LoaMgr_TloaAvail_Cnt_M_lgc 1 1 LoaMgr_TloaAvail_Cnt_M_lgc 1 1 LoaMgr_TloaMgr_ScsV_Cnt_M_u08 0 0 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 173, 175} {170, 173, 175} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) <td></td> <td></td> <td></td> <td></td>				
LoadMgr_IvtrLoaMtghEn_Cnt_M_u08 0 LoadMgr_LVtrRespSV_Cnt_M_u08 5 5 LoadMgr_LOASt_State_M_enum 5 5 LoadMgr_LoaScaleFctr_Uls_M_f32 500 500 LoadMgr_LoaScaleFctr_Uls_M_f32 0 0 LoadMgr_MaxMtgtnResp_Cnt_M_u08 5 5 LoadMgr_MotAgLoaMtgtnEn_Cnt_M_u08 0 0 LoadMgr_MotAgLoaMtgtnEn_Cnt_M_u08 0 0 LoadMgr_MotCurrloaMtgtnEn_Cnt_M_u08 5 5 LoadMgr_MotCurrloaMtgtnEn_Cnt_M_u08 0 0 LoadMgr_MotCurrloaMtgtnEn_Cnt_M_u08 0 0 LoadMgr_MotCurrloaMtgtnEn_Cnt_M_u08 1 1 LoadMgr_MotCurrloaMtgtnEn_Cnt_M_u08 2 2 LoadMgr_TloaDL_Cnt_M_u08 2 2 LoadMgr_TloaDL_Cnt_M_u0e 1 1 LoadMgr_TloaDL_Cnt_M_loc 1 1 LoadMgr_TloaDL_Cnt_M_loc 1 1 LoadMgr_TloaDL_Cnt_M_loc 0 0 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Rtrac_Cnt_T_enum) (170, 173, 175) (170, 173, 175)				
LoaMgr_LVTRespSV_Cnt_M_u008 5 5 5 LoaMgr_LCASt_State_M_enum 5 5 5 LoaMgr_LoaRateLimit_UlspS_M_f32 500 500 500 LoaMgr_LoaRateLimit_UlspS_M_f32 0 0 0 LoaMgr_MaxMttghResp_Cnt_M_u08 5 5 5 LoaMgr_MotAgltplSigSV_Cnt_M_u08 0 0 0 LoaMgr_MotAgltplSigSV_Cnt_M_u08 0 0 0 LoaMgr_MotAgltplSigSV_Cnt_M_u08 5 5 5 LoaMgr_MotAgltplSigSV_Cnt_M_u08 0 0 0 LoaMgr_MotCurrIdplSigSV_Cnt_M_u08 5 5 5 LoaMgr_MotCurrIdplSigSV_Cnt_M_u08 0 0 0 0 LoaMgr_MotCurrLoaMtghEn_Cnt_M_lgc 1 1 1 LoaMgr_MotCurrLoaMtghEn_Cnt_M_lgc 1 1 1 LoaMgr_TloaAvail_Cnt_M_lgc 1 1 1 LoaMgr_TloaDi_Cnt_M_lgc 1 1 1 LoaMgr_TloaDi_Cnt_M_lgc 1 1 1 LoaMgr_VltgMdSrcSV_Cnt_M_u08 0 0 0 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_Tu08) (1,1,1) (0,0,0,0) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Cataus_Cnt_Tenum) (1,1,1) (1,1,1) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 0 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc_value 0 0 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc_value 0 0 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc_value 0 0 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cn				
LoaMgr_LOaSt_State_M_enum				
LoaMgr_LoaRateLimit_UlspS_M_f32 500 500 LoaMgr_LoaScaleFctr_Uls_M_f32 0 0 LoaMgr_MaxMlgtnResp_Cnt_M_u08 5 5 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc 0 0 LoaMgr_MotAgRespSV_Cnt_M_u08 5 5 LoaMgr_MotCurrloptSigSV_Cnt_M_u08 5 5 LoaMgr_MotCurrLoaMtgnEn_Cnt_M_lgc 1 1 LoaMgr_MotCurrRespSV_Cnt_M_u08 2 2 LoaMgr_MotCurrRespSV_Cnt_M_u08 2 2 LoaMgr_TloaAvail_Cnt_M_lgc 1 1 LoaMgr_TloaAvail_Cnt_M_lgc 1 1 LoaMgr_TloaDi_Cnt_M_lgc 1 1 LoaMgr_VltgMdSrcsV_Cnt_M_u08 0 0 Re_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) (170, 173, 175) (170, 173, 175) Rtc_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) (170, 173, 175) (170, 173, 175) Rtc_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) (1, 1, 1) (1, 1, 1) Rtc_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) (1, 1, 1) (1, 1, 1) Rtc_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTC				~
LoaMgr_LoaScaleFctr_UIs_M_132 0 0 LoaMgr_MaxMtgtnResp_Cnt_M_u08 5 5 LoaMgr_MotAgldptSigSV_Cnt_M_u08 0 0 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc 0 0 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 5 5 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 0 0 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc 1 1 LoaMgr_MotCurrRespSV_Cnt_M_u08 2 2 LoaMgr_TloaAvail_Cnt_M_lgc 1 1 LoaMgr_TloaDi_Cnt_M_lgc 1 1 LoaMgr_VltgMSrcSV_Cnt_M_u08 0 0 Rte_Call_Ap_LoaMgr_NtxtDiagMgr_SetNTCStatus(NTC_Cnt_Tenum) (170, 173, 175) (170, 173, 175) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_Tu08) (1, 1, 1) (1, 1, 1) (1, 1, 1) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum) (1, 1, 1) (1, 1, 1) (1, 1, 1) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 0 Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f2(data) 0 0 0 Rte_Unite_CoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	·			•
LoaMgr_MaxMtgtnResp_Cnt_M_u08 5 5 LoaMgr_MotAgldptSigSV_Cnt_M_u08 0 0 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc 0 0 LoaMgr_MotAgrespSV_Cnt_M_u08 5 5 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 0 0 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 1 1 LoaMgr_MotCurrRespSV_Cnt_M_u08 2 2 LoaMgr_TloaAvail_Cnt_M_lgc 1 1 LoaMgr_TloaDi_Cnt_M_lgc 1 1 LoaMgr_VltgMdSrcSV_Cnt_M_u08 0 0 Rec_Call_Ap_LoaMgr_NttrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 173, 175} {170, 173, 175} Rec_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {1, 1, 1} {0, 0, 0, 0, 0} 3 Rec_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} 4 Rec_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {0, 0, 0, 0, 0} 3 Rec_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {0, 0, 0, 0, 0} 3 Rec_Call_Ap_LoaMgr_LoaScaleFctr_Uls_Sf2(data) 500 0 0				✓
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc 0 0 LoaMgr_MotAgRespSV_Cnt_M_u08 5 5 LoaMgr_MotCurrloptSigSV_Cnt_M_u08 0 0 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 1 1 LoaMgr_MotCurrRespSV_Cnt_M_u08 2 2 LoaMgr_TloaAvail_Cnt_M_lgc 1 1 LoaMgr_TloaDi_Cnt_M_lgc 1 1 LoaMgr_VttgMotScSV_Cnt_M_u08 0 0 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 173, 175} {170, 173, 175} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0} X Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {0, 0, 0, 0, 0} X Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} X Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 X Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 X rarget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0 0 target_LoaMgr_Per1_LoASt_State_enum.value		5	5	✓
LoaMgr_MotAgRespSV_Cnt_M_u08 5 5 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 0 0 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 1 1 LoaMgr_MotCurrRespSV_Cnt_M_u08 2 2 LoaMgr_TloaAvail_Cnt_M_lgc 1 1 LoaMgr_TloaDi_Cnt_M_lgc 1 1 LoaMgr_VitgMosrcsV_Cnt_M_u08 0 0 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 173, 175} {170, 173, 175} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0} *** Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} ** Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} ** Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 ** Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 ** target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0 0 target_LoaMgr_Per1_LoASt_State_enum.value 5 5 ** target_LoaMgr_Per1_MotAgLoaMt	LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08 0 0 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 1 1 LoaMgr_MotCurrRespSV_Cnt_M_u08 2 2 LoaMgr_TloaAvail_Cnt_M_lgc 1 1 LoaMgr_TloaDi_Cnt_M_lgc 1 1 LoaMgr_VltgMdSrcSV_Cnt_M_u08 0 0 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 173, 175} {170, 173, 175} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0} 3 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} 41, 1, 1} Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 5 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 0 Rte_Write_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0 0 target_LoaMgr_Per1_LvtrLoaMtgtnEn_Cnt_lgc.value 0 0 0 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0 0	LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 1 1 LoaMgr_MotCurrRespSV_Cnt_M_u08 2 2 LoaMgr_TloaAvail_Cnt_M_lgc 1 1 LoaMgr_TloaDi_Cnt_M_lgc 1 1 LoaMgr_VltgMdSrcSV_Cnt_M_u08 0 0 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 173, 175} {170, 173, 175} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0} 3 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} {1, 1, 1} Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 500 ± 0.01 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 0 Rte_Write_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0 0 target_LoaMgr_Per1_LotASt_State_enum.value 5 5 0 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0 0	LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrRespSV_Cnt_M_u08 2 2 LoaMgr_TloaAvail_Cnt_M_lgc 1 1 LoaMgr_VltgMdSrcSV_Cnt_M_u08 0 0 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 173, 175} {170, 173, 175} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 Rte_Write_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0 target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value 0 0 target_LoaMgr_Per1_LOASt_State_enum.value 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0	LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_TloaAvail_Cnt_M_lgc 1 1 1 LoaMgr_VltgMdSrcSV_Cnt_M_u08 0 0 0 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 173, 175} {170, 173, 175} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 Rte_Write_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0 target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value 0 0 target_LoaMgr_Per1_LOASt_State_enum.value 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0				~
LoaMgr_TloaDi_Cnt_M_lgc 1 1 0 LoaMgr_VltgMdSrcSV_Cnt_M_u08 0 0 0 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 173, 175} {170, 173, 175} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 Atarget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0 Atarget_LoaMgr_Per1_lotrLoaMtgtnEn_Cnt_lgc.value 0 0 Atarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0 Atarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0				~
LoaMgr_VltgMdSrcSV_Cnt_M_u08 0 0 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 173, 175} {170, 173, 175} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0} 3 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} {1, 1, 1} Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 500 ± 0.01 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 0 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0 0 target_LoaMgr_Per1_lotrLoaMtgtnEn_Cnt_lgc.value 0 0 0 target_LoaMgr_Per1_LOASt_State_enum.value 5 5 0 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0 0				
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 173, 175} {170, 173, 175} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 darget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0 darget_LoaMgr_Per1_lotrLoaMtgtnEn_Cnt_lgc.value 0 0 darget_LoaMgr_Per1_LOASt_State_enum.value 5 5 darget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0				V
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1} Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0 target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value 0 0 target_LoaMgr_Per1_LOASt_State_enum.value 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0				
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_Tenum) {1, 1, 1} {1, 1, 1} Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0 target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value 0 0 target_LoaMgr_Per1_LOASt_State_enum.value 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0				
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0 target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value 0 0 target_LoaMgr_Per1_LOASt_State_enum.value 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0				~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 0 0 ± 0.001 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0 target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value 0 0 target_LoaMgr_Per1_LOASt_State_enum.value 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0		1		
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1				
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value 0 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1				~
target_LoaMgr_Per1_LOASt_State_enum.value 5 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 0 0				~
0 - 0 - 0 - 0		5	5	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value 1	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	✓
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.20 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_IvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target Rte Inst Ap LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrlvtrMtgtnScaZeroEn Cnt lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_UIs_f32	0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	1	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt	t_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lg	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target LoaMgr Per1 LoaScaDi Cnt Igc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr	nt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaAvail Cnt Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	/Court
LoaMgr HwTgldptSigSV Cnt M u08	0	0	-
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
	0	0	-
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	5	5	
LoaMgr_lvtrRespSV_Cnt_M_u08	5		-
LoaMgr_LOASt_State_M_enum		5	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	-
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	Y
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	Y
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170}	{170}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1}	{1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 2.21 (Repeat Count = 1)	The state of the s
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV Cnt M u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2 4
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08 k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0 0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1 0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_real_noabl_cnt_gc.value target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Nte_inst_Ap_Loaivigi.Loaivigi_Feri_iviotAgLoaivitgtiiLin_Crit_igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc

LoaMgr_Per1





Name	Input Value			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc			
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	0	0	•	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	•	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	✓	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	•	
LoaMgr_LOASt_State_M_enum	5	5	✓	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	•	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	✓	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	✓	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	✓	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	✓	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	✓	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	•	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓	
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~	
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓	
LoaMgr_TloaDi_Cnt_M_lgc	0	0	✓	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	✓	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~	
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓	

Test Step Call Trace ✓				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	✓
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.22 (Repeat Count = 1)		88
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	*none*	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*	
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*	
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*	
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*	
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

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Input Value



Name



name	Input value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k LoaMgr FadeOutStRate UlspS f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
	2		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08			
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
	1		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32			
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_IvtrMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k LoaMgr MotAgMtgtnScaZeroEn Cnt Igc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
	1		
k_LoaMgr_RedcdStRate_UlspS_f32			
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target LoaMgr Per1 TloaAvail Cnt Igc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
		-4	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt	_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Ig	jc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cn	t lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08			
	target_LoaMgr_Per1_MotCurrloaMtgtpEp_Ct		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
	0	0	ocuit
LoaMgr_AgSV_Cnt_M_lgc			
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	-
			~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
		0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0		~
LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08	0 0	0	
LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	0 0 5 5	0 5 5	~
LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	0 0 5 5 5	0 5 5 5 500	*
LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	0 0 5 5 5 500	0 5 5 5 500 1	\rightarrow \right
LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	0 0 5 5 5 500 1 5	0 5 5 5 500 1 5	· · · · · · · · · · · · · · · · · · ·
LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	0 0 5 5 5 500 1 5	0 5 5 5 500 1 5 0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	0 0 5 5 5 500 1 5	0 5 5 5 500 1 5	· · · · · · · · · · · · · · · · · · ·





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 2.23 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_lvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Maura	Input Value		
Name	1		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08			
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	1		
k_LoaMgr_MotAgMtathSoaZaraEn_Cot_loa	0		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSigFltThd Cnt u08	0		
k LoaMgr MotCurrMtgtnRate UlspS f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target LoaMgr Per1 TloaAvail Cnt Igc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	ent lac	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target LoaMgr Per1 DiagcStslvtr2Inactv C		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqldptSig Cnt u08	target LoaMgr Per1 HwTqldptSig Cnt u08	,	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr HwTgldptSigSV Cnt M u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	-
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr lvtrldptSigSV Cnt M u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr LOASt State M enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr MaxMtgtnResp Cnt M u08	5	5	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	•
LoaMgr MotAgLoaMtgtnEn Cnt M lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08			
LoaMgr TloaAvail Cnt M lgc	2	2	✓
	2	2 0	<i>y</i>
LoaMgr_TloaDi_Cnt_M_lgc			\ \ \ \ \ \ \ \
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	· ·
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0 0 0	0 0 0	-
	0	0 0 0 {170, 173, 174}	y
LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	0 0 0 {170, 173, 174}	0 0 0	✓ ✓
LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	0 0 0 {170, 173, 174} {1, 1, 1}	0 0 0 {170, 173, 174} {0, 0, 0, 0, 0, 0}	×
LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	0 0 0 {170, 173, 174} {1, 1, 1} {1, 1, 1}	0 0 0 {170, 173, 174} {0, 0, 0, 0, 0, 0} {1, 1, 1}	×
LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	0 0 0 {170, 173, 174} {1, 1, 1} {1, 1, 1} 500	0 0 0 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01	×
LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0 0 0 {170, 173, 174} {1, 1, 1} {1, 1, 1} 500 0	0 0 0 {170, 173, 174} {0, 0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 ± 0.001	X V V V V V V V V V
LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0 0 0 {170, 173, 174} {1, 1, 1} {1, 1, 1} 500 0	0 0 (170, 173, 174) {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0	\(\frac{1}{2}\)
LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0 0 0 {170, 173, 174} {1, 1, 1} {1, 1, 1} 500 0	0 0 (170, 173, 174) {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 0 0	X V V V V V V V V V



Test Step Call Trace					
Actual Function	Count	Expected Function	Count	Result	
CalclvtrState	2	CalclvtrState	2	~	
LatchInputs	1	LatchInputs	1	~	
RqstRespConds	1	RqstRespConds	1	~	
RqstHwTqResp	1	RqstHwTqResp	1	~	
ArbResp	1	ArbResp	1	~	
ChooseFinalResp	1	ChooseFinalResp	1	~	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~	
AssignScale	1	AssignScale	1	~	
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~	
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~	

Test Step 2.24 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	*none*
LoaMgr_HwTqldptSigSV_Cnt_M_u08	*none*
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_HwTqRespSV_Cnt_M_u08	*none*
LoaMgr_lvtrldptSigSV_Cnt_M_u08	*none*
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_IvtrRespSV_Cnt_M_u08	*none*
LoaMgr_MotAgldptSigSV_Cnt_M_u08	*none*
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotAgRespSV_Cnt_M_u08	*none*
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	*none*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	*none*
LoaMgr_MotCurrRespSV_Cnt_M_u08	*none*
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	*none*
LoaMgr_VltgMdSrcSV_Cnt_M_u08	*none*
Rte_Inst_Ap_LoaMgr	target Rte Inst Ap LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrlvtrMtgtnScaZeroEn Cnt Igc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1
	·





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target LoaMgr Per1 HwTqldptSig Cnt u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1I	nacty Cnt loc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target LoaMgr Per1 DiagcStslvtr2l		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target LoaMgr Per1 HwTqldptSig		
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqLoaMtgtnEn Cnt Igc	target_LoaMgr_Per1_HwTqLoaMtgt	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnE		
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_		
target_Rte_Inst_Ap_LoaMgr_Deathgr_Fer1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtg		
target_Rte_Inst_Ap_LoaMgr_Der1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSi		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMi		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig		
target_Rte_Inst_Ap_LoaMgr_Der1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target LoaMgr Per1 TloaAvail Cnt		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target LoaMgr Per1 TloaDi Cnt Ig		
			Daguit
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	<u> </u>
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	✓
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	✓



Test Case 3: PIM_R Test

Specification

Performance Metrics (With "None" Instrumentation and WithPS Environment)
CPU Cycles: TS3.1 5058.00 Cycles
TS3.2 4977.00 Cycles
TS3.3 4970.00 Cycles
TS3.3 4970.00 Cycles
TS3.4 5001.00 Cycles
TS3.5 5063.00 Cycles
TS3.6 5014.00 Cycles
TS3.7 4991.00 Cycles
TS3.8 5009.00 Cycles
TS3.10 4960.00 Cycles
TS3.11 5102.00 Cycles
TS3.12 5027.00 Cycles
TS3.13 5004.00 Cycles
TS3.14 4980.00 Cycles
TS3.15 4998.00 Cycles
TS3.17 5095.00 Cycles
TS3.18 5068.00 Cycles
TS3.18 5068.00 Cycles
TS3.19 4981.00 Cycles TS3.19 4981.00 Cycles TS3.20 5026.00 Cycles TS3.21 4954.00 Cycles TS3.22 5041.00 Cycles TS3.22 5014.00 Cycles TS3.24 4989.00 Cycles TS3.25 5024.00 Cycles TS3.26 4982.00 Cycles TS3.27 5011.00 Cycles TS3.28 4960.00 Cycles TS3.29 5024.00 Cycles TS3.30 5076.00 Cycles TS3.30 5076.00 Cycles TS3.31 5038.00 Cycles TS3.32 5056.00 Cycles

TS3.31 5038.00 Cycles TS3.32 5056.00 Cycles TS3.33 4975.00 Cycles TS3.34 5023.00 Cycles TS3.35 4968.00 Cycles TS3.36 5057.00 Cycles TS3.37 5025.00 Cycles TS3.38 5037.00 Cycles

Description

Vector Description:

TS3.1LoaMgr_HwTqldptSigSV_Cnt_M_u08==>Min TS3.2LoaMgr_HwTqldptSigSV_Cnt_M_u08==>Max TS3.2LoaMgr_HwTqldptSigSV_Cnt_M_u08==>Max
TS3.3LoaMgr_HwTqldptSigSV_Cnt_M_u08==>Pos
TS3.4LoaMgr_HwTqldptSigSV_Cnt_M_u08==>Min
TS3.5LoaMgr_HwTqldptSigSV_Cnt_M_u08==>Max
TS3.6LoaMgr_HwTqldptSigSV_Cnt_M_u08==>Pos
TS3.7LoaMgr_MotCurrldptSigSV_Cnt_M_u08==>Min
TS3.8LoaMgr_MotCurrldptSigSV_Cnt_M_u08==>Max
TS3.9LoaMgr_MotCurrldptSigSV_Cnt_M_u08==>Mot
TS3.11LoaMgr_lvtrldptSigSV_Cnt_M_u08==>Min
TS3.11LoaMgr_lvtrldptSigSV_Cnt_M_u08==>Max
TS3.11LoaMgr_lvtrldptSigSV_Cnt_M_u08==>Max
TS3.11LoaMgr_StgSt_Cnt_M_u08==>Max
TS3.11LoaMgr_StgSt_Cnt_M_u08==>Max
TS3.11LoaMgr_StgSt_Cnt_M_u08==>Max
TS3.11LoaMgr_StgSt_Cnt_M_u08==>Max
TS3.11LoaMgr_StgSt_Cnt_M_u08==>Min TS3.12LoaMgr_VtrIdptSigSV_Cnt_M_u08==>Pos
TS3.13LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc==>Min
TS3.14LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc==>Max
TS3.15LoaMgr_MotAgRespSV_Cnt_M_u08==>Min
TS3.16LoaMgr_MotAgRespSV_Cnt_M_u08==>Max
TS3.17LoaMgr_MotAgRespSV_Cnt_M_u08==>Max
TS3.17LoaMgr_MotCurrRespSV_Cnt_M_u08==>Min
TS3.18LoaMgr_MotCurrRespSV_Cnt_M_u08==>Min
TS3.19LoaMgr_MotCurrRespSV_Cnt_M_u08==>Pos
TS3.21LoaMgr_VtrRespSV_Cnt_M_u08==>Pos
TS3.21LoaMgr_VtrRespSV_Cnt_M_u08==>Pos
TS3.22LoaMgr_IvtrRespSV_Cnt_M_u08==>Pos
TS3.24LoaMgr_IvtrRespSV_Cnt_M_u08==>Pos
TS3.24LoaMgr_IvtrRespSV_Cnt_M_u08==>Pos
TS3.25LoaMgr_HwTqRespSV_Cnt_M_u08==>Pos
TS3.25LoaMgr_HwTqRespSV_Cnt_M_u08==>Pos
TS3.26LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc==>Min
TS3.28LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc==>Min
TS3.29LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc==>Min TS3.29LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc==>Min TS3.30LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc==>Max TS3.31LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc==>Min TS3.32LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc==>Max TS3.33LoaMgr_lvtrLoaMtgtnEn_Cnt_M_Igc==>Min TS3.34LoaMgr_lvtrLoaMtgtnEn_Cnt_M_Igc==>Max TS3.35LoaMgr_VltgMdSrcSV_Cnt_M_u08==>Max
TS3.36LoaMgr_VltgMdSrcSV_Cnt_M_u08==>Min
TS3.37LoaMgr_AgSV_Cnt_M_lgc==>Max TS3.38LoaMgr_AgSV_Cnt_M_lgc==>Pos

Test Step 3.1 (Repeat Count = 1)		🔀
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_lvtrRespSV_Cnt_M_u08	4	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	

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Name		
Leading MoCourrisposity Crit M, 108		
Loading MoCourtechNigners Cort. Milgs		
Leading Sendshopkenia Col. My LoS Leading Sendshopkenia Col. My Sendshopkenia Co		
Leading Surfaloxidopinal C.M. Mujo Leading Surfaloxidopinal C.M. Mujo Leading Surfaloxidopinal C.M. Mujo Re_Jens A_Leading		
LoaMgr VinjekSicsV Crit My USB		
Rite_Inst_Ap_Loaklogr		
Loading FarbotistRate Uliss 122 1		
Loading-Indeptising-Trans-Aurilland Lings-192		
K_LOMARy_HVTGQHSSQTTOAAvailResp_Crt_U08 2 K_LOMARY_HVTGQHSSQTTOAAvailResp_Crt_U08 1 K_LOMARY_HVTGQHSSQTTOAAvailResp_Crt_U08 0 K_LOMARY_HVTGQHSSQTTOAAVailResp_Crt_U08 0 K_LOMARY_HVTGQHSSQTFOATARASP_Crt_U08 0 K_LOMARY_HVTGQHSSQFRITHO_FU_008 1 K_LOMARY_HVTGQHSSQFRITHO_FU_008 1 K_LOMARY_HVTGQHSSQFRITHO_FU_008 0 K_LOMARY_HVTGQHSSQFRASP_Crt_U08 1 K_LOMARY_HVTGQHSSQFRASP_Crt_U08 5 K_LOMARY_HVTGQHSSQFRASP_Crt_U08 1 K_LOMARY_HVTGQHSSQFRASP_Crt_U08 1 K_LOMARY_HVTGQHSSQFRASP_Crt_U08 2 K_LOMARY_HVTGQHSSQFRASP_Crt_U08 2 K_LOMARY_HVTGQHSSQFRASP_Crt_U08 2 K_LOMARY_HVTGQHSSQFRASP_Crt_U08 2 K_LOMARY_HVTGQHSSQFRASP_Crt_U08 2 <t< td=""><td></td><td></td></t<>		
K_LOMARY_HVTQISSSSTRONDTARESP_CRT_U08		
k_Loading + HvTqiqbSig2TrabaAvaiResp_Cnt_u08 1 k_Loading + HvTqiqbSig2TrabaResp_Cnt_u08 0 k_Loading + HvTqiqbSig2TrabaResp_Cnt_u08 0 k_Loading + HvTqiqbSig2TrabaResp_Cnt_u08 1 k_Loading + HvTqiqbSig2TrabaCnt_u08 1 k_Loading + HvTqiqbSig1Resp_Cnt_u08 1 k_Loading + HvTqiqbSig1Resp_Cnt_u08 0 k_Loading + HvTqiqbSig1Resp_Cnt_u08 5 k_Loading + HvTqiqbSig1Sig1NasimaResp_Cnt_u08 5 k_Loading + MvTqiqbSig1Sig1NasimaResp_Cnt_u08 5 k_Loading + MvTqiqbSig1Sig1Resp_Cnt_u08 1 k_Loading + MvTqiqbSig1Sig1Resp_Cnt_u08 2 k_Loading + MvTqiqbSig1Sig1Resp_Cnt_u08 2 k_Loading + MvTqiqbSig1Resp_Cnt_u08 2 k_Loading + MvTqiqbSig1Resp_Cnt_u08 2 k_Loading + MvTqiqbSig1Resp_Cnt_u08 2 k_Loading + MvTqiqbSig1Resp_Cnt_u08 2 </td <td></td> <td></td>		
L. Loakigr HVTqlqpSig2TtabaDiResp, Cnt_U08		
k_Loaking + MvTqdpdSigaPtasp_Cnt_u08 0 k_Loaking + MvTqdpdSigaPtasp_Cnt_u08 1 k_Loaking + MvTqdpdSigaPtasp_Cnt_u08 1 k_Loaking + MvTqdpdSigaPtasp_Cnt_u08 0 k_Loaking + MvTqdpdSigaPtasp_Cnt_u08 0 k_Loaking + MvTqdpSigaPtasp_Cnt_u08 0 k_Loaking + MvTqdpSigaPtasp_Cnt_u08 0 k_Loaking + MvTqdpSigaPtasp_Cnt_u08 0 k_Loaking + MvTqdpdSigaPtasp_Cnt_u08 0 k_Loaking + MvTqdpdSigaPtasp_Cnt_u08 1 k_Loaking - MvTqdpdSigaPtasp_Cnt_u08 5 k_Loaking - MvTqdpdSigaPtasp_Cnt_u08 5 k_Loaking - MvTqdpdSigaPtasp_Cnt_u08 1 k_Loaking - MvTqdpdSigaPtasp_Cnt_u08 2 k_		
k_Loakig_HwTqMighrisca_Uis_S2 1 k_Loakig_HwTqMighrisca_Uis_S2 1 k_Loakig_HwTqMisigrResp_Cnt_U08 0 k_Loakig_HwTqMisigrResp_Cnt_U08 0 k_Loakig_HwTqMighrisca_Uis_S2 1 k_Loakig_HwTqMighrisca_Uis_S2 1 k_Loakig_HwTqMighrisca_Uis_S2 1 k_Loakig_HwTqMighrisca_Uis_S2 1 k_Loakig_HwTqMighrisca_Uis_S2 1 k_Loakig_MwTqMighrisca_Uis_S2 1 k_Loakig_MwTqMighrisca_Uis_S2 1 k_Loakig_MwTqMighrisca_Uis_S2 1 k_Loakig_MwTqMighrisca_Uis_S3 2 k_Loakig_MwTqMighrisca_Uis_S3 2 k_Loakig_MwTqMighrisca_Uis_S3 1 k_Loakig_MwTqMighrisca_Uis_S2 1		
k_Loaklg_HwTqMtghSig1Resp_Cnt_U08 0 k_Loaklg_IvirtighSig1Resp_Cnt_U08 0 k_Loaklg_IvirtighSig1Resp_Cnt_U08 0 k_Loaklg_IvirtighSig1Resp_Cnt_U08 0 k_Loaklg_IvirtighSig1Resp_Cnt_U08 1 k_Loaklg_IvirtighResc_Uls_IS_22 1 k_Loaklg_IvirtighSig1RoSmaResp_Cnt_U08 5 k_Loaklg_ModighSig1Resp_Cnt_U08 5 k_Loaklg_ModighSig1Resp_Cnt_U08 0 k_Loaklg_ModighSig1Resp_Cnt_U08 0 k_Loaklg_ModighSig1Resp_Cnt_U08 2 k_Loaklg_ModighTig1Resp_Cnt_U08 2 k_Loaklg_ModighTig1Resp_Cnt_U08 2 k_Loaklg_ModighTig1Resp_Cnt_U08 0 k_Loaklg_ModighTig1Resp_Cnt_U08 0 k_Loaklg_ModighTig1Resp_Cnt_U08 0 k_Loaklg_ModighTig1Resp_Cnt_U08 0 k_Loa		
k_Loaklgr_Ivridp/Sig/Resp_Cnt_U08 0 k_Loaklgr_Ivridp/Sig/Resp_Cnt_U08 0 k_Loaklgr_IvriMghrRate_UlspS_132 1 k_Loaklgr_IvriMghrRate_UlspS_132 1 k_Loaklgr_IvriMghrSazeroEn_Cnt_Up 0 k_Loaklgr_IvriMghrSazeroEn_Cnt_Up 1 k_Loaklgr_MotAglotpSig/Resp_Cnt_Up 1 k_Loaklgr_MotAglotpSig/Resp_Cnt_Up 1 k_Loaklgr_MotAglotpSig/Resp_Cnt_Up 1 k_Loaklgr_MotAglotpSig/Resp_Cnt_Up 2 k_Loaklgr_MotCurridpsiSig/Resp_Cnt_Up 2 k_Loaklgr_MotCurridpsiSig/Resp_Cnt_Up 0 k_Loaklgr_MotCurridpsiSig/Resp_Cnt_Up 0 k_Loaklgr_MotCurridpsiSig/Resp_Cnt_Up 0 k_Loaklgr_MotCurridpsiSig/Resp_Cnt_Up 0 k_Loaklgr_MotCurridpsiSig/Resp_Cnt_Up 0 k_Loaklgr_MotCurridpsiSig/Resp_Cn		
k_Loaklgr_IvridptSig2Resp_Cnt_u08 0 k_Loaklgr_IvridptSig2Resp_Cnt_u08 0 k_Loaklgr_IvridptSig2Resp_Cnt_u02 1 k_Loaklgr_IvridptSig2Resp_Cnt_u08 1 k_Loaklgr_WorklightSig2Resp_Cnt_u08 5 k_Loaklgr_MorLiqdptSig2Resp_Cnt_u08 1 k_Loaklgr_MorLiqdptSig2Resp_Cnt_u08 0 k_Loaklgr_MorLiqdptSig2Resp_Cnt_u08 0 k_Loaklgr_MorLiqdptSig3Resp_Cnt_u08 2 k_Loaklgr_MorLiqdptSig3Resp_Cnt_u08 0 k_Loaklgr_MorLiqdptSig3Resp_Cnt_u08 0 k_Loaklgr_MorLiqdptSig3Resp_Cnt_u08 0 k_Loaklgr_MorLiqdptSig3Resp_Cnt_u08 0 k_Loaklgr_MorLiqdptSig3Resp_Cnt_u08 0 k_Loaklgr_MorLiqdptSig3Resp_Cnt_u08 0 k_Loaklgr_MorLiqdptSig3Resp_C		
k_Loaklgr_IvrhftghrRate_UlspS_132 1 k_Loaklgr_IvrhftghrRate_UlspS_132 1 k_Loaklgr_IvrhftghrSacZeroEn_Cnt _UlspC 0 k_Loaklgr_IvrhftghrSacZeroEn_Cnt_Uo8 5 k_Loaklgr_MortAgloptsigRate_UspS_10SmaResp_Cnt_uo8 1 k_Loaklgr_MortAgloptsigResp_Cnt_uo8 1 k_Loaklgr_MortAgloptsigResp_Cnt_uo8 2 k_Loaklgr_MortAgloptsigResp_Cnt_uo8 2 k_Loaklgr_MortAghtghrSac _UlspS_132 1 k_Loaklgr_MortAghtghrSac _UlspS_132 1 k_Loaklgr_MortAghtghrSac _UlspS_132 1 k_Loaklgr_MortAghtghrSac _UlspS_132 1 k_Loaklgr_MortCurridpsIsjGResp_Cnt_uo8 2 k_Loaklgr_MortCurridpsIsjGResp_Cnt_uo8 2 k_Loaklgr_MortCurridpsIsjGResp_Cnt_uo8 0 k_Loaklgr_MortCurridpsIsjGResp_Cnt_uo8 0 </td <td></td> <td></td>		
k_Loaklgr_IvrifughtRighr_IvrifughtRate_UlspS_132 1 k_Loaklgr_IvrifughtRate_UlspS_132 0 k_Loaklgr_IvrifughtSacZeroEn_Cnt_Ug 0 k_Loaklgr_MorkJightSig1NoSmaResp_Cnt_u08 5 k_Loaklgr_MorkJightSig1NoSmaResp_Cnt_u08 1 k_Loaklgr_MorkJightSig1Resp_Cnt_u08 2 k_Loaklgr_MorkJightSig1Resp_Cnt_u08 2 k_Loaklgr_MorkJightSig2Resp_Cnt_u08 2 k_Loaklgr_MorkJightSig2Resp_Cnt_u08 2 k_Loaklgr_MorkJightSig2Resp_Cnt_u08 2 k_Loaklgr_MorkJightSig2Resp_Cnt_u08 2 k_Loaklgr_MorkJightSig2Resp_Cnt_u08 2 k_Loaklgr_MortUndrySig1Resp_Cnt_u08 2 k_Loaklgr_MortUndrySig1Resp_Cnt_u08 0		
k_Loaklgr_IvriNtginScaZeroEn_Cnt_igc 0 k_Loaklgr_NotAlginSica_Uis_ft22 1 k_Loaklgr_MotAglqinSiig1Rosp_Cnt_iu08 5 k_Loaklgr_MotAglqinSiig1Rosp_Cnt_iu08 1 k_Loaklgr_MotAglqinSiig1Rosp_Cnt_iu08 0 k_Loaklgr_MotAglqinSiig1Rosp_Cnt_iu08 2 k_Loaklgr_MotAgMtginScaZeroEn_Cnt_igc 0 k_Loaklgr_MotAgMtginScaZeroEn_Cnt_igc 0 k_Loaklgr_MotAgMtginSca_Uis_ft32 1 k_Loaklgr_MotAgMtginSca_Uis_ft32 1 k_Loaklgr_MotAgMtginSca_Uis_ft32 1 k_Loaklgr_MotAgMtginSca_Dis_ft28 2 k_Loaklgr_MotAgMtginSca_Dis_ft28 2 k_Loaklgr_MotCurridptSigTeresp_Cnt_iu08 2 k_Loaklgr_MotCurridptSigTeresp_Cnt_iu08 0 k_Loaklgr_MotCurridptSigTeresp_Cnt_iu08 0 k_Loaklgr_MotCurridptSigTeresp_Cnt_iu08 0 k_Loaklgr_MotCurridptSigTeresp_Cnt_iu08 0 k_Loaklgr_MotCurridptSigTeresp_Cnt_iu08 1 k_Loaklgr_MotCurridptSigCatt_iu08 1 k_Loaklgr_MotCurridptSigCatt_iu08 1 k_Loaklgr_MotCurridptSigCatt_iu08 1 k_Loaklgr		
k_Loakigr_IvriNtiginSca_Uis_732 1 k_Loakigr_IvriNtiginSca_Uis_732 1 k_Loakigr_MokajdighSiginNsSmaResp_Cnt_u08 5 k_Loakigr_MokajdighSiginNsSmaResp_Cnt_u08 0 k_Loakigr_MokajdighSiginSca_Cnt_u08 0 k_Loakigr_MokajdighSiginSca_Cnt_u08 2 k_Loakigr_MokajdighSiginSca_ZeroEn_Cnt_u08 2 k_Loakigr_MokajdiginSca_ZeroEn_Cnt_u08 2 k_Loakigr_MokajdiginSca_Dis_132 1 k_Loakigr_MokajdiginSca_Dis_132 1 k_Loakigr_MokajdiginSca_Dis_132 1 k_Loakigr_MokajdiginSca_Dis_132 1 k_Loakigr_MokajdiginSca_Dis_132 1 k_Loakigr_MokajdiginSca_Dis_132 2 k_Loakigr_MokajdiginSca_Dis_132 2 k_Loakigr_MokajdiginSca_Dis_132 0 k_Loakigr_MokalidiginSca_Dis_132 1		
k_Loak/gr_Motkglopkis/ankosmaResp_Cnt_u08 5 k_Loak/gr_Motkglopkis/ankosmaResp_Cnt_u08 1 k_Loak/gr_Motkglopkis/ankosmaResp_Cnt_u08 1 k_Loak/gr_Motkglopkis/ankosmaResp_Cnt_u08 0 k_Loak/gr_Motkglopkis/ankosmaResp_Cnt_u08 2 k_Loak/gr_Motkglopkis/ankosmaResp_Cnt_u08 1 k_Loak/gr_Motkglopkis/ankosmaResp_Cnt_u08 2 k_Loak/gr_MotkgrankosmaResp_Cnt_u08 2 k_Loak/gr_MotCurridpts/apsa-AvairResp_Cnt_u08 2 k_Loak/gr_MotCurridpts/apsa-pc_nt_u08 0 k_Loak/gr_MotCurridpts/apsa-pc_nt_u08 1 k_Loak/gr_MotCurridpts/apsa-pc_nt_u08 1 k_Loak/gr_MotCurridpts/apsa-pc_nt_u08 1 k_Loak/gr_MotCurridpts/apsa-pc_nt_u08 1 k_Loak/gr_MotCurridpts/apsa-pc_nt_u0		
k_Loaklgr_MokJqloptSig1NoSmaResp_Cnt_u08 1 k_Loaklgr_MokJqloptSig2Resp_Cnt_u08 1 k_Loaklgr_MokJqloptSig3Resp_Cnt_u08 0 k_Loaklgr_MokJqloptSig3Resp_Cnt_u08 2 k_Loaklgr_MokJqloptSig3Resp_Cnt_u08 2 k_Loaklgr_MokJqklightSig2Resp_Cnt_u19c 0 k_Loaklgr_MokJqklightSig2 Lills_f32 1 k_Loaklgr_MokJqklightSig2Resp_Cnt_u08 2 k_Loaklgr_MokCurridptSig1Resp_Cnt_u08 2 k_Loaklgr_MokCurridptSig1Resp_Cnt_u08 0 k_Loaklgr_MokCurridptSig2Resp_Cnt_u08 1 k_Loaklgr_MokCurridptSig2Resp_Cnt_u08 1 k_Loaklgr_MokCurridptSig2Resp_Cnt_u08 1 k_Loaklgr_MokCurridptSig2Resp_Cnt_u08 1 k_Loaklgr_MokCurridptSig2Resp_Cnt_u08 1 k_Loaklgr_MokCurridptSig2Resp_Cnt_u08 1		
k_Loaklgr_MokAgldptSig2Resp_Cnt_u08 0 k_Loaklgr_MokAgldptSig1Resp_Cnt_u08 0 k_Loaklgr_MokAgldptSig1Resp_Cnt_u08 2 k_Loaklgr_MokAgldptSig1Resp_Cnt_u08 2 k_Loaklgr_MokAgMtgnRate_UlspS_132 1 k_Loaklgr_MokAgMtgnRase_Us_152 1 k_Loaklgr_MokAgMtgnRase_Us_152 1 k_Loaklgr_MokCurridptSig0Resp_Cnt_u08 2 k_Loaklgr_MokCurridptSig0Resp_Cnt_u08 0 k_Loaklgr_MokCurridptSig1Resp_Cnt_u08 0 k_Loaklgr_MokCurridptSig1Resp_Cnt_u08 0 k_Loaklgr_MokCurridptSig1Resp_Cnt_u08 0 k_Loaklgr_MokCurridptSig1Resp_Cnt_u08 0 k_Loaklgr_MokCurridptSig1Resp_Cnt_u08 0 k_Loaklgr_MokCurridptSig2Sig1Resp_Cnt_u08 1 k_Loaklgr_MokCurridptSig2Sig1Resp_G2 1 k_Loaklgr_RedGSIRate_Ulsp_5132 1 k_Loaklgr_RedGSIRate_Ulsp_5132 1 k_Loaklgr_RedGSIRate_Ulsp_5132 1 k_Loaklgr_RedGSIRate_Ulsp_5132 1 k_Loaklgr_Per1_HTVTqlqptSig_Cnt_u08.value 1 target_Loaklgr_Per1_HTVTqlqptSig_Cnt_u08.value 1 target_Loaklgr_		
k_LoaMgr_MorAgidptSigFitThd_Cnt_u08 2 k_LoaMgr_MorAgMitghRate_UispS_132 1 k_LoaMgr_MorAgMitghRate_UispS_132 1 k_LoaMgr_MorAgMitghRate_UispS_132 1 k_LoaMgr_MorAgMitghRate_UispS_132 1 k_LoaMgr_MorAgMitghRate_UispS_132 1 k_LoaMgr_MorCurridptSigGResp_Cnt_u08 2 k_LoaMgr_MotCurridptSigGResp_Cnt_u08 0 k_LoaMgr_MotCurridptSigFitThd_Cnt_u08 0 k_LoaMgr_MotCurridptSigGResp_Cnt_u08 0 k_LoaMgr_MotCurridptSigGResp_Cnt_u08 0 k_LoaMgr_MotCurridptSigFitThd_Cnt_u08 0 k_LoaMgr_MotCurridptSigFitThd_Cnt_u08 0 k_LoaMgr_MotCurridptSigCa_UispS_122 1 k_LoaMgr_RedodSiRate_UispS_72 1 k_LoaMgr_RedodSiRate_UispS_732 1 k_LoaMgr_RedodSiRate_UispS_732 1 k_LoaMgr_RedodSiRate_UispS_732 1 k_LoaMgr_RedodSiRate_UispS_0.Cnt_u08.value 1 target_LoaMgr_Per1_DiagcStsivtr1inactv_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStsivtr2inactv_ont_lgc.value 1 target_LoaMgr_Per1_MorCurridptSig_Cnt_u08.value 1		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32 1 k_LoaMgr_MotAgMtgtnSca_Uls_f32 0 k_LoaMgr_MotAgMtgtnSca_Uls_f32 1 k_LoaMgr_MotAgMtgtnSca_Uls_f32 2 k_LoaMgr_MotCurridptSigOResp_Cnt_u08 2 k_LoaMgr_MotCurridptSigOResp_Cnt_u08 0 k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08 0 k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08 0 k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08 0 k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08 0 k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08 1 k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08 1 k_LoaMgr_MotCurridptSigFitTnd_Cnt_lgc 1 k_LoaMgr_MotCurridptSigSig_Cnt_u32 1 k_LoaMgr_ReddSiSca_Uls_f32 1 k_LoaMgr_ReddSiSca_Uls_f32 1 target_LoaMgr_Per1_DiagcSistvitrInactv_Cnt_lgc value 1 target_LoaMgr_Per1_DiagcSistvitTnactv_Cnt_lgc value 1 target_LoaMgr_Per1_MotCurridptSig_Cnt_u08 value 1 target_LoaMgr_Per1_MrPosliptSig_Cnt_u08 value 1 target_LoaMgr_Per1_MrPosliptSig_Cnt_u08 value 1 target_LoaMgr_Per1_TioaOl_Cnt_lgc value 0		
k_LoaMgr_MotAgMtgtnSca_ZeroEn_Cnt_lgc 0 k_LoaMgr_MotAgMtgtnSca_Us_132 1 k_LoaMgr_MotAgMtgtnSca_Us_132 1 k_LoaMgr_MotCurridgtSigResp_Cnt_u08 2 k_LoaMgr_MotCurridgtSigResp_Cnt_u08 0 k_LoaMgr_MotCurridgtSigResp_Cnt_u08 0 k_LoaMgr_MotCurridgtSigResp_Cnt_u08 0 k_LoaMgr_MotCurridgtSigResp_Cnt_u08 0 k_LoaMgr_MotCurridgtnSca_UspS_132 1 k_LoaMgr_MotCurridgtnSca_UspS_132 1 k_LoaMgr_RedcdSiRate_UspS_132 1 k_LoaMgr_RedcdSiRate_UspS_132 1 k_LoaMgr_Per1_DiagcStshvtr1lnactv_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStshvtr2lnactv_Cnt_lgc.value 1 target_LoaMgr_Per1_HortQdptSig_Cnt_u08 value 1 target_LoaMgr_Per1_MotCurridptSig_Cnt_u08 value 1 target_LoaMgr_Per1_MotCurridptSig_Cnt_u08 value 1 target_LoaMgr_Per1_MotCurridptSig_Cnt_u08 value 1 target_LoaMgr_Per1_ToaAvail_Cnt_lgc value 1 target_LoaMgr_Per1_ToaAvail_Cnt_lgc value 1 target_LoaMgr_Per1_ToaAvail_Cnt_lgc value 1 target_LoaMgr_Per1_ToaD_LoaMgr_LoaMgr_Per1_Dia		
k_LoaMgr_MotAgMtgtnSca_Uls_f32 1 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 2 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_MotCurridptSig2Sig2 1 k_LoaMgr_MotCurridptSig2Sig2 1 k_LoaMgr_RedcdStRate_UlspS_f32 1 k_LoaMgr_RedcdStSca_Uls_f32 1 k_LoaMgr_Perd_DiagcStsIvtr2Inactv_Cnt_Igc.value 1 target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value 1 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value 1 target_LoaMgr_Per1_MotCurridptSig_Cnt_u08 value 1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 value 1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 value 1 target_LoaMgr_Per1_ToaDi_Cnt_Igc.value 0 target_LoaMgr_Per1_ToaDi_Cnt_Igc.value 1 target_LoaMgr_Per1_ToaDi_Cnt_Igc.value 0		
k_LoaMgr_MotOgSmaAvailResp_Cnt_u08 2 k_LoaMgr_MotCurridptSig0Resp_Cnt_u08 2 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig5Ps_CaroEn_Cnt_lgc 1 k_LoaMgr_MotCurridptSig_Cnt_U5 1 k_LoaMgr_ReddSitRate_UispS_f32 1 k_LoaMgr_ReddSitRate_UispS_f32 1 k_LoaMgr_Per1_HwTqldptSig_Cnt_U08_value 1 target_LoaMgr_Per1_HwTqldptSig_Cnt_U08_value 1 target_LoaMgr_Per1_MotCurridptSig_Cnt_U08_value 1 target_LoaMgr_Per1_ToaAvail_Cnt_lgc_value 1 target_LoaMgr_Per1_ToaAvail_C		
k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 2 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig1FitTnd_Cnt_u08 0 k_LoaMgr_MotCurrMtgtnReate_UlspS_f32 1 k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc 1 k_LoaMgr_MotCurrMtgtnSca_Uls_f32 1 k_LoaMgr_RedcdStRate_UlspS_f32 1 k_LoaMgr_RedcdStSca_Uls_f32 1 k_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc.value 1 target_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc.value 1 target_LoaMgr_Perd_LoaScaDi_Cnt_u08.value 1 target_LoaMgr_Perd_MotCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Perd_MotCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Perd_MotCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Perd_MotCurrldptSig_Cnt_u08.value 3 target_LoaMgr_Perd_SteerMod_Cnt_enum.value 1 target_LoaMgr_Perd_MotCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Perd_TloaDi_Cnt_lgc.value 0 target_Re_Inst_Ap_LoaMgr_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc target_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_P		
k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Trind_Cnt_u08 1 k_LoaMgr_MotCurridptSig2Sig2 1 k_LoaMgr_MotCurridptSig2Sig2 1 k_LoaMgr_RedcdStRate_UispS_f32 1 k_LoaMgr_RedcdStSca_Uis_f32 1 target_LoaMgr_Perd_DiagcStsivtr1inactv_Cnt_lgc_value 1 target_LoaMgr_Perd_DiagcStsivtr2inactv_Cnt_lgc_value 1 target_LoaMgr_Perd_LoaScaDi_Cnt_lgc_value 1 target_LoaMgr_Perd_MicCurridptSig_Cnt_u08_value 1 target_LoaMgr_Perd_MicCurridptSig_Cnt_u08_value 1 target_LoaMgr_Perd_MicrosloptSig_Cnt_u08_value 1 target_LoaMgr_Perd_IntrPosloptSig_Cnt_u08_value 1 target_LoaMgr_Perd_IntrPosloptSig_Cnt_u08_value 1 target_LoaMgr_Perd_Incavaii_Cnt_lgc_value 1 target_LoaMgr_Perd_Incavaii_Cnt_lgc_value 1 target_LoaMgr_Perd_Incavaii_Cnt_lgc_value 1 target_LoaMgr_Perd_Incavaii_Cnt_lgc_value 1 target_Loa		
k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 k_LoaMgr_MotCurriMtgtnRate_UlspS_f32 k_LoaMgr_MotCurriMtgtnRate_UlspS_f32 k_LoaMgr_MotCurriMtgtnSca_Uls_f32 k_LoaMgr_MotCurriMtgtnSca_Uls_f32 k_LoaMgr_MotCurriMtgtnSca_Uls_f32 k_LoaMgr_MotCurriMtgtnSca_Uls_f32 k_LoaMgr_MotCurriMtgtnSca_Uls_f32 k_LoaMgr_RedcdSiRate_UlspS_f32 k_LoaMgr_RedcdSiRate_UlspS_f32 k_LoaMgr_Perd_DiagcStslvtrInactv_Cnt_lgc_value larget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc_value larget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc_value larget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc_value larget_LoaMgr_Per1_MotCurridptSig_Cnt_u08_value larget_LoaMgr_Per1_MotCurridptSig_Cnt_u08_value larget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08_value larget_LoaMgr_Per1_MtrPosldptSig_Cnt_u08_value larget_LoaMgr_Per1_TloaAvail_Cnt_lgc_value larget_LoaMgr_Per1_TloaDi_Cnt_lgc_value larget_LoaMgr_Per1_TloaDi_Cnt_lgc_value larget_LoaMgr_Per1_TloaDi_Cnt_lgc_value larget_LoaMgr_Per1_TloaDi_Cnt_lgc_value larget_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc larget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc larget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc larget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc larget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc larget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum larget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc larget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc larget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc larget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc larget_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc larget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc larget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc larget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc larget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc larget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc larget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc larget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc larget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc larget_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaD		
k_LoaMgr_MotCurridptSigFitTnd_Cnt_u08 k_LoaMgr_MotCurrMigtnRate_UispS_f32 k_LoaMgr_MotCurrMigtnScaZeroEn_Cnt_lgc lt_LoaMgr_MotCurrMigtnScaZeroEn_Cnt_lgc lt_LoaMgr_MotCurrMigtnSca_Uis_f32 lt_LoaMgr_RedcdSiRate_UispS_f32 lt_LoaMgr_RedcdSiRate_UispS_f32 lt_LoaMgr_RedcdSiRate_UispS_f32 lt_arget_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc.value ltarget_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc.value ltarget_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc.value ltarget_LoaMgr_Per1_HorTqldptSig_Cnt_u08.value ltarget_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value ltarget_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value ltarget_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value ltarget_LoaMgr_Per1_MotCurridptSig_Cnt_u08.value ltarget_LoaMgr_Per1_TloaPoidptSig_Cnt_u08.value ltarget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value ltarget_LoaMgr_Per1_TloaAvail_Cnt_lgc.value ltarget_LoaMgr_Per1_TloaDi_Cnt_lgc.value ltarget_LoaMgr_Per1_TloaDi_Cnt_lgc.value ltarget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc ltarget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtrZinactv_Cnt_lgc ltarget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc ltarget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc ltarget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum ltarget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaStaDi_Cnt_lgc ltarget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaStaDi_Cnt_lgc ltarget_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc ltarget_Re_Inst_Ap_LoaMgr_Red_IndotCurridptSig_Cnt_u08		
k_LoaMgr_MotCurrMigtnRate_UlspS_f32 k_LoaMgr_MotCurrMigtnSca_Uls_f32 k_LoaMgr_MotCurrMigtnSca_Uls_f32 k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStRate_UlspS_f32 t_LoaMgr_RedcdStSta_Uls_f32 1 k_LoaMgr_RedcdStSta_Uls_f32 1 target_LoaMgr_Per1_DiagcStstvtr1Inactv_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStstvtr2Inactv_Cnt_lgc.value 1 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08_value 1 target_LoaMgr_Per1_LoaScaD_icn_lgc.value 1 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08_value 1 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MirPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaD_icn_lgc.value 1 target_LoaMgr_Per1_TloaD_icn_lgc.value 1 target_LoaMgr_Per1_TloaD_icn_lgc.value 1 target_LoaMgr_Per1_TloaD_icn_lgc.value 1 target_LoaMgr_Per1_TloaD_icn_lgc.value 1 target_LoaMgr_Per1_DoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_LwTqLoaMtgtnEn_Cnt_lgc target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_LoASc_State_enum target_Rte_inst_Ap_LoaMgr_LoaMgr_Per1_LoaSc_State_enum target_LoaMgr_Per1_MotAgLoaMtgnEn_Cnt_lgc target_Rte_inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc		
k_LoaMgr_MotCurrNtgtnSca_Uis_32 k_LoaMgr_RedcdStRate_UispS_f32 k_LoaMgr_RedcdStRate_UispS_f32 k_LoaMgr_RedcdStSca_Uis_f32 target_LoaMgr_Perl_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Perl_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Perl_HwTqldptSig_Cnt_uo8.value target_LoaMgr_Perl_MotCurrldptSig_Cnt_uo8.value target_LoaMgr_Perl_MotCurrldptSig_Cnt_uo8.value target_LoaMgr_Perl_MtrPosldptSig_Cnt_uo8.value target_LoaMgr_Perl_MtrPosldptSig_Cnt_uo8.value target_LoaMgr_Perl_SteerMod_Cnt_enum.value target_LoaMgr_Perl_TloaDi_Cnt_lgc.value target_LoaMgr_Perl_TloaDi_Cnt_lgc.value target_LoaMgr_Perl_TloaDi_Cnt_lgc.value target_LoaMgr_Perl_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_HwTqldptSig_Cnt_uo8 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_HwTqldptSig_Cnt_uo8 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_HwTqloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_HvTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Perl_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Perl_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Perl_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Perl_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Perl_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Perl_MotAgLoaMtgtnEn_Cnt_lgc		
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1lnactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqlcaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_VrtLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotCurrldptSig_Cnt_u08		
k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1lnactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr1lnactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_Mord_LoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_Mord_LoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_Mord_LoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_Mord_LoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_Mord_LoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_Mord_LoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_Mord_LoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_Mord_LoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_Mord_LoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_Mord_LoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_Mord_LoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_Mord_LoaMtgnEn_Cnt_lgc target_LoaMgr_Per1_Mord_LoaMtgnEn_Cnt_lgc		
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target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
$target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 \\ target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 \\$		
target Rte Inst Ap LoaMgr.LoaMgr.Per1 MotCurrLoaMtgtnEn Cnt Igc target LoaMgr.Per1 MotCurrLoaMtgtnEn Cnt Igc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name Actual Value Expected Value	Re	Result
LoaMgr_AgSV_Cnt_M_lgc 0		~
LoaMgr_HwTqldptSigSV_Cnt_M_u08 0		✓
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc 1		~
LoaMgr_HwTqRespSV_Cnt_M_u08 2		~
LoaMgr_lvtrldptSigSV_Cnt_M_u08 0		~
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc 0 0		~





Name	Actual Value	Expected Value	Result
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	✓
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	✓
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 175}	{170, 172, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.2 (Repeat Count = 1)	13
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	4
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1





Name	Input Value		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	2		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08 k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	:nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C	:nt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	3	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	t_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Ci		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_ut target_LoaMgr_Per1_SteerMod_Cnt_enum	JO	
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_Igc	target LoaMgr Per1 TloaAvail Cnt lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_Igc	target LoaMgr Per1 TloaDi Cnt Igc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	- Toodit
LoaMgr_HwTqldptSigSV_Cnt_M_u08	4	4	✓
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	3	3	✓
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	1	1	*
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	-
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	-
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	-
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 175}	{170, 173, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~





Name	Actual Value	Expected Value	Result
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc.value	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	•
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	•
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•

Test Step 3.3 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k LoaMgr lvtrMtgtnScaZeroEn Cnt lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k LoaMgr MotCurrldptSig2Resp Cnt u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0

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LoaMgr_Per1

Name	Input Value		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k LoaMgr RedcdStSca Uls f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target LoaMgr Per1 LoaScaDi Cnt Igc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Ont lac	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C		
target Rte Inst Ap LoaMgr.LoaMgr Per I I I I I I I I I I I I I I I I I I I			
	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	igc	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LoASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	tat Inc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_HwTqRespSV_Cnt_M_u08	3	3	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	✓
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	✓
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	-
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	✓
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	-
LoaMgr MotAgLoaMtgtnEn Cnt M lgc	1	1	-
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	✓
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	-
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	-
3 2 3 2 3 3 2 2 2 2 3			
LoaMar MotCurrRespSV Cnt M u08		2	
LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail Cnt M loc	2	2	*
LoaMgr_TloaAvail_Cnt_M_lgc	2	1	ž
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	2 1 0	1 0	•
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	2 1 0 0	1 0 0	•
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	2 1 0 0 (170)	1 0 0 (170)	~
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2 1 0 0 (170) {1}	1 0 0 {170} {0, 0, 0, 0, 0}	\(\frac{1}{2}\)
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	2 1 0 0 (170) (1) {1}	1 0 0 {170} {0, 0, 0, 0, 0} {1}	\(\frac{1}{2}\)
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	2 1 0 0 (170) (1) (1) (1) 500	1 0 0 {170} {0, 0, 0, 0, 0} {1} 500 ± 0.01	**************************************
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	2 1 0 0 (170) (1) (1) (1) 500	1 0 0 {170} {0, 0, 0, 0, 0} {1} 500 ± 0.01 0 ± 0.001	**************************************
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	2 1 0 0 (170) (1) (1) (1) 500 0	1 0 0 {170} {0, 0, 0, 0, 0} {1} 500 ± 0.01 0 ± 0.001	**************************************
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	2 1 0 0 (170) {1} {1} 500 0	1 0 0 {170} {0, 0, 0, 0, 0} {1} 500 ± 0.01 0 ± 0.001 1	**************************************
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lotrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	2 1 0 0 (170) {1} {1} 500 0 1 0 5	1 0 0 {170} {0, 0, 0, 0, 0} {1} 500 ± 0.01 0 ± 0.001 1 0 5	**************************************
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	2 1 0 0 (170) {1} {1} 500 0	1 0 0 {170} {0, 0, 0, 0, 0} {1} 500 ± 0.01 0 ± 0.001 1	**************************************



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	~
AssignScale	1	AssignScale	1	✓
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.4 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
	2
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1
1	





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Ci	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	-	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target Rte Inst Ap LoaMgr.LoaMgr Per1 MtrPosldptSig Cnt u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M Igc	0	0	✓ ×
LoaMgr HwTqldptSiqSV Cnt M u08	2	2	_
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	-
LoaMgr lvtrldptSigSV Cnt M u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_IvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum	5	5	·
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	_
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	-
LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	-
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 173, 175}	{170, 173, 175}	-
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_endin)	{1, 1, 1}	{0, 0, 0, 0, 0}	W
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)		{1, 1, 1}	
Rte Write Ap LoaMgr LoaRateLimit UlspS f32(data)	{1, 1, 1} 500	500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_hw1qLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	
target LoaMgr Per1 LOASt State enum.value	5	5	_
· - ·	0	0	
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	-
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	✓
RqstHwTqResp	1	RqstHwTqResp	1	✓
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Test Step 3.5 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	5
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4 2
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k LoaMgr MotAgMtgtnRate UlspS f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k LoaMgr MotAgMtgtnSca Uls f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_UIs_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMigr_Per1_Loascabi_Crit_igc target_LoaMigr_Per1_MotAgLoaMigtnEn_Crit_igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
	,





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	5	5	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08	1	1	~
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	3	3	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	-
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Fest Step 3.6 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	5
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	1		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08 k LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08 k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IVtrLoaMtgtnEn_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 MotAgLoaMtgtnEn Cnt Igc	target_LoaMgr_Per1_LoaScaDi_Cit_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_t	Cnt lac	
target Rte Inst Ap LoaMgr.LoaMgr Per1 MotCurrldptSig Cnt u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target LoaMgr Per1 MotCurrLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enur		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	5	5	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	2	2	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	✓
LoaMgr_MotAglopMatteEp_Cot_M_u08	2	2	*
LoaMgr_MotAgRospSV_Cpt_M_ugs	0 5	0 5	
LoaMgr MotAgRespSV Cnt M u08	3	3	V

LoaMgr_MotAgRespSV_Cnt_M_u08

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LoaMgr_Per1



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	✓
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	✓
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.7 (Repeat Count = 1)		
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_lvtrRespSV_Cnt_M_u08	2	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	4	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0	
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1	
k_LoaMgr_FadeOutStRate_UlspS_f32	1	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2	
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4	
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2	
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1	
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0	
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0	
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1	
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1	
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1	
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0	
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0	
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1	
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0	
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1	
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5	

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N	In most Malora		
Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k LoaMgr MotAgMtgtnRate UlspS f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32 k_LoaMgr_RedcdStRate_UlspS_f32	1		
k LoaMgr RedcdStSca Uls f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inact		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inact		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_IvtrLoaMtgtnEn_C target_LoaMgr_Per1_LOASt_State_enu		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEr		
target_Rte_Inst_Ap_LoaMgr_Der1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnl	_	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_en	um	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name			
TAILLY	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	Actual Value	Expected Value	Result
			Result
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1 2 1	1 2 1	· ·
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08	1 2 1 4	1 2 1 4	· ·
LoaMgr_Ag\$V_Cnt_M_lgc LoaMgr_HwTqldptSig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_lvtrldptSig\$V_Cnt_M_u08	1 2 1 4 0	1 2 1 4 0	· ·
LoaMgr_Ag\$V_Cnt_M_lgc LoaMgr_HwTqldptSig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_lvtrldptSig\$V_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1 2 1 4 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	1 2 1 4 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	· ·
LoaMgr_Ag\$V_Cnt_M_lgc LoaMgr_HwTqldptSig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_lvtrldptSig\$V_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrResp\$V_Cnt_M_u08	1 2 1 4 0 1 5 5	1 2 1 4 0 1 1 5	~
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	1 2 1 4 0 1 5 5 5	1 2 1 4 0 1 5 5 5	• • • • • • • • • • • • • • • • • • •
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	1 2 1 4 0 1 5 5 5 500	1 2 1 4 0 1 5 5 5 500	
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32	1 2 1 4 0 0 1 5 5 5 500 1 1	1 2 1 4 0 1 5 5 5 500 1 1	
LoaMgr_Ag\$V_Cnt_M_Igc LoaMgr_HwTqldpt\$ig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_Ivtrldpt\$ig\$V_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrResp\$V_Cnt_M_u08 LoaMgr_IvtrResp\$V_Cnt_M_u08 LoaMgr_LOA\$t_\$tate_M_enum LoaMgr_LoaRateLimit_Ulsp\$_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 2 1 4 0 0 1 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5	1 2 1 4 0 0 1 5 5 5 500 1 5 5	
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaState_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	1 2 1 4 0 0 1 5 5 5 500 1 1	1 2 1 4 0 1 5 5 5 500 1 1	
LoaMgr_Ag\$V_Cnt_M_Igc LoaMgr_HwTqldpt\$ig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_Ivtrldpt\$ig\$V_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrResp\$V_Cnt_M_u08 LoaMgr_IvtrResp\$V_Cnt_M_u08 LoaMgr_LOA\$t_\$tate_M_enum LoaMgr_LoaRateLimit_Ulsp\$_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 2 1 4 0 0 1 5 5 5 5 5 0 0 0 0 0 0 0 0 0 0 0 0	1 2 1 4 0 0 1 5 5 5 500 1 5 0 0	
LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaState_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1 2 1 4 0 0 1 1 5 5 5 500 1 1 5 0 0 1 1	1 2 1 4 0 0 1 5 5 5 500 1 1 5 0 1	
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaState_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08	1 2 1 4 0 0 1 1 5 5 5 5 500 1 1 5 0 0 1 4 4	1 2 1 4 0 0 1 1 5 5 5 500 1 1 5 0 1 4	
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaSt_ate_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08	1 2 1 4 0 0 1 1 5 5 5 5 0 0 1 1 4 0 0	1 2 1 4 0 1 5 5 5 500 1 5 0 1 4 0 0	
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgCoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIoaMtgtnEn_Cnt_M_Igc	1 2 1 4 0 0 1 1 5 5 5 5 0 0 1 1 4 0 0 0 0 0 0 0	1 2 1 4 0 0 1 5 5 5 5 0 0 1 4 4 0 0 0 0 0 0 0	
LoaMgr_Ag\$V_Cnt_M_lgc LoaMgr_HwTqldptSig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_IvtrldptSig\$V_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrResp\$V_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_Ulsp\$S_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MoxAgldptSig\$V_Cnt_M_u08 LoaMgr_MoxAgldptSig\$V_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	1 2 1 1 4 0 0 1 1 5 5 5 5 5 0 0 1 1 4 4 0 0 0 0 3 3 0 0 0 0	1 2 1 4 0 1 5 5 5 500 1 1 4 0 0 0 3 3 0 0 0 0	
LoaMgr_Ag\$V_Cnt_M_lgc LoaMgr_HwTqldptSig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_IvtrldptSig\$V_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrResp\$V_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSig\$V_Cnt_M_u08 LoaMgr_MotAgldptSig\$V_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSig\$V_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMd\$rc\$V_Cnt_M_u08	1 2 1 1 4 0 0 1 1 5 5 5 5 5 0 0 1 1 4 4 0 0 0 0 3 3 0 0 0 0 1 1	1 2 1 4 0 1 5 5 5 500 1 1 4 0 0 0 3 3 0 0 0 1 1	
LoaMgr_Ag\$V_Cnt_M_lgc LoaMgr_HwTqldptSig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_IvtrldptSig\$V_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrResp\$V_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_Ulsp\$_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSig\$V_Cnt_M_u08 LoaMgr_MotAgldptSig\$V_Cnt_M_u08 LoaMgr_MotAgResp\$V_Cnt_M_u08 LoaMgr_MotCurrldptSig\$V_Cnt_M_u08 LoaMgr_MotCurrldptSig\$V_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VtlgMdSrc\$V_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	1 2 1 4 0 0 1 5 5 5 5 500 1 1 5 5 0 0 1 1 4 4 0 0 0 0 3 3 0 0 0 0 1 1 {170, 173, 174}	1 2 1 4 0 0 1 5 5 5 500 1 1 4 0 0 0 3 3 0 0 0 1 {170, 173, 174}	
LoaMgr_Ag\$V_Cnt_M_lgc LoaMgr_HwTqldptSig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_IvtrldptSig\$V_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrResp\$V_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_Ulsp\$_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSig\$V_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrResp\$V_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMd\$rc\$V_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTC\$tatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTC\$tatus(Param_Cnt_T_u08)	1 2 1 4 0 0 1 5 5 5 5 500 1 1 5 5 0 0 1 1 4 4 0 0 0 0 3 3 0 0 0 1 1 {170, 173, 174}	1 2 1 1 4 0 0 1 1 5 5 5 500 1 1 5 0 0 0 1 1 4 0 0 0 0 3 0 0 0 1 1 {170, 173, 174} {0, 0, 0, 0, 0, 0}	
LoaMgr_Ag\$V_Cnt_M_lgc LoaMgr_HwTqldptSig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_IvtrldptSig\$V_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrResp\$V_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_Ulsp\$_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSig\$V_Cnt_M_u08 LoaMgr_MotAgldptSig\$V_Cnt_M_u08 LoaMgr_MotAgResp\$V_Cnt_M_u08 LoaMgr_MotCurrloptSig\$V_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMd\$rc\$V_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTC\$tatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTC\$tatus(Status_Cnt_T_enum)	1 2 1 4 0 0 1 5 5 5 5 500 1 1 5 5 0 0 1 1 4 4 0 0 0 0 3 3 0 0 0 0 1 {170, 173, 174} {1, 1, 1} {1, 1, 1}	1 2 1 4 0 1 5 5 5 500 1 5 0 1 4 0 0 1 4 0 0 1 4 0 0 1 4 0 0 1 {170, 173, 174} {0, 0, 0, 0, 0, 0} {1, 1, 1}	
LoaMgr_Ag\$V_Cnt_M_lgc LoaMgr_HwTqldptSig\$V_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqResp\$V_Cnt_M_u08 LoaMgr_IvtrldptSig\$V_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrResp\$V_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_Ulsp\$_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSig\$V_Cnt_M_u08 LoaMgr_MotAgdLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrldptSig\$V_Cnt_M_u08 LoaMgr_MotCurrldptSig\$V_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrc\$V_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	1 2 1 4 0 0 1 5 5 5 5 500 1 1 5 5 0 0 1 1 4 4 0 0 0 0 3 3 0 0 0 0 1 {170, 173, 174} {1, 1, 1} {1, 1, 1} 500	1 2 1 4 0 1 5 5 5 500 1 5 0 1 4 0 0 1 4 0 0 1 4 0 0 1 4 0 0 1 {170, 173, 174} {0, 0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01	
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_TloaAvaii_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UIspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	1 2 1 4 0 0 1 1 5 5 5 5 500 1 1 5 0 0 0 1 1 4 4 0 0 0 0 0 0 0 0 0 0 0 0 0	1 2 1 4 0 1 5 5 5 500 1 5 0 1 4 0 0 1 4 0 0 1 4 0 0 1 4 1 7 1 7 1 7 1 7 1 7 1 7 1 7 1 7 1 7	
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotOurrIdptSigSV_Cnt_M_u08 LoaMgr_MotOurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotOurrRespSV_Cnt_M_u08 LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_StltpSigMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UIspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	1 2 1 4 0 0 1 5 5 5 5 500 1 1 5 5 0 0 1 1 4 4 0 0 0 0 3 3 0 0 0 0 1 {170, 173, 174} {1, 1, 1} {1, 1, 1} 500	1 2 1 4 0 1 5 5 5 500 1 5 0 1 4 0 0 1 4 0 0 1 4 0 0 1 4 0 0 1 {170, 173, 174} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 1	
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_VttgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UIspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	1 2 1 4 0 1 5 5 5 500 1 5 0 1 4 0 0 1 4 1 4 0 0 0 1 4 170, 173, 174} {1, 1, 1} 500 1	1 2 1 4 0 1 5 5 5 500 1 5 0 1 4 0 0 1 4 0 0 1 4 0 0 1 4 1 7 1 7 1 7 1 7 1 7 1 7 1 7 1 7 1 7	
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotOurrIdptSigSV_Cnt_M_u08 LoaMgr_MotOurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotOurrRespSV_Cnt_M_u08 LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_StltpSigMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UIspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	1 2 1 4 0 1 5 5 5 5 0 1 4 0 0 1 5 0 1 4 0 0 1 4 1 4 0 0 1 {170, 173, 174} {1, 1, 1} 500 1 1 1 1	1 2 1 4 0 1 5 5 5 500 1 5 0 1 4 0 0 1 4 0 0 1 4 0 0 1 {170, 173, 174} {0, 0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001 1 1	
LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_LoaRateLimit_UIspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_StltpSidGyr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UIspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_LOASt_State_enum.value	1 2 1 4 0 1 5 5 5 5 0 1 4 0 0 1 5 0 1 4 0 0 1 4 1 4 0 0 1 {170, 173, 174} {1, 1, 1} 500 1 1 1 5	1 2 1 4 0 1 5 5 5 500 1 5 0 1 4 0 0 1 4 0 0 1 4 0 0 1 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 1 1 5	



est Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.8 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr MotAgldptSigSV Cnt M u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr MotCurrLoaMtgtnEn Cnt M Igc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target LoaMgr Per1 LoaScaDi Cnt Igc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	· 1000.11
LoaMgr HwTqldptSigSV Cnt M u08	0	0	_
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	·
LoaMgr_IvtrRespSV_Cnt_M_u08	0	0	
LoaMgr_LOASt_State_M_enum	5	5	V
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	_
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	
LoaMgr_TloaDi_Cnt_M_lgc	1	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 172}	{170, 172}	
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_endin)	{1, 1}	{0, 0, 0, 0, 0}	S
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_hw1qLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	
target LoaMgr Per1 LOASt State enum.value	5	5	
target_LoaMgr_Per1_LOASt_State_enum.value target LoaMgr_Per1_MotAgLoaMtgtnEn Cnt lgc.value	1	1	
		1	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~



Test Step 3.9 (Repeat Count = 1)	E .
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32 k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	1 5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 k LoaMgr MotCurrMtgtnScaZeroEn Cnt Igc	1
k LoaMgr MotCurrMtgtnSca Uls f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtrInlactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc



LoaMgr_Per1

Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	•
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	✓
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	•
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	✓
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•

Test Step 3.10 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Input Value



Name



Name	Input value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k LoaMgr HwTqMtgtnRate UlspS f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
	0		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08			
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
K_EGAINGI_TCGCGGGGGGGGG			
	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1 3		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1 3 0		
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target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgl.oaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08	1 3 0 2 0 2 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u0& target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_let target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0& target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Crt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_ct target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0& target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1	nt_lgc 3 t_lgc gc nt_lgc 08 Cnt_lgc 08 Expected Value 0	Result
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target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc.value target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MttPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_Igc.value target_LoaMgr_Per1_TloaDi_Cnt_Igc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqlaeAttptEn_Cnt_M_u08 LoaMgr_HwTqlaeAttptEn_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrloaRtet_Limit_UlspS_M_f32	1 3 0 2 0 2 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotCurrloptSig_Cnt_uCt target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ct target_LoaMgr_Per1_MtrPosIdptSig_Cnt_uCt target_LoaMgr_Per1_MtrPosIdptSig_Cnt_uCt target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 1 0 0 0 5 5 5 5000	nt_lgc st_lgc st_lgc gc nt_lgc 08 Cnt_lgc 08 Expected Value 0 1 1 0 0 0 5 5 5	***************************************
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_igc.value target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_igc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_igc.value target_LoaMgr_Per1_TloaDi_Cnt_igc.value target_LoaMgr_Per1_TloaDi_Cnt_igc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_igc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_igc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_igc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_igc Name LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqlaptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaScaleFctr_Uis_M_f32 LoaMgr_LoaScaleFctr_Uis_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 3 0 2 0 2 1 0 2 1 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_DiagcStsIvtr2Inactv_C target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrlotpSig_Cnt_uC target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cr target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cr target_LoaMgr_Per1_TioaAvail_Cnt_lgc target_LoaMgr_Per1_TioaDi_Cnt_lgc Actual Value 0 1 1 0 0 0 5 5 5 500 0	nt_lgc st_lgc gc st_lgc gc st_lgc st_	***************************************
target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc.value target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MttPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_Igc.value target_LoaMgr_Per1_TloaDi_Cnt_Igc.value target_LoaMgr_Per1_TloaDi_Cnt_Igc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMfgnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrloaRtgtnEn_Cnt_M_Igc LoaMgr_IvtrloaRtgtnEn_Cnt_M_Igc LoaMgr_IvtrloaRtgtnEn_Cnt_M_Igc LoaMgr_IvtrloaRtgtnEn_Cnt_M_Igc LoaMgr_IvtrloaRtgtnEn_Cnt_M_Igc LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	1 3 0 2 0 2 1 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_legt_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotQLoaMtgtnEn_Crt_larget_LoaMgr_Per1_MotCurrloaMtgtnEn_Crt_larget_LoaMgr_Per1_MotCurrloaMtgtnEn_Ctl_larget_LoaMgr_Per1_MtrPosldptSig_Cnt_u01 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_SteerMod_Cnt_lgc target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 1 0 0 0 5 5 5 500 0 0 5	nt_lgc st_lgc st_lgc gc nt_lgc os Expected Value 0 1 1 0 0 0 5 5 5 500 0 5	***************************************

target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value

LoaMgr_Per1

2018-05-18, 13:31:17+0530



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	✓
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	✓
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 175}	{170, 173, 175}	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	✓
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	✓
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target LoaMgr Per1 MotAgLoaMtgtnEn Cnt lgc.value	1	1	_

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.11 (Repeat Count = 1)	3
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5

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Name	Innut Value			
Name	Input Value			
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0			
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2			
k LoaMgr MotAgMtgtnRate UlspS f32	1			
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1			
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2			
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2			
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0			
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0			
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1			
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1			
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32	1			
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc.value	0			
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value	0			
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3			
target LoaMgr Per1 LoaScaDi Cnt Igc.value	1			
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2			
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2			
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0			
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Ina	actv_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Ina			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cr	_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnE			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_er			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_I target_LoaMgr_Per1_MotAgLoaMtgtnl			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgt	_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_0			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_e	_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_k			
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	-		
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	0	0	~	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	✓	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~	
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~	
LoaMgr_IvtrldptSigSV_Cnt_M_u08	2	2	~	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~	
LoaMgr_lvtrRespSV_Cnt_M_u08	1	1	~	
LoaMgr_LOASt_State_M_enum	5	5	Y	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500		
LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr MaxMtgtnResp Cnt M u08	5	5		
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0		
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1		
LoaMgr MotAgRespSV Cnt M u08	5	5	_	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	✓	
LoaMgr_MotCurrRespSV_Cnt_M_u08				
	3	3	✓	
LoaMgr_TloaAvail_Cnt_M_lgc		3 0	· ·	
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	3		· ·	
	3 0	0		
LoaMgr_TloaDi_Cnt_M_lgc	3 0 1	0	· ·	
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	3 0 1 0 {170}	0 1 0	**************************************	
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	3 0 1 0 {170} (1) {1}	0 1 0 {170} {0, 0, 0, 0, 0} {1}	¥	
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	3 0 1 0 {170} (1) {1} 500	0 1 0 {170} {0, 0, 0, 0, 0} {1} 500 ± 0.01	¥	
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	3 0 1 0 {170} {1} {1} 500	0 1 0 {170} {0, 0, 0, 0, 0} {1} 500 ± 0.01 1 ± 0.001	V	
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	3 0 1 0 {170} {1} {1} 500	0 1 0 {170} {0, 0, 0, 0, 0} {1} 500 ± 0.01 1 ± 0.001 0	¥	
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	3 0 1 0 {170} {1} {1} 500 1 0	0 1 0 {170} {0, 0, 0, 0, 0} {1} 500 ± 0.01 1 ± 0.001 0	V	
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	3 0 1 0 {170} {1} {1} 500 1 0 0	0 1 0 {170} {0, 0, 0, 0, 0} {1} 500 ± 0.01 1 ± 0.001 0 0 5	U	
LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	3 0 1 0 {170} {1} {1} 500 1 0	0 1 0 {170} {0, 0, 0, 0, 0} {1} 500 ± 0.01 1 ± 0.001 0	V	



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.12 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_UIs_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
	0
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotAgMtgtnSca_UIs_f32	2
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	
k_LoaMgr_MotCurridptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0 0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	1
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value	1		
target LoaMgr Per1 HwTqldptSig Cnt u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1In	nactv Cnt Igc	
target Rte Inst Ap LoaMgr.LoaMgr Per1 DiagcStslvtr2Inactv Cnt Igc	target_LoaMgr_Per1_DiagcStslvtr2In		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtr	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_e		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgti		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtg		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_	_	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgo		
Name	Actual Value	Expected Value	Result
	0	0	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	Ž
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	5	5	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr_LOASt_State_M_enum			
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	Ž
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgloptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	5	0 5	Ž
LoaMgr_MotAgRespSV_Cnt_M_u08			
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	*
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	•
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	V
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 175}	{170, 175}	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	•
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	*
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	✓





Name	Test Step 3.13 (Repeat Count = 1)	
Looking American American	Name	
Looking Info/Looking Dec N. Mode		·
Loading Junifergency Cmt Juni 0	LoaMgr_HwTqldptSigSV_Cnt_M_u08	
Landing - Medicaphility Cort M, USB	LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
Landing Medical May 100 M 100	LoaMgr_HwTqRespSV_Cnt_M_u08	1
Loading Junifequentity Corp. My 100	LoaMgr_lvtrldptSigSV_Cnt_M_u08	
Looking MacApigenesis Cost M. 196	LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	
Loading - Manage and light price - (Circ M, 1968) 1		
Loading MacQuardings Core M. 108 Loading MacQuardings MacQuarding MacQuarding MacQuardings MacQuardin		
Loading / McContradigners Cott / Miles		
Leading Michael Angles Cont. M. Sec. Leading Leading Control Angles Control Angle		
Condings Semintality American Condings		
Contings		
Loaday_Loaday Lipuday Lipu		0
L. Loadey_ControlAntigenSaze_Leips_Cot go	LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Lipadeg_CurrishMightScar_Use_D12 Lipadeg_Curris	Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
	k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
	k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	
L. Looking-Invitorising/Informations-Cort_Looking		
	k_LoaMgr_FadeOutStRate_UlspS_f32	
L. Loadey_HVTqdpdSigyTanDiResp_Crt_UBS		
K. Looking-In-WingloSigeResp. Cnt. U08		
Loading-HwiTqidisSpiRites_Cnt_U8		
		1
L. LosAgr_MridginSca_Dis_Tase_DCn_LosB		
L.coMgr_IntriptSig1Resp_Crt_U08		
L. Louking / Intriducis/SigPITITed, Cnt, U08	k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_Looklogr_IvrinfignRate_UispS_132 1 k_Looklogr_IvrinfignSca_UispS_2 0 k_Looklogr_IvrinfignSca_UispS_2 1 k_Looklogr_MoldgidpSignResp_Cnt_u08 5 k_Looklogr_MoldgidpSignResp_Cnt_u08 0 k_Looklogr_MoldgidpSignResp_Cnt_u08 0 k_Looklogr_MoldgidpSignResp_Cnt_u08 2 k_Looklogr_MoldgidpSignResp_Cnt_u08 2 k_Looklogr_MoldgidpSignResp_Cnt_u08 2 k_Looklogr_MoldgidpSignResp_Cnt_u08 2 k_Looklogr_MoldgidpSignResp_Cnt_u08 2 k_Looklogr_MoldgidpSignResp_Cnt_u08 2 k_Looklogr_MoldcurridpSignResp_Cnt_u08 2 k_Looklogr_MoldcurridpSignResp_Cnt_u08 2 k_Looklogr_MoldcurridpSignResp_Cnt_u08 0 k_Looklogr_MoldcurridpSignResp_Cnt_u08 0 k_Looklogr_MoldcurridpSignResp_Cnt_u08 0 k_Looklogr_MoldcurridpSignResp_Cnt_u08 0 k_Looklogr_MoldcurridpSignResp_Cnt_u08 0 k_Looklogr_MoldcurridpSignResp_UispS_122 1 k_Looklogr_MoldcurridpSignResp_UispS_122 1 k_Looklogr_MoldcurridpSignResp_Cnt_u08 0	k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k, LouMgr_InvintignSca_Uis_IS2 1 k, LouMgr_MoNapidsQiSig1NSmResp_Cnt_U08 5 k, LouMgr_MoNapidsSig3Resp_Cnt_U08 1 k, LouMgr_MoNapidsSig3Resp_Cnt_U08 1 k, LouMgr_MoNapidsSig3Resp_Cnt_U08 2 k, LouMgr_MoNapidsSig3Resp_Cnt_U08 2 k, LouMgr_MoNapidsSig3Resp_Cnt_U08 2 k, LouMgr_MoNapidsSig3Resp_Cnt_U08 2 k, LouMgr_MoNapidsSig2Resp_Cnt_U08 2 k, LouMgr_MoNapidsSig2Resp_Cnt_U08 2 k, LouMgr_MolcurridptSig2Resp_Cnt_U08 2 k, LouMgr_MolcurridptSig2Resp_Cnt_U08 2 k, LouMgr_MolcurridptSig2Resp_Cnt_U08 2 k, LouMgr_MolcurridptSig2Resp_Cnt_U08 0 k, LouMgr_MolcurridptSig2Resp_Cnt_U08 1 k, LouMgr_MolcurridptSig2Resp_Cnt_U08 1 k, Lou	k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
	k_LoaMgr_IvtrMtgtnRate_UlspS_f32	
LoaMgr MorlAgidptSig1NoSmaResp_Cnt_u08	k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_Igc	
LoaMgr_MotAgidptSig2Resp_Cnt_u08		
k_LoaMgr_MorAgidptSig3Resp_Cnt_u08 0 k_LoaMgr_MorAgidptSig1FiThd_Cnt_u08 2 k_LoaMgr_MorAgidptSig1FiThd_Cnt_u08 1 k_LoaMgr_MorAgMignRate_UBsp5_f22 1 k_LoaMgr_MorAgMignSca_Uis_f32 1 k_LoaMgr_MorAgMignSca_Uis_f32 1 k_LoaMgr_MorAgSmaAvailResp_Cnt_u08 2 k_LoaMgr_MorCurridptSig1Resp_Cnt_u08 2 k_LoaMgr_MorCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MorCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MorCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MorCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MorCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MorCurridptSig1Resp_Sf22 1 k_LoaMgr_MorCurridptSig1Resp_Usp_f32 1 k_LoaMgr_MorCurridptSig1Resp_Usp_f32 1 k_LoaMgr_RedodSiRea_Usp_f32 0 <		
K_LoaMgr_MotAgldptSigFitTnd_Cnt_u08 2 K_LoaMgr_MotAghtghtRate_UlspS_f32 1 K_LoaMgr_MotAghtghtScace_Cnt_u08 0 K_LoaMgr_MotAghtghtScace_Uls_f32 1 K_LoaMgr_MotAghtghtScace_Cnt_u08 2 K_LoaMgr_MotCurridptSigGResp_Cnt_u08 0 K_LoaMgr_MotCurridptSigGResp_Cnt_u08 0 K_LoaMgr_MotCurridptSigGResp_Cnt_u08 0 K_LoaMgr_MotCurridptSigFitTnd_Cnt_u08 0 K_LoaMgr_MotCurridptSigFitTnd_Cnt_u08 0 K_LoaMgr_MotCurridptSigFitTnd_Cnt_u08 0 K_LoaMgr_MotCurridptSigFitTnd_Cnt_u08 0 K_LoaMgr_MotCurridptSigSezEeroE_not_log 1 K_LoaMgr_MotCurridptSigSezEeroE_not_log 1 K_LoaMgr_RedcdSiRae_UlspS_f32 1 K_LoaMgr_RedcdSiRae_UlspS_f32 1 K_LoaMgr_RedcdSiRae_UlspS_f32 1 K_LoaMgr_Pert_DiagcStsivtrInactv_Cnt_lgc value 0 Larget_LoaMgr_Pert_DiagcStsivtrInactv_Cnt_lgc value 0 Larget_LoaMgr_Pert_LoaScaDi_Cnt_lgc-value 0 Larget_LoaMgr_Pert_Incaceadity_cnt_loads_value 0 Larget_LoaMgr_Pert_MrPotalptSig_Cnt_u08 value 0 <td></td> <td></td>		
K_LoaMgr_MotAyMighRate_UlspS_f32 1 K_LoaMgr_MotAyMighRacaZencEn_Cnt_igc 0 K_LoaMgr_MotAyMighRacaZencEn_Cnt_igc 1 K_LoaMgr_MotAyMighRacaZencEn_Cnt_igc 1 K_LoaMgr_MotAyMighRaca_UlspS_Cnt_iu08 2 K_LoaMgr_MotCurridpsIsigPSeps_Cnt_iu08 0 K_LoaMgr_MotCurridpsIsigPSeps_Cnt_iu08 0 K_LoaMgr_MotCurridpsIsigPsipTind_Cnt_iu08 0 K_LoaMgr_MotCurridpsisigPsipTind_Cnt_iu08 0 K_LoaMgr_MotCurridpsisigPsipTind_Cnt_iu08 0 K_LoaMgr_MotCurridpsisigPsipTind_Cnt_iu08 1 K_LoaMgr_MotCurridpsisigPsipTindscot_Cnt_igc 1 K_LoaMgr_MotCurridpsisigPsipTinactv_Cnt_igc 1 K_LoaMgr_RedcdSiRae_ulspS_122 1 K_LoaMgr_Pert_indsign_Cnt_iu08_value 0 Larget_LoaMgr_Pert_indsign_Cnt_iu08_value 1 Larget_LoaMgr_Pert_indsign_Cnt_iu08_value 0 Larget_LoaMgr_Pert_indrousinglisg_Cnt_iu08_value 2 Larget_LoaMgr_Pert_indrousinglisg_Cnt_iu08_value 0 Larget_LoaMgr_Pert_indrousinglisg_Cnt_iu08_value 0 Larget_LoaMgr_Pert_indrousinglisg_Cnt_iu08_value 0 Larg		
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k_LoaMgr_MotAgdinaNca_Uls_f32 1 k_LoaMgr_MotAgdinaNca_Uls_f32 2 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 2 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_MotCurridptSig2Cnt_u08 1 k_LoaMgr_MotCurridptSig2Cnt_u08 1 k_LoaMgr_MotCurridptSig2Cnt_u08 1 k_LoaMgr_MotCurridptSig2Cnt_u08 1 k_LoaMgr_Perd_DiagcStsivtTilnactv_Cnt_gc value 1 target_LoaMgr_Pert_MotGuridptSig_Cnt_u08 value 1 target_LoaMgr_Pert_MotGuridptSig_Cnt_u08 value 2 target_LoaMgr_Pert_MotGuridptSig_Cnt_u08 value 2 target_LoaMgr_Pert_ToaApl_Cnt_gc_value 0 target_LoaMgr_Pert_ToaApl_Cnt_gc_value 0		
k_LoaMgr_MotCurridptSig0Resp_Cnt_u08 2 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptRignEate_UispS_132 1 k_LoaMgr_RedcdSiRate_UispS_32 1 k_LoaMgr_RedcdSiRate_UispS_32 1 k_LoaMgr_Pert_DiagcSistvir1nactv_Cnt_lgc.value 0 target_LoaMgr_Pert_DiagcSistvir2nactv_Cnt_lgc.value 1 target_LoaMgr_Pert_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Pert_Mr_LogsDi_Cnt_u08 value 0 target_LoaMgr_Pert_MrProsliptSig_Cnt_u08 value 0 target_LoaMgr_Pert_MrProsliptSig_Cnt_u08 value 2 target_LoaMgr_Pert_TloaDi_Cnt_lgc.value 0 target_LoaMgr_Pert_TloaDi_Cnt_lgc.value 0 target_LoaMgr_Pert_TloaDi_Cnt_lgc.value 0 target_Re_Inst_Ap_LoaMgr_Pert_DiagcStsIvtrInactv_Cnt_lgc target_LoaMgr_Pert_DiagcStsIvtrInactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_Pert_DiagcStsIvtrInactv_Cnt_lgc target_LoaMgr_Pert_Mr_Inactv_Cnt_lgc target_Re_I	k LoaMgr MotAgMtgtnSca Uls f32	1
k_LoaMgr_MolCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MolCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MolCurridptSigFilThd_Cnt_u08 0 k_LoaMgr_MolCurrMtgtnRsez_UlspS_132 1 k_LoaMgr_MolCurrMtgtnSca_Uls_152 1 k_LoaMgr_RedcdStRate_UlspS_132 1 k_LoaMgr_RedcdStRate_UlspS_132 1 k_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc_value 0 target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc_value 0 target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc_value 1 target_LoaMgr_Per1_MolCurridptSig_Cnt_u08 value 0 target_LoaMgr_Per1_MolCurridptSig_Cnt_u08 value 0 target_LoaMgr_Per1_MolCurridptSig_Cnt_u08 value 2 target_LoaMgr_Per1_SteerMod_Cnt_enum_value 2 target_LoaMgr_Per1_ToaDi_Cnt_lgc_value 0 target_DaMgr_Per1_ToaDi_Cnt_lgc_value 0 target_LoaMgr_Per1_ToaDi_Cnt_lgc_value 0 target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_HavTqlofsig_Cnt_u08 target_LoaMgr_Per1_HavTqloaMgtnEn_Cnt_lgc target_Rel_inst_Ap_LoaMgr_LoaMgr_Per1_wrtLoaMtgtnEn_Cnt_lgc <	k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MolCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MolCurridptSigFitThd_Cnt_u08 0 k_LoaMgr_MolCurrMtgtnScaZeroEn_Cnt_lgc 1 k_LoaMgr_MolCurrMtgtnScaZeroEn_Cnt_lgc 1 k_LoaMgr_MolCurrMtgtnSca_Uls_f32 1 k_LoaMgr_RedddSlSca_Uls_f32 1 k_LoaMgr_Perd_DiagcStsIvtr1inactv_Cnt_lgc_value 0 target_LoaMgr_Per1_DiagcStsIvtr2inactv_Cnt_lgc_value 1 target_LoaMgr_Per1_MolGUrrldptSig_Cnt_u08 value 0 target_LoaMgr_Per1_MolGurrldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MolGurrldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_MolGurrldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_TinaOavail_Cnt_lgc_value 0 target_LoaMgr_Per1_TinaOavail_Cnt_lgc_value 0 target_Re_inst_Ap_LoaMgr_Per1_DiagcStsIvtr1inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStsIvtr2inactv_Cnt_lgc target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMigtnEn_Cnt_lgc target_LoaMgr_Per1_DiagcStsIvtr2inactv_Cnt_lgc target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMigtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMigtnEn_Cnt_lgc target_Re_inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMigtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMigtnEn_Cnt_lgc target_LoaMgr_Per1_LoaMgr_LoaM	k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_Loaklgr_MotCurridptSigFitThd_Cn_u08 0 k_Loaklgr_MotCurridptRate_UlspS_f32 1 k_Loaklgr_MotCurridptRate_UlspS_f32 1 k_Loaklgr_MotCurridptRisca_Uls_f32 1 k_Loaklgr_RedodStRate_UlspS_f32 1 k_Loaklgr_RedodStRate_UlspS_f32 1 k_Loaklgr_Per1_DiagcStsivtr1lnactv_Cnt_lgc.value 0 target_Loaklgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc.value 1 target_Loaklgr_Per1_HvTqldptSig_Cnt_u08.value 0 target_Loaklgr_Per1_HvTqldptSig_Cnt_u08.value 0 target_Loaklgr_Per1_MotCurridptSig_Cnt_u08.value 0 target_Loaklgr_Per1_MotCurridptSig_Cnt_u08.value 0 target_Loaklgr_Per1_SteerMod_Cnt_enum.value 2 target_Loaklgr_Per1_ToaAvail_Cnt_lgc.value 0 target_Rel_nst_Ap_Loaklgr_Loaklgr_Per1_DiagcStsivtr1lnactv_Cnt_lgc target_Loaklgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc target_Rel_nst_Ap_Loaklgr_Loaklgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc target_Loaklgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc target_Rel_nst_Ap_Loaklgr_Loaklgr_Per1_HvTqldptSig_Cnt_u08 target_Loaklgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc target_Rel_nst_Ap_Loaklgr_Loaklgr_Per1_HvTqldptSig_Cnt_u08 target_Loaklgr_Per1_HvTqldptSig_Cnt_u08 target_Rel_nst_A	k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
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k_LoaMgr_MotCurrMtgtnSca_UIs_f32 k_LoaMgr_MotCurrMtgtnSca_UIs_f32 k_LoaMgr_RedcdStRate_UIspS_f32 t_LoaMgr_RedcdStSca_UIs_f32 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc.value target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_ToaDi_Cnt_Igc.value target_LoaMgr_Per1_ToaDi_Cnt_Igc.value target_LoaMgr_Per1_ToaDi_Cnt_Igc.value target_LoaMgr_Per1_ToaDi_Cnt_Igc.value target_LoaMgr_Per1_ToaDi_Cnt_Igc.value target_LoaMgr_Per1_ToaDi_Cnt_Igc.value target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_NoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_NoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtLoaMtgtnEn_Cnt_		
k_LoaMgr_RedcdSiRate_UispS_f32 k_LoaMgr_RedcdSiRate_UispS_f32 k_LoaMgr_RedcdSiRate_UispS_f32 k_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_MrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SterMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_NtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_NtrLoaMgr_Per1_NtrLoaMgr_Per1_NtrLoaMgr_Per1_NtrLoaMgr_Per1_NtrLoaMgr_Per1_NtrLoaMgr_Per1_NtrLoaMgr_Per1_NtrLoaMgr_Per1_NtrLoaMgr_Per1_NtrLoaMgr_Per1_NtrLoaMgr_Per1_NtrLoaMgr_Per1_NtrLoaMgr_Per1_NtrLoaMgr_Per1_NtrLoaMgr_Per1_NtrLoaMgr_Per		
k_LoaMgr_RedcdStRate_UispS_f32		
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target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr.Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	-

Test Step 3.14 (Repeat Count = 1)	8
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Name	Input Value		
k_LoaMgr_CurrlytrMtgtnScaZeroEn_Cnt_Igc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32 k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k LoaMgr HwTqldptSig0TloaAvailResp Cnt u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k LoaMgr HwTgldptSig1TloaAvailResp Cnt u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_Igc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	0		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc			
k_LoaMgr_MotAgMtgtnSca_Uls_f32 k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k LoaMgr MotCurrldptSig1Resp Cnt u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_I	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Inc	target_LoaMgr_Per1_LoaScaDi_Cpt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt lac	
target_kte_inst_Ap_LoaMgr.LoaMgr_Peri_motAgLoaMtgtnEn_Cnt_igc target_kte_inst_Ap_LoaMgr.LoaMgr_Peri_motCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C target_LoaMgr_Per1_MotCurrIdptSig_Cnt_t		
target Rte Inst Ap LoaMgr.LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_ul		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaDi Cnt Igc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	_
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	-
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08	3	3	-
LoaMgr_LOASt_State_M_enum	5	5	•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	•
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	•
	5	5	-
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	11	
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	•
			•

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Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	✓
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	✓
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	✓
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.15 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	0
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv	v_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inact	v_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_	_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enur		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cr	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnE		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt	_	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_en	um	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc	1	1
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	•
LoaMgr_IvtrIdptSigSV_Cnt_M_u08	0	0	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	· ·
LoaMgr_LoASt_State_M_enum	500	500	
LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	-
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.16 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr MotCurrLoaMtgtnEn Cnt M lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





LoaMgr_HwTqlqbfsigsV_Cnt_M_u08 2 2 2 2 3 3 LoaMgr_HwTqlqbfsigsV_Cnt_M_u08 0 0 0 0 LoaMgr_LwftqfsigsV_Cnt_M_u08 0 0 0 LoaMgr_LwftqbfsigsV_Cnt_M_u08 0 0 0 LoaMgr_LwftqbfsigsV_Cnt_M_u08 1 LoaMgr_lwftqbfsigsV_Cnt_M_u08 5 5 5 LoaMgr_LoASt_State_M_enum 5 5 5 LoaMgr_LoASt_State_M_enum 5 5 5 LoaMgr_LoASt_State_M_enum 5 LoaMgr_LoaSt_Limit_UlspS_M_f32 1 LoaMgr_LoaStaelFrit_Ulsp M_f32 1 LoaMgr_MarktqfnResp_Cnt_M_u08 5 LoaMgr_LoaStaelFrit_Ulsp M_f32 1 LoaMgr_MarktqfnResp_Cnt_M_u08 5 LoaMgr_MarktqfnResp_Cnt_M_u08 1 LoaMgr_MotAgloaMtgtnEn_Cnt_M_u08 1 LoaMgr_MotAgloaMtgtnEn_Cnt_M_u08 1 LoaMgr_MotAgloaMtgtnEn_Cnt_M_u08 1 LoaMgr_MotAgloaMtgtnEn_Cnt_M_u08 1 LoaMgr_MotAgloaMtgtnEn_Cnt_M_u08 1 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 1 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 1 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 1 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 1 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 1 LoaMgr_ToaAvail_Cnt_M_u08 1 Lo	Name	Input Value		
	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
Surger_Loaking_Pert_Tisorchio_Cir_Communication Surger_Loaking_Pert_Tisorchio_Cir_Communication Surger_Loaking_Pert_Tisorchio_Cir_Communication Surger_Loaking_Pert_Tisorchio_Cir_Communication Surger_Loaking_Pert_Tisorchio_Cir_Cir_Communication Surger_Loaking_Pert_Tisorchio_Cir_Cir_Communication Surger_Relege_Index_Loaking_Pert_Tisorchio_Cir_Cir_Cir_Cir_Cir_Cir_Cir_Cir_Cir_Cir	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
singel_Loaking_Pert_Toakina_En_Crit_govulue 2	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3		
strayet_Loadey_Pert_Track_Coadey_Loadey_Loadey_Pert_Disposibilitrinacity_Crit_lgo target_Loadey_Loadey_Loadey_Pert_Disposibilitrinacity_Crit_lgo target_Loadey_Pert_Disposibilitrinacity_Crit_lgo target_Loadey_Pert_Disposibilitrinacity_Crit_lgo target_Loadey_Pert_Pert_Disposibilitrinacity_Crit_lgo target_Loadey_Pert_Pert_Disposibilitrinacity_Crit_lgo target_Loadey_Pert_Pert_Disposibilitrinacity_Crit_lgo target_Loadey_Pert_Pert_Pert_Disposibilitrinacity_Crit_lgo target_Loadey_Pert_Pert_Pert_Disposibilitrinacity_Crit_lgo target_Loadey_Pert_Pert_Disposibilitrinacity_Crit_lgo target_Loadey_Pert_Disposibilitrinacity_Crit_lgo target_Loadey_Pert_	target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
Integral Ris., Int. Ap. Looking Looking Pert Disgosishind Interval Cit. Looking Looking Pert Disgosishind Interval Engage Ris. Int. Ap. Looking Looking Pert Disgosishind Interval Engage Ris. Int. Ap. Looking Pert HwiTqiotsing Cit. Looking Ris. Ris. Ris. Ap. Looking Pert HwiTqiotsing Cit. Looking Ris. Ris. Ris. Ap. Looking Pert HwiTqiotsing Cit. Looking Ris. Ris. Ris. Ris. Ris. Ap. Looking Pert Ris. Ris. Ap. Ris.	target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
staget_Ris_inst_Ap_LoaMgr_LoaMgr_Pert_DiagoStshir/Zinacv_Cnt_igo staget_LoaMgr_Pert_DiagoStshir/Zinacv_Cnt_igo staget_Lo	target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_	Cnt_lgc	
target_Ris_Inst_Ap_LoaMgrLoaMgr_Part_HM*ToLoaMignEn_Cnt_lgc target_Ris_Inst_Ap_LoaMgrLoaMgr_Part_LM*ToLoaMignEn_Cnt_lgc target_Ris_Inst_Ap_LoaMgrLoaMgr_Part_LOAS_State_enum target_Ris_Inst_Ap_LoaMgrLoaMgr_Part_LOAS_State_enum target_Ris_Inst_Ap_LoaMgrLoaMgr_Part_LOAS_State_enum target_Ris_Inst_Ap_LoaMgrLoaMgr_Part_MoRignEn_Cnt_lgc target_Ris_Inst_Ap_LoaMgrLoaMgr_Part_MoRignEn_Cnt_U8 target_Ris_Inst_Ap_LoaMgrLoaMgr_Part_MoRignEn_Cnt_U8 target_Ris_Inst_Ap_LoaMgrLoaMgr_Part_MoRignEn_Cnt_U8 target_Ris_Inst_Ap_LoaMgrLoaMgr_Part_MoRignEn_Cnt_U8 target_LoaMgr_Part_MoRignEn_Cnt_U8 target_LoaMgr_Part_MoRignEn_Cnt_U8 target_LoaMgr_Part_MoRignEn_Cnt_U8 target_Ris_Inst_Ap_LoaMgr_LoaMgr_Part_MoRignEn_Cnt_U8 target_LoaMgr_Part_MoRignEn_Cnt_U8 target_Ris_Inst_Ap_LoaMgr_LoaMgr_Part_StateMoRignEn_Cnt_U8 target_Ris_Inst_Ap_LoaMgr_LoaMgr_Part_MoRignEn_Cnt_U8 target_Ris_Inst_Ap_LoaMgr_LoaMgr_Part_StateMoRignEn_Cnt_U8 target_LoaMgr_Part_MoRignEn_Cnt_U8 target_LoaMgr_Part_MoRignEn_Cnt_U8 target_LoaMgr_Part_MoRignEn_Cnt_U8 target_LoaMgr_Part_MoRignEn_Cnt_U8 target_LoaMgr_Part_MoRignEn_Cnt_U8 target_LoaMgr_Part_NoRignEn_Cnt_U8 target_LoaMgr_Part_NoRig	target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_	Cnt_lgc	
larget_Ris_Inst_Ap_Loaklgr_Loaklgr_Pert_LOAS_State_enum target_Loaklgr_Pert_LOAS_State_enum target_Ris_Inst_Ap_Loaklgr_Loaklgr_Pert_LOAS_State_enum target_Loaklgr_Pert_LOAS_State_enum target_Loaklgr_Pert_LOAS_State_enum target_Loaklgr_Pert_	target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u(08	
target, Rie, Inst, Ap, Loadigr, LoaMign, Pert L. LOASE, State, enum target, LoaMign, Pert L. LOASE, State, enum target, LoaMign, Pert L. LoASE, Ch. 1 gc target, Iris, Inst, Ap, LoaMign, LoaMign, Pert, Molduconstigntien, Ont. 1 target, LoaMign, Pert,	target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
	target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	lgc	
			-	
target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_MoNogucaMtgnEnc_Ort_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_MoNogurdaMtgnEnc_Ort_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_MoNogurdaMtgnEnc_Ort_Igc target_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_MoNogurdaMgr_Pert_MonogurdaMgr_Per				
larget_Rie_Inst_Ap_LoaMgr_LoaMgr_Pert_MolCurrLoaMignEn_Cnt_U08 larget_LoaMgr_Pert_MolCurrLoaMignEn_Cnt_U08 larget_Rie_Inst_Ap_LoaMigr_LoaMgr_Pert_MolCurrLoaMignEn_Cnt_U08 larget_Rie_Inst_Ap_LoaMigr_LoaMgr_Pert_MolCurrLoaMignEn_Cnt_U08 larget_Rie_Inst_Ap_LoaMigr_LoaMgr_Pert_MolCurrLoaMignEn_Cnt_U08 larget_Rie_Inst_Ap_LoaMigr_Dert_NoaMign_Pert_		target LoaMgr Per1 MotAgLoaMtgtnEn 0	Ont lgc	
larget_Rie_Inst_Ap_LoaMgr_LoaMgr_Per1_MirPositghSig_Cnt_U08 target_LoaMgr_Per1_MirPositghSig_Cnt_U08 target_Rie_Inst_Ap_LoaMgr_LoaMgr_Per1_TimePositghSig_Cnt_U08 target_LoaMgr_Per1_SteemMod_Cnt_enum target_Rie_Inst_Ap_LoaMgr_LoaMgr_Per1_TimeAvail_Cnt_Igc target_LoaMgr_Per1_TimeDi_Cnt_Igc target_LoaMgr_Per1_TimeDi_Cnt_Igc target_LoaMgr_Per1_TimeDi_Cnt_Igc target_LoaMgr_Per1_TimeDi_Cnt_Igc target_LoaMgr_Per1_TimeDi_Cnt_Igc target_LoaMgr_Per1_TimeDi_Cnt_Igc				
target_Rel_inst_Ap_LoaMgr_LoaMgr_Perl_SteerMod_Cnt_enum target_LoaMgr_Perl_SteerMod_Cnt_enum target_Rel_inst_Ap_LoaMgr_LoaMgr_Perl_SteerMod_Cnt_go target_LoaMgr_Perl_TloaMail_Cnt_Igo target_LoaMgr_Perl_TloaMail_Cnt_Igo target_LoaMgr_Perl_TloaMail_Cnt_Igo target_LoaMgr_Perl_TloaMail_Cnt_Igo target_LoaMgr_Perl_TloaMail_Cnt_Igo target_LoaMgr_Perl_TloaMail_Cnt_Igo target_LoaMgr_Perl_TloaMail_Cnt_Igo target_LoaMgr_Perl_TloaMail_Cnt_Igo target_LoaMgr_Perl_TloaMill_Cnt_Igo target_LoaMgr_Perl_TloAAMgr_Parl_TloaMill_Cnt_Igo target_LoaMgr_Perl_TloAAMgr_Parl_TloaAud_Cnt_Igo target_LoaMgr_Perl_TloAAMgr_Parl_TloAAMgr_Parl_TloaAud_Cnt_Igo target_LoaMgr				
target_Re_inst_Ap_LoaMgr_LoaMgr_Pert_SteerMod_Cnt_enum target_LoaMgr_Pert_SteerMod_Cnt_enum target_Re_inst_Ap_LoaMgr_LoaMgr_Pert_TloaAvail_Cnt_igc target_LoaMgr_Pert_TloaAvail_Cnt_igc target_LoaMgr_Pert_Deady_Cnt_igc				
target_Re_inst_Ap_LoaMgr_LoaMgr_Pert_TloaAvail_Cnt_lgc target_LoaMgr_Pert_TloaDi_Cnt_lgc target_LoaMgr_Pert_TloaDi_Cnt_lgc target_LoaMgr_Pert_TloaDi_Cnt_lgc target_LoaMgr_Pert_TloaDi_Cnt_lgc target_LoaMgr_Pert_TloaDi_Cnt_lgc Result LoaMgr_AgSV_Cnt_M_lgc 1				
Rarget_Re_Inst_Ap_LoaMgr_LoaMgr_Pert_TioaDi_Cnt_Igc				
Name	· · · · · ·			
LoaMgr_HwTqlqbfsigsV_Cnt_M_u08 2 2 2 2 3 3 LoaMgr_HwTqlqbfsigsV_Cnt_M_u08 0 0 0 0 LoaMgr_LwftqfsigsV_Cnt_M_u08 0 0 0 LoaMgr_LwftqbfsigsV_Cnt_M_u08 0 0 0 LoaMgr_LwftqbfsigsV_Cnt_M_u08 1 LoaMgr_lwftqbfsigsV_Cnt_M_u08 5 5 5 LoaMgr_LoASt_State_M_enum 5 5 5 LoaMgr_LoASt_State_M_enum 5 5 5 LoaMgr_LoASt_State_M_enum 5 LoaMgr_LoaSt_Limit_UlspS_M_f32 1 LoaMgr_LoaStaelFrit_Ulsp M_f32 1 LoaMgr_MarktqfnResp_Cnt_M_u08 5 LoaMgr_LoaStaelFrit_Ulsp M_f32 1 LoaMgr_MarktqfnResp_Cnt_M_u08 5 LoaMgr_MarktqfnResp_Cnt_M_u08 1 LoaMgr_MotAgloaMtgtnEn_Cnt_M_u08 1 LoaMgr_MotAgloaMtgtnEn_Cnt_M_u08 1 LoaMgr_MotAgloaMtgtnEn_Cnt_M_u08 1 LoaMgr_MotAgloaMtgtnEn_Cnt_M_u08 1 LoaMgr_MotAgloaMtgtnEn_Cnt_M_u08 1 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 1 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 1 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 1 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 1 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 1 LoaMgr_ToaAvail_Cnt_M_u08 1 Lo			Expected Value	Rosult
LoaMgr HwTqlcaNtglnEn_Cnt_M_lgc			· ·	
LoaMgr_HwTqLoaMtghEn_Cnt_M_lgc				
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LoaMgr_IvrtroJaNtgnEn_Cnt_M_upc 1 1 1 LoaMgr_Ivrtro-aMtgnEn_Cnt_M_upc 1 1 1 LoaMgr_LoASt_State_M_enum 5 5 5 LoaMgr_LoaRateLimit_Ulsps_M_f32 500 500 500 LoaMgr_MaxMtgnResp_Cnt_M_u08 5 5 5 LoaMgr_MotAglotSigSV_Cnt_M_u08 1 1 1 LoaMgr_MotAglespSy_Cnt_M_u08 5 5 5 LoaMgr_MotQuridptSigSV_Cnt_M_u08 5 5 5 LoaMgr_MotCurrLoaMtgnEn_Cnt_M_u08 1 1 1 LoaMgr_MotCurrLoaMtgnEn_Cnt_M_u08 1 1 1 LoaMgr_MotCurrLoaMtgnEn_Cnt_M_u08 1 1 1 LoaMgr_MotCurrLoaMtgnEn_Cnt_M_u08 1 1 1 LoaMgr_TloaDi_Cnt_M_u08 1 1 1 LoaMgr_TloaDi_Cnt_M_u08 1 1 1 LoaMgr_TloaDi_Cnt_M_u0s 1 1 1 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170} {170} Rte_Call_Ap_LoaMgr_NxtrDiagM				
LoaMgr_MrtroaMigtnEn_Cnt_M_lgc				
LoaMgr_LorRespSV_Cnt_M_u08			1	
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LoaMgr_LoaScaleFctr_Uls_M_132				
LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 1 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCagRespSV_Cnt_M_u08 1 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 1 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 1 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 1 LoaMgr_MotCurrRespSV_Cnt_M_u08 1 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc 0 0 LoaMgr_TloaD_Cnt_M_lgc 0 0 1 1 1 Y LoaMgr_VtlgMdSrcSV_Cnt_M_u08 1 1 1 Y Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rto_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 1 1 1 1 1 1 1 1 1 1 1 1 1				
LoaMgr_MotAgloptSigSV_Cnt_M_u08 1 1 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc 1 1 LoaMgr_MotAgRespSV_Cnt_M_u08 5 5 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 1 1 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 1 1 LoaMgr_MotCurrRespSV_Cnt_M_u08 1 1 LoaMgr_TloaAvail_Cnt_M_lgc 0 0 LoaMgr_TloaDi_Cnt_M_lgc 0 0 LoaMgr_VltgMdSrcsV_Cnt_M_u08 1 1 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170} {170} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1} {0, 0, 0, 0, 0} ** Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1} {1} \$0 \$0, 0, 0, 0, 0} ** Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1} {1} \$0 \$00 ± 0.01 ** Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 500 500 ± 0.01 ** ** Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 1 1 ± 0.001 ** target_LoaMgr_Per1_IntrLoaMtgtnEn_Cnt_lgc.value 1 1 1 **				
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc 1 1 V LoaMgr_MotAgRespSV_Cnt_M_u08 5 5 V LoaMgr_MotCurrloptSigSV_Cnt_M_u08 1 1 1 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 1 1 1 LoaMgr_MotCurrRespSV_Cnt_M_u08 1 1 1 LoaMgr_TloaAvail_Cnt_M_lgc 0 0 0 LoaMgr_TloaDi_Cnt_M_lgc 0 0 0 LoaMgr_VttgMdSrcSV_Cnt_M_u08 1 1 1 Rte_Call_Ap_LoaMgr_SctNTCStatus(NTC_Cnt_T_enum) {170} {170} X Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1} {1,00,0,0,0} X Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1} {1,00,0,0,0,0} X Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 X Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 1 1 ± 0.001 X Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 1 1 ± 0.001 X target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 1 1 1 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 1				
LoaMgr_MotAgRespSV_Cnt_M_u08 5 5 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 1 1 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 1 1 LoaMgr_MotCurrRespSV_Cnt_M_u08 1 1 LoaMgr_TloaAvail_Cnt_M_lgc 0 0 LoaMgr_TloaDi_Cnt_M_lgc 0 0 LoaMgr_VltgMdSrcSV_Cnt_M_u08 1 1 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170} {170} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1} {0, 0, 0, 0, 0} 3 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1} {1} {1} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1} {1} {1} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1} {1} {1} Rte_Virte_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 > Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 1 1 ± 0.001 > Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 1 1 ± 0.001 > rtarget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.va				
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 1 1 4 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 1 1 4 LoaMgr_MotCurrRespSV_Cnt_M_u08 1 1 4 LoaMgr_TloaAvail_Cnt_M_lgc 0 0 0 LoaMgr_VitgMdSrcSV_Cnt_M_u08 1 1 4 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170} {170} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1} {0, 0, 0, 0, 0} 3 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1} {1} {1} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1} {1} {1} Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 3 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 1 1 ± 0.001 4 rtarget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 1 1 1 rtarget_LoaMgr_Per1_LoASt_State_enum.value 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 1 1 4				
LoaMgr_MotCurrLoaMfgtnEn_Cnt_M_lgc 1 1 LoaMgr_MotCurrRespSV_Cnt_M_u08 1 1 LoaMgr_TloaAvail_Cnt_M_lgc 0 0 LoaMgr_VitgMdSrcSV_Cnt_M_u08 1 1 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170} {170} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1} {0, 0, 0, 0, 0} ** Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1} {1} {1} ** Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1} {1} {1} ** Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 ** Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 1 1 ± 0.001 ** rtarget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 1 1 1 ** rtarget_LoaMgr_Per1_LOASt_State_enum.value 5 5 ** target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 1 1 **				
LoaMgr_MotCurrRespSV_Cnt_M_u08 1 1 V LoaMgr_TloaAvail_Cnt_M_lgc 0 0 0 LoaMgr_VltgMdSrcSV_Cnt_M_u08 1 1 1 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170} {170} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1} {0, 0, 0, 0, 0} X Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1} {1} {1} Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 * Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 1 1 ± 0.001 * Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 1 1 ± 0.001 * rtarget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 1 1 1 rtarget_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value 1 1 1 rtarget_LoaMgr_Per1_LOASt_State_enum.value 5 5 * target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 1 1 *				
LoaMgr_TloaAvail_Cnt_M_lgc 0 0 ✓ LoaMgr_TloaDi_Cnt_M_lgc 0 0 ✓ LoaMgr_VltgMdSrcSV_Cnt_M_u08 1 1 (170) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170} (170) ✓ Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1} {0, 0, 0, 0, 0} X Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1} {1} ✓ Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 ✓ Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 1 1 ± 0.001 ✓ rtarget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 1 1 1 target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value 1 1 1 target_LoaMgr_Per1_LOASt_State_enum.value 5 5 ✓ target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 1 1 ✓		· ·		
LoaMgr_TloaDi_Cnt_M_lgc 0 0 LoaMgr_VltgMdSrcSV_Cnt_M_u08 1 1 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170} {170} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1} {0, 0, 0, 0, 0} X Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1} {1} {1} Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 ✓ Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 1 1 ± 0.001 ✓ target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 1 1 1 target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value 1 1 1 target_LoaMgr_Per1_LOASt_State_enum.value 5 5 ✓ target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 1 1 ✓				
LoaMgr_VitgMdSrcSV_Cnt_M_u08 1 1 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1} Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 S00 ± 0.01 * Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 1 1 ± 0.001 **darget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 1 1 **target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value 1 1 **target_LoaMgr_Per1_LOASt_State_enum.value 5 5 **target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 1 1				
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1} Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 Stop_to_toaMgr_LoaScaleFctr_Uls_f32(data) 1 1 1 ± 0.001 4target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 1 1 1 4target_LoaMgr_Per1_LoASt_State_enum.value 5 5 5 4target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 1				
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1} {0, 0, 0, 0, 0} Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1} {1} Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 1 1 ± 0.001 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 1 1 target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value 1 1 target_LoaMgr_Per1_LOASt_State_enum.value 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 1 1				
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1} {1} Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 1 1 ± 0.001 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 1 1 target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value 1 1 target_LoaMgr_Per1_LOASt_State_enum.value 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 1 1				
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) 500 500 ± 0.01 Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 1 1 ± 0.001 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 1 1 target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value 1 1 target_LoaMgr_Per1_LOASt_State_enum.value 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 1 1				
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 1 1 ± 0.001 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 1 1 target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value 1 1 target_LoaMgr_Per1_LOASt_State_enum.value 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 1 1				
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value				
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value 1 1 1 target_LoaMgr_Per1_LOASt_State_enum.value 5 5 5 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 1 1 2				~
target_LoaMgr_Per1_LOASt_State_enum.value 5 5 4 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 1 1 4	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 1 1	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
	target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value 1	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•





Test Step 3.17 (Repeat Count = 1)	3
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2 4
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08 k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32 k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	1 0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08 k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0
target_LoaMgr_Per1_MttPosiaptSig_Cnt_uus.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrlopMitthEn_Cnt_loc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_lvtrRespSV_Cnt_M_u08	4	4	~
LoaMgr_LOASt_State_M_enum	4	4	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	4	4	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	3	3	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{171, 173, 174}	{171, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	4	4	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.18 (Repeat Count = 1)	8
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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LoaMgr_Per1



Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	5		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 k LoaMgr MotAgldptSig2Resp Cnt u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k LoaMgr MotAgldptSigFltThd Cnt u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0 1 0		
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target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASc_ADi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08	0 1 0 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Citarget_LoaMgr_Per1_DiagcStsIvtr2Inactv_Citarget_LoaMgr_Per1_DiagcStsIvtr2Inactv_Citarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LoAScaDi_Cnt_lgctarget_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrlopMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0target_LoaMgr_Per1_TioaAvail_Cnt_lgctarget_LoaMgr_Per1_TioaDi_Cnt_lgctarget_LoaMgr_Per1_TioaDi_Cnt_lgctarget_LoaMgr_Per1_TioaDi_Cnt_lgctarget_LoaMgr_Per1_TioaDi_Cnt_lgctarget_LoaMgr_Per1_TioaDi_Cnt_lgctarget_LoaMgr_Per1_TioaDi_Cnt_lgctarget_Diagnatic_target_D	nt_lgc t_lgc gc at_lgc gc at_lgc ga Expected Value 1 0 0 4 1 1 1 2	***
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASc_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASc_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAg_LoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAg_LoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	0 1 0 0 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Ctarget_LoaMgr_Per1_DiagcStsIvtr2Inactv_Ctarget_LoaMgr_Per1_DiagcStsIvtr2Inactv_Ctarget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_loamstarget_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_loamstarget_LoaMgr_Per1_LoAScaDi_Cnt_logctarget_LoaMgr_Per1_LoaScaDi_Cnt_logctarget_LoaMgr_Per1_MotQurrIdptSig_Cnt_u1carget_LoaMgr_Per1_MotCurrIdptSig_Cnt_u1carget_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0target_LoaMgr_Per1_SteerMod_Cnt_enumtarget_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_LoaMg	nt_lgc t_lgc gc at_lgc gc at_lgc ga Expected Value 1 0 0 4 1 1 1 2 5	***************************************
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HvTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	0 1 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Citarget_LoaMgr_Per1_DiagcStslvtr2Inactv_Citarget_LoaMgr_Per1_DiagcStslvtr2Inactv_Citarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_target_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Ctarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0target_LoaMgr_Per1_TioaAvail_Cnt_lgctarget_LoaMgr_Per1_TioaDi_Cnt_lgctarget_LoaMgr_Per1_TioaDi_Cnt_lgctarget_LoaMgr_Per1_TioaDi_Cnt_lgctarget_LoaMgr_Per1_TioaDi_Cnt_lgctarget_LoaMgr_Per1_TioaDi_Cnt_lgctarget_LoaMgr_Per1_TioaDi_Cnt_lgctarget_DoaMgr_Per1_	Expected Value	***
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASC_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqloaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	0 1 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Citarget_LoaMgr_Per1_DiagcStslvtr2Inactv_Citarget_LoaMgr_Per1_DiagcStslvtr2Inactv_Citarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnttarget_LoaMgr_Per1_LoASt_State_enumtarget_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnttarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnttarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnttarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnttarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0target_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_DoaMgr_Per1_	Expected Value	***
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	0 1 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Citarget_LoaMgr_Per1_DiagcStslvtr2Inactv_Citarget_LoaMgr_Per1_DiagcStslvtr2Inactv_Citarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnttarget_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnttarget_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnttarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnttarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnttarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnttarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0target_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_DoaMgr_Per1_DoaMgr_Per1_DoaMgr_Per1_DoaMgr_Per1_DoaMgr_Per1_DoaMgr_Per1_DoaM	Expected Value	***
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrIoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	0 1 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Citarget_LoaMgr_Per1_DiagcStslvtr2Inactv_Citarget_LoaMgr_Per1_DiagcStslvtr2Inactv_Citarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnttarget_LoaMgr_Per1_LoASt_State_enumtarget_LoaMgr_Per1_LoaScaDi_Cnt_lgctarget_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnttarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnttarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnttarget_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnttarget_LoaMgr_Per1_MtrPosldptSig_Cnt_u0target_LoaMgr_Per1_TloaAvail_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_LoaMgr_Per1_TloaDi_Cnt_lgctarget_DoaMgr_Per1_	Expected Value	***

2018-05-18, 13:31:17+0530



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 174}	{170, 172, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	✓
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

est Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.19 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	5
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	5
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1In	actv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2In	actv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_C	cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtn	En_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_e	num	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_	<u>lgc</u>	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtr	nEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig	_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtg	tnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_	Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_	enum	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name		Expected Value	Result
	target_LoaMgr_Per1_TloaDi_Cnt_lgc		Result
Name	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	Expected Value	Result
Name LoaMgr_AgSV_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1	Expected Value	~
Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1	Expected Value 1	,
Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1	Expected Value 1 1 1	•
Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 0	Expected Value 1 1 0	•
Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 0 1 1 1	Expected Value 1 1 1 0 1 1	•
Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 1 1 5	Expected Value 1 1 1 0 1 1 1 5	
Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 0 1 1 1	Expected Value 1 1 1 0 1 1	
Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 1 1 5	Expected Value 1 1 1 0 1 1 1 5	
Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 1 1 5 500	Expected Value 1 1 1 0 1 1 5 500 1 5	
Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 1 1 5 500 1	Expected Value 1 1 1 0 1 1 5 500 1	
Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_logc	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 1 5 500 1 5 1 1	Expected Value 1 1 1 0 1 1 1 5 500 1 5 1 1	
Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 0 1 1 5 500 1 5 1 1 3	Expected Value 1 1 1 0 1 1 5 500 1 5 1 1 1 3	
Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 0 1 1 5 500 1 5 1 1 3 0	Expected Value 1 1 1 0 1 1 1 5 500 1 5 1 1 1 3 0	
Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 0 1 1 5 500 1 5 1 1 1 1 1 1 1 1 1 1 1	Expected Value 1 1 1 0 1 1 1 5 500 1 5 1 1 1 3 0 1	
Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 1 1 5 500 1 5 1 1 1 3 0 1 5	Expected Value 1 1 1 1 0 1 1 5 500 1 1 5 1 1 1 1 5 5 5 1 1 1 5 5 5 1 1 1 5 5 5 1 1 1 5 5 5 1 1 1 5 5 5 1 1 1 5 5 5 1 1 1 5 5 5 1 1 1 5 5 5 1 1 1 5 5 5 1 1 1 5 5 5 1 1 1 5 5 5 1 1 1 5 5 5 1 1 1 5 5 5 6 6 6 7 7 8 7 8 8 8 8 8 8 8 8 8 8 8 8 8	
Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 1 1 5 500 1 1 1 1 1 5 1 1 1 1 1 1 1	Expected Value 1 1 1 1 0 1 1 5 500 1 1 5 1 1 1 1 5 1 1 1 1	
Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 1 1 5 500 1 1 1 1 5 1 1 1 1 1 1 1 1	Expected Value 1 1 1 1 0 1 1 5 500 1 1 1 1 1 5 1 1 1 1	
Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRaleLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAglcaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 1 1 5 500 1 1 1 1 5 1 1 1 1 1 1 1 1	Expected Value 1 1 1 1 0 1 1 5 500 1 1 1 1 3 0 1 1 5 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrlcaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VtgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 1 1 5 500 1 5 1 1 1 1 1 1 1 1 1 1 1	Expected Value 1 1 1 1 0 1 1 5 500 1 1 5 1 1 1 1 1 1	
Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrtCaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 0 1 1 5 500 1 5 1 1 1 1 1 1 1 1 1 1 1	Expected Value 1 1 1 1 0 1 1 5 500 1 1 5 1 1 1 1 3 0 1 1 5 1 1 1 3 0 1 5 1 1 0 1 5 1 0 1 1 0 0 1 {170, 172, 173}	
Name LoaMgr_AgsV_Cnt_M_lgc LoaMgr_HwTqldptSigsV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigsV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 0 1 1 5 500 1 5 1 1 1 1 1 1 1 1 1 1 1	Expected Value 1 1 1 1 0 1 1 1 5 500 1 5 1 1 1 1 1 3 0 1 1 5 1 1 1 1 3 0 1 1 5 1 1 0 1 1 1 1 1 1 1 1 1 1 1 1 1	
Name LoaMgr_AgsV_Cnt_M_lgc LoaMgr_HwTqldptSigsV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigsV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VitgMdSrcsV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 0 1 1 5 500 1 5 1 1 1 3 0 1 5 1 1 1 1 1 1 1 5 500 1 5 1 1 1 1 1 1 1 1 1 1 1	Expected Value 1 1 1 1 0 1 1 1 5 500 1 5 1 1 1 1 1 1	
Name LoaMgr_AgsV_Cnt_M_lgc LoaMgr_HwTqldptSigsV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigsV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAglcoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 0 1 1 5 500 1 5 1 1 1 1 1 1 1 1 5 500 1 1 1 1 1 1 1 1 1 1 1 1	Expected Value 1 1 1 1 0 1 1 1 1 5 500 1 5 1 1 1 1 1	
Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotOcurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VttgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 0 1 1 5 500 1 5 1 1 1 1 1 1 1 5 500 1 1 1 1 1 1 1 1 1 1 1 1	Expected Value 1 1 1 1 0 1 1 1 1 5 500 1 5 1 1 1 1 1	
Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 0 1 1 5 500 1 5 1 1 1 1 1 1 5 5 1 1 1 1	Expected Value 1 1 1 1 0 1 1 1 1 5 5 500 1 5 1 1 1 1	
Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgloaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VtlgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_loASt_State_enum.value	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 0 1 1 1 5 500 1 5 1 1 1 1 1 5 5 1 1 1 1	Expected Value 1 1 1 1 0 1 1 1 1 5 5 500 1 5 1 1 1 1	
Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 1 0 1 1 5 500 1 5 1 1 1 1 1 1 5 5 1 1 1 1	Expected Value 1 1 1 1 0 1 1 1 1 5 5 500 1 5 1 1 1 1	



Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	✓
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	✓

Test Step 3.20 (Repeat Count = 1)	E
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M Igc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	0
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr MotAgldptSigSV Cnt M u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0

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target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value 0 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 3 target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_TloaAvail_Cnt_Igc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value 0 target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 0	
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 0	
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 2 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 0	
target_LoaMgr_Per1_SteerMod_Cnt_enum.value 0 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 0	
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 0	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 0	
0 = 0 = = = = = = = = = = = = = = = = =	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr.Per1_HwTqLoaMtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc	
target Rte Inst Ap LoaMgr.LoaMgr.Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum	
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_SteerMod_Cnt_enum	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	
target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc	
	sult
	Suit
3 3 2 2 2 2 3	~
0.02 1770 0.25 2.25	,
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc 0 0	~
LoaMgr_HwTqRespSV_Cnt_M_u08 4 4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08 1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc 1 1	
LoaMgr_lvtrRespSV_Cnt_M_u08 3 3	V
LoaMgr_LOASt_State_M_enum 5	V
LoaMgr_LoaRateLimit_UlspS_M_f32 500 500	~
LoaMgr_LoaScaleFctr_Uls_M_f32 1 1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08 5 5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08 2	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc 1	~
LoaMgr_MotAgRespSV_Cnt_M_u08 3	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08 0 0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc 1 1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08 4	~
LoaMgr_TloaAvail_Cnt_M_lgc 0	•
LoaMgr_TloaDi_Cnt_M_lgc 0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08 1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) {170, 173, 174} {170, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) {1, 1, 1} {0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) {1, 1, 1} {1, 1, 1}	~
Rte_Write_Ap_LoaRdeLimit_UlspS_f32(data) 500 500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) 1 1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value 0 0	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value 1	~
target_LoaMgr_Per1_LOASt_State_enum.value 5	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value 1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value 1	•

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalcivtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	✓





Test Step 3.21 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0 0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32 k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	1 2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k LoaMgr HwTqldptSig2TloaDiResp Cnt u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08 k_LoaMgr_lvtrMtgtnRate_UlspS_f32	0 1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1 2
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1.
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc.value	0 4
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target LoaMgr Per1 MotCurrldptSig Cnt u08.value	1
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	3
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScabl_Crit_igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Crit_igc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
1 2 2 2 1 3 3 2 2 0. 2. 2. 2.	,





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	~
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	✓
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	✓
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	•
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170}	{170}	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1}	{1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.22 (Repeat Count = 1)	12
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	5
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

2018-05-18, 13:31:17+0530





Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32 k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	1 2		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSig2Resp Cnt u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	2 0		
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target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2 0 2 3 2		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	2 0 2 3		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	2 0 2 3 2 1	nt Igc	
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target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	2 0 2 3 2 1 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr	nt_lgc	
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	2 0 2 3 2 1 1 target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cr target_LoaMgr_Per1_HwTqIdptSig_Cnt_u08	nt_lgc _lgc	
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target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TsteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoaScaleFctr_Uls_M_632 LoaMgr_LoaScaleFctr_Uls_M_632	2 3 2 1 1 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_HwTqlcptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotQurrldptSig_Cnt_u0i target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0i target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0i target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 0 0 5 5 5 500 0	Ligc	***************************************
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotQualtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqlaMgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08	2 0 2 3 2 1 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lg target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0t_target_LoaMgr_Per1_MotCurrloaMtgtnEn_Cnt_target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0t_target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 0 0 5 5 5 500 0 0 5	Ligc Ligc	***************************************
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TioaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TioaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TioaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TioaAvail_Cnt_lgc Name LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	2 3 2 1 1 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cr target_LoaMgr_Per1_HwTqlcptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoASt_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotQurrldptSig_Cnt_u0i target_LoaMgr_Per1_MotCurrldptSig_Cnt_u0i target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0i target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 0 1 0 0 0 5 5 5 500 0	Ligc	***************************************

2018-05-18, 13:31:17+0530



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	✓
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 175}	{170, 175}	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	✓
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data)	0	0 ± 0.001	✓
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.23 (Repeat Count = 1)	N .
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	0
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSigFltThd Cnt u08	0		
k LoaMgr MotCurrMtgtnRate UlspS f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
	1		
k_LoaMgr_RedcdStSca_Uls_f32	0		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value			
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target LoaMgr Per1 DiagcStslvtr2Inactv (Cnt lgc	
target Rte Inst Ap LoaMgr.LoaMgr Per1 HwTqldptSig Cnt u08	target LoaMgr Per1 HwTqldptSig Cnt u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_		
target_Rte_Inst_Ap_LoaMgr_Dearly_Test_VALDASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	90	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc	at las	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc	Expected Value	Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc		Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	Expected Value	Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	Expected Value	~
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0	Expected Value 1 0 0	~
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 0 4	Expected Value 1 0 0 4	· ·
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target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 0 4 1 1 3 5 500 1 5 0 1 2 0 1	Expected Value 1 0 0 4 1 1 3 5 500 1 5 0 1 2 0 1	· · · · · · · · · · · · · · · · · · ·
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 0 4 1 1 3 5 5 500 1 5 0 1 2 0 1 2	Expected Value 1 0 0 4 1 1 3 5 500 1 2 0 1 2 1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum LoaMgr_LOASt_State_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgldoaMtgtnEn_Cnt_M_Igc LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrloptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_VltgMdSrcSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 0 4 1 1 3 5 500 1 2 0 1 2 1 1 1 1 1 1	Expected Value 1 0 0 4 1 1 3 5 500 1 5 0 1 2 0 1 2 1 1 1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrlOaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_state_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgCaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrIoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 0 4 1 1 3 5 500 1 5 0 1 2 0 1 2 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Expected Value 1 0 0 4 1 1 3 5 500 1 5 0 1 2 0 1 2 1 1 1 1 1 1 {170, 172, 173, 174}	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrlOaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_state_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAglCaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrlOaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrlOaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 0 4 1 1 3 5 500 1 5 0 1 2 0 1 2 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Expected Value 1 0 0 4 1 1 1 3 5 500 1 5 0 1 2 0 1 2 1 1 1 1 {170, 172, 173, 174}	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrlOaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 0 4 1 1 1 3 5 500 1 5 0 1 2 0 1 2 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Expected Value 1 0 0 4 1 1 1 3 5 500 1 5 0 1 2 0 1 2 1 1 1 (170, 172, 173, 174) {0, 0, 0, 0, 0, 0} {1, 1, 1, 1}	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrldptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LoASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgIdptSigSV_Cnt_M_u08 LoaMgr_MotAgGloaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_u08 LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_MotCurrloaMtgtnEn_Cnt_M_lgc LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 0 4 1 1 1 3 5 500 1 1 2 0 0 1 2 1 1 1 1 1 1 1 1 1 1 1 1	Expected Value 1 0 0 4 1 1 1 3 5 500 1 5 0 1 2 0 1 2 1 1 1 1(170, 172, 173, 174) {0, 0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LOASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgCoMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRcale-Irmit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 0 4 1 1 1 3 5 5 500 1 1 2 0 1 1 2 1 1 1 1 1 1 1 1 1 1 1 1	Expected Value 1 0 0 4 1 1 1 3 5 5 500 1 5 0 1 2 0 1 2 1 1 1 {170, 172, 173, 174} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LoASt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgCont_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIdptSigSV_Cnt_M_u08 LoaMgr_MotCurrIcoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvaii_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_StyltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 0 4 1 1 1 3 5 500 1 1 2 0 1 1 2 1 1 1 1 1 1 1 1 1 1 1 1	Expected Value 1 0 0 4 1 1 1 3 5 5 500 1 5 0 1 2 0 1 1 2 1 1 1 {170, 172, 173, 174} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 0	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgCont_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_StyldpdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 0 4 1 1 1 3 5 5 500 1 5 0 1 2 0 1 2 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Expected Value 1 0 0 4 1 1 1 3 5 5 500 1 5 0 1 2 0 1 1 2 1 1 1 {170, 172, 173, 174} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 0 1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LoASt_State_M_enum LoaMgr_LoaScaleFctr_UIs_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrlcoaMtgtnEn_Cnt_M_Igc LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_StipDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_LOASt_State_enum.value	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 0 4 1 1 1 3 5 5 500 1 5 0 1 2 0 1 1 2 1 1 1 1 1 1 1 1 1 1 1 1 1	Expected Value 1 0 0 4 1 1 1 3 5 5 500 1 5 0 1 2 0 1 1 2 1 1 1 {170, 172, 173, 174} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 0 1 5	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_IwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_LoASt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08 LoaMgr_MotAgldptSigSV_Cnt_M_u08 LoaMgr_MotAgCont_M_u08 LoaMgr_MotAgRespSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrldptSigSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_MotCurrRespSV_Cnt_M_u08 LoaMgr_TloaAvail_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_TloaDi_Cnt_M_Igc LoaMgr_StyldpdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 0 0 4 1 1 1 3 5 5 500 1 5 0 1 2 0 1 2 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Expected Value 1 0 0 4 1 1 1 3 5 5 500 1 5 0 1 2 0 1 1 2 1 1 1 {170, 172, 173, 174} {0, 0, 0, 0, 0, 0} {1, 1, 1, 1, 1} 500 ± 0.01 1 ± 0.001 0 1	



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	•
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.24 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	1
LoaMgr HwTqRespSV Cnt M u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr MotAgldptSigSV Cnt M u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	2
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k LoaMgr CurrlytrMtgtnScaZeroEn Cnt lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_Igc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k LoaMgr MotAgldptSig2Resp Cnt u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
gocongi_i or ibiagootoret illiacte_Ont_igo.ealac	1

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Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	0		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvt	r1Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvt	r2Inactv_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSi	ig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaM	ltgtnEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtr	nEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State	te enum	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_(
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target LoaMgr Per1 MotAgLoaM		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldpt	· ·	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoa		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target LoaMgr Per1 MtrPosIdptS		
target Rte Inst Ap LoaMgr.LoaMgr Per1 SteerMod Cnt enum	target_LoaMgr_Per1_SteerMod_0	·	
target Rte Inst Ap LoaMgr.LoaMgr Per1 TloaAvail Cnt Igc	target_LoaMgr_Per1_TloaAvail_0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt		
Name	Actual Value	Expected Value	Result
	0	0	Nesuit
LoaMgr_AgSV_Cnt_M_lgc	2	2	_
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	3	3	
LoaMgr_HwTqRespSV_Cnt_M_u08	1	1	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	·		
LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_IvtrRespSV_Cnt_M_u08	3 5	3 5	
LoaMgr_LOASt_State_M_enum			•
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	V
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	V
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	V
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3	3	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 174}	{170, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	(4 4)	{1, 1}	~
	{1, 1}	500 . 0.04	✓
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	
	500	0 ± 0.001	
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	500		•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	500	0 ± 0.001	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Igc.value	500 0 1	0 ± 0.001	•
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500 0 1 0	0 ± 0.001 1 0	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	•
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	•
AssignScale	1	AssignScale	1	•
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	✓





Test Step 3.25 (Repeat Count = 1)	13
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	5
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc LoaMgr_MotAgRespSV_Cnt_M_u08	4
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	5
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08 k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k LoaMgr MotCurrMtgtnScaZeroEn Cnt lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	0 2
target_LoaMgr_Peri_MtrPosiqptsig_Cnt_uu8.value target_LoaMgr_Peri_MtrPosiqptsig_Cnt_uu8.value	0
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc
5	0





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	5	5	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_lvtrRespSV_Cnt_M_u08	2	2	~
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	4	4	~
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	5	5	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 174}	{170, 173, 174}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.26 (Repeat Count = 1)	3
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	3
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	2
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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LoaMgr_Per1



Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	1		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08 k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_UIs_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08 k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_(
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_0 target_LoaMgr_Per1_HwTqIdptSig_Cnt_u0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum	.90	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	int_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	V
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	· ·
LoaMgr_LoaRstol imit_Ulass_M_f32	5	5	Ž
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMar LoaScaleEctr Lile M f22	1	1	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	~





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2	2	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173, 175}	{170, 172, 173, 175}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1, 1}	{1, 1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	✓

Test Step Call Trace				_
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	✓
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	4	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.27 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	4
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Nama	Innut Value		
Name k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	Input Value		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08 k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	0		
k_LoaMgr_MotCurrMtgtnNate_otspo_joz	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target LoaMgr Per1 TloaDi Cnt Igc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Ir	nactv Cnt Igc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Ir		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_0	Cnt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtr	nEn_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_IvtrLoaMtgtnEr	n_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrLoaMty target_LoaMgr_Per1_MtrPosIdptSig_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lge		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	✓
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	V
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1 2	2	
LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	5	5	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	·
LoaMgr MaxMtgtnResp Cnt M u08	5	5	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	~
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc			
	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	•
LoaMgr_TloaAvail_Cnt_M_lgc	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc	2 1 1	2 1 1	· ·
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	2 1 1 0	2 1 1 0	· · · · · · · · · · · · · · · · · · ·
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	2 1 1 0 {170, 173, 174}	2 1 1 0 {170, 173, 174}	• • • • • • • • • • • • • • • • • • •
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08	2 1 1 0	2 1 1 0	V V V V V V V V V V V V V V V V V V V
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	2 1 1 0 {170, 173, 174} {1, 1, 1}	2 1 1 0 {170, 173, 174} {0, 0, 0, 0, 0, 0}	
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	2 1 1 0 {170, 173, 174} {1, 1, 1} {1, 1, 1}	2 1 1 0 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001	
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	2 1 1 0 {170, 173, 174} {1, 1, 1} {1, 1, 1} 500 1	2 1 1 0 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001 0	
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	2 1 1 0 {170, 173, 174} {1, 1, 1} {1, 1, 1} 500 1	2 1 1 0 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001 0	
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	2 1 1 0 {170, 173, 174} {1, 1, 1} {1, 1, 1} 500 1 0 1 5	2 1 1 0 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001 0 1	\(\frac{1}{2}\)
LoaMgr_TloaAvail_Cnt_M_lgc LoaMgr_TloaDi_Cnt_M_lgc LoaMgr_VltgMdSrcSV_Cnt_M_u08 Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08) Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum) Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data) Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	2 1 1 0 {170, 173, 174} {1, 1, 1} {1, 1, 1} 500 1	2 1 1 0 {170, 173, 174} {0, 0, 0, 0, 0} {1, 1, 1} 500 ± 0.01 1 ± 0.001 0	**************************************



Test Step Call Trace				V
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	•

Test Step 3.28 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr MotAgldptSigSV Cnt M u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k LoaMgr HwTqldptSig4Resp Cnt u08	0
	1
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	0
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1





Name	Input Value		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	3		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_	u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u	08	
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc	target LoaMgr Per1 TloaAvail Cnt Igc		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	
LoaMgr HwTqldptSigSV Cnt M u08	2	2	•
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	1	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	✓
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_lvtrRespSV_Cnt_M_u08	3	3	_
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	✓
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	✓
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	
LoaMgr TloaAvail Cnt M lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(NTC Cnt T enum)	{170, 173}	{170, 173}	_
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte Call Ap LoaMgr NxtrDiagMgr SetNTCStatus(Status Cnt T enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	-
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~
v = =v			

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	✓
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	•
Rte Exit Ap LoaMgr EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~



Test Step 3.29 (Repeat Count = 1)	
Name LoaMgr_AgSV_Cnt_M_lgc	Input Value
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	1
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	4
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	target_Rte_Inst_Ap_LoaMgr
k LoaMgr CurrivtrMtgtnScaZeroEn Cnt lgc	0
k_LoaMgr_CurrlytrMtgtnSca_UIs_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0 0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08 k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2
k_LoaMgr_MotCurridptSig0Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08	0
k LoaMgr MotCurrMtgtnRate UlspS f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k LoaMgr MotCurrMtgtnSca UIs f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc



Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0	8	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_HwTqRespSV_Cnt_M_u08	2	2	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_lvtrRespSV_Cnt_M_u08	1	1	~
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	•
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	•
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	✓
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	4	4	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	•
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	•
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	✓

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	✓
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	✓
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	✓

Test Step 3.30 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	3
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

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Input Value



Name



k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
	2		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1		
	0		
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08			
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1		
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k LoaMgr MotCurrldptSig2Resp Cnt u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
	1		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc			
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
	1		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	1 1 0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1 1 0		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1 1 0 2		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtPosldptSig_Cnt_u08.value	1 1 0 2 0		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1 1 0 2 0 2		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1 1 0 2 0 2 0	nt_lgc	
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1 1 0 2 0 2 0 2		
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	1 1 0 2 0 2 0 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt_lgc	
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	1 1 0 2 0 2 0 0 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ctarget_LoaMgr_Per1_DiagcStslvtr2Inactv_C	nt_lgc	
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08	1 1 0 2 0 2 0 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Ctarget_LoaMgr_Per1_DiagcStslvtr2Inactv_Ctarget_LoaMgr_Per1_HwTqldptSig_Cnt_u08	nt_lgc ; t_lgc	
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target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASc_Di_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_u08 LoaMgr_IvtrloaRtet_Init_UlspS_M_f32	1 1 0 2 0 2 0 1 2 0 1 arget_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTql.oaMtgtnEn_Cnt_tgret_LoaMgr_Per1_LoaScaDi_Cnt_tgretarget_LoaMgr_Per1_LoaScaDi_Cnt_tgretarget_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_tgretarget_LoaMgr_Per1_TloaDi_Cnt_tgretarget_LoaMgr_Per	Expected Value	***
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotQurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrloaRtelmin_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	1 1 0 2 0 2 0 1 2 0 1 arget_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTql.oaMtgtnEn_Cnt_tgtarget_LoaMgr_Per1_LoaScaDi_Cnt_tgc target_LoaMgr_Per1_LoaScaDi_Cnt_tgc target_LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_tgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 0 0 1 1 1 3 5 5 500 0	Expected Value	***
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoAScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotQurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqlptSigSV_Cnt_M_u08 LoaMgr_HwTqlptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrLoaRstelmit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	1 1 0 2 0 2 0 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqloaMtgtnEn_Cnt_larget_LoaMgr_Per1_Index_State_enum target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Crt_target_LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrloaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_MotCurrLoaMtgtnEn_Ctarget_LoaMgr_Per1_ToaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 0 0 1 1 1 3 5 500 0 5	Expected Value	***
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotQurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaMtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaNtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaNtgtnEn_Cnt_M_lgc LoaMgr_IvtrloaRset_Limit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	1 1 0 2 0 2 0 1 2 0 1 arget_LoaMgr_Per1_DiagcStslvtr1Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_DiagcStslvtr2Inactv_C target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTql.oaMtgtnEn_Cnt_tgtarget_LoaMgr_Per1_LoaScaDi_Cnt_tgc target_LoaMgr_Per1_LoaScaDi_Cnt_tgc target_LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrloptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08 target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_tgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 1 0 0 1 1 1 3 5 5 500 0	Expected Value	***

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LoaMgr_Per1



Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	✓
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt lgc.value	0	0	✓

Test Step Call Trace			V	
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.31 (Repeat Count = 1)	N .
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_IvtrRespSV_Cnt_M_u08	3
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





Name	Innut Value		
Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k LoaMgr MotAgMtgtnRate UlspS f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Ont_Igc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4		
target LoaMgr Per1 LoaScaDi Cnt Igc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	0		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv	_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LoASt_State_enur		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cr		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enu		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	~
LoaMgr_lvtrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	Y
LoaMgr_LOASt_State_M_enum	5	5 500	Ž
LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	-
LoaMgr MaxMtgtnResp Cnt M u08	5	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	-
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	
LoaMgr MotAgRespSV Cnt M u08	2	2	~
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)		0 ± 0.001	✓
	0		
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	*
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	<i>y</i>
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_LOASt_State_enum.value	0 0 5	0 0 5	\rightarrow \frac{1}{2}
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	0	0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalcivtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.32 (Repeat Count = 1)	
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	2
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	0
LoaMgr MotAgldptSigSV Cnt M u08	2
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrivtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrivtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnKate_Oisp3_i32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
	0
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_lvtrMtgtnSca_Uls_f32	5
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	1
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	0
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	2
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	1
k_LoaMgr_MotAgMtgtnSea_ZoaSp_Cat. Isa	0
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
	2
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	
k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	0		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_C	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_	.lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt	u08	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum	1	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr AgSV Cnt M lgc	0	0	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	✓
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	-
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	✓
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	2	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_lvtrRespSV_Cnt_M_u08	0	0	
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	✓
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08	2	2	✓
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	1	1	✓
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	✓
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	-
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	•
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	-
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	•

Test Step Call Trace			✓	
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~





Injust Value Injus V	Test Step 3.33 (Repeat Count = 1)	13
Looking American American		
Looking Invited activity Dec Dec		
Loading Junispies Loading Loading Loading Loading Junispies Loading		3
Landing - Mendenghage Cort M. 108 Loaning - Menden	LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0
Landing Medigenesity Crit M. 108 Landing Medicensesity Crit Medicensesity Crit Medicensesity Crit Medicensesity Crit Medicensesity Crit Medicensesity Crit Medicenses Crit Medicensesity Crit Medicenses Crit Medice	LoaMgr_HwTqRespSV_Cnt_M_u08	2
Loading Mandages SV Cris M. vi0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	LoaMgr_lvtrldptSigSV_Cnt_M_u08	
Looking MacApigenesis Cort M. 196		
Coadags MacAge analoge Corp. 12 15		
Loading MacGardings Cort M, USB Loading MacGardings Cort M, USB Loading MacGardings Cort M, Mag Rein Find R, Loading C, Loading L, Loading L, Loading C, Loadings C, Loading MacGardings Cort M, Mag Landing MacGardings L, Loading C, Loadings C, Loading C, Loadings C, Loa		
Loading / McContradigners Cott / M. 196 Loading / McContradigners / M. 196 Loading / McContradig		1
Leading MoConfreeDescription (M. Mgs Leading MoConfreeDescription (M. Mgs Leading MoConfreeDescription (M. Mgs Leading Leading (M. Mgs Leading		
Loadleg. Control Margines Mar	LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	
Loadley_Carly Agriculture Loadley_Carly Lines Loadley_Carly	LoaMgr_MotCurrRespSV_Cnt_M_u08	0
Restands Lander	LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
	LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Linable_Driftonthalpriss_Driftonthalpr	Rte_Inst_Ap_LoaMgr	V =
Loading-InvitoglisignTrans-Availance_Crit_UBB Loading-InvitoglisignTrans-Availance_Crit_UBB Loading-InvitoglisignTrans-Availance_Crit_UBB Loading-InvitoglisignTrans-Availance_Crit_UBB Loading-InvitoglisignTrans-Availance_Crit_UBB Loading-InvitoglisignTrans-Availance_Crit_UBB Loading-InvitoglisignTrans-Crit_UBB Loading-		
L. Loading-In-Minigrosity-Transcription, Crit. (Job. 1)		
K. Loadey_HwTopicsSg2TranDirects_Crd_Lu08		
	k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	
	k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	
		0
	k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
K_LosMgr_MridghtSeap_Cnt_U08	k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
Loadig_NvtightSigResp_Crt_u08	k_LoaMgr_HwTqMtgtnRate_UlspS_f32	
K_LosMgr_Intringtsig2Resp_Cnt_U08		
L. Louking / Intriducising Fittind_Cnt_U08		
k_Looklogr_IvrinfignRate_UlspS_132 1 k_Looklogr_IvrinfignScac_UlspS_2 0 k_Looklogr_IvrinfignScac_UlspS_2 1 k_Looklogr_MoldgidpSig2Resp_Cnt_u08 5 k_Looklogr_MoldgidpSig2Resp_Cnt_u08 0 k_Looklogr_MoldgidpSig2Resp_Cnt_u08 0 k_Looklogr_MoldgidpSig2Resp_Cnt_u08 2 k_Looklogr_MoldgidpSig2Resp_Cnt_u08 2 k_Looklogr_MoldgidpSig2Resp_Cnt_u08 2 k_Looklogr_MoldgidpSig2Resp_Cnt_u08 2 k_Looklogr_MoldgidpSig2Resp_Cnt_u08 2 k_Looklogr_MoldgidpSig2Resp_Cnt_u08 2 k_Looklogr_MoldcurridpSig1Resp_Cnt_u08 2 k_Looklogr_MoldcurridpSig1Resp_Cnt_u08 0 k_Looklogr_MoldcurridpSig1Resp_Cnt_u08 0 <t< td=""><td></td><td></td></t<>		
K, LouMgr_InvintignSca_Uis_IS2 1 K, LouMgr_InvintignSca_Uis_IS2 1 K, LouMgr_MoNajdipSig1NSignSresp_Cnt_U08 5 K, LouMgr_MoNajdipSig1NSignSresp_Cnt_U08 1 K, LouMgr_MoNajdipSig1NSignSresp_Cnt_U08 2 K, LouMgr_MoNajdipSigNSignSresp_Cnt_U08 2 K, LouMgr_MoNajdipSignSresp_Cnt_U08 2 K, LouMgr_MoNajdipSignSresp_Cnt_U08 0 K, LouMgr_MoNajdipSison_Uis_I32 1 K, LouMgr_MoNajdipSison_Uis_I32 1 K, LouMgr_MoNajdipSison_Uis_I32 2 K, LouMgr_MoldupSison_Uis_I32 1 K, LouMgr_MoldupSison_Uis_I32 1 K, LouMgr_MoldupSison_Uis_I32 2 K, LouMgr_MoldupSison_Uis_I32 0 K, LouMgr_MoldupSison_Uis_I32 0 K, LouMgr_MoldupSison_Uis_I32 0 K, LouMgr_MoldupSison_Uis_I32 1 K, Lo		
LoaMgr MoRajdiptSig3Resp_Cnt_U08		
k_LoaMgr_MorAgidptSig3Resp_Cnt_u08 0 k_LoaMgr_MorAgidptSig1FiThd_Cnt_u08 2 k_LoaMgr_MorAgidptSig1FiThd_Cnt_u08 1 k_LoaMgr_MorAgMignRate_UBsp5_f22 1 k_LoaMgr_MorAgMignSca_Uis_f32 1 k_LoaMgr_MorAgMignSca_Uis_f32 1 k_LoaMgr_MorAgSmaAvaliResp_Cnt_u08 2 k_LoaMgr_MorCurridptSig1Resp_Cnt_u08 2 k_LoaMgr_MorCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MorCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MorCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MorCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MorCurridptSig1Resp_G2 1 k_LoaMgr_MorCurridptSig1Resp_G2 1 k_LoaMgr_MorCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MorCurridptSig1Resp_Usp_f32 1 k_LoaMgr_MorCurridptSig1Resp_Usp_f32 1 k_LoaMgr_RedodSiRea_Usp_f32 1 k_LoaMgr_RedodSiRea_Usp_f32 1 k_LoaMgr_RedodSiRea_Usp_f32 1 k_LoaMgr_RedodSiRea_Usp_f32 1 k_LoaMgr_RedodSiRea_Usp_f32 1 k_LoaMgr_RedodSiRea_Usp_f32 1	k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
K_LoaMgr_MotAgldplSigFitTnd_Cnt_u08 2 K_LoaMgr_MotAghtghtRate_UlspS_132 1 K_LoaMgr_MotAghtghtScace_Cnt_crt_ge 0 K_LoaMgr_MotAghtghtScace_Uls_132 1 K_LoaMgr_MotAghtghtScace_Cnt_u08 2 K_LoaMgr_MotCurridptSigGeRes_Cnt_u08 2 K_LoaMgr_MotCurridptSigGeRes_Cnt_u08 0 K_LoaMgr_MotCurridptSigGeRes_Cnt_u08 0 K_LoaMgr_MotCurridptSigFitTnd_Cnt_u08 0 K_LoaMgr_MotCurridptSigFitTnd_Cnt_u08 0 K_LoaMgr_MotCurridptSigFitTnd_Cnt_u08 0 K_LoaMgr_MotCurridptSigFitTnd_Cnt_u08 0 K_LoaMgr_MotCurridptsigSeZeroEn_Cnt_lgc 1 K_LoaMgr_MotCurridptsigSeZeroEn_Cnt_lgc 1 K_LoaMgr_RedcdSiRae_Uls_52 1 K_LoaMgr_RedcdSiRae_Uls_52 1 K_LoaMgr_RedcdSiRae_Uls_52 1 K_LoaMgr_Pert_DiagcSistvtTlnactv_Cnt_lgc_value 0 target_LoaMgr_Pert_DiagcSistvtTlnactv_Cnt_lgc_value 1 target_LoaMgr_Pert_HwTiQfstSig_Cnt_u08 value 1 target_LoaMgr_Pert_InCurridptSig_Cnt_u08 value 1 target_LoaMgr_Pert_MirrostoftSig_Cnt_u08 value 3	k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1
K_LoaMgr_MotAyMightRate_UlspS_f32 1 K_LoaMgr_MotAyMightSacZencEn_Cnt_igc 0 K_LoaMgr_MotAyMightSacZencEn_Cnt_igc 1 K_LoaMgr_MotAyMightSac_Ulsp_f32 1 K_LoaMgr_MotCurridps/SigResp_Cnt_u08 2 K_LoaMgr_MotCurridps/SigResp_Cnt_u08 0 K_LoaMgr_MotCurridps/SigResp_Cnt_u08 0 K_LoaMgr_MotCurridps/SigResp_Cnt_u08 0 K_LoaMgr_MotCurridps/SigResp_Cnt_u08 0 K_LoaMgr_MotCurridps/SigResp_Sig2 1 K_LoaMgr_MotCurridps/SigResp_Sig2 1 K_LoaMgr_MotCurridps/SigResp_Sig2 1 K_LoaMgr_RedcdSisRet_ulsp_Sig2 1 K_LoaMgr_Perd_SigResp_Sig2 1 K_LoaMgr_Perd_DiagcSistvirlinactv_Cnt_igc value 0 Larget_LoaMgr_Perd_DiagcSistvirlinactv_Cnt_igc value 1 Larget_LoaMgr_Perl_DiagcSistvirlinactv_Cnt_igc value 1 Larget_LoaMgr_Perl_MotCuridpsig_Cnt_u08 value 1 Larget_LoaMgr_Perl_MotCuridpsig_Cnt_u08 value 1 Larget_LoaMgr_Perl_MotCuridpsig_Cnt_u08 value 1 Larget_LoaMgr_Perl_TioaDi_Cnt_igc value 0 Larget_LoaMgr_Perl_TioaMgr_DiagcSistvirlinactv_Cnt_	k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0
K_LoaMgr_MotAgMtgthSca_ZeroEn_Cn_Igc 0 K_LoaMgr_MotAgMtgthSca_Uls_132 1 K_LoaMgr_MotAgMtgthSca_Uls_132 2 K_LoaMgr_MotCurridptSigGResp_Cnt_u08 2 K_LoaMgr_MotCurridptSigGresp_Cnt_u08 0 K_LoaMgr_MotCurridptSigFiltThd_Cnt_u08 0 K_LoaMgr_MotCurridptSigFiltThd_Cnt_u08 0 K_LoaMgr_MotCurridptSigFiltThd_Cnt_u08 0 K_LoaMgr_MotCurridptSigFiltThd_Cnt_u08 1 K_LoaMgr_MotCurridptSigFiltThd_Cnt_u08 1 K_LoaMgr_MotCurridptSigScreen_Cnt_u0g 1 K_LoaMgr_MotCurridptSig_Cnt_u08 1 K_LoaMgr_ReddSiSca_Uls_5122 1 K_LoaMgr_Pert_DiagcStsivtr1lnack_Cnt_lgc_value 1 Larget_LoaMgr_Pert_DiagcStsivtr2lnack_Cnt_lgc_value 1 Larget_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value 4 Larget_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value 1 Larget_LoaMgr_Pert_MotCurridptSig_Cnt_u08 value 3 Larget_LoaMgr_Pert_Tolaqvail_Cnt_lgc_value 0 Larget_LoaMgr_Pert_Tolaqvail_Cnt_lgc_value 0 Larget_LoaMgr_Pert_Tolaqvail_Cnt_lgc_value 0 Larget_LoaMgr_Pert_Tolaqvail_C	k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	
k_LoaMgr_MotAgMiginSca_Ulis_f32 1 k_LoaMgr_MotAgSmaAvaiiResp_Cnt_u08 2 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 1 k_LoaMgr_MotCurridptSig2Sig2 1 k_LoaMgr_MotCurridptSig2Sig2Sig2 1 k_LoaMgr_MotCurridptSig2Sig2Sig2 1 k_LoaMgr_ReddSiSca_Uis_f32 1 k_LoaMgr_ReddSiSca_Uis_f32 1 k_LoaMgr_Pert_DiagcStsivfTinactv_Cnt_gc value 1 target_LoaMgr_Pert_DiagcStsivfTinactv_Cnt_gc value 1 target_LoaMgr_Pert_MrPosidptSig_Cnt_u08 value 1 target_LoaMgr_Pert_Tinach_Cnt_lgc value 0 target_LoaMgr_Pert_Tinach_Cnt_lgc value 0 targ	k_LoaMgr_MotAgMtgtnRate_UlspS_f32	
k_LoaMgr_MotOurdiptSig0Resp_Cnt_u08 2 k_LoaMgr_MotCurrdiptSig0Resp_Cnt_u08 2 k_LoaMgr_MotCurrdiptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurrdiptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurrdiptSig1FIThd_Cnt_u08 0 k_LoaMgr_MotCurrMigInSca_Usp_S_132 1 k_LoaMgr_MotCurrMigInSca_Usp_S_132 1 k_LoaMgr_MotCurrMigInSca_Usp_S_132 1 k_LoaMgr_MotCurrMigInSca_Usp_S_132 1 k_LoaMgr_RedGISSca_Usp_S_132 1 k_LoaMgr_Pert_DiagcSisivIrtInactv_Cnt_igc_value 0 target_LoaMgr_Pert_DiagcSisivIrtInactv_Cnt_igc_value 1 target_LoaMgr_Pert_MotQisiig_Cnt_u08_value 4 target_LoaMgr_Pert_MotCurridptSiig_Cnt_u08_value 1 target_LoaMgr_Pert_MotCurridptSiig_Cnt_u08_value 1 target_LoaMgr_Pert_MotCurridptSiig_Cnt_u08_value 1 target_LoaMgr_Pert_MotQuridptSiig_Cnt_u08_value 1 target_LoaMgr_Pert_MotQuridptSiig_Cnt_u08_value 1 target_LoaMgr_Pert_TioaDi_Cnt_igc_value 0 target_LoaMgr_Pert_TioaDi_Cnt_igc_value 0 target_LoaMgr_Pert_TioaDi_Cnt_igc_value 0 target_		1
k_LoaMgr_MotCurridptSig0Resp_Cnt_u08 2 k_LoaMgr_MotCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurridptRate_UlspS_132 1 k_LoaMgr_RedcdSiRate_UlspS_32 1 k_LoaMgr_RedcdSiRate_UlspS_132 1 k_LoaMgr_Perd_DiagcSistvir1nactv_Cnt_lgc.value 0 target_LoaMgr_Per1_DiagcSistvir2nactv_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcSistvir2nactv_Cnt_lgc.value 1 target_LoaMgr_Per1_Mr_LoaMgr_Der1_U08 value 4 target_LoaMgr_Per1_Mr_LoaMgr_Der1_U08 value 1 target_LoaMgr_Per1_MrPosliptSig_Cnt_u08 value 1 target_LoaMgr_Per1_ToaDr_Cnt_lgc.value 1 target_LoaMgr_Per1_ToaDr_Cnt_lgc.value 0 target_LoaMgr_Per1_ToaDr_Cnt_lgc.value 0 target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc target_LoaMgr_Per1_DiagcStsIvtrInactv_Cnt_lgc target_Re_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMgr_Enc_nt_lgc target_LoaMgr_Per1_HwTqLoaMgr_Enc_nt_lgc <	_ 0_ 0	
k_LoaMgr_MolCurridptSig1Resp_Cnt_u08 0 k_LoaMgr_MolCurridptSig2Resp_Cnt_u08 0 k_LoaMgr_MolCurridptSigFilThd_Cnt_u08 0 k_LoaMgr_MolCurrMtgtnRsez_UlspS_132 1 k_LoaMgr_MolCurrMtgtnSea_UlspS_132 1 k_LoaMgr_RedcdStRate_UlspS_132 1 k_LoaMgr_RedcdStRate_UlspS_132 1 k_LoaMgr_Perd_DiagcStsIvtrInactv_Cnt_lgc_value 0 target_LoaMgr_Perf_DiagcStsIvtrInactv_Cnt_lgc_value 0 target_LoaMgr_Perf_IndeptSig_Cnt_u08_value 4 target_LoaMgr_Perf_MolCurridptSig_Cnt_u08_value 1 target_LoaMgr_Perf_MolCurridptSig_Cnt_u08_value 1 target_LoaMgr_Perf_MolCurridptSig_Cnt_u08_value 1 target_LoaMgr_Perf_SteerMod_Cnt_enum_value 1 target_LoaMgr_Perf_ToaDi_Cnt_lgc_value 0 target_LoaMgr_Perf_ToaDi_Cnt_lgc_value 0 target_LoaMgr_Perf_ToaDi_Cnt_lgc_value 0 target_Rel_inst_Ap_LoaMgr_LoaMgr_Perf_DiagcStsIvtrInactv_Cnt_lgc target_LoaMgr_Perf_DiagcStsIvtrInactv_Cnt_lgc target_Rel_inst_Ap_LoaMgr_LoaMgr_Perf_HavTqlofsig_Cnt_u08 target_LoaMgr_Perf_HavTqloaMigtnEn_Cnt_lgc target_Rel_inst_Ap_LoaMgr_LoaMgr_Perf_HvTLoaMtgtnEn_Cnt_lgc ta		
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target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 3 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_LoaMgr_Per1_HwTqlotptSig_Cnt_u08 1 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc 1 target_LoaMgr_Per1_HwTqlotptSig_Cnt_u08 1 target_LoaMgr_Per1_HwTqlotptSig_Cnt_u08 1 target_LoaMgr_Per1_LoaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_LoaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc 1 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc 1 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 1 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 1 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 1 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08 1 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
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target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvai_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaSt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoASt_State_enum target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0
target_Rte_Inst_Ap_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqldptSig_Cnt_u08 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_ivtrLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LOASt_State_enum target_LoaMgr_Per1_LOASt_State_enum target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_LoaScaDi_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08		
	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	3	3	•
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	•
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	1	1	~
LoaMgr_LOASt_State_M_enum	4	4	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	~
LoaMgr_MaxMtgtnResp_Cnt_M_u08	4	4	~
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotAgRespSV_Cnt_M_u08	2	2	~
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{171}	{171}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1}	{1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	4	4	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	1	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.34 (Repeat Count = 1)	12
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_IvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1

2018-05-18, 13:31:17+0530

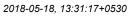




Name	Input Value		
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_FadeOutStRate_UlspS_f32	1		
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2		
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4		
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	1		
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08 k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0		
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0		
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1		
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1		
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1		
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_IvtrMtgtnRate_UlspS_f32 k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_Igc	1 0		
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1		
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1		
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k_LoaMgr_MotAgMtgtnSca_UIs_f32	1 2		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08 k LoaMgr MotCurrldptSig0Resp Cnt u08	2 2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_UIs_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value	1		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_Citarget LoaMgr Per1 DiagcStslvtr2Inactv Citarget LoaMgr Per1 DiagcStslvtr2Inact		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Perl_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target LoaMgr Per1 HwTqLoaMtgtnEn Cnt		
target Rte Inst Ap LoaMgr.LoaMgr Per1 lvtrLoaMtgtnEn Cnt lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lg		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrIdptSig_Cnt_ui		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_C	_,	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u0	0	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target the mot rip country country i of i floarivali official			
	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc		Expected Value	Result
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc	Expected Value	Result
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value	· ·	
target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 2	1 2 1	*
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 2 1 3	1 2 1 3	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lwtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 2 1 3	1 2 1 3 0	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 2 1 3 0	1 2 1 3 0 1 1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
target_Rte_Inst_Ap_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08	target_LoaMgr_Per1_TloaAvaii_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 2 1 3 0 1 5	1 2 1 3 0 1 5 5	***
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrSespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	target_LoaMgr_Per1_TloaAvaii_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 2 1 3 0 1 5 5	1 2 1 3 0 1 5 5 5	***
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOaSt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32	target_LoaMgr_Per1_TloaAvaii_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 2 1 3 0 1 5 5 5	1 2 1 3 0 1 5 5 5 5 500	***
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOASt_State_M_enum	target_LoaMgr_Per1_TloaAvaii_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 2 1 3 0 1 5 5	1 2 1 3 0 1 5 5 5	***
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_Igc Name LoaMgr_AgSV_Cnt_M_Igc LoaMgr_HwTqIdptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_Igc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_IvtrIdptSigSV_Cnt_M_u08 LoaMgr_IvtrLoaMtgtnEn_Cnt_M_Igc LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_IvtrRespSV_Cnt_M_u08 LoaMgr_LOaSt_State_M_enum LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32	target_LoaMgr_Per1_TloaAvaii_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 2 1 3 0 1 5 5 5 500	1 2 1 3 0 1 5 5 5 5 500 1 1	******
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_TloaDi_Cnt_lgc Name LoaMgr_AgSV_Cnt_M_lgc LoaMgr_HwTqldptSigSV_Cnt_M_u08 LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc LoaMgr_HwTqRespSV_Cnt_M_u08 LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_lvtrRespSV_Cnt_M_u08 LoaMgr_LoaSt_State_M_enum LoaMgr_LoaRateLimit_UlspS_M_f32 LoaMgr_LoaScaleFctr_Uls_M_f32 LoaMgr_MaxMtgtnResp_Cnt_M_u08	target_LoaMgr_Per1_TloaAvaii_Cnt_lgc target_LoaMgr_Per1_TloaDi_Cnt_lgc Actual Value 1 2 1 3 0 1 5 5 500 1 5	1 2 1 3 0 1 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5	***

LoaMgr_MotAgRespSV_Cnt_M_u08

LoaMgr_Per1





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	•
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	•
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 175}	{170, 173, 175}	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	✓
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	1	1	•
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	•
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc.value	0	0	✓

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclytrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	-
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	-
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	-
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.35 (Repeat Count = 1)	3
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	1
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	3
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_lvtrRespSV_Cnt_M_u08	4
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1
LoaMgr_MotAgRespSV_Cnt_M_u08	1
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	1
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	0
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k_LoaMgr_FadeOutStRate_UlspS_f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_IvtrMtgtnSca_Uls_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5





N	In most Walter		
Name	Input Value		
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0		
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2		
k LoaMgr MotAgMtgtnRate UlspS f32	1		
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0		
k LoaMgr MotAgMtgtnSca Uls f32	1		
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2		
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSig2Resp_Cnt_u08	0		
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1		
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1		
k_LoaMgr_RedcdStRate_UlspS_f32	1		
k_LoaMgr_RedcdStSca_Uls_f32	1		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1		
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target LoaMgr Per1 SteerMod Cnt enum.value	1		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target LoaMgr Per1 TloaDi Cnt Igc.value	1		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inac	ty Cnt lac	
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inact		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_C		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enu		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgr	0	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEr	n_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_C	nt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnl	En_Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cr	ıt_u08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_er	ium	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	1	1	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	2	~
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_HwTqRespSV_Cnt_M_u08	3	3	· ·
LoaMgr_lvtrldptSigSV_Cnt_M_u08 LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	
LoaMgr LOASt State M enum	5	5	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	·
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	_
LoaMgr MotAgldptSigSV Cnt M u08	1	1	•
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	•
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	1	1	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	~
LoaMgr_TloaDi_Cnt_M_lgc	1	1	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173, 175}	{170, 173, 175}	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	×
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)		 {1, 1, 1}	✓
Dto Write An LooMan LooDatal imit III 0 (00/d-t-)	{1, 1, 1}		
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	500	500 ± 0.01 1 ± 0.001	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value	500 1 1	500 ± 0.01 1 ± 0.001	· ·
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	500 1 1 1	500 ± 0.01 1 ± 0.001 1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value target_LoaMgr_Per1_LOASt_State_enum.value	500 1 1 1 1 5	500 ± 0.01 1 ± 0.001 1 1 5	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_UIs_f32(data) target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	500 1 1 1	500 ± 0.01 1 ± 0.001 1	· · · · · · · · · · · · · · · · · · ·



Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	-
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.36 (Repeat Count = 1)	E Company of the Comp
Name	Input Value
LoaMgr_AgSV_Cnt_M_lgc	0
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2
LoaMgr HwTqLoaMtgtnEn Cnt M lgc	1
LoaMgr_HwTqRespSV_Cnt_M_u08	1
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_lvtrRespSV_Cnt_M_u08	2
LoaMgr MotAgldptSigSV Cnt M u08	3
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotAgRespSV_Cnt_M_u08	5
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	2
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0
LoaMgr_MotCurrRespSV_Cnt_M_u08	0
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0
k_LoaMgr_CurrlvtrMtgtnSca_Uls_f32	1
k LoaMgr FadeOutStRate UlspS f32	1
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1
k LoaMgr HwTqldptSig2TloaEnaResp Cnt u08	0
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1
k_LoaMgr_HwTqMtgtnSca_Uls_f32	1
k_LoaMgr_IvtrldptSig1Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSig2Resp_Cnt_u08	0
k_LoaMgr_IvtrldptSigFltThd_Cnt_u08	0
k_LoaMgr_IvtrMtgtnRate_UlspS_f32	1
k_LoaMgr_IvtrMtgtnScaZeroEn_Cnt_Igc	0
k_LoaMgr_IvtrMtgtnSca_UIs_f32	1
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5
k LoaMgr MotAgldptSig2Resp Cnt u08	1
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2
	1
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	0
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc k_LoaMgr_MotAgMtgtnSca_Uls_f32	1
	2
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08	
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08	2
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08	0 0
k_LoaMgr_MotCurrNdptSigFltThd_Cnt_u08	1
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1
k_LoaMgr_RedcdStRate_UlspS_f32	1
k_LoaMgr_RedcdStSca_Uls_f32	1
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0





Name	Input Value		
target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value	1		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0		
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value	2		
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1		
target_LoaMgr_Per1_SteerMod_Cnt_enum.value	2		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	1		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	1		
target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_0	Cnt_lgc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u0	В	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cr	nt_lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_l	gc	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr_Per1_LoaScaDi_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_C	nt lgc	
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_ui		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	✓
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_HwTqRespSV_Cnt_M_u08	0	0	✓
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	✓
LoaMgr_lvtrRespSV_Cnt_M_u08	2	2	~
LoaMgr_LOASt_State_M_enum	5	5	✓
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	✓
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	-
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	✓
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_MotAgRespSV_Cnt_M_u08	5	5	✓
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	2	2	•
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_MotCurrRespSV_Cnt_M_u08	0	0	~
LoaMgr_TloaAvail_Cnt_M_lgc	1	1	✓
LoaMgr_TloaDi_Cnt_M_lgc	1	1	✓
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	0	0	•

Test Step Call Trace				
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	~
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	~
ArbResp	1	ArbResp	1	~
ChooseFinalResp	1	ChooseFinalResp	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~



Manual	Test Step 3.37 (Repeat Count = 1)	
Leading Inforcing Sign Core M. 106 Leading Leading Sign Core M. 1		
Leading Laring participation Corp. M. go		·
Leahily Invitalishing Circ M Mg		
Leading Part Teach Communication Com		
Loading Load		
Leading Infragacy Cmt Mull		
Loaking_MedigleSyCort_M_108 1		0
Loading JackAge Service (M. M. 198 0 1 1 1 1 1 1 1 1 1		5
Leading MAGNETISSES	LoaMgr_MotAgldptSigSV_Cnt_M_u08	1
Leading IndOCUTRISTISSISV CT, M, USB 0	LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0
Loading MoCurrico Antilogin E., Coff Murgo Loading Sharisah GokgAreai Cort, Murgo Loading Currivrihing Maria Lyans, 1932 1 Loading Lyans Currivrihing Maria Lyans	LoaMgr_MotAgRespSV_Cnt_M_u08	3
Leading StretsHortAparal Coll AL Use	· · ·	
Loading		
Loadigr		
Ris_Inst_A_Loaklogr	v = v = - = - v	
K_LOMAN_CONTHONINGINCAZEONE CONT_USE_STATE K_LOMAN_CONT_USE_STATE K_LOMAN_C		
k_Loaking CurrientMignisca_Us_122 1 k_Loaking_FaceOutSirRate_Uslay_522 1 k_Loaking_FaceOutSirRate_Uslay_522 1 k_Loaking_FaceOutSirRate_Uslay_522 2 k_Loaking_FaceOutSirRate_Uslay_522 4 k_Loaking_FaceOutSirRate_Uslay_522 4 k_Loaking_FaceOutSirRate_Uslay_522 4 k_Loaking_FaceOutSirRate_Uslay_522 1 k_Loaking_FaceOutSirRate_Uslay_522 0 k_Loaking_FaceOutSirRate_Out_Uslay_522 1 k_Loaking_FaceOutSirRate_Out_Uslay_522 1 k_Loaking_FaceOutSirRate_Out_Uslay_522 1 k_Loaking_FaceOutSirRate_Out_Uslay_522 1 k_Loaking_FaceOutSirRate_Out_Uslay_522 1 k_Loaking_FaceOut_Uslay_522 1 k_Loaking_FaceOut_Uslay_522 1 k_Loaking_FaceOut_Uslay_522 1 k_Loaking_FaceOut_Uslay_5222 1 k_Loaking_FaceOut_Uslay_5222 1 k_Loaking_FaceOut_Uslay_5222 1 k_Loaking_ModeQidSigSirRate_Out_Uslay_522 1 k_Loaking_ModeQidSigSirRate_Out_Uslay_522 1 k_Loaking_ModeQidSigSirRate_Out_Uslay_522	:	, · · - ·
LoaMgr_FadeOutsRirate_UispS_G2		
k_Loaking* HwTqidpiSig*NoTioaAvailResp_Cnt_u08 4 k_Loaking* HwTqidpiSig*IToaAvailResp_Cnt_u08 2 k_Loaking* HwTqidpiSig*IToaAvailResp_Cnt_u08 1 k_Loaking* HwTqidpiSig*IToaAvailResp_Cnt_u08 0 k_Loaking* HwTqidpiSig*IToaAvailResp_Cnt_u08 0 k_Loaking* HwTqidpiSig*IToaAvailResp_Cnt_u08 0 k_Loaking* HwTqidpiSig*IToaChi. 0.00 1 k_Loaking* HwTqidpiSig*IToaChi. 0.00 1 k_Loaking* HwTqidpiSig*IToaChi. 0.00 1 k_Loaking* HwTqidpiSig*IToaChi. 0.00 0 k_Loaking* HwTqidpiSig*IToaChi. 0.00 0 k_Loaking* JwTdqisiSig*IToaChi. 0.00 0 k_Loaking* JwTdqisiSig*IToaChi. 0.00 0 k_Loaking* JwTdqishSig*IToaChi. 0.00 0 k_Loaking* JwTdqishSig*IToaChi. 0.00 0 k_Loaking* JwTdqishSig*IToaChi. 0.00 0 k_Loaking* JwTdqishSig*IToaChi. 0.00 1 k_Loaking* JwTdqishSig*IToaChi. 0.00 1 k_Loaking* JwTdqishSig*IToaChi. 0.00 5 k_Loaking* JwTdqishSig*IToaChi. 0.00 1 k_Loaking* JwTdqishSig*IToaChi. 0.00 0 k_Loaking* JwTdqishSig*IToaChi. 0.00		
k_Loaking - HvTqdpiSigri ThoAvaiResp_Cnt_u08 4 k_Loaking - HvTqdpiSigriToakvaiResp_Cnt_u08 1 k_Loaking - HvTqdpiSigriZoalicaResp_Cnt_u08 0 k_Loaking - HvTqdpiSigriZoalicaResp_Cnt_u08 0 k_Loaking - HvTqdpiSigriZoalicaResp_Cnt_u08 0 k_Loaking - HvTqdpiSigriZoalicaResp_Cnt_u08 1 k_Loaking - HvTqdpiSigriZoalicaResp_Cnt_u08 1 k_Loaking - HvTqdpiSigriZoalicaResp_Cnt_u08 0 k_Loaking - IvtqdpiSigriZoap_Cnt_u08 1 k_Loaking - IvtqdpiSigriZoap_Cnt_u08 5 k_Loaking - IvtqdpiSigriZoap_Cnt_u08 1 k_Loaking - IvtqdpiSigriZoap_Cnt_u08 1 k_Loaking - IvtqdpiSigriZoap_Cnt_u08 1 k_Loaking - IvtqdpiSigriZoap_Cnt_u08 2 k_Loaking - IvtqdpiSigriZoap_Cnt_u08 2 k_Loaking - MoCaphigniSaa_Loap_Cnt_u08 2 k_Loaking - MoCaphigniSaa_Loap_C		
k_LoaMgr_HvTqldplSgrTnladvarilResp_Cnt_u08 2 k_LoaMgr_HvTqldplSgrZnladDResp_Cnt_u08 0 k_LoaMgr_HvTqldplSgrAspCanEnResp_Cnt_u08 0 k_LoaMgr_HvTqldplSgrAspCanEnResp_Cnt_u08 0 k_LoaMgr_HvTqldplSgrAspCanEnResp_Cnt_u08 1 k_LoaMgr_HvTqMglnRase_Ulls_SZ 1 k_LoaMgr_HvTqMglnRase_Ulls_SZ 1 k_LoaMgr_VrtdpSlSgrResp_Cnt_u08 0 k_LoaMgr_VrtdpSlSgrResp_Cnt_u08 0 k_LoaMgr_VrtdplSgrResp_Cnt_u08 0 k_LoaMgr_VrtdplSgrResp_Cnt_u08 0 k_LoaMgr_VrtdplSgrResp_Cnt_u08 0 k_LoaMgr_VrtdplSacZereEn_Cnt_Uge 0 k_LoaMgr_MorAgldpSlSgrResp_Cnt_u08 5 k_LoaMgr_MorAgldpSlSgrResp_Cnt_u08 5 k_LoaMgr_MorAgldpSlSgrResp_Cnt_u08 1 k_LoaMgr_MorAgldpSlSgrResp_Cnt_u08 2 k_LoaMgr_MorAgldpSlSgrResp_Cnt_u08 2 k_LoaMgr_MorAgldpSlSgrResp_Cnt_u08 2 k_LoaMgr_MorAgldplSsgrResp_Cnt_u08 2 k_LoaMgr_MorAgldplSsgrResp_Cnt_u08 2 k_LoaMgr_MorAgldpSsgrResp_Cnt_u08 0 k_LoaMgr_MorCundpSsgrResp_Cnt_u08		
K_LoaMgr_HvTqlpslSig4Resp_Cnt_u08		1
k_Loakigr_HvrqldptSigrIrthd_Cnt_u08 1 k_Loakigr_HvrqldptSiag_Ulsp5_f32 1 k_Loakigr_HvrtqhtghtSiag_Ulsp5_f32 1 k_Loakigr_IvrtdptSig_Resp_Cnt_u08 0 k_Loakigr_IvrtdptSig_Resp_Cnt_u08 0 k_Loakigr_IvrthdptSig_Resp_Cnt_u08 0 k_Loakigr_IvrthdptSig_Resp_Cnt_u08 0 k_Loakigr_IvrthdptSig_Resp_Cnt_u08 0 k_Loakigr_IvrthdptSig_Resp_Cnt_u08 1 k_Loakigr_MotAgiotptSig_Resp_Cnt_u08 1 k_Loakigr_MotAgiotptSig_Resp_Cnt_u08 1 k_Loakigr_MotAgiotptSig_Resp_Cnt_u08 2 k_Loakigr_MotAgidptSig_Resp_Cnt_u08 2 k_Loakigr_MotAgidptSig_Resp_Cnt_u08 2 k_Loakigr_MotAgidptSig_Resp_Cnt_u08 2 k_Loakigr_MotAgidps_Los_Uls_32 1 k_Loakigr_MotAgidps_Los_Uls_32 1 k_Loakigr_MotAgidps_Los_Uls_32 1 k_Loakigr_MotAgidps_Accent_uo8 2 k_Loakigr_MotCurridpsSig_Resp_Cnt_u08 2 k_Loakigr_MotCurridpsSig_Resp_Cnt_u08 0 k_Loakigr_MotCurridpsSig_Resp_Cnt_u08 0 k_Loakigr_MotCurridpsSig_Resp_Cnt_u08	k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0
	k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0
k_Loak/gr_HwTqMtginSca_Uis_132 1 k_Loak/gr_IwtriptSig/Resp_Cnt_u08 0 k_Loak/gr_IwtriptSig/Resp_Cnt_u08 0 k_Loak/gr_IwtriptSig/Resp_Cnt_u08 0 k_Loak/gr_IwtriptSig/Resp_Cnt_UspS_132 1 k_Loak/gr_IwtriptSig/Resp_Cnt_UspS_132 1 k_Loak/gr_MotAglotSig1NoSmaResp_Cnt_u08 5 k_Loak/gr_MotAglotSig1NoSmaResp_Cnt_u08 1 k_Loak/gr_MotAglotSig1Resp_Cnt_u08 2 k_Loak/gr_MotAglotSig1Resp_Cnt_u08 2 k_Loak/gr_MotAglotSig1Resp_Cnt_u08 2 k_Loak/gr_MotAglotSig1Resp_Cnt_u08 2 k_Loak/gr_MotAglotSig1Resp_Cnt_u08 2 k_Loak/gr_MotAglotSig1Resp_Cnt_u08 2 k_Loak/gr_MotCurridptSig1Resp_Cnt_u08 2 k_Loak/gr_MotCurridptSig1Resp_Cnt_u08 2 k_Loak/gr_MotCurridptSig1Resp_Cnt_u08 2 k_Loak/gr_MotCurridptSig1Resp_Cnt_u08 0 k_Loak/gr_MotCurridptSig1Resp_Cnt_u08 0 k_Loak/gr_MotCurridptSig1Resp_Cnt_u08 0 k_Loak/gr_MotCurridptSig2Resp_Cnt_u08 0 k_Loak/gr_MotCurridptSig2Resp_Cnt_u08 0 k_Loak/g	k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	
k_Loak/gr_IvridptSig1Resp_Cnt_u08 0 k_Loak/gr_IvridptSig1Resp_Cnt_u08 0 k_Loak/gr_IvridptSig1Resp_Cnt_u08 0 k_Loak/gr_IvriMiginRate_UlspS_G32 1 k_Loak/gr_IvriMiginSca_Uls_S122 1 k_Loak/gr_IvriMiginSca_Uls_S132 1 k_Loak/gr_MotAgloptSig1NoSmaResp_Cnt_u08 5 k_Loak/gr_MotAgloptSig2Resp_Cnt_u08 1 k_Loak/gr_MotAgloptSig3Resp_Cnt_u08 0 k_Loak/gr_MotAgloptSig3Resp_Cnt_u08 2 k_Loak/gr_MotAgloptSig3Resp_Cnt_u08 2 k_Loak/gr_MotAglitginSca_Uls_S2 1 k_Loak/gr_MotAglitginSca_Uls_S2 1 k_Loak/gr_MotAglitginSca_Uls_S2 1 k_Loak/gr_MotAglitginSca_Uls_S2 1 k_Loak/gr_MotCurridptSigFresp_Cnt_u08 2 k_Loak/gr_MotCurridptSigFresp_Cnt_u08 0 k_Loak/gr_MotCurridptSigFresp_Cnt_u08 0 k_Loak/gr_MotCurridptSigFritind_Cnt_u08 0 k_Loak/gr_MotCurridptSigFresp_Cnt_u08 0 k_Loak/gr_MotCurridptSigFritind_Cnt_u08 1 k_Loak/gr_MotCurridptSigFresp_Cnt_u08 1 k_Loak/gr_MotCurridptSigFresp_		
k_Loaklgr_IvridptSig2Resp_Cn_U08 0 k_Loaklgr_IvridptSig1RThd_Cn_U08 0 k_Loaklgr_IvriktgInRate_Ulsp5_f32 1 k_Loaklgr_IvriktgInRate_Ulsp5_f32 1 k_Loaklgr_IvriktgInRate_Ulsp5_f32 1 k_Loaklgr_IvriktgInRate_Ulsp5_f32 1 k_Loaklgr_MokAjdpIdsiSig1RosnaResp_Cn_Lu08 5 k_Loaklgr_MokAjdpIdsiSigResp_Cn_Lu08 1 k_Loaklgr_MokAjdpIdsiSigResp_Cn_Lu08 2 k_Loaklgr_MokAjdMIgnSacZeroEn_Cn_Llgc 0 k_Loaklgr_MokAjdMIgnSacZeroEn_Cn_Llgc 0 k_Loaklgr_MokAjdMIgnSacZeroEn_Cn_Llgc 0 k_Loaklgr_MokAjdMIgnSacZeroEn_Cn_Llgc 1 k_Loaklgr_MokAjdMIgnSacZeroEn_Cn_Llgc 2 k_Loaklgr_MokCurridptSig1Resp_Cn_Lu08 2 k_Loaklgr_MokCurridptSig1Resp_Cn_Lu08 2 k_Loaklgr_MokCurridptSig2Resp_Cn_Lu08 0 k_Loaklgr_MokCurridptSig2Resp_Cn_Lu08 0 k_Loaklgr_MokCurridptSig2Resp_Cn_Lu08 0 k_Loaklgr_MokCurridptSig2Resp_Cn_Lu08 0 k_Loaklgr_MokCurridptSig2Resp_Cn_Lu08 0 k_Loaklgr_MokCurridptSig2Resp_Cn_Lu08 0		
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k_Loaklgr_IvtrMtginScaZeroEn_Cnt_lgc 0 k_Loaklgr_IvtrMtginSca_Uls_f32 1 k_Loaklgr_Mch2gldtSigNSmaResp_Cnt_u08 5 k_Loaklgr_Mch2gldtSig3Resp_Cnt_u08 0 k_Loaklgr_Mch2gldtSig3Resp_Cnt_u08 0 k_Loaklgr_Mch2gldtSig1Resp_Cnt_u08 2 k_Loaklgr_Mch2gldtSig1Resp_Cnt_u08 2 k_Loaklgr_Mch2gldtSig1Resp_Cnt_u08 0 k_Loaklgr_Mch2gldtSig2Resp_Cnt_u08 2 k_Loaklgr_Mch2gldtSig2Resp_Cnt_u08 2 k_Loaklgr_MclCurridptSig1Resp_Cnt_u08 2 k_Loaklgr_MclCurridptSig1Resp_Cnt_u08 0 k_Loaklgr_MclCurridptSig2Resp_Cnt_u08 0 k_Loaklgr_MclCurridptSig2Resp_Cnt_u08 0 k_Loaklgr_MclCurridptSig2Resp_Cnt_u08 0 k_Loaklgr_MclCurridptSigFitThd_Cnt_u08 0 k_Loaklgr_MclCurridptSigFitThd_Cnt_u08 0 k_Loaklgr_MclCurridptSigFitThd_Cnt_u08 0 k_Loaklgr_MclCurridptSig-Tittd_Cnt_u08 1 k_Loaklgr_MclCurridptSig-Tittd_Cnt_u08 0 k_Loaklgr_MclCurridptSig-Tittd_Cnt_u08 0 k_Loaklgr_MclCurridptSig-Tittd_Cnt_u08 1 <td< td=""><td></td><td></td></td<>		
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k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08 5 k_LoaMgr_MotAgldptSig2Resp_Cnt_u08 1 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 0 k_LoaMgr_MotAgldptSig3Resp_Cnt_u08 2 k_LoaMgr_MotAgMtgnRate_UlspS_f32 1 k_LoaMgr_MotAgMtgnScazeroEn_Cnt_lgc 0 k_LoaMgr_MotAgMtgnSca_Uls_f32 1 k_LoaMgr_MotAgMtgnSca_Uls_f32 1 k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 2 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08 2 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurrldptSigFitInd_Cnt_u08 0 k_LoaMgr_MotCurrldptSigFitInd_Cnt_u08 0 k_LoaMgr_MotCurrldptSigFitInd_Cnt_u08 0 k_LoaMgr_MotCurrldptSig-SazeroEn_Cnt_lgc 1 k_LoaMgr_MotCurrldptSig-SazeroEn_Cnt_lgc 1 k_LoaMgr_RedcdSiRate_UlspS_f32 1 k_LoaMgr_RedcdSiRate_UlspS_f32 1 k_LoaMgr_Per1_DiagcSistvtrInactv_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcSistvtrInactv_Cnt_lgc.value 1 target_LoaMgr_Per1_MrVoldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MrVoldptSig_Cnt_u08.value 1		
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k_LoaMgr_MotCurrIdptSig0Resp_Cnt_u08 2 k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurrIdptSigFitThd_Cnt_u08 0 k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 1 k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 1 k_LoaMgr_MotCurrMtgtnSca_Uls_f32 1 k_LoaMgr_RedcdStRate_UlspS_f32 1 k_LoaMgr_RedcdStRate_UlspS_f32 1 k_LoaMgr_RedcdStSca_Uls_f32 1 k_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_lgc.value 0 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 4 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaAvgr_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStsIvtr1Inactv_Cnt_lgc		
k_LoaMgr_MotCurrIdptSig1Resp_Cnt_u08 0 k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurrIdptSigFitThd_Cnt_u08 0 k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 1 k_LoaMgr_MotCurrMtgtnSca_Uls_f32 1 k_LoaMgr_MotCurrMtgtnSca_Uls_f32 1 k_LoaMgr_RedcdStRate_UlspS_f32 1 k_LoaMgr_RedcdStRate_UlspS_f32 1 k_LoaMgr_Perd_DiagcStsivtr1lnactv_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStsivtr2lnactv_Cnt_lgc.value 0 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 4 target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStslvtr1lnactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr1lnactv_Cnt_lgc		
k_LoaMgr_MotCurrIdptSig2Resp_Cnt_u08 0 k_LoaMgr_MotCurrIdptSigFltThd_Cnt_u08 0 k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 1 k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc 1 k_LoaMgr_MotCurrMtgtnSca_Uls_f32 1 k_LoaMgr_RedcdStRate_UlspS_f32 1 k_LoaMgr_RedcdStSca_Uls_f32 1 k_LoaMgr_Per1_DiagcStslvtr1nactv_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStslvtr2lnactv_Cnt_lgc.value 0 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 4 target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStslvtr1lnactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr1lnactv_Cnt_lgc		
k_LoaMgr_MotCurrIdptSigFitThd_Cnt_u08 k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 t_LoaMgr_MotCurrMtgtnSca_Uls_f32 t_LoaMgr_MotCurrMtgtnSca_Uls_f32 t_LoaMgr_RedcdStSca_Uls_f32 t_LoaMgr_RedcdStSca_Uls_f32 t_LoaMgr_RedcdStSca_Uls_f32 t_loaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_LoaScaDi_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc		
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32 1 k_LoaMgr_MotCurrMtgtnSca_Uls_f32 1 k_LoaMgr_RedcdStRate_UlspS_f32 1 k_LoaMgr_RedcdStSca_Uls_f32 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value 0 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value 4 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32		
k_LoaMgr_MotCurrMtgtnSca_Uls_f32 k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc		
k_LoaMgr_RedcdStRate_UlspS_f32 k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc		
k_LoaMgr_RedcdStSca_Uls_f32 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value 0 target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc		
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc		
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc		1
target_LoaMgr_Per1_LoaScaDi_Cnt_Igc.value 1 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value 1 target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 1 target_LoaMgr_Per1_TloaAvail_Cnt_Igc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value 1 target_LoaMgr_Per1_TloaDi_Cnt_Igc.value 1 target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	0
target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08.value target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value target_LoaMgr_Per1_TloaDi_Cnt_lgc.value target_LoaMgr_Per1_Dagi_Cnt_lgc.value target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	4
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value 0 target_LoaMgr_Per1_SteerMod_Cnt_enum.value 1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_LoaMgr_Per1_DagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	1
target_LoaMgr_Per1_SteerMod_Cnt_enum.value 1 target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc		
target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value 0 target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc		
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value 1 target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc		
$target_Rte_Inst_Ap_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc \\ target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc \\ target_LoaMgr_Per1_DiagcStslvtr1Inactv_DiagcStslvtr1Inactv_DiagcStslvtr1Inactv_DiagcStslvtr1Inactv_DiagcStslvtr1Inactv_DiagcStslvtr1Inactv_DiagcStslvtr1Inactv_DiagcStslvtr1Inactv_DiagcStslvtr1Inactv_DiagcStslvtr1Inactv_DiagcStslvtr1Inactv_DiagcStslvtr1Inactv_DiagcStslvtr1Inactv_DiagcStslvtr1Inactv_DiagcStslvtr1Inactv_DiagcStslvtr1Inactv_DiagcStslvtr1Inactv_DiagcStslvtr1Inactv_DiagcStslvtr1Inactv_DiagcStslvtr1Ina$		
target_Rte_inst_Ap_LoaMgr.LoaMgr.Per1_blagcStsivitzinactv_Cnt_igc target_LoaMgr_Per1_blagcStsivitzinactv_Cnt_igc target_LoaMgr_Per1_hwTqldptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_DiagcStsIvtr2Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc
target_Rte_inist_Ap_LoaMgr.LoaMgr.Per1_HwTqLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc		
target_Rte_inst_Ap_LoaMgr.LoaMgr.Per1_nw1qLoaMigtnEn_Cnt_igc target_Rte_inst_Ap_LoaMgr.LoaMgr.Per1_ivtrLoaMtgtnEn_Cnt_igc target_LoaMgr.Per1_ivtrLoaMtgtnEn_Cnt_igc		
target_Rte_Inst_Ap_LoaMgr.Per1_LOASt_State_enum target_LoaMgr.Per1_LOASt_State_enum		
target_Rte_Inst_Ap_LoaMgr.Per1_LoaScaDi_Cnt_Igc target_LoaMgr_Per1_LoaScaDi_Cnt_Igc		
target_Rte_Inst_Ap_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrldptSig_Cnt_u08 target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrIdptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u08
target_Rte_Inst_Ap_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc





Name	Input Value		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosIdptSig_Cnt_u(08	
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum	target_LoaMgr_Per1_SteerMod_Cnt_enum		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_lgc	target_LoaMgr_Per1_TloaAvail_Cnt_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc		
Name	Actual Value	Expected Value	Result
LoaMgr_AgSV_Cnt_M_lgc	0	0	~
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	✓
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	✓
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	0	0	~
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~
LoaMgr_LOASt_State_M_enum	5	5	~
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~
LoaMgr_LoaScaleFctr_Uls_M_f32	1	1	✓
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5	5	✓
LoaMgr_MotAgldptSigSV_Cnt_M_u08	0	0	✓
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	0	✓
LoaMgr_MotAgRespSV_Cnt_M_u08	3	3	•
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	•
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	•
LoaMgr_MotCurrRespSV_Cnt_M_u08	2	2	•
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	•
LoaMgr_TloaDi_Cnt_M_lgc	1	1	•
LoaMgr_VltgMdSrcSV_Cnt_M_u08	0	0	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 173}	{170, 173}	•
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1}	{1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	•
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	1	1 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	0	0	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_Igc.value	0	0	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	~
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	0	0	~
target_LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_lgc.value	1	1	~

Test Step Call Trace				✓
Actual Function	Count	Expected Function	Count	Result
CalclvtrState	2	CalclvtrState	2	~
LatchInputs	1	LatchInputs	1	✓
RqstRespConds	1	RqstRespConds	1	~
RqstHwTqResp	1	RqstHwTqResp	1	•
ArbResp	1	ArbResp	1	•
ChooseFinalResp	1	ChooseFinalResp	1	✓
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	2	•
AssignScale	1	AssignScale	1	~
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~

Test Step 3.38 (Repeat Count = 1)		X
Name	Input Value	
LoaMgr_AgSV_Cnt_M_lgc	1	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	2	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	2	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	
LoaMgr_MotAgldptSigSV_Cnt_M_u08	3	
LoaMgr_MotAgLoaMtgtnEn_Cnt_M_lgc	0	
LoaMgr_MotAgRespSV_Cnt_M_u08	1	
LoaMgr_MotCurrldptSigSV_Cnt_M_u08	0	
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	
LoaMgr_SnsrlsMotAgAvail_Cnt_M_lgc	1	
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	
Rte_Inst_Ap_LoaMgr	target_Rte_Inst_Ap_LoaMgr	
k_LoaMgr_CurrlvtrMtgtnRate_UlspS_f32	1	

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Name	Input Value			
k_LoaMgr_CurrlvtrMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_CurrivtrMtgtnSca_Uls_f32	1			
k_LoaMgr_FadeOutStRate_UlspS_f32	1			
k_LoaMgr_HwTqldptSig0TloaAvailResp_Cnt_u08	2			
k_LoaMgr_HwTqldptSig1NoTloaResp_Cnt_u08	4			
k_LoaMgr_HwTqldptSig1TloaAvailResp_Cnt_u08	2			
k_LoaMgr_HwTqldptSig2TloaDiResp_Cnt_u08	1			
k_LoaMgr_HwTqldptSig2TloaEnaResp_Cnt_u08	0			
k_LoaMgr_HwTqldptSig4Resp_Cnt_u08	0			
k_LoaMgr_HwTqldptSigFltThd_Cnt_u08	1			
k_LoaMgr_HwTqMtgtnRate_UlspS_f32	1			
k_LoaMgr_HwTqMtgtnSca_Uls_f32 k_LoaMgr_lvtrldptSig1Resp_Cnt_u08	1			
k_LoaMgr_lvtrldptSig2Resp_Cnt_u08	0			
k_LoaMgr_lvtrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_lvtrMtgtnRate_UlspS_f32	1			
k_LoaMgr_lvtrMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_lvtrMtgtnSca_Uls_f32	1			
k_LoaMgr_MotAgldptSig1NoSmaResp_Cnt_u08	5			
k_LoaMgr_MotAgldptSig2Resp_Cnt_u08	1			
k_LoaMgr_MotAgldptSig3Resp_Cnt_u08	0			
k_LoaMgr_MotAgldptSigFltThd_Cnt_u08	2			
k_LoaMgr_MotAgMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotAgMtgtnScaZeroEn_Cnt_lgc	0			
k_LoaMgr_MotAgSmgAygilRoan_Cat_u08	1 2			
k_LoaMgr_MotAgSmaAvailResp_Cnt_u08				
k_LoaMgr_MotCurrldptSig0Resp_Cnt_u08 k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	2			
k_LoaMgr_MotCurrldptSig1Resp_Cnt_u08	0			
k_LoaMgr_MotCurrldptSigFltThd_Cnt_u08	0			
k_LoaMgr_MotCurrMtgtnRate_UlspS_f32	1			
k_LoaMgr_MotCurrMtgtnScaZeroEn_Cnt_lgc	1			
k_LoaMgr_MotCurrMtgtnSca_Uls_f32	1			
k_LoaMgr_RedcdStRate_UlspS_f32	1			
k_LoaMgr_RedcdStSca_Uls_f32	1			
target_LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_lgc.value	0			
target_LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc.value	1			
target_LoaMgr_Per1_HwTqldptSig_Cnt_u08.value	1			
target_LoaMgr_Per1_LoaScaDi_Cnt_lgc.value	0			
target_LoaMgr_Per1_MotCurrIdptSig_Cnt_u08.value	1			
target_LoaMgr_Per1_MtrPosldptSig_Cnt_u08.value	1			
target_LoaMgr_Per1_SteerMod_Cnt_enum.value target_LoaMgr_Per1_TloaAvail_Cnt_lgc.value	0			
target_LoaMgr_Per1_TloaDi_Cnt_lgc.value	0			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr1Inactv_Cnt_Igc	target_LoaMgr_Per1_DiagcStslvtr1Inactv_C	nt lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_DiagcStslvtr2Inactv_Cnt_lgc	target_LoaMgr_Per1_DiagcStslvtr2Inactv_C			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqldptSig_Cnt_u08	target_LoaMgr_Per1_HwTqldptSig_Cnt_u08			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cn	t_lgc		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc	target_LoaMgr_Per1_lvtrLoaMtgtnEn_Cnt_lo	gc		
target_Rte_Inst_Ap_LoaMgr_Per1_LOASt_State_enum	target_LoaMgr_Per1_LOASt_State_enum			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_LoaScaDi_Cnt_Igc	target_LoaMgr_Per1_LoaScaDi_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cr			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrldptSig_Cnt_u08	target_LoaMgr_Per1_MotCurrldptSig_Cnt_u			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_MotCurrLoaMtgtnEn_Cnt_Igc	target_LoaMgr_Per1_MotCurrLoaMtgtnEn_0			
target_Rte_Inst_Ap_LoaMgr_LoaMgr_Per1_MtrPosIdptSig_Cnt_u08	target_LoaMgr_Per1_MtrPosldptSig_Cnt_u0	08		
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_SteerMod_Cnt_enum target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaAvail_Cnt_Igc	target_LoaMgr_Per1_SteerMod_Cnt_enum target_LoaMgr_Per1_TloaAvail_Cnt_lgc			
target_Rte_Inst_Ap_LoaMgr.LoaMgr_Per1_TloaDi_Cnt_lgc	target_LoaMgr_Per1_TloaDi_Cnt_lgc			
Name	Actual Value	Expected Value	Result	
LoaMgr_AgSV_Cnt_M_lgc	1	1	Kesuit	
LoaMgr_HwTqldptSigSV_Cnt_M_u08	1	1	·	
LoaMgr_HwTqLoaMtgtnEn_Cnt_M_lgc	1	1	-	
LoaMgr_HwTqRespSV_Cnt_M_u08	4	4	✓	
LoaMgr_lvtrldptSigSV_Cnt_M_u08	1	1	~	
LoaMgr_lvtrLoaMtgtnEn_Cnt_M_lgc	1	1	•	
LoaMgr_lvtrRespSV_Cnt_M_u08	5	5	~	
LoaMgr_LOASt_State_M_enum	5	5	✓	
LoaMgr_LoaRateLimit_UlspS_M_f32	500	500	~	
LoaMgr_LoaScaleFctr_Uls_M_f32	0	0	~	
		5	✓	
LoaMgr_MaxMtgtnResp_Cnt_M_u08	5			
LoaMgr_MotAgldptSigSV_Cnt_M_u08	1	1	•	
			, , , , , , , , , , , , , , , , , , ,	





Name	Actual Value	Expected Value	Result
LoaMgr_MotCurrIdptSigSV_Cnt_M_u08	0	0	~
LoaMgr_MotCurrLoaMtgtnEn_Cnt_M_lgc	1	1	~
LoaMgr_MotCurrRespSV_Cnt_M_u08	3	3	~
LoaMgr_TloaAvail_Cnt_M_lgc	0	0	~
LoaMgr_TloaDi_Cnt_M_lgc	0	0	~
LoaMgr_VltgMdSrcSV_Cnt_M_u08	1	1	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(NTC_Cnt_T_enum)	{170, 172, 173}	{170, 172, 173}	~
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Param_Cnt_T_u08)	{1, 1, 1}	{0, 0, 0, 0, 0}	*
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus(Status_Cnt_T_enum)	{1, 1, 1}	{1, 1, 1}	~
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32(data)	500	500 ± 0.01	✓
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32(data)	0	0 ± 0.001	~
target_LoaMgr_Per1_HwTqLoaMtgtnEn_Cnt_lgc.value	1	1	✓
target_LoaMgr_Per1_IvtrLoaMtgtnEn_Cnt_lgc.value	1	1	~
target_LoaMgr_Per1_LOASt_State_enum.value	5	5	✓
target_LoaMgr_Per1_MotAgLoaMtgtnEn_Cnt_lgc.value	1	1	~
target LoaMgr Per1 MotCurrLoaMtgtnEn Cnt Igc.value	1	1	✓

Test Step Call Trace							
Actual Function	Count	Expected Function	Count	Result			
CalclvtrState	2	CalcivtrState	2	~			
LatchInputs	1	LatchInputs	1	~			
RqstRespConds	1	RqstRespConds	1	~			
RqstHwTqResp	1	RqstHwTqResp	1	~			
ArbResp	1	ArbResp	1	~			
ChooseFinalResp	1	ChooseFinalResp	1	~			
Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	Rte_Call_Ap_LoaMgr_NxtrDiagMgr_SetNTCStatus	3	~			
AssignScale	1	AssignScale	1	~			
Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Enter_Ap_LoaMgr_EaLoaScaleAndRate	1	~			
Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	Rte_Write_Ap_LoaMgr_LoaRateLimit_UlspS_f32	1	~			
Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	Rte_Write_Ap_LoaMgr_LoaScaleFctr_Uls_f32	1	~			
Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	Rte_Exit_Ap_LoaMgr_EaLoaScaleAndRate	1	~			