Integration Manual HwPwUp

Table of Contents

[1 Dependencies 2](#_Toc390780816)

[1.1 SWCs 2](#_Toc390780817)

[1.2 Global Functions(Non RTE) to be provided to Integration Project 2](#_Toc390780818)

[2 Configuration 3](#_Toc390780819)

[2.1 Build Time Config 3](#_Toc390780820)

[2.2 Configuration Files to be provided by Integration Project 3](#_Toc390780821)

[2.2.1 Da Vinci Parameter Configuration Changes 3](#_Toc390780822)

[2.2.2 DaVinci Interrupt Configuration Changes 3](#_Toc390780823)

[2.2.3 Manual Configuration Changes 3](#_Toc390780824)

[3 Integration 4](#_Toc390780825)

[3.1 Required Global Data Inputs 4](#_Toc390780826)

[3.2 Required Global Data Outputs 4](#_Toc390780827)

[3.3 Specific Include Path present 4](#_Toc390780828)

[4 Runnable Scheduling 5](#_Toc390780829)

[5 Memory Mapping 6](#_Toc390780830)

[5.1 Mapping 6](#_Toc390780831)

[5.2 Usage 6](#_Toc390780832)

[5.3 Non RTE NvM Blocks 6](#_Toc390780833)

[5.4 RTE NvM Blocks 6](#_Toc390780834)

[6 Compiler Settings 6](#_Toc390780835)

[6.1 Preprocessor MACRO 6](#_Toc390780836)

[6.2 Optimization Settings 6](#_Toc390780837)

[7 Revision Control Log 7](#_Toc390780838)

# Dependencies

## SWCs

|  |  |
| --- | --- |
| Module | Required Feature |
| None |  |

Note : Referencing the external components should be avoided in most cases. Only in unavoidable circumstance external components should be refered. Developer should track the references.

## Global Functions(Non RTE) to be provided to Integration Project

None

# Configuration

## Build Time Config

|  |  |  |
| --- | --- | --- |
| Modules | Notes |  |
| None |  |  |

## Configuration Files to be provided by Integration Project

Ap\_ HwPwUp \_Cfg.h generated by Ap\_HwPwUp\_Cfg.h.tt

### Da Vinci Parameter Configuration Changes

|  |  |  |
| --- | --- | --- |
| Parameter | Notes | SWC |
| HwPwUpGeneral/HwPwUpCPEnable | To enable checkpoints | HwPwUp |

### DaVinci Interrupt Configuration Changes

|  |  |  |  |
| --- | --- | --- | --- |
| ISR Name | VIM # | Priority Dependency | Notes |
| None |  |  |  |

### Manual Configuration Changes

|  |  |  |
| --- | --- | --- |
| Constant | Notes | SWC |
| None |  |  |

# Integration

## Required Global Data Inputs

None

## Required Global Data Outputs

None

## Specific Include Path present

No

# Runnable Scheduling

This section specifies the required runnable scheduling.

|  |  |  |
| --- | --- | --- |
| Init | Scheduling Requirements | Trigger |
| None |  |  |

|  |  |  |
| --- | --- | --- |
| Runnable | Scheduling Requirements | Trigger |
| HwPwUp\_Per1 | It should run before torque oscillation calculation | RTE 2ms |

**.**

# Memory Mapping

## Mapping

|  |  |  |
| --- | --- | --- |
| Memory Section | Contents | Notes |
| HWPWUP\_START\_SEC\_VAR\_CLEARED\_BOOLEAN |  |  |
| HWPWUP\_START\_SEC\_VAR\_CLEARED\_UNSPECIFIED |  |  |
| RTE\_START\_SEC\_AP\_ HWPWUP \_APPL\_CODE |  |  |

\* Each …START\_SEC… constant is terminated by a …STOP\_SEC… constant as specified in the AUTOSAR Memory Mapping requirements.

## Usage

|  |  |  |
| --- | --- | --- |
| Feature | RAM | ROM |
| <Memmap usuage info> |  |  |

Table 1: ARM Cortex R4 Memory Usage

## Non RTE NvM Blocks

|  |
| --- |
| Block Name |
| None |

Note : Size of the NVM block if configured in developer

## RTE NvM Blocks

|  |
| --- |
| Block Name |
| None |

Note : Size of the NVM block if configured in developer

# Compiler Settings

## Preprocessor MACRO

None

## Optimization Settings

None

# Revision Control Log

|  |  |  |  |
| --- | --- | --- | --- |
| **Rev #** | **Change Description** | **Date** | **Author** |
| 1 | Initial version | 17-June-14 | nzt9hv |
|  |  |  |  |