

STM32WL_QFN48_IPD_4Layers_IPD_TX_LowPower

MB1848_LP

Table of contents

Sheet 1: Project overview (this page)

Sheet 2: Reference_Design_STM32WL_4L_IPD_ST_V2_LP

Legend

General comment such as function title, configuration, ...

Text to be added to silkscreen.

Warning text.

▲ Notes to generate the board layout.

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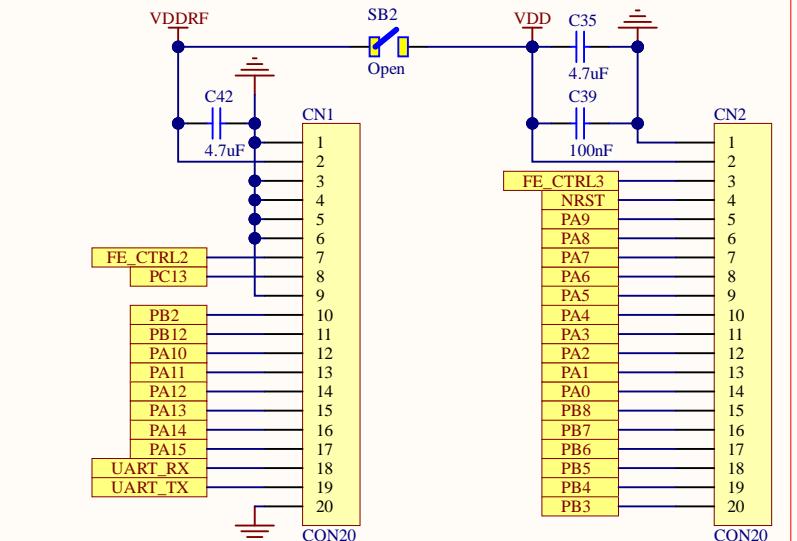
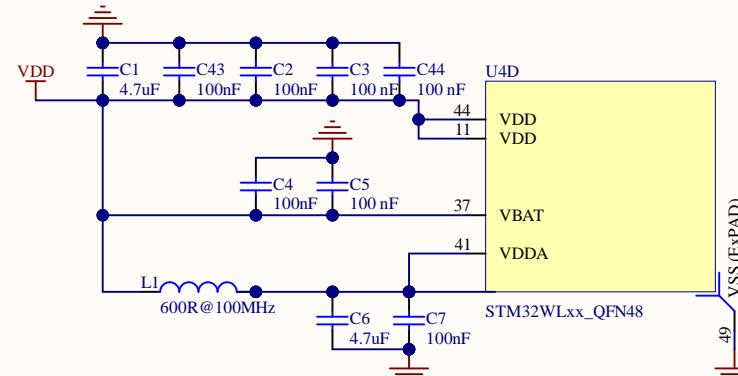
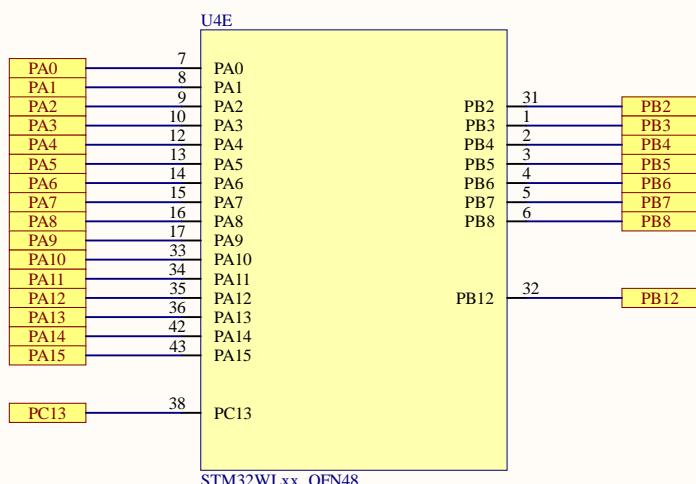
U_Top
Reference_Design_STM32WL_4L_IPD_ST_V2_LPSchDoc



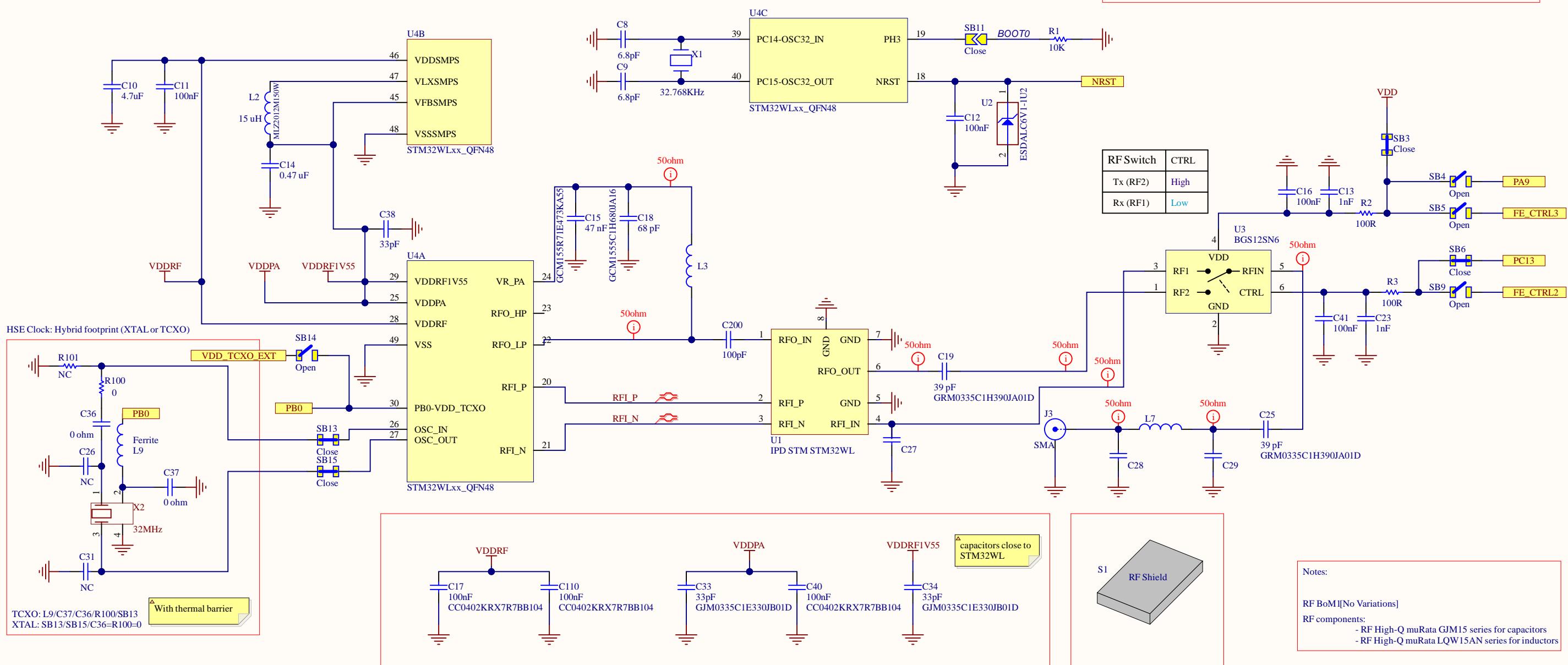
| | | |
|---|-----------------------------|----------------------|
| Title: Schematic sheet title to update | | |
| Project: STM32WL_QFN48_IPD_4Layers_IPD_TX_LowPower | | |
| Variant: [No Variations] | | |
| Revision: A-01 | Reference: MB1848_LP | |
| Size: A4 | Date: June 2022 | Sheet: 1 of 2 |

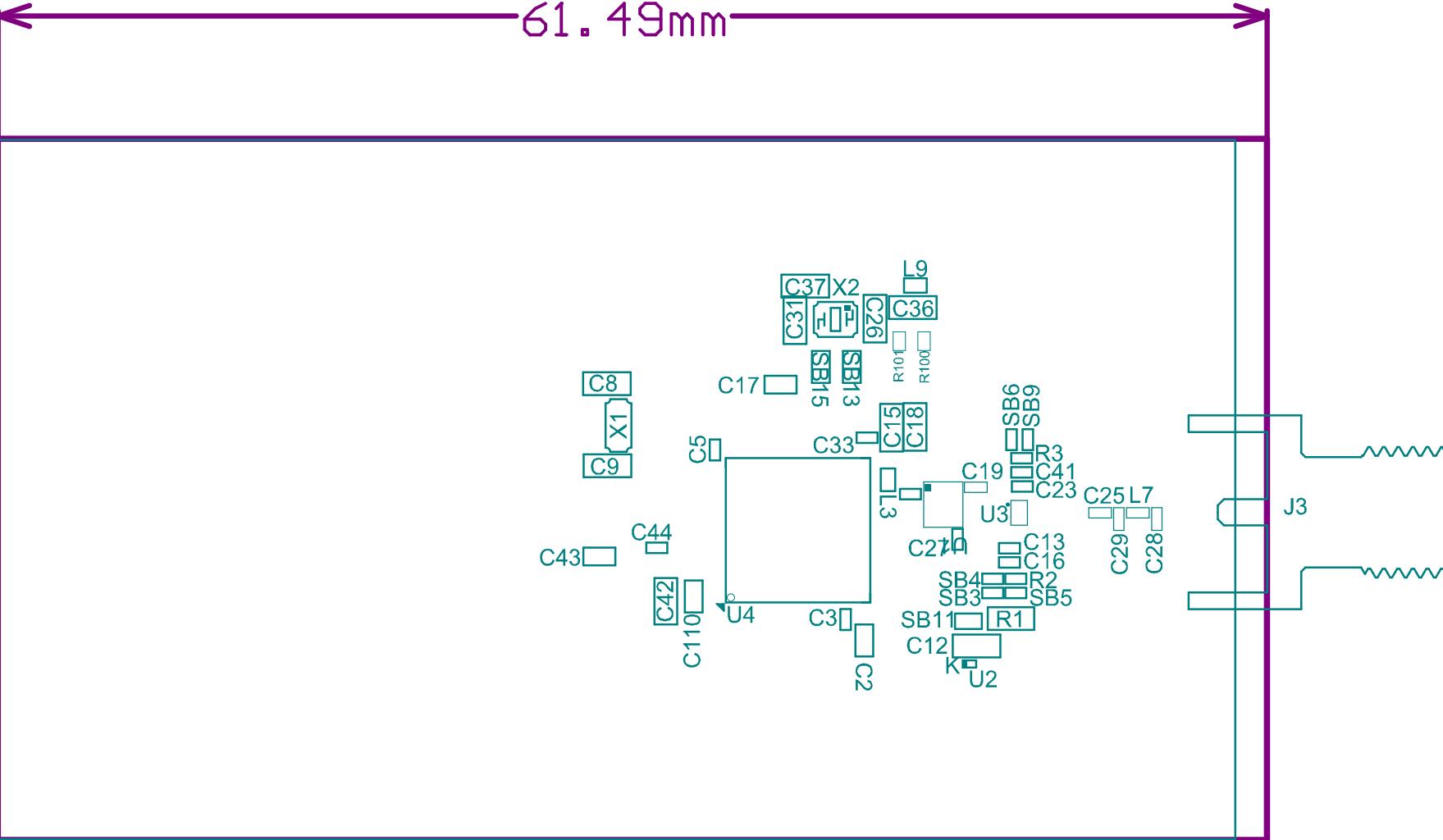


STM32WL QFN 4 Layers IPD - Low Power Mode - Reference Board



Connectors from daughter board (TOP LAYER)





Project: STM32WL_QFN48_IPD_4Layers_IPD_TX_LowPower

Layer: Top Assembly

Gerber: GM14

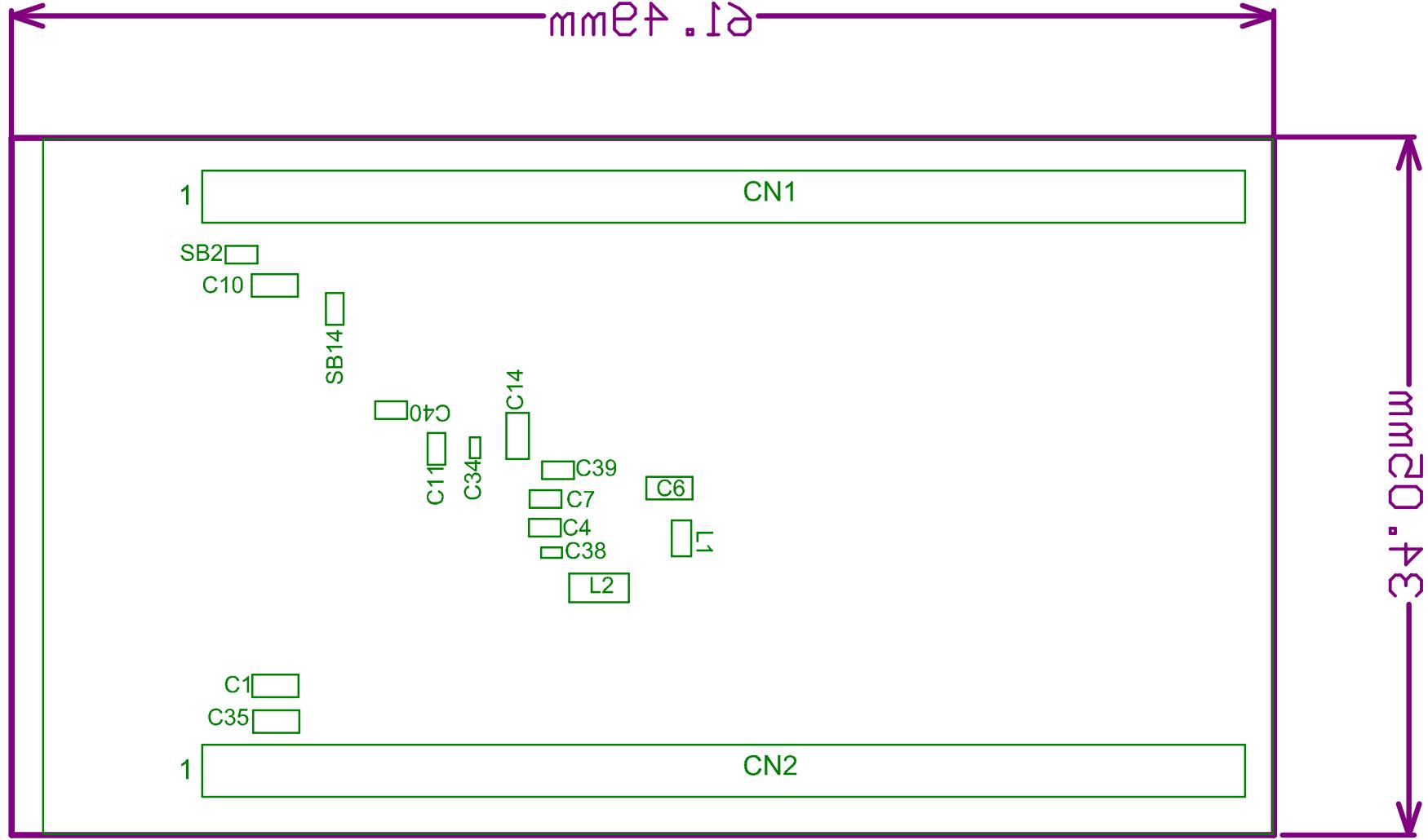
Variant: [No Variations]

Ref: MB1848_LP

Date: June 2022

Rev: A





Project: STM32WL_QFN48_IPD_4Layers_IPD_TX_LowPower

Layer: Bottom Assembly

Gerber: .GM15

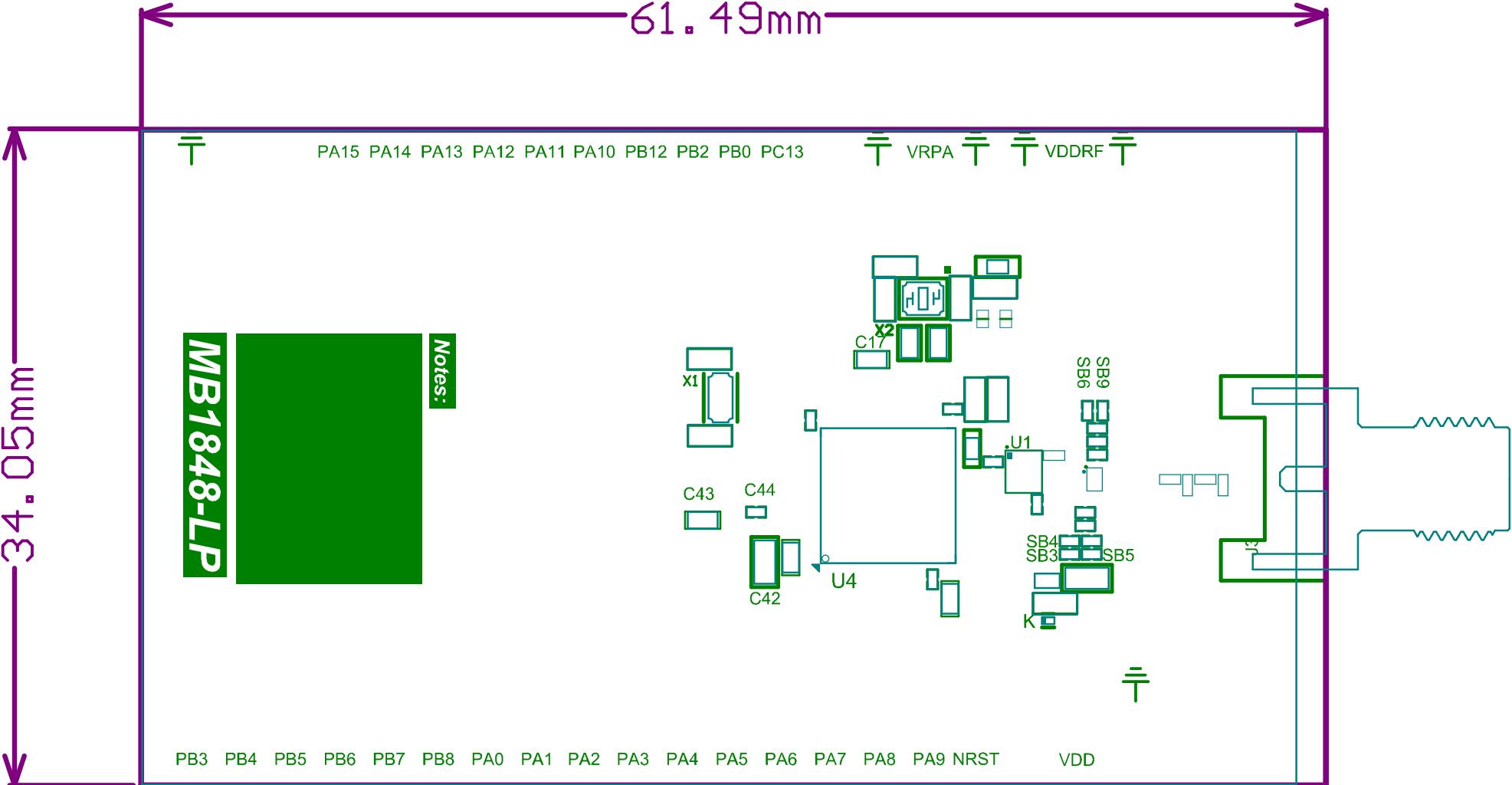
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Ref: MB1848_LP

Date: June 2022

Rev: A





Top Assembly

.GM04

