

Contact

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www.linkedin.com/in/shubhanshu-sharma-34b247125 (LinkedIn)

Top Skills

Verilog

Field-Programmable Gate Arrays (FPGA)

Linux

Certifications

Programming for Everybody (Getting Started with Python) by University of Michigan on Coursera

Shubhanshu Sharma

Senior Member Of Technical Staff at Mentor Graphics
Delhi

Summary

I am an Electronics and Communication Engineer from GGSIPU with a passion for ASIC and SoC design.

Experience

Mentor Graphics

2 years 3 months

Senior Member Of Technical Staff

January 2020 - Present (8 months)

Noida, Uttar Pradesh, India

Member Technical Staff

June 2018 - January 2020 (1 year 8 months)

Noida Area, India

DKOP Labs Pvt. Ltd.

Trainee

September 2016 - April 2018 (1 year 8 months)

New Delhi Area, India

Bharat Electronics

Intern

June 2016 - July 2016 (2 months)

Ghaziabad, Uttar Pradesh, India

Education

Guru Gobind Singh Indraprastha University

Bachelor of Technology (B.Tech.), Electrical, Electronics and Communications Engineering · (2014 - 2018)

Titiksha Public School

Class 11 - Class 12, Science · (2012 - 2014)

Bharti public school,swasthya vihar,delhi
class 10, Science · (2002 - 2012)