

Low-entropy masking scheme compatible for ARX

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Introduction

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Low-entropy masking scheme compliant with Boolean and Arithmetic computations

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Security of low-entropy masking schemes

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Second-order evaluation of low-entropy masking schemes?

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Conclusion and perspectives

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Conclusion and perspectives

- Sensitive computations must be secured against **non-invasive attacks**, which attempt to correlate the leakage of some operations with a hypothetical model.
- A protection against this threat is the **masking countermeasure**.
- Masking consists in changing the **intermediate variables** of the computation into **randomized** versions, which are thus decorrelated from the unprotected variables, each being a potential target for a side-channel attack.

Protection schemes have proposed when operations are either:



- **Boolean**, or

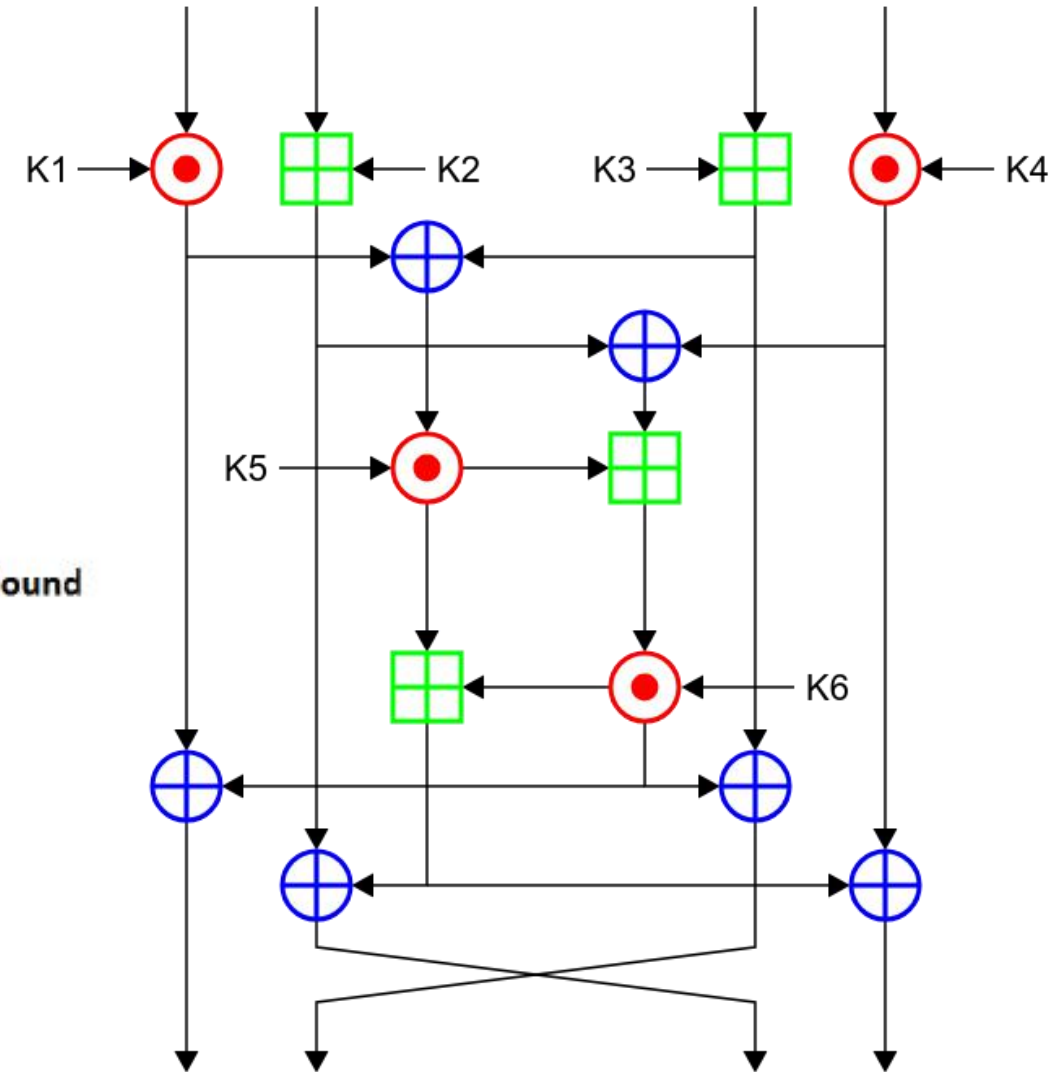
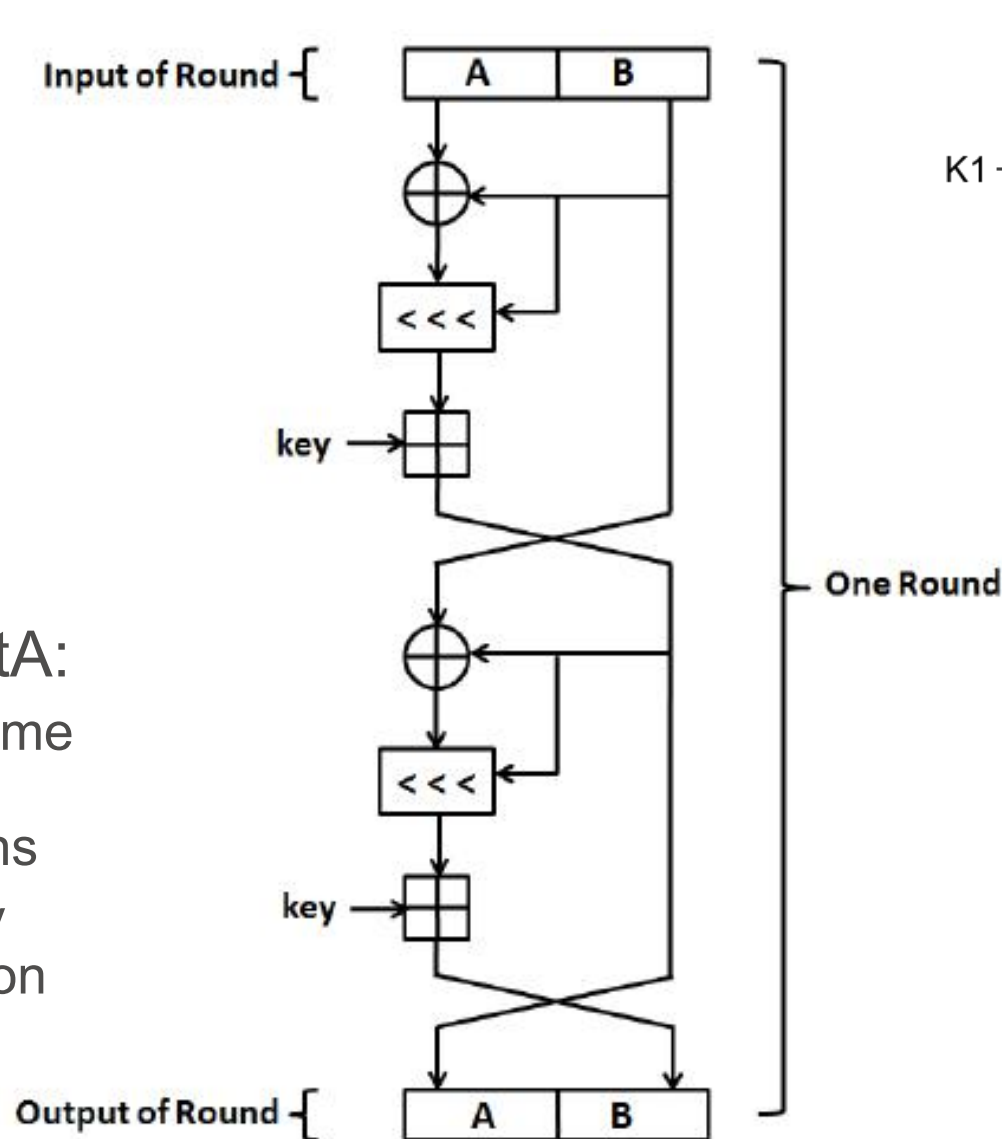


- **Arithmetic**.

Both of which are additive groups.

ARX: Add, Rotate, XOR

- IDEA
- RC5
- XTEA
- SipHash
- ...
- Masking SotA:
 - Cumbersome A2B, B2A conversions
 - No latency preservation



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2.1 Addition

Let $a = (a_{n-1}, \dots, a_0)_2$ and $b = (b_{n-1}, \dots, b_0)_2$ two n -bit integers, represented as a string of bits.

The bitwise operations for arithmetic addition are

- $d_i = a_i \oplus b_i \oplus c_i$,
- $c_{i+1} = \text{MAJ}(a_i, b_i, c_i)$,

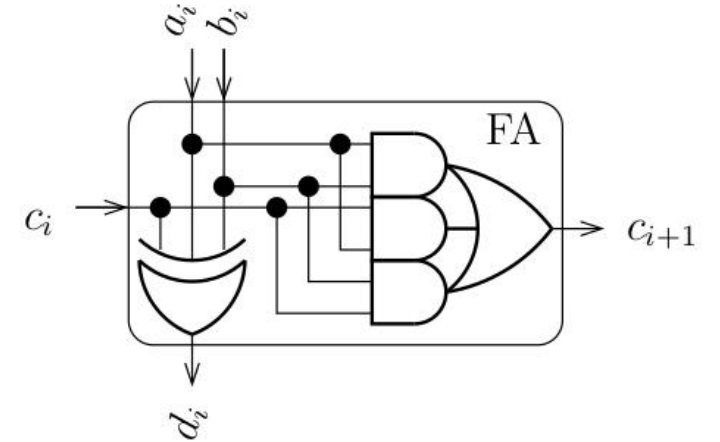


Figure 1: Schematic for the Full Adder

where MAJ is the majority function, namely

$$\begin{aligned} \text{MAJ}(a_i, b_i, c_i) &= (a_i \wedge b_i) \vee (b_i \wedge c_i) \vee (c_i \wedge a_i) \\ &= (a_i \wedge b_i) \oplus (b_i \wedge c_i) \oplus (c_i \wedge a_i). \end{aligned}$$

The FA (Full Adder) is the one-bit slice of an adder, depicted in Fig. 1.

Addition: Masking arithmetic operations

Now have the nice property that, for all $m \in \{0, 1\}$,

- $d_i \oplus m = (a_i \oplus m) \oplus (b_i \oplus m) \oplus (c_i \oplus m)$,
- $c_{i+1} \oplus m = \text{MAJ}(a_i \oplus m, b_i \oplus m, c_i \oplus m)$.

The first property is due to the associativity of the XOR, namely $(a_i \oplus b_i) \oplus c_i = a_i \oplus (b_i \oplus c_i) = a_i \oplus b_i \oplus c_i$. The second property is inherent to the majority: its dual function is also the majority. The majority of ones (at least two ones amongst three bits) is the opposite of the majority of zeroes (at least two zeroes amongst three bits). Mathematically, this writes:

$$\text{MAJ}(\neg a_i, \neg b_i, \neg c_i) = \neg \text{MAJ}(a_i, b_i, c_i).$$

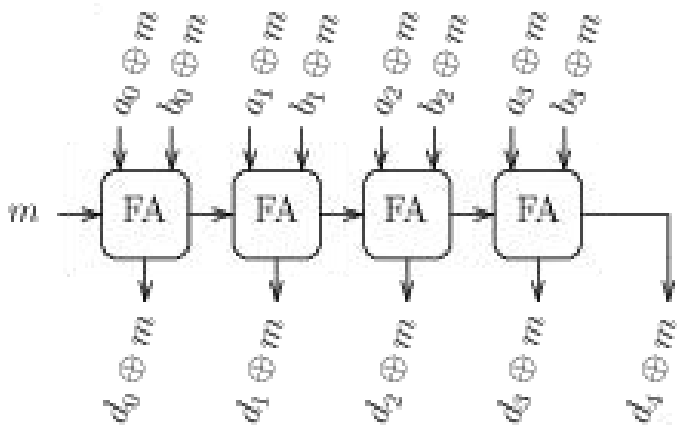
So, all in one:

$$\forall m \in \{0, 1\}, \quad \text{MAJ}(a_i \oplus m, b_i \oplus m, c_i \oplus m) = \text{MAJ}(a_i, b_i, c_i) \oplus m.$$

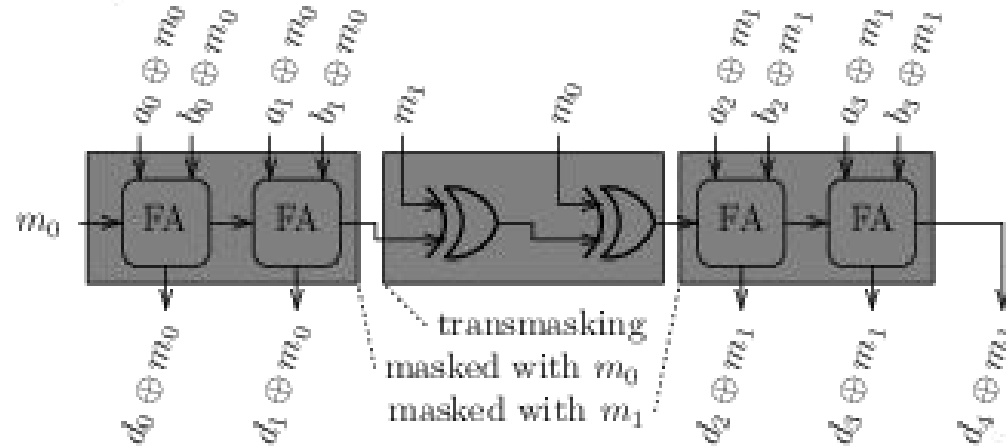
This means that the addition is transparent to masking, provided the carries are also inverted. This is illustrated in Fig. 2 when the same mask is shared for both operands and all the bits.

Remark 1. *This masking is first-order secure provided the two operands are independent, and also provided that the leakage model is linear (bits or words are pairwise not combined by the leakage function).*

- **Boolean operations**
 - XOR on bit-vectors
- **Arithmetic operations**
 - Modulo 2^{32} or 2^{64} additions (with carry) on integers



$n=4, k=1$



$n=4, k=2$

Drop the outbound carry if computing not in \mathbf{Z} but in $\mathbf{Z}/2^n\mathbf{Z}$

2.2 Reduction

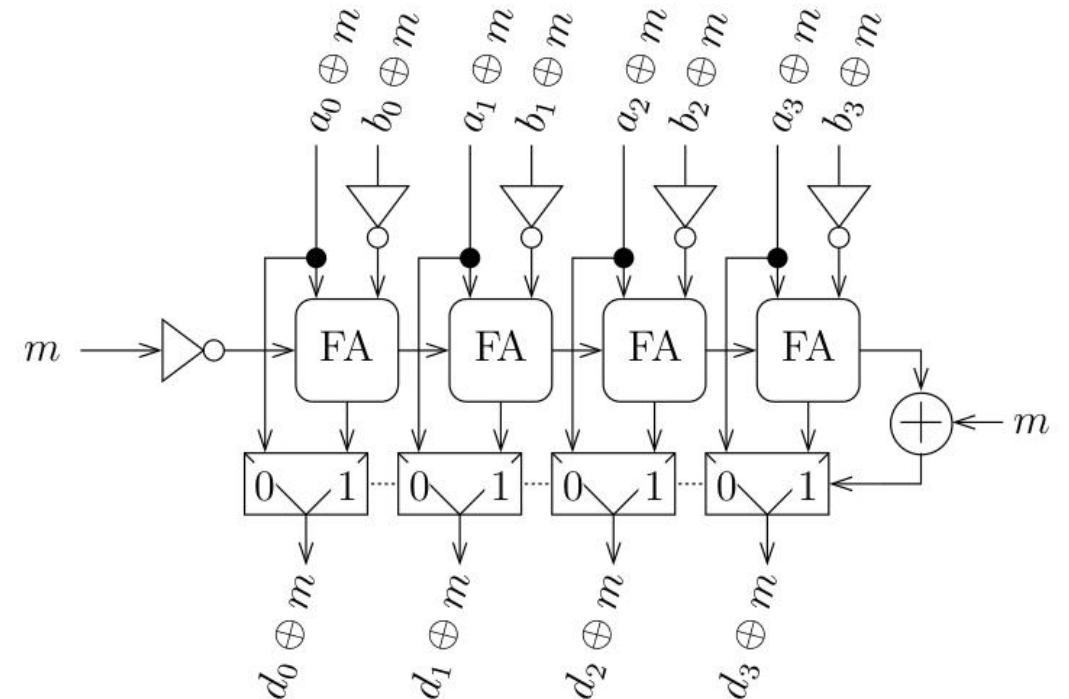
In some algorithms, it might be demanded to *reduce* a number a modulo another number b . Let us further assume that $a = (a_{n-1}, \dots, a_0)_2$ and $b = (b_{n-1}, \dots, b_0)_2$ fit on n bits. The result of the reduction is denoted by $d = (d_{n-1}, \dots, d_0)_2$, which is such that $d = a \bmod b$. Here, we consider the case where the MSB b_{n-1} of b is set to 1. Then:

$$d = a \bmod b = \begin{cases} a - b & \text{if } a \geq b, \text{ or} \\ a & \text{otherwise.} \end{cases} \quad (1)$$

Indeed, $-b = \neg(b-1)$. e.g., on 4 bits:

$-3 = 16-3 = 13$ and $\neg 2 = \neg(0010)_2 = (1101)_2 = 13$.

$-2 = 16-2 = 14$ and $\neg 1 = \neg(0001)_2 = (1110)_2 = 14$.



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- Security model

§ This scheme is first order secure

§ SPA, DPA, DEMA, etc. will fail (whatever the number of traces)

§ If leakage function is symmetrical, $E[L(X+M) \mid X=x] = 1/2 (L(x) + L(\neg x)) = \text{constant}$

§ Second-order attacks are possible

§ Combining the information “a + m” with mask “m”

§ The optimal distinguisher is given below:

Let $n = 8$ and $k|n$, typically $k = 1, 2, 4, 8$. Let us call $\phi : \mathbb{F}_2^k \rightarrow \mathbb{F}_2^n$ the injective masking function, such that

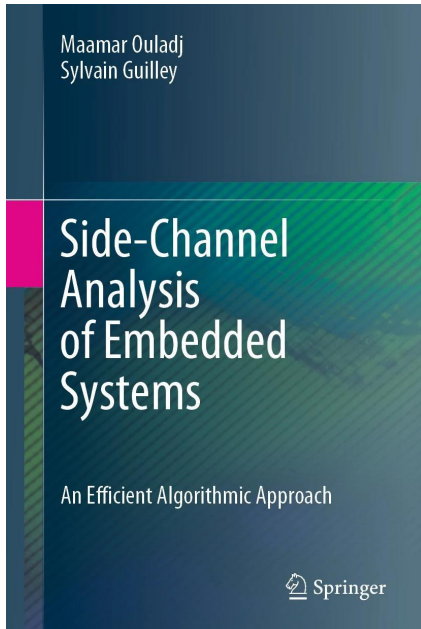
$$\phi(m) = \underbrace{(m, \dots, m)}_{n/k \text{ times}}.$$

The optimal bi-variate distinguisher is:

$$\hat{k} = \arg \max_k \sum_q \log \sum_m \exp - \frac{1}{2\sigma^2} \left\{ \left(X_q^{(0)} - w_H(\phi(m) \oplus S[t_q \oplus k]) \right)^2 + \left(X_q^{(1)} - w_H(m) \right)^2 \right\}$$

Computational speed-up:

- The convolution can be optimized with a Fourier transform.
 - On bitvectors, the Fourier transform is called the Walsh-Hadamard transform, denoted by the «hat» notation
- See also our book:



Let f and g two functions $\mathbb{F}_2^k \rightarrow \mathbb{R}$. Typically,

$$f(m) = \exp - \frac{1}{2\sigma^2} \left(X_q^{(0)} - w_H(\phi(m) \oplus S[t_q \oplus k]) \right)^2$$

and

$$g(m) = \exp - \frac{1}{2\sigma^2} \left(X_q^{(1)} - w_H(m) \right)^2$$

where:

- q is the trace index,
- X_q is the bivariate trace measurement,
- t_q is the plaintext,
- S is the substitution box.

The optimal distinguisher also consists in computing:

$$\begin{aligned} & \sum_{m \in \mathbb{F}_2^k} f(\phi(m) \oplus S)g(m) \\ &= \sum_{m' \in \text{Im}(\phi)} f(m' \oplus S)g(\phi^{-1}(m')) \\ &= \sum_{m' \in \mathbb{F}_2^n} 1_{\text{Im}(\phi)}(m') f(m' \oplus S)g \circ \phi^{-1}(m') \end{aligned} \quad (2)$$

where, for example, $\phi^{-1}(m_1, \dots, m_{n/k}) = m_1$.

The line (2) is amenable to be computed efficiently leveraging Walsh-Hadamard transform [3] as:

$$\widehat{f 1_{\text{Im}(\phi)} \times g \circ \phi^{-1}(S)}.$$

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Results

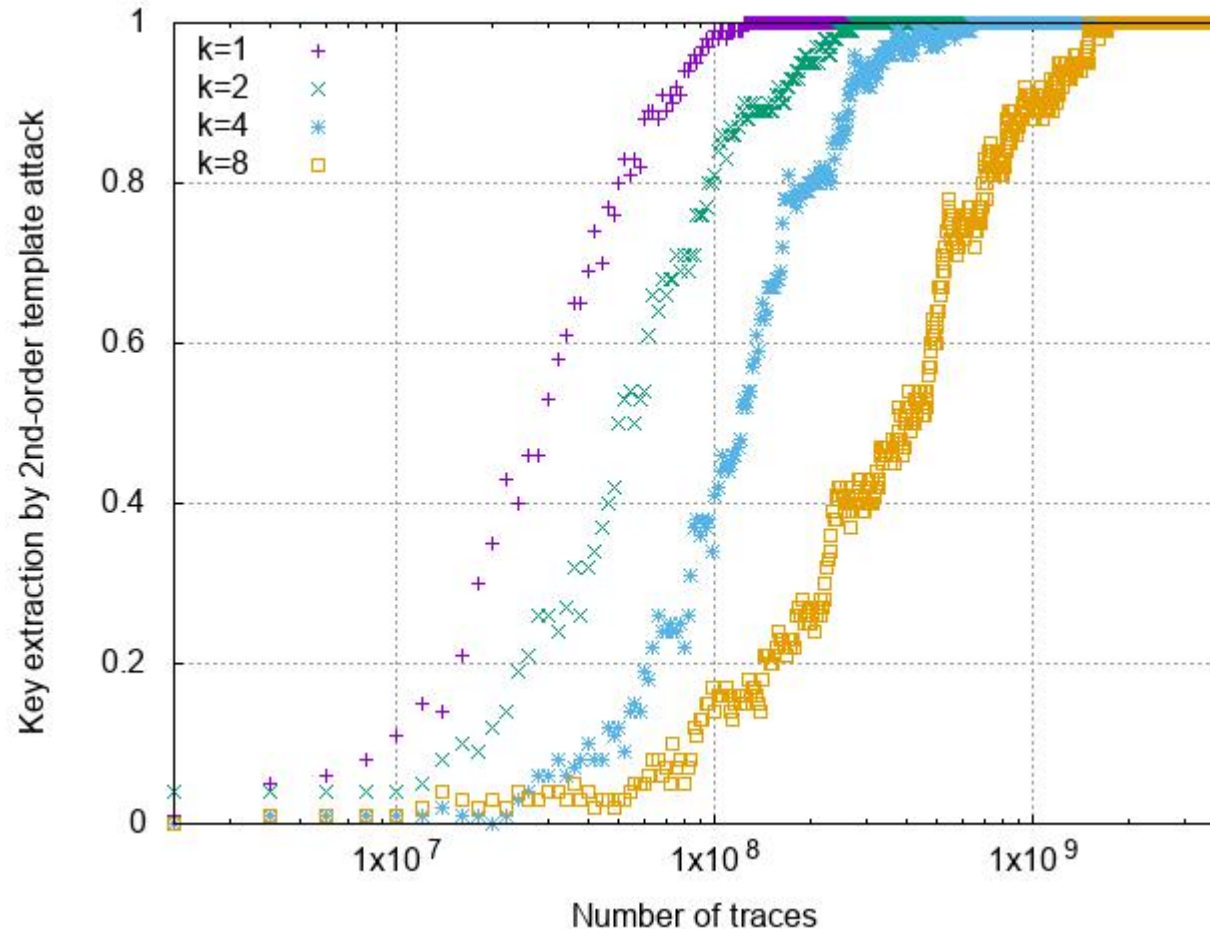
- Simulations

§ Hamming weight model:

§ k out of n=8 bits of entropy

§ The area is defined where the base index is 100 for the 128-bit entropy version. The security level is defined for 2nd order zero-offset attack (with $\text{SNR}=10^{-4}$), the model is derived from ISO/IEC 20085-2:2020(E).

Entropy	128 bits	64 bits	32 bits	16 bits
Area	100	70	50	35
Security level	800 million traces	400 million traces	200 million traces	100 million traces



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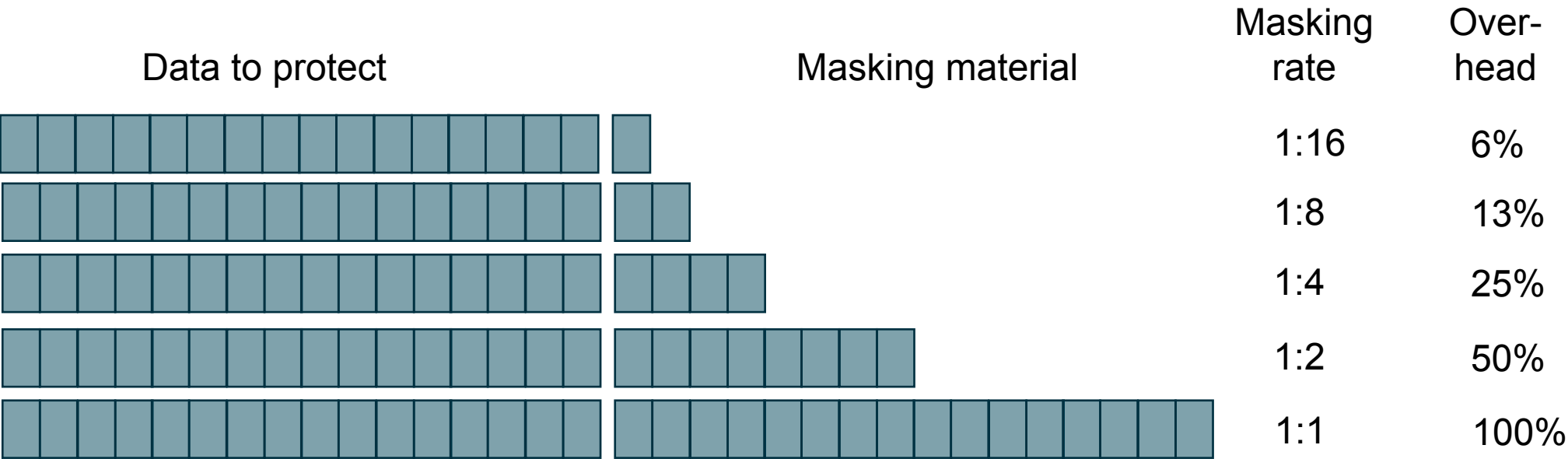
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Conclusion and perspectives

- Another perspective on Arithmetic to Boolean (and vice-versa) masking conversions
- Amenable to some lightweight algorithms
- Scalable masking:



THANK YOU FOR YOUR ATTENTION

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