

[illegible]

Sim8085 - A 8085 microprocessi x

sim8085.com

Help About GitHub

Registers ↻

A/PSW 0x 04 93

BC 0x 08 02

DE 0x 04 00

HL 0x 35 01

SP 0x FF FF

PC 0x 08 17

Flags ↻

Z ☐

S ☒

P ☐

C ☒

AC ☒

main.asm

1 LXI H,3500H

2 MOV B,B

3 INX H

4 MOV C,M

5 MVI D,00H

6 MOV A,B

7 LOOP:SUB C

8 INR D

9 CMP C

10 JNC LOOP

11 STA 3503H

12 MOV A,D

13 STA 3502

14 HLT

Load at 0x0800

Memory View ↻

0x 3500

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
350	08	02	04	00	00	00	00	00	00	00	00	00	00	00	00	00
351	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
352	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
353	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
354	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
355	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
356	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
357	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
358	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
359	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
35A	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
35B	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
35C	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
35D	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
35E	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
35F	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00

Start Address at: 0x 3300

Type here to search

8:35 AM

6/14/2024

Registers

A/PSW

0x 01 97

BC

0x 02 07

DE

0x 00 00

HL

0x 00 00

SP

0x FF FF

PC

0x 08 1B

Flags

Z

☐

S

☒

P

☒

C

☒

AC

☒

main.asm

```
1 LDA 8501H
2 MOV B,A
3 LDA 8500H
4 MVI C,00
5 LOOP: CMP B
6 JC LOOP1
7 SUB B
8 INR C
9 JMP LOOP
10 STA 8503H
11 DCR C
12 MOV A,C
13 LOOP1: STA 8502H
14 HLT
```

Memory View

0x 8501

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
850	0F	02	01	00	00	00	00	00	00	00	00	00	00	00	00	00
851	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
852	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
853	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
854	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
855	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
856	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
857	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
858	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
859	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
85A	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
85B	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
85C	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
85D	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
85E	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
85F	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00

Start Address at: 0x 7700

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13 STA 3502

14 HLT

Memory View

0x3500

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
350	08	02	04	00	00	00	00	00	00	00	00	00	00	00	00	00
351	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
352	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
353	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
354	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
355	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
356	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
357	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
358	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
359	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
35A	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
35B	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
35C	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
35D	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
35E	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
35F	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00

Start Address at: 0x3300

97°F
Mostly sunny

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Registers

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Load at 0x0800

Memory View

0x3500

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
350	08	02	04	00	00	00	00	00	00	00	00	00	00	00	00	00
351	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
352	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
353	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
354	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
355	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
356	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
357	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
358	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
359	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
35A	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
35B	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
35C	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
35D	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
35E	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
35F	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00

Start Address at: 0x3300

97°F
Mostly sunny

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FileResetAssemblerDebugHelp

Registers

A0A

BC0600

DE0000

HL0000

PSW0000

PC4210

SPFFFF

Int-Reg00

Flag

S0

Z0

AC0

P1

C0

Decimal - Hex Conversion

Decimal

Hex

00

00

To Hex

To Dec

I/O Ports

0-+00

Update Port Value

Memory

0-+00

Update Memory

Load me at

1

2

3

4

5

6

7

8

9

10

11

12

13

14

15

16

17

<Program title>

jmp start

start: nop

LDA 8500

MOV B,A

LDA 8501

ADD B

STA 8502

RST 1

hlt

DataStackKeyPadMemoryI/O Ports

Start8500OK

Address (Hex)AddressData

213485006

213585014

2136850210

213785030

213885040

213985050

213A85060

213B85070

213C85080

213D85090

213E85100

213F85110

214085120

214185130

Line NoAssembler Message

0Program assembled successfully

Simulator: Idle

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GNUSim8085 - 8085 Microprocessor Simulator

FileResetAssemblerDebugHelp

Registers

A0A

BC0200

DE0000

HL0000

PSW0000

PC421E

SPFFFF

Int-Reg00

Flag

S0

Z1

AC0

P1

C0

Decimal - Hex Conversion

DecimalHex

00

To HexTo Dec

I/O Ports

0-+00

Update Port Value

Memory

0-+00

Update Memory

Load me at

1234567891011121314151617181920212223

<Program title>

jmp start

<data>

<code>

start: nop

LDA 8500

MOV B,A

LDA 8501

MOV C,A

CPI 00

JZ LOOP

XRA A

LOOP1: ADD B

DCR C

JZ LOOP

JMP LOOP1

LOOP: STA 8502

hlt

DataStackKeyPadMemoryI/O Ports

Start8500OK

Address (Hex)AddressData

213485002

213585015

2136850210

213785030

213885040

213985050

213A85060

213B85070

213C85080

213D85090

213E85100

213F85110

214085120

214185130

Line NoAssembler Message

0Program assembled successfully

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ENGIN

1:14 PM6/13/2024

GNUSim8085 - 8085 Microprocessor Simulator

FileResetAssemblerDebugHelp

Registers

A03

BC0400

DE0000

HL0000

PSW0000

PC4210

SPFFFF

Int-Reg00

Flag

S0

Z0

AC0

P1

C0

Decimal - Hex Conversion

DecimalHex

00

To HexTo Dec

I/O Ports

0-+00

Update Port Value

Memory

0-+00

Update Memory

Load me at

1

2;<Program title>

3

4jmp start

5

6;data

7

8

9;code

10start: nop

11LDA 8000

12MOV B,A

13LDA 8001

14SUB B

15STA 8002

16RST 1

17hlt

DataStackKeyPadMemoryI/O Ports

Start8000OK

Address (Hex)AddressData

1F4080004

1F4180017

1F4280023

1F4380030

1F4480040

1F4580050

1F4680060

1F4780070

1F4880080

1F4980090

1F4A80100

1F4B80110

1F4C80120

1F4D80130

Line NoAssembler Message

0Program assembled successfully

Simulator: Idle

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