Experiment List

1.To verify the truth table of various logic gates using ICs.

2 .To realize half adder and full adder.

3 . Study of flip flop IC.

4 .To implement ripple carry adder.

5. To implement carry look ahead adder.

6. To implement Booth's algorithm.

7. To implement restoring division algorithm.

8. To implement non restoring division algorithm.

9.To implement ALU design.

10.To implement CPU design.