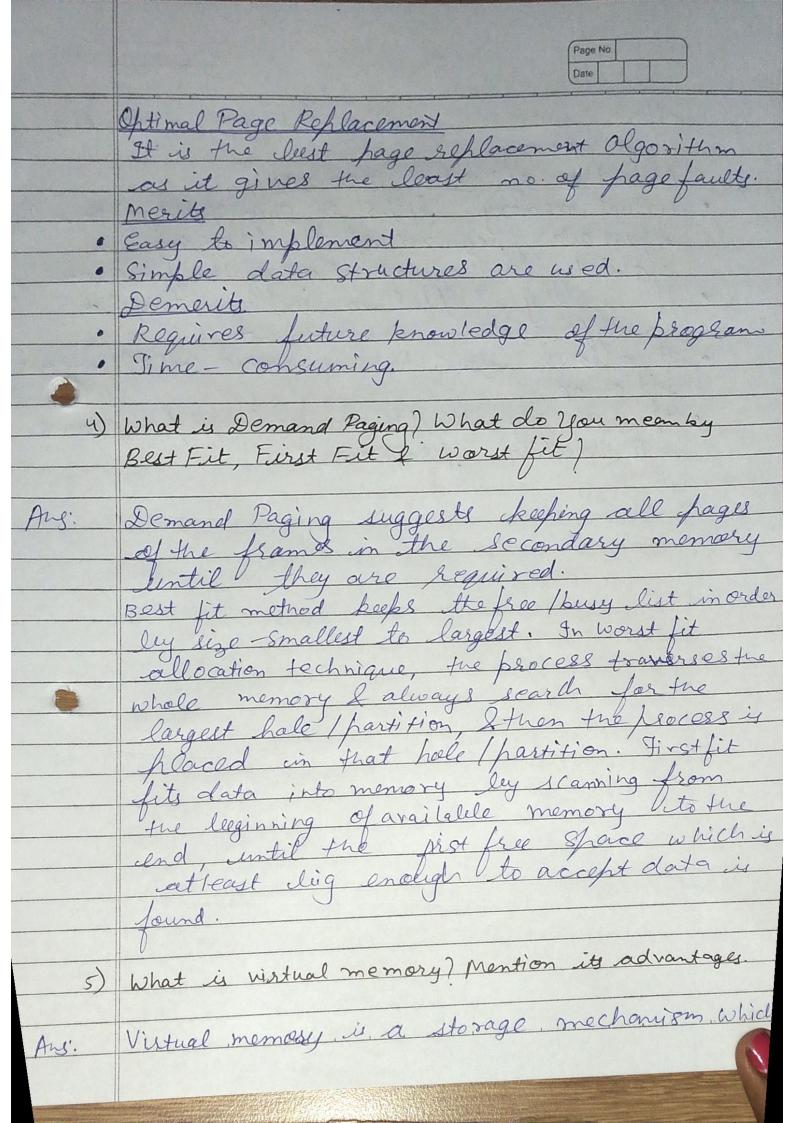
Assignment 4 91) Fill in the blanks Demand paging is the conclit in which a process is copied into main memory from the secondary memory according to the requirement. 2) If relocation is static and is done at assembly or lead time, compaction cannot be done. 3) The first fit, least fit and worst fit are strategies to select a free hole from a set of available holes. 4) When mencory is divided into several fixed into ex sized fartitions, each fartition may contain exactly one process. 5) In fixed sized partition, the degree of multiprograming is bounded by the number of pastitions. 92) Choose Correct options. D which one of the following is the address generated by CPU? Ans: C. logical address.

2) Effective access time is directly proportional to ich

Ars: A. page - fault sate.

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	internal fragmentation occurs
	Iransient aperating system code is code that:
	Comes and goes as needed.
Q3)	Answer the following questions
5)	Explain faging scheme for memory management, discuss the faging hardware & Paging model.
Ans:	Paging is a memory management scheme that eliminates the need for Continguous allocation of physical memory. This scheme permits the physical address space of a process to be non-Continguous. Logical Address or Virtual Address (represented en lits): An address generated ley the CPU Logical Address Space or Virtual Address Space (represented in words or leytes): The set of all logical address generated ley a program. Physical Address (represented in lits): An address actually available on memory unit Physical Address Space (represented in cords or leytes): The set of all physical addresses or leytes): The set of all physical addresses Corresponding to the logical addresses.
2)	Explain the basic concept of seg mentation in dotal
Ang:	Segmentation is a memory management technique in which the memory is divided into the variable

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		-
	eize farts. Each part is known as a segment	
	which can be allocated to a process. The	ty.
	details about each segment are stored in a	
	table Called a segment table. Segment	
	table is stored in one of the segments.	
3)	Emplain with the help of examples FIFO LIRU, optical	
	hage replacement algorithms with example	en
	reference st ring. Mention the merity & demerits	
	of each of the above diagram.	
fus:	Einst In First Out (FIFO)	
	In this algorithm, the OS maintains a queue front	08
	keeps track of all the hages in memory, with	211
33	the oldest plage at the front and the most	ry
	socent page at the back.	order
	merits Ci 10 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	
	Simple & easy to implement:	the
	Demerite	
	Poor perfor moence	ig
866	Doesn't consider the lemma of 11100 or	
	last used time simply schales the older	
Ny day y	Doesn't consider the fraguncy of uses or last used time, simply separces the oldest page.	e
	Least Recently Used (IRU)	hig
	It keeps track of page usage over a short	is
	It kæps track of page usage over a short period of time.	
	mority D	
w	Efficient . Doesn't suffer from Belady's Annaly.	3.
9	Demeri+8	- 1 1 1
•	Complex Implementation Ezepensine.	hich



offers user an illusion of having main memory. Advantage of Virtual Memory: · Virtual memory helps to gain speed when only a particular segment of the program is required for the execution of the program . It is very helpful in emplomenting a multiprograming environment. · It allones you many large programs