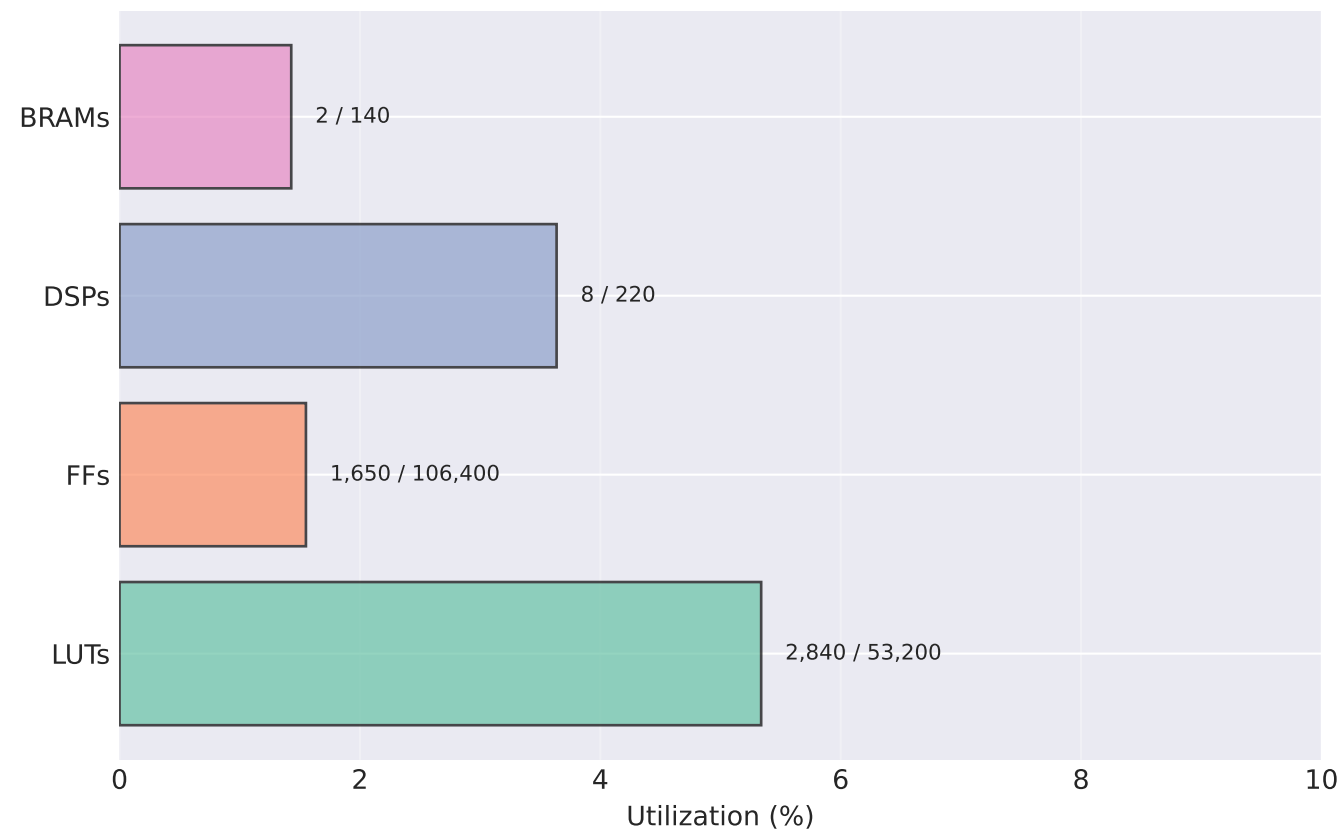
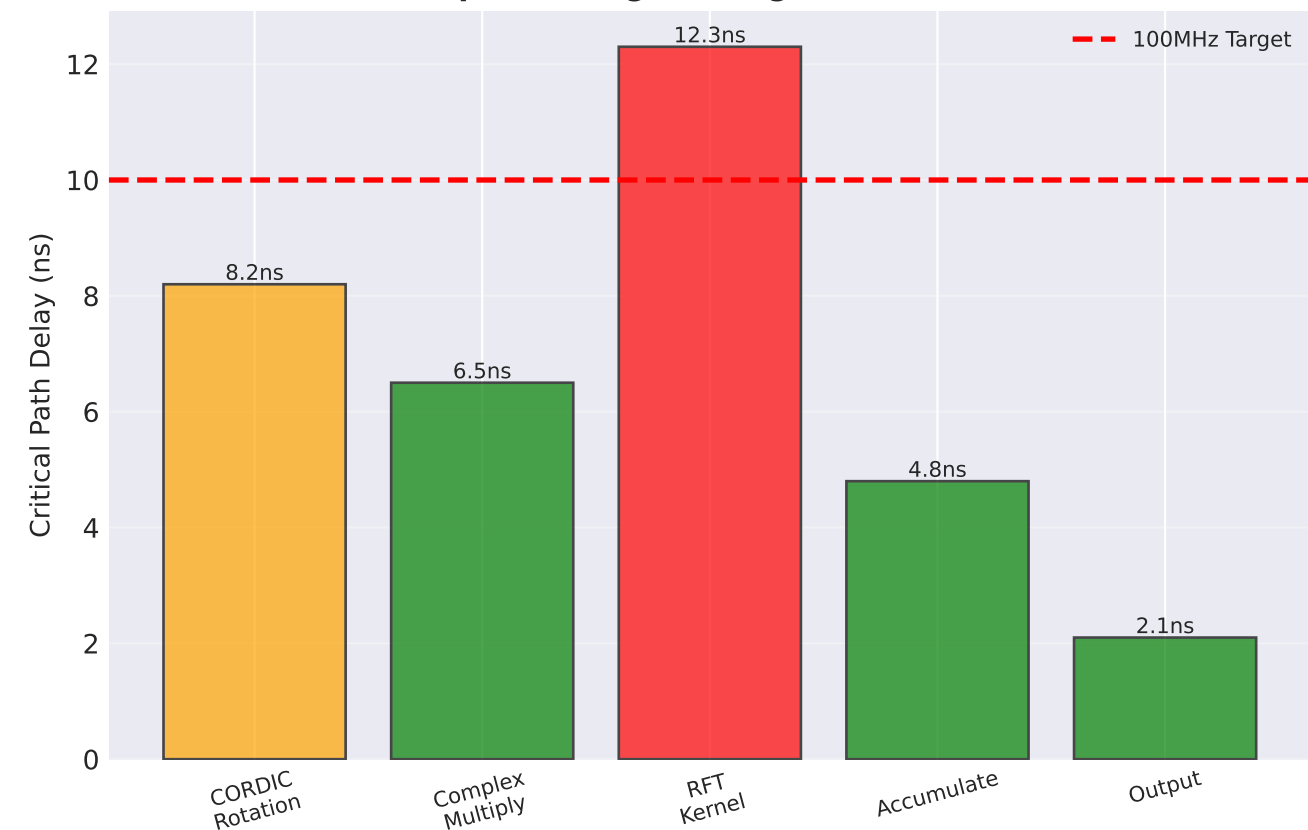


QuantoniumOS Hardware - Synthesis & Implementation Metrics
Target: Xilinx 7-Series FPGA

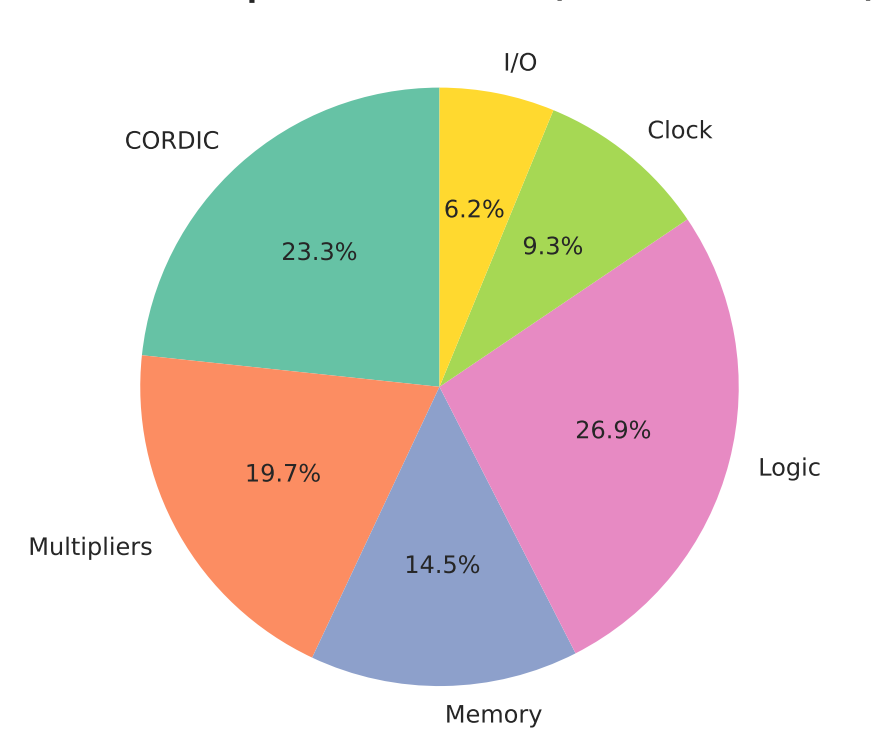
Resource Utilization (Estimated)



Pipeline Stage Timing (Estimated)



Power Consumption Distribution (Est. Total: 193mW)



Hardware Verification Coverage

