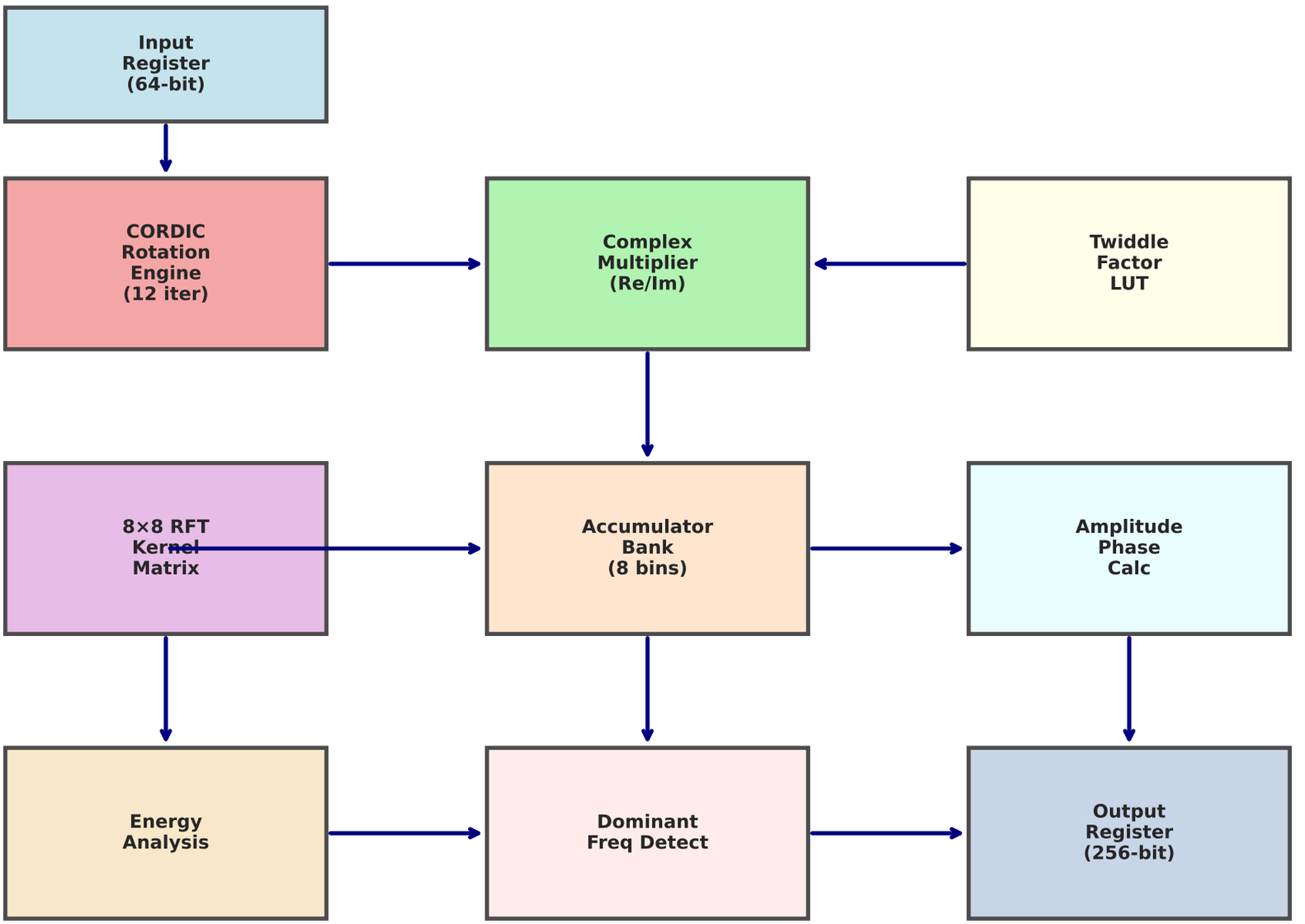
QuantoniumOS Hardware Architecture
RFT Middleware Engine Block Diagram



SPECIFICATIONS

- Transform: 8×8 RFT
- Arithmetic: Q1.15 Fixed
- CORDIC: 12 iterations
- Pipeline: 3 stages
- Clock: Configurable
- I/O: AXI-Stream Ready
- VCD: Waveform Export
- Test: 10 patterns ✓