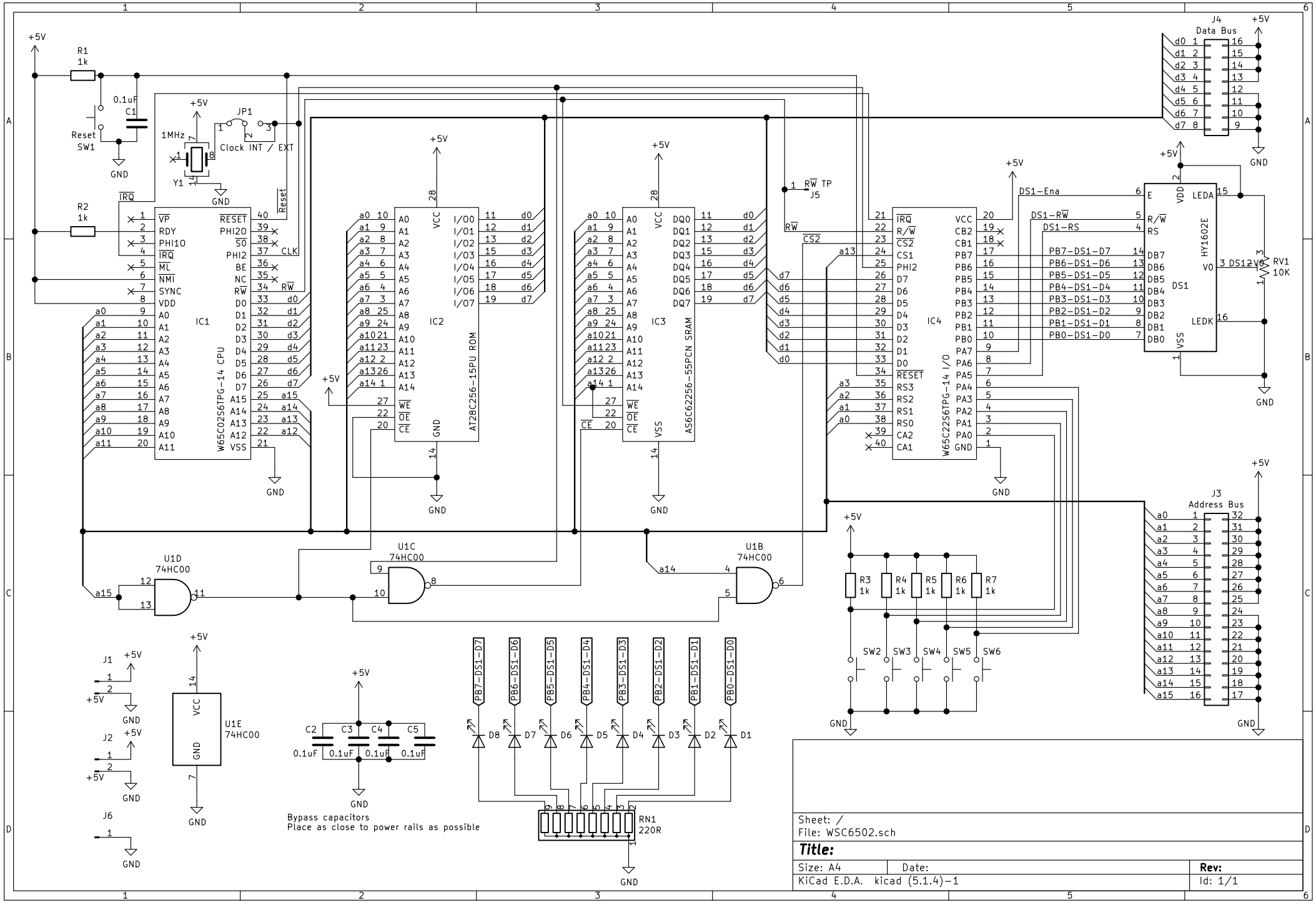
IC1
W65C02S6TPG-14 CPU
IC2
AT28C256-15PU ROM
IC3
AS6C62256-55PCN SRAM
IC4
W65C22S6TPG-14 I/O
DS1
HY1602E
J4
Data Bus
J3
Address Bus
J5
RW TP
JP1
Clock INT / EXT
Y1
1MHz
Reset
SW1
R1
1k
R2
1k
C1
0.1uF
U1D
74HC00
U1C
74HC00
U1B
74HC00
U1E
74HC00
R3
1k
R4
1k
R5
1k
R6
1k
R7
1k
SW2
SW3
SW4
SW5
SW6
RV1
10K
RN1
220R
C2
C3
C4
C5
0.1uF
Bypass capacitors
Place as close to power rails as possible
J1
J2
J6
+5V
GND
Sheet: /
File: WSC6502.sch
Title:
Size: A4
Date:
Rev:
KiCad E.D.A. kicad (5.1.4)-1
Id: 1/1