**CS322-Lab 10: Study of MIPS, Single/Mult-Cycle/Pipelined Processor Architecture**

Task 1: Study the given piplelined-mips implementation of the processor and test using the following test program ( create a new mem.dat )

***add*** *$s0, $0, $0*

***add*** *$s1, $0, $0*

***addi****$t0, $0, 10*

*loop:*

***slt*** *$t1, $s0, $t0*

***beq*** *$t1, $0, done*

***add*** *$s1, $s1, $s0*

***addi*** *$s0, $s0, 1*

***j****loop*

*done:*

**40 points**

Task 2: Compare given single-cycle, multi-cycle and pipe-line implementation. Run the above program and compute the number of cycles required in each of the case. Compute the CPI

6***0 points***

**Submission (solution in doc /pdf file).**

[**https://u.pcloud.com/#page=puplink&code=ksO7ZQ0BnD0yIb5hxhDs4eK9lojuTswlX**](https://u.pcloud.com/#page=puplink&code=ksO7ZQ0BnD0yIb5hxhDs4eK9lojuTswlX)

**Due on 19th Nov** 2019, 5PM**.**