**Lab 8: Implement a RISC single Cycle Processor**

Task 1: Using blocks from lab 7 and other glue logic, implement single cycle processor RISC which could run instructions/test file given.( you could use 32 bit reg file and 32 bit ALU from lab 7)

**75 Points**

Task 2: Write a program to add 5 numbers and store the result in data memory location X. ( show the encoding process and machine language instruction with comments in the report). Also add screen shots of numbers in memory and result location in the report.

**25 Points**

**Submission :**

Submit single doc/pdf file with above answer and \*.circ file(s) . **Due on 6**th November 2022, 11.30PM**. Submission link.**

[**https://u.pcloud.com/#page=puplink&code=a2E7ZfjUqWsGprVSLK1HtgW6n0XnqJhNX**](https://u.pcloud.com/#page=puplink&code=a2E7ZfjUqWsGprVSLK1HtgW6n0XnqJhNX)