**Lab 09:Implement a multi- Cycle Processor**

Task 1 Study the given multi-cycle implementation of the processor, and identity error in the design (if any) **30 points**

Task 2: Study the given multi-cycle implementation of the processor and test using your own test program (for eg. Sum of 5 numbers using basic instruction; create a new mem.dat )

**30 points**

Task 3: Add one new instructions to the given architecture and test using new test program.

***40 points***

**Submission :** Submit single zip file (with doc/pdf file with above answers and \*.circ files)

<https://u.pcloud.com/#page=puplink&code=7sO7ZtPL7h6lCxP4GxYGf60juF8Mo4sJk>

1. Report should include errors that you identified in the design.
2. Submit a tassk 2( program and machine code) and report should include details with comments in the test program
3. New instruction encoding details and new test program

**Due on 13th Nov** 2019, 11.45 PM**.**