Meeting	Date	Topic	Note
1	8/22/2018	Welcome Message and Introduction	
2	8/27/2018	(Module 1.1 + 1.2)	
3	8/29/2018	(Module 1.3)	
	9/3/2018	Labor Day, No class	
4	9/5/2018	Chapter 2 - Parallel Programming Models	
		(Module 2.1)	
5	9/10/2018	Chapter 3 - Shared Memory Parallel	
		Programming (Module 3.1 - Module 3.3)	
6	9/12/2018	(Module 3.2 + Module 3.3)	
7	9/17/2018	Cancelled due to Hurricane Florence	
8	9/19/2018	(Modules 3.4 - 3.7)	
9	9/24/2018	(Modules 3.8 - 3.10)	
10	9/26/2018	Chapter 5 - Introduction to Memory Hierarchy	
		(Modules 5.1 + 5.2)	
11	10/1/2018	(Module 5.3 - 5.5)	
12	10/3/2015	(Module 5.6 - 5.8)	
13	10/8/2018	Chapter 6 - Introduction to Shared Memory	
		Multiprocessors	
14	10/10/2018	Chapter 6 - Introduction to Shared Memory	
		Multiprocessors	
15	10/15/2018	Buffer	
16	10/17/2018	MIDTERM EXAM, in class	
17	10/22/2018	Chapter 7 - Basic Cache Coherence Issues (Module	
		7.1 + 7.2)	
18	10/24/2018	(Module 7.3 - 7.5)	
19	10/29/2018	Chapter 8 - Hardware Support for Synchronization	
		(Module 8.1)	
20	10/31/2018	(Module 8.2 - 8.3)	
21	11/5/2018	Chapter 9 - Memory Consistency Models	
22	11/7/2018	Chapter 10 - Advanced Cache Coherence Issues	
		(Module 10.1 - 10.3)	
23	11/12/2018	(Module 10.4 + 10.5)	
24	11/14/2018	Chapter 11 - Distributed Shared Memory	
		Multiprocessors (Module 11.1)	
25	11/19/2018	Buffer	
	11/21/2018	- Thanksgiving Break, no class meeting -	
26	11/26/2018	(Module 11.2)	
27	11/28/2018	Chapter 12 - Interconnection Network	
		(Module 12.1)	
30	12/3/2018	(Module 12.2)	
31	12/5/2018	Buffer	
-	12/14/2018	FINAL EXAM, in class	
		8:00AM - 11:00AM	