Q1
s_1 = 1 as same H(64)&W(64) for output
s_2 = 2 as H/2(32)&W/2(32) for output
s_3 = 1 as same H/2(32)&W/2(32) for output
s_4 = 2 as H/4(16)&W/4(16) for output
e.g. [(64+2*1-3)/s_2 + 1] = 32, therefore s_2 = 2

Q3
The C_in:
for IncepEnc1 = 640
for IncepEnc2,3,4,5,6,7,8 & IncepDec1 = 256
for IncepDec2,3,4,5,6,7,8 = 512

Q4
The size should be 1 as output the same H'&W'

Q5
The number of times = 16*4 = 64

Q6
The C_out = 256 or 640
The kernal size = (3,3), (5,5), (7,7), (11,11)

Q7
The values are 64

Q8
The s_1 = (2,2)
The s_2 = (1,1)
The s_3 = (2,2)
The s_4 = (1,1)