

I am proud to offer my recommendation to **Mr.Manikanta Krishnamurthy**, B.Tech, Electronics and Communications Engineering at **Indian Institute of Technology, Guwahati**, who has worked under me at **Hanyang University,ERICA Campus, HPIC Design Lab, Ansan-si, Gyeonggi-do, South Korea** during the period from 3.06.2024 to 21.07.2024 as a **Research Intern** under me.

During the period he successfully completed the following assignments and compiled the reports for the same: (i) An Analysis of Frequency of Ring Oscillators, (ii) A Phase-Noise Analysis of Multiphase Clock Generators and (iii) Analysis of PLL Circuits Based on Phase-Noise Performance. The simulations for the same were performed using HSpice and Verilog-A, and the reports were made using Latex. He also was interested in analysing the characteristics of a **28nm Conventional Digital PLL**.

One of the standout qualities of Manikanta is his **hardworking nature, analysing skills and the keenness for learning**. Such experiences have prepared him well for the Electronics field which makes him a strong candidate for your consideration.

In summary, I am confident that Manikanta Krishnamurthy possess the dedication, intelligence, and interpersonal skills that are crucial for success in this field. Please feel free to contact me if you require any further information.

Sincerely,

Min-Seong Choo,  
Assistant Professor

School of Electrical Engineering,  
HPIC Design Lab,  
Hanyang University.  
mschoo@hanyang.ac.kr

