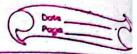
Name: Manoj Shrestha
Group: CE -
Roll: 53
Yearlsem: TIT/IT





			who said the		
	To an ale	arm system	ac bank, th	ree senso	ors are
	implemen	ited and th	në alarm is	triggered	when at
	least to	a concare de	exect the cho	inge. Hissu	mind sensors
14.	to output	t divital valu	res design	a combine	ational logic
	circuit or	or alarm sys	tem:		
		a coopii aga			
10	solution				
-		table is:			Le brokens (100 to
100	110			11-11-6	
1	A	B	C	У	
	0	0	0 1	· · · · · O ·	
	0	0	1	0	
1 1	0		O	6	
	0	1	1	1	
	the July Shi	0	6	0	
	4	0	1 2	1	_
		100	0	1	
	1	1	1	1 . 1	Service Control
					and the second

wing	K-map	
USULY	17 Journa	,
, 0.		

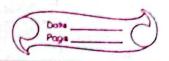
A BC				
	00	01		10
0		(1	D	
1			6	1

Y = A'C + AB



				A	
The circuit	diagram is		THE YELL	rada tiraki	o addition
	0 0 616			11.11	
ar is all	28 10 B	WEST		in A	e in the
	re la Villa			4-1	
A Do	Second fine		of legation	adirent to	ioral a.
C -211		MATERIAL STATE OF THE STATE OF	I kno lite		a second control of the second control of th
21 0	ran ala				
		V 199	11 301110		hailba
В —			- 5- i	Stelle Colores	+ ndTie
	Ya. Ya.	0		a l	ъ.
		0	6	0	0
	0	L.	3 5		
Design	a combination	mal log	ic airculit	that trike	ag two b
inputs,	A and B, a	nd out	puts 1 lub	en the bi	معن مس
represent	ed by A is	greate	e than th	e binoxy	number
and the second s	sted by B. c				0)-
				ber Lind	
solution	<u> </u>	1.	716		
The tru	th table i	S: 0	10	7	<u> </u>
	<u> </u>		1	0	
A	OB	0	7	O.	1
0	00		0 1	0 +	
0	01	3 6	100	1	
1	00	Sel L	40		
-					4
1	1 1	)	01		

					6	Pops
4	The ci	rcuit diag	ram is,	7.	Carrier M. Non	100
		A		Y = AB'	a character to be a	
	The state of the s	B ->>>				
Service of the servic				C.		
All Comments						
	0 0>		121	nia discu	it that take	e four
	s. Design	o d combin	on Condition	D and a	it that take	he
	bisas	Judius 1	APIC CIO	ted by	utputs 1 if t	ie
			erwise it			-10
	0-111	auth table		s authur		1
	A	A B		0	\ \ \ \	
	0			0		
•	.0	0	0	1		
deliver of	0	Dilyt	1-11-00	10 <b>0</b> loss	ir and an in	0.000.000
other i	10-120		11/11/1 r. 211	The state of the s		The Carlo
3	Jourge	Marodick =	dt Opde	CATONEL	1. 6 hisa-to	
	0	1 0 1 Kin	1150-1-5	itu Coder	3 40 46	Direction of
	10	1		0		
	0	1	1	ĺ	0	
	1	0.	0	0		1
	1	0			3 711	
	1	0		Ô		
	1	0	1- 00	1	0	
	11.	ì	00	0	0	-
	l i	ď	0	1	0	
	1	1	1(1)	0	0.0	
	1	7	1	7	0,	-
					1 1	14=1



Y= A'B'C'D' + A'BCD' + AB'C'D+ ABCD

word king king in the state of a tree that is the state of the

AB CD

OO 01 11 10

OO 1 1

O1 1

11

10 1

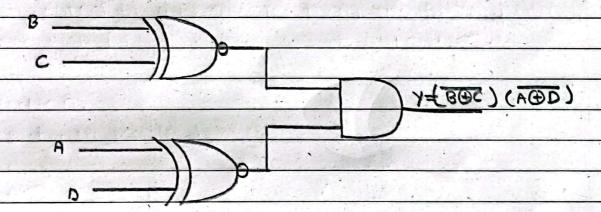
Y = A'B'C'D' + A'BCD' + AB'C'D + ABCD

= A'D' CB'C'+BC) + AD CB'C +BC)

= (B'c'+BC) (A'O'+AD)

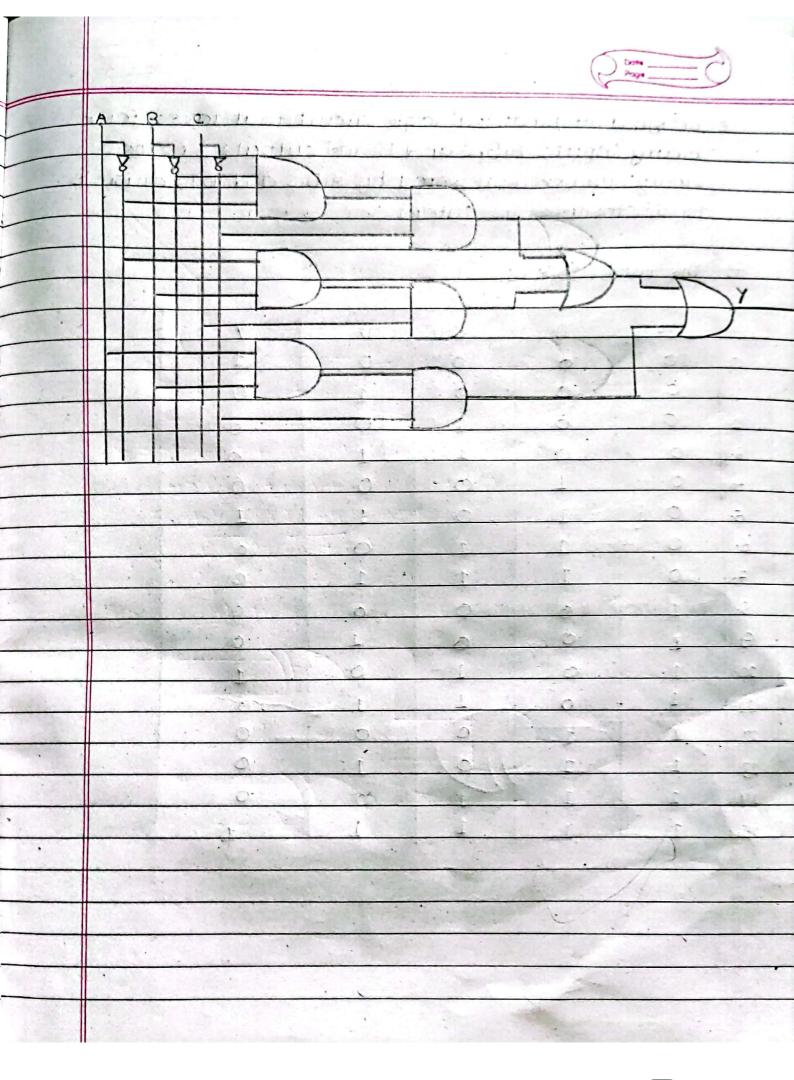
= (BAC) (ADAD)

The circuit diagram is,

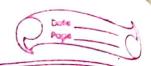




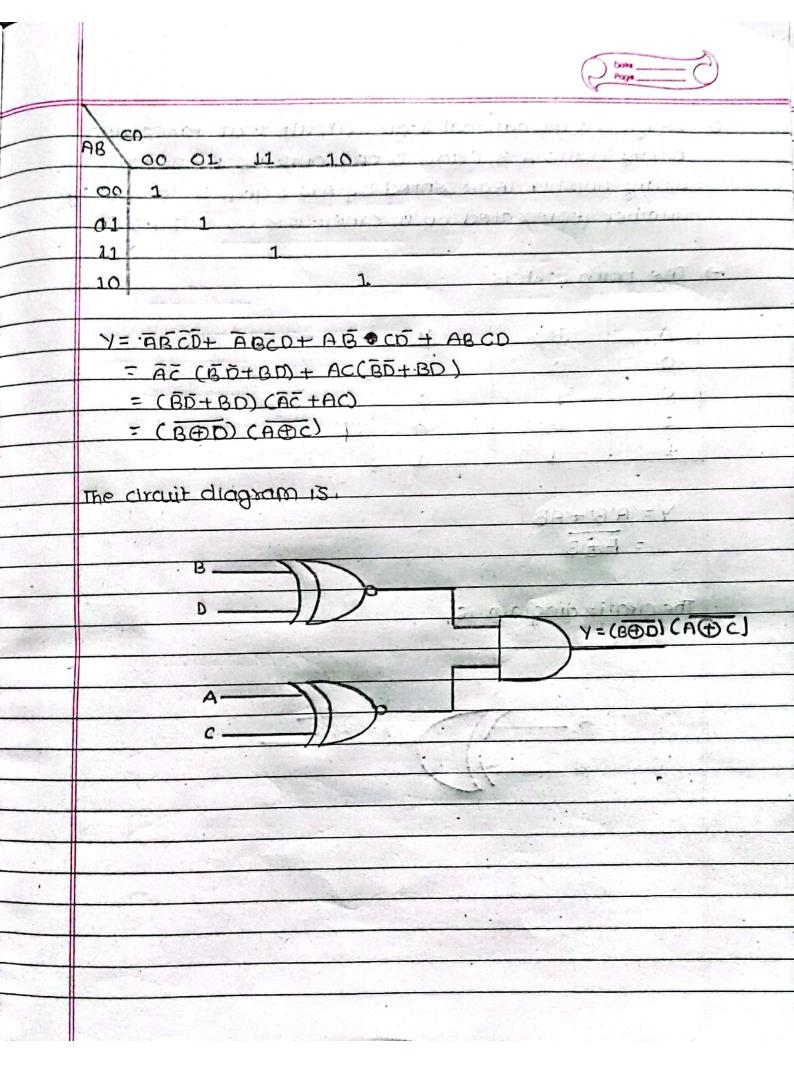
4.	Design	a cam bin	atlocal Logi	c circuit	that takes three
Date of the last o	binaru	inpute	A.B.ande	and outp	cité 1 if the bir
Market de avec de avec	repres	ented by	A,B, and	is a muli	tiple of 3, other
	It out	purts O.			The same of the sa
					· · · · · · · · · · · · · · · · · · ·
·- <del>)</del>	The tr	uth tabl	eis,		
T			<u> </u>	Y	
- SN		B		01	
0	0	0	1	0	and the second
10	0	_0_	0	0	
3 0	0	D. Care	06.14.05	197 - 100	- # 12 NA JEK
	1	Ö	00-	7 747 - 0	9-13-11-
4.0	1	0	1	MA On	(00 = 21 = =
6 0	1	1	0	(6)	13/5£9/5
7.63.	4		1	0	
- do:		The second second		L. On	mair Man
		ВС			
	A	00	0 1 11	10	
	15-100				
	1 - 10	0 1		a tree a fee	
		1			The state of the s
- 01		- 1	4	_ 9	
	7:	= ABC	+ ABC+ AF	3C	
		5			
	The cir	cuit dia	gram is,	and the second	A. A. A.
			V		
			and the same of the country of the c	The second secon	







					A	
				ia circuit	that take	s four
- 5	Design a	combinat	tional log	2 200 0	utpute 1 if	the
	bimry i	nputs A	ibic and	D and	utpute 1 if	divisible
	DIOONIO	in hov re	1) FSEIL	- 0	c and Dis	
	by 5, 04	henvise i	te onton	10.		
		1				
	The thu	htableis		- marine		
1		1 12		and the same of th	7	
Sin	) A 7	- 8		<u> </u>	1	
		0	0	0	0	
	. 0	.0	0	1 1		
2	. 0	0	1	0	0	
	. 0	0	1	1	A	
	. 0	1	. 0	0	1	
.   6	5.	1	0	1	+	
6	. 0	1	1	0	. 0	
7	. 0	1	1	1	. 0	
8		0	0	0.	0 .	
q		0		1	6	-
10	1	Ö		<u> </u>	1	
1	1	0	1	1	0	
12	Alaca .	1	0	0	0	
12		1	0	1	0	
110		1	1	-0	• • • •	
15		1	1	J	7	
		165				





binary number resolved	nber rep epresente	cand by oversented	his Aig equ	at takes two  I when the  at to the birant  is outputs o.
binary number resolved the truth  A  O  1  1  Y = A'B'  = AC	nber rep epresente	cand by oversented	his Aig equ	al to the bican
birary number resident the truth  A C C C The truth  Y = A'B' = ACT	inber rep epresente	ed by G, C	MIL TIP	
The circuit	epresente	ed by G, C	otherwise it	s outputs o.
The truth  A  C  O  1  Y = A'B'  = A $\oplus$ The circuit				
A  O  1  1  Y = A'B'  = A $\oplus$ The Circuit	tableis:	1 2		
A  O  1  1  Y = A'B'  = A $\oplus$ The Circuit	Hableis:	1 2		
$ \begin{array}{c c} C & & \\ C & & \\ \hline 1 & & \\ Y = A'B' \\ \hline = AG \end{array} $ The Circuit	B 0	<u>'</u> Y		
$ \begin{array}{c c} C & & \\ C & & \\ \hline 1 & & \\ Y = A'B' \\ \hline = AG \end{array} $ The Circuit	B O I	<u>'</u> 'Y 1		
$ \begin{array}{c} 0 \\ 1 \\  7 = A'B' \\ = A \oplus \end{array} $ The circuit	0	1		
Y = A'B' = AŒ  The circuit	1	~		
Y = A'B' = AŒ  The circuit		The second secon		
Y = A'B' = AŒ  The circuit	The second second			
Y = A'B' = AŒ  The circuit				
The circuit				
The circuit				:/
The circuit	+AB			
The circuit	)B			
				1.
	Hidorop	n ic		
	andian	1 10;		
	A	11 1.	Y = A⊕B	
	β			
		1		
				To the second second
		-		
			and the second s	