

Fig.1 The proposed UGC converter - PSIM Model.

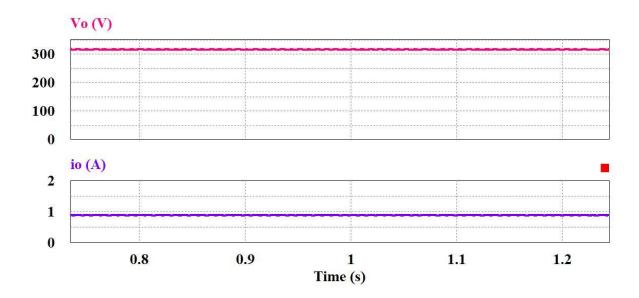


Fig.2 Simulated results: output voltage and load current.

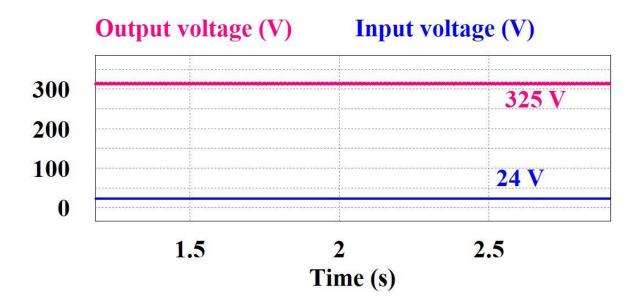


Fig.3 Simulated results: output voltage and input voltage.

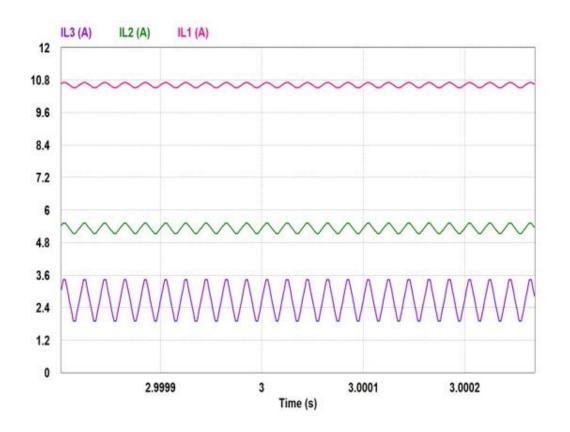


Fig.4 Simulated results: inductor current waveforms.

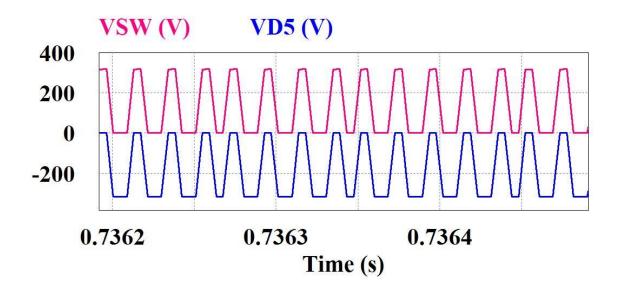


Fig.5 Simulated results: voltage stress across switch and voltage across diode D5.