

## Description

This lab teaches you to create a heart rate sensor device by measuring an analog signal input on the PSoC 4 BLE device and reporting the measured heart rate value to a BLE enabled device such as an iPhone.

## Pre-Reading

### **BLE Heart Rate Profile**

The BLE Heart Rate Profile defines how the user's heart rate information is communicated from one device to another. This is used in health and fitness applications in modern wearable devices.

The Heart Rate Profile is a combination of two types of devices – a Sensor and a Collector. The Sensor detects the heart rate and stores the information – acting as a GATT Server. It then sends this information to a Collector, which acts as a GATT Client. The Sensor side is implemented in fitness bands and activity monitors. The Collector side is implemented in mobile phones and tablets.

### **Heart Rate Service (HRS)**

The Heart Rate Service defines three Characteristics, listed in [Table 1](#).

Table 1: Heart Rate Service Details

Characteristic	Details	Properties	Descriptors
Heart Rate Measurement	Carries a heart rate measurement.	Notify	Client Characteristic Configuration
Body Sensor Location	Informs the GATT Client of the location of the heart rate sensor.	Read	None
Heart Rate Control Point	Enables a Heart Rate Collector to control the Sensor's behavior.	Write	None

A Characteristic is composed of three elements: Declaration, Value and Descriptor(s).

- A Declaration is the start of the Characteristic; it groups all the other Attributes for this Characteristic.
- The Value is an Attribute that contains the actual value for this Characteristic.
- The Descriptors hold additional information or configuration for this Characteristic.

The Heart Rate Measurement Characteristic is the one we are primarily interested in, and it has a number of Attributes. Each of these fields is one or two bytes in length, and together these fields constitute a Heart Rate Measurement Characteristic packet. The fields are described in [Table 2](#).

Table 2: Heart Rate Measurement Characteristic

Field Name	Field Requirement	Size in Bytes	Additional Information	
Flags	Mandatory	1	Bit [0]	0: Heart rate is 1 byte 1: Heart rate is 2 bytes
			Bits [2:1]	Sensor contact feature related information
			Bit [3]	0: Energy expended field is not present 1: Energy expended field is present
			Bit [4]	0: RR-interval values are not present 1: RR-interval values are present See <a href="#">Figure 2</a> on Page 4 for an explanation on RR-interval. It is the time interval between successive heart beats.
			Bits [7:5]	Reserved
Heart Rate Measurement value	Mandatory	1 or 2	The size depends on Bit[0] of the Flags field.	
Energy Expended	Optional	2	The amount of energy expended by the user, in Joules. This field exists based on Bit[3] of the Flags field.	
RR-interval	Optional	2	RR-interval measured in seconds. This field exists based on Bit[4] of the Flags field.	

The Body Sensor Location Characteristic can have different values to represent the various body parts the sensor is attached to. These include – Chest, Wrist, Finger, Hand, Ear Lobe, Foot, and Other.

The Heart Rate Control Point Characteristic can be used to reset the Energy Expended field in the Heart Rate Measurement Characteristic. We are not using this Characteristic in this lab.

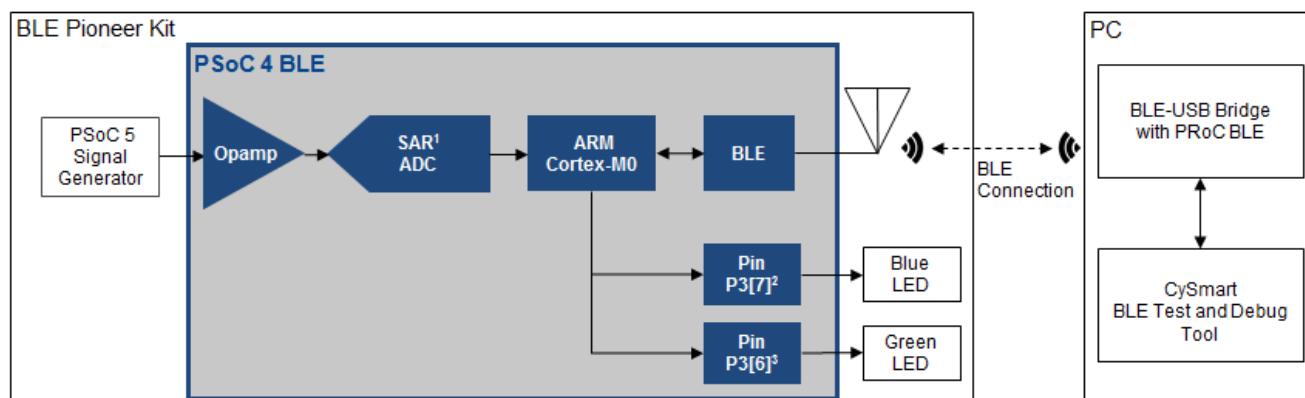
## Objectives

1. Measure simulated heart rate using the Programmable Analog Blocks
2. Implement a Heart Rate Profile and send the data over BLE
3. Optimize the design for low power consumption using Sleep, Deep-Sleep and Hibernate modes

Requirements	Details
Hardware	BLE Pioneer Kit (CY8CKIT-042-BLE)
	2 jumper wires
Software	PSoC Creator 3.1 (or newer)
	CySmart 1.0
	CySmart iOS or CySmart Android Mobile App

## Block Diagram

Figure 1: Lab #3 Block Diagram



## Background Check

This lab requires a basic knowledge of PSoC Creator and PSoC 4 BLE. Ensure that you have completed Lab 1 and Lab 2 before proceeding.

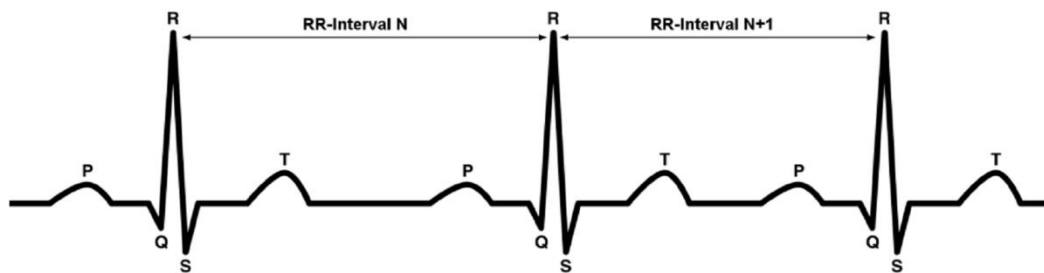
## Theory

This lab shows how to create a heart rate sensor device by measuring an analog signal input on the PSoC 4 BLE and reporting the measured heart rate value to a BLE enabled device such as an iPhone.

## Heart Rate Signal

A representative heart rate electrical signal is shown in Figure 2. Different parts of the signal have different labels. The R peaks represent the time when the heart beats. The heart rate is measured by identifying the time interval between successive R peaks (also known as the RR-interval) and then extrapolating it to the number of RR-intervals over a minute. This gives us the heart rate in beats per minute.

Figure 2: Heart Rate Signal



## BLE Implementation

Our lab focuses on creating a HRS Sensor device. There are specific events generated by the BLE Component for HRS, and we need to handle those events explicitly. Additionally, we need to generate a notification every second for the heart rate value. The BLE Pioneer continues to act as the GATT Server. Security settings for this lab are the minimum settings.

## Analog Front End (AFE)

Heart rate detection is done by implementing an AFE on the PSoC 4 BLE chip. To generate the heart rate signal, we will bootload the PSoC 5 on the kit with a prebuilt project. To keep the AFE simple, this signal is detected by using an Operational Amplifier (opamp) as an input buffer and then passing the signal to the ADC. The detected signal is compared to a threshold and whenever a beat is detected, its time of occurrence is noted. The time difference between successive beats is extrapolated to 60 seconds to get a corresponding heart rate value.

The AFE implementation is already present as part of the project template provided with this lab manual. For advanced users, the PSoC 4 BLE has four Operational Amplifiers, two Low Power Comparators, and two current DACs. These can be used to build a more sophisticated AFE.

## Low Power Implementation

We also demonstrate the low-power modes of PSoC 4 BLE in this lab. For this purpose, we implement a continuous Active – Deep-Sleep power mode cycle, with the watchdog timer acting as a periodic wakeup source. The Active mode performs the ADC scan for the heart rate signal, and sends the heart rate measurement notification every second. The BLE block can also wakeup the device from the Deep-Sleep mode when a new BLE connection interval approaches. This happens automatically, and the device can be put back to Deep-Sleep once the corresponding Receive/Transmit is complete. The device then waits for the watchdog interrupt to wake it up for the next cycle. If the device is disconnected or its advertising times out, the system enters the Hibernate mode and can be woken up using a user-input via the button SW2.

## Procedure

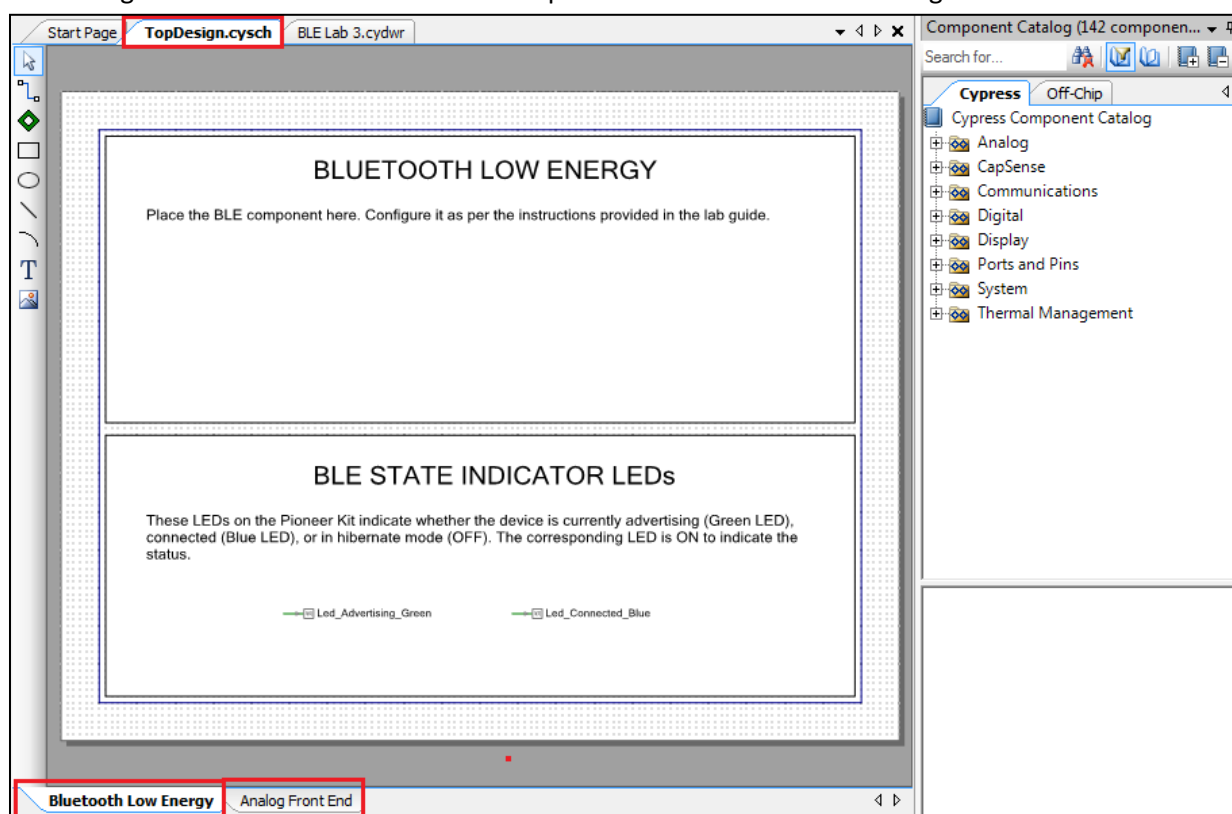
Start this lab from the template project that is provided, to save time. The template project has some details already completed, and you need to just fill in the blanks as instructed.

### Configure Schematic

Open the template project named **BLE Lab 3** and follow these steps to get started:

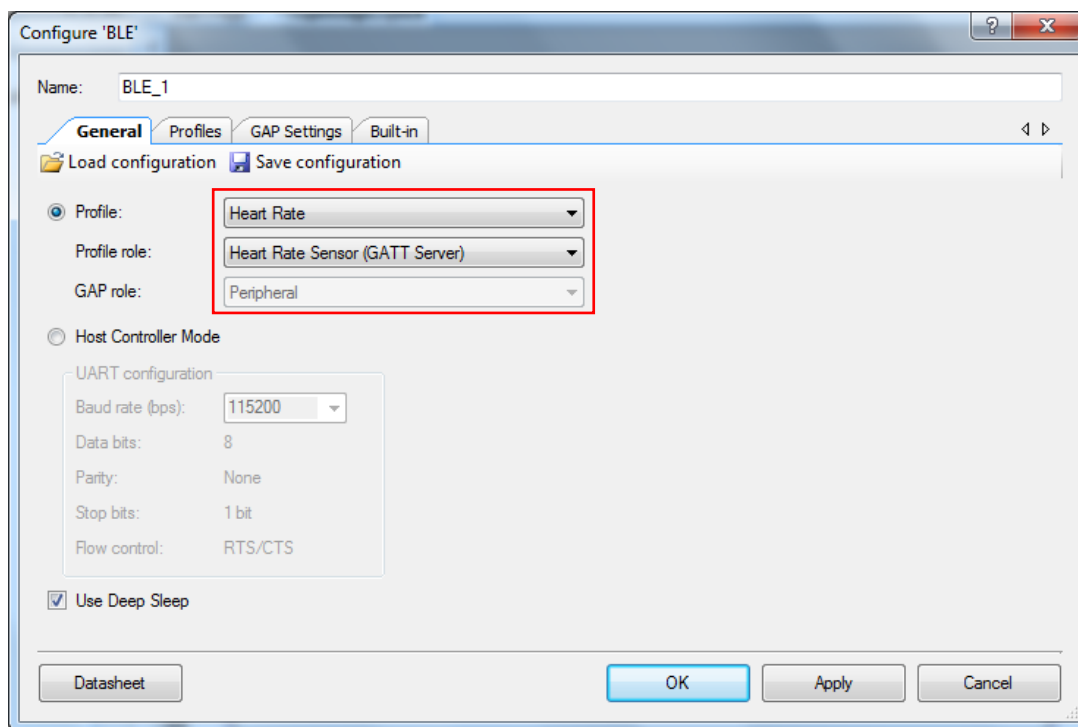
1. Open the schematic by double clicking **TopDesign.cysch** in the **Workspace Explorer**. Note that there are two sheets in the schematic indicated by tabs at the bottom of the schematic editor. See [Figure 3](#).

Figure 3: The Schematic Editor Has Separate Sheets for BLE and Analog Front End



2. In the **Bluetooth Low Energy** sheet of the schematic, place the **BLE Component**. Double-click it to configure the Component. Refer to the Component datasheet to learn more about the configuration parameters.
3. **General Tab** - Set the **Profile** to **Heart Rate** and the **Profile role** to **Heart Rate Sensor (GATT Server)**. See [Figure 4](#).

Figure 4: BLE Component Configuration – General Tab

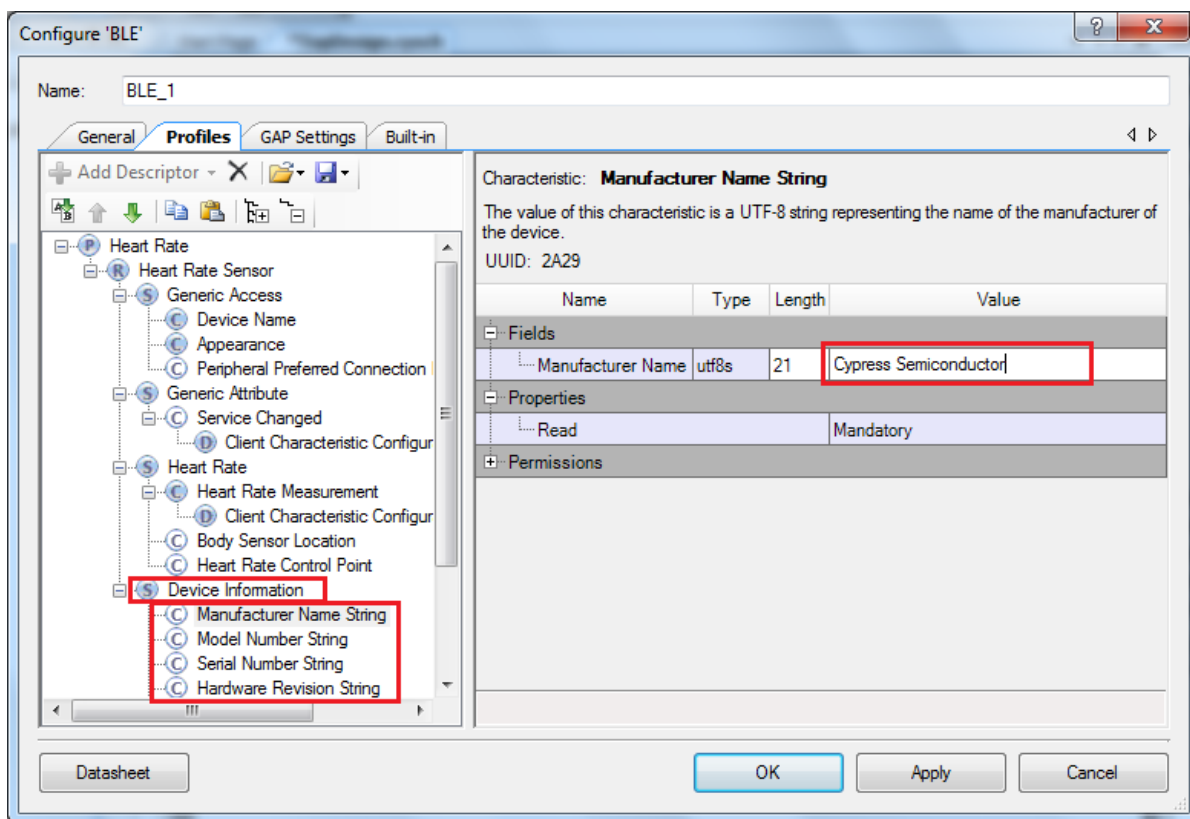


4. **Profiles Tab** - This tab is automatically populated with the required Services and Characteristics. The **Device Information Service (DIS)** is a part of the **Heart Rate Profile** and shows up on the left side, similar to Figure 5. Assign the values to the Characteristics of the DIS as shown in Table 3. These values can be read on the GATT Client BLE device.

Table 3: Device Information Service Characteristics

Characteristic	Field	Value
Manufacturer Name String	Manufacturer Name	Cypress Semiconductor
Model Number String	Model Number	BLE Pioneer Kit
Serial Number String	Serial Number	1
Hardware Revision String	Hardware Revision	**
Firmware Revision String	Firmware Revision	1.0

Figure 5: Device Information Service



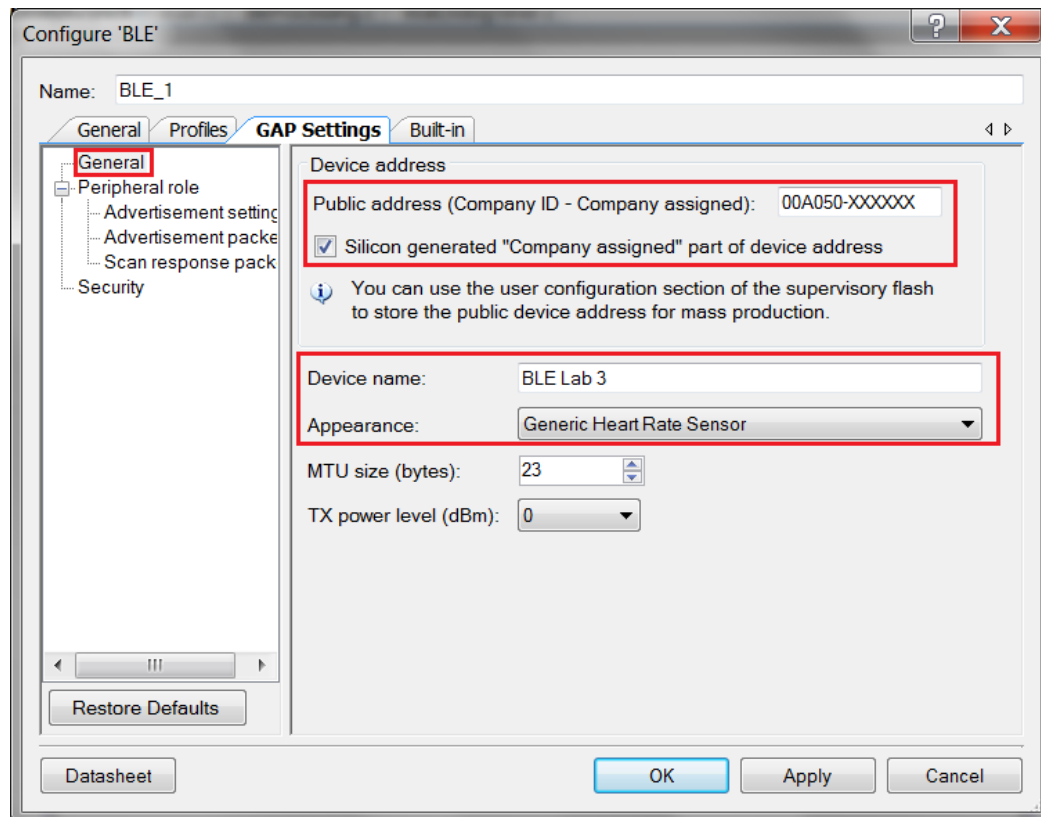
## 5. GAP Settings Tab -

### 5.1. General

To learn more about these parameters, refer to the Bluetooth Component datasheet

- Set the **Device name** to any text of your choice.
- Set the device **Appearance** to an appropriate value that represents your design.
- Set the Maximum Transmission Units **MTU (bytes)** size to **23**.
- Set the **TX power level (dBm)** to **0**.

Figure 6: GAP Settings - General



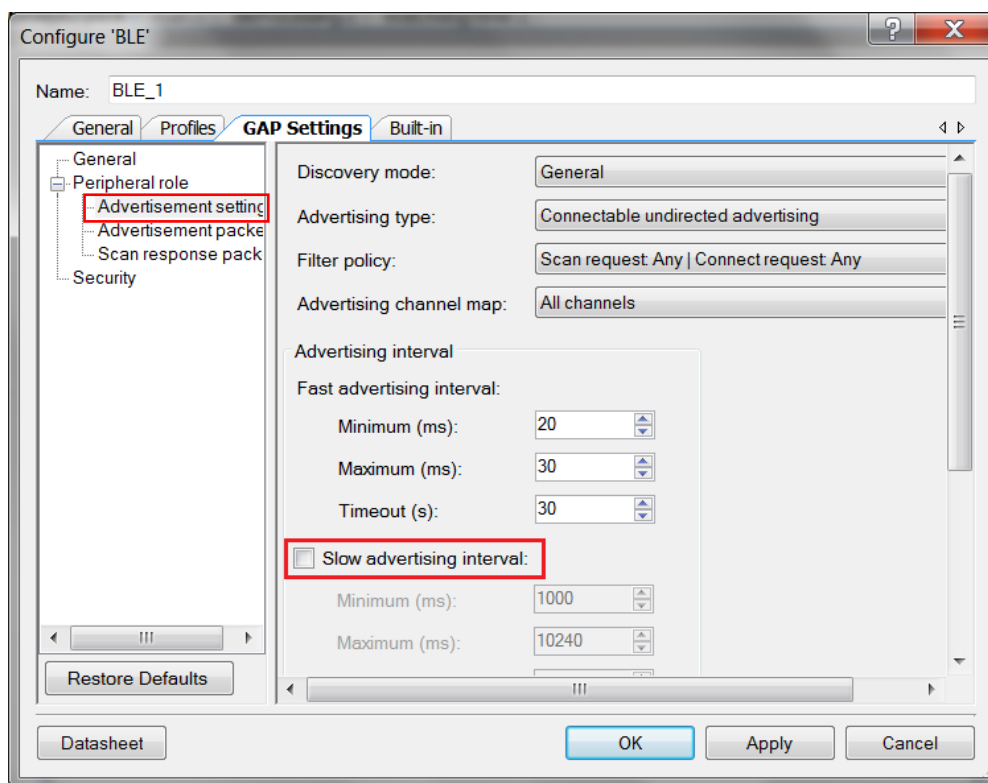


## 5.2. Peripheral Role -> Advertisement Settings

To learn more about these parameters, refer to the Bluetooth Component Datasheet.

- Discovery mode:** Select **General**
- Advertisement type:** Select **connectable undirected advertising**
- Filter policy:** Select **Scan request: Any | Connect request: Any**
- Advertising channel map:** Advertise on **All channels**.
- Fast advertising interval:** Select **20** for **minimum (ms)** and **30** for **maximum (ms)** interval. The **timeout (s)** should be **30**.
- Slow advertising interval:** Uncheck to disable this setting
- Connection parameters:** Leave them at the default values

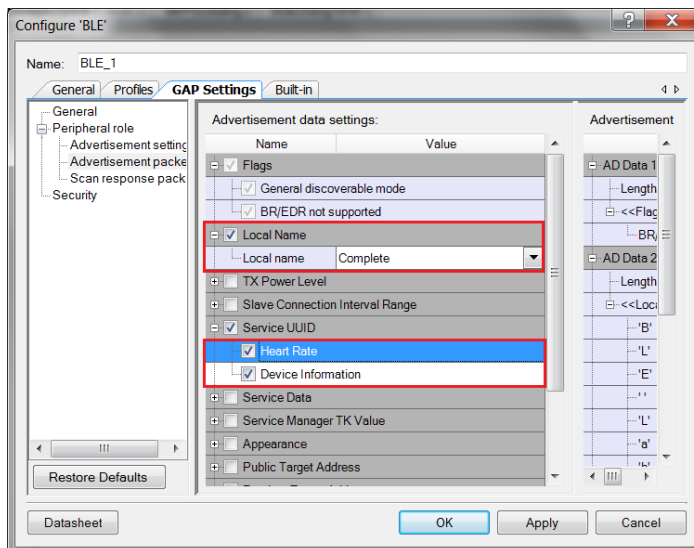
Figure 7: GAP Settings – Advertisement Settings



## 5.3. Peripheral Role -> Advertisement Packet

Enable **Advertisement packet** details per your choice (**Flags**, **Local Name**, and **Service UUID** are recommended), while ensuring that the length of the advertisement packet does not exceed 31 bytes. This is the maximum size possible for an advertisement packet. If you exceed this limit, the wizard indicates an error by showing a red exclamation mark in front of the actual advertisement packet. See [Figure 8](#).

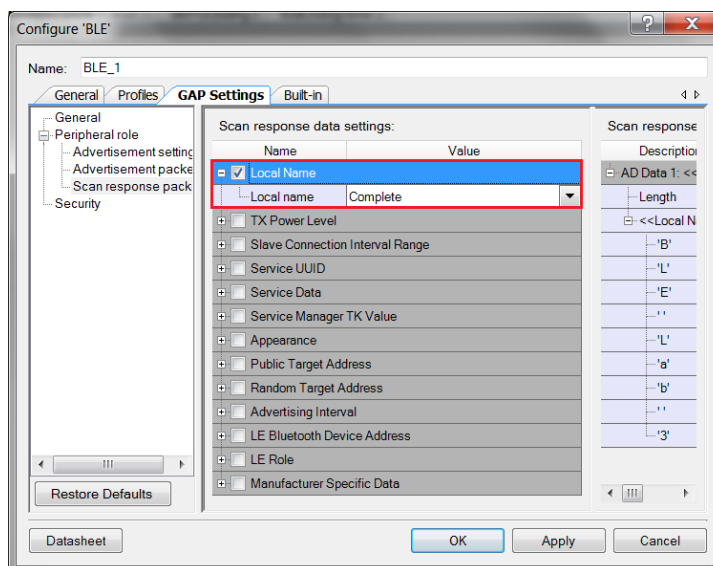
Figure 8: GAP Settings – Advertisement Packets



## 5.4. Peripheral Role -> Scan Response Packet

Enable **Scan response packet** details per your choice (**Local Name** is recommended), while ensuring that the length of the scan response packet does not exceed 31 bytes. This condition is similar to the advertisement packet size condition. See [Figure 9](#).

Figure 9: GAP Settings – Scan Response Packet

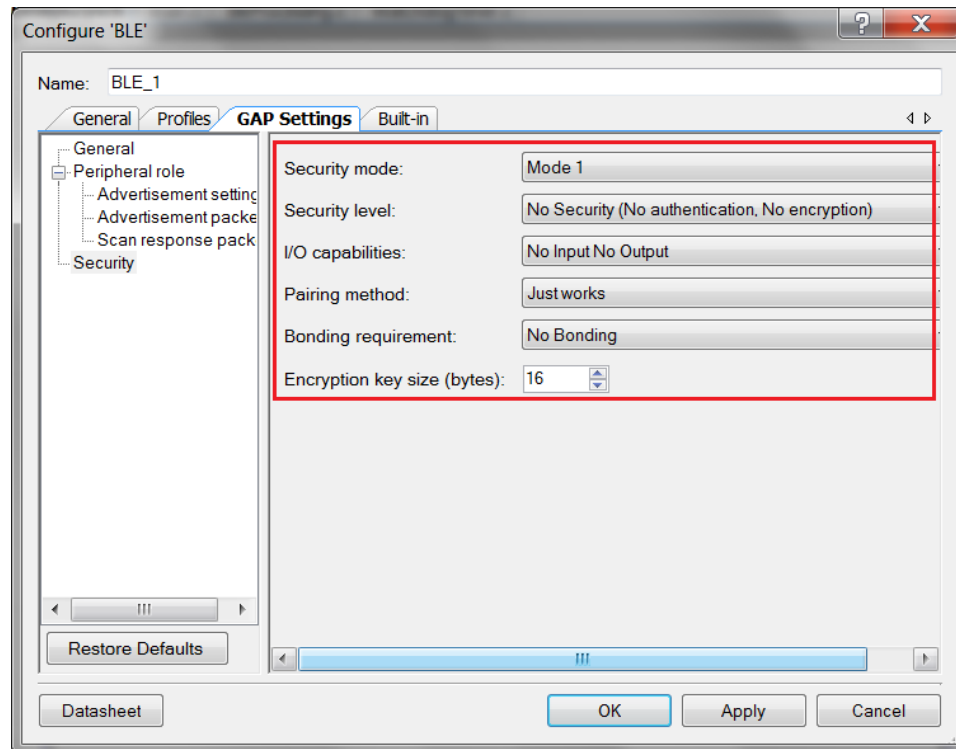


## 5.5. Security

To learn more about these parameters, refer to the Bluetooth Component Datasheet.

- Security mode:** Select **Mode 1** security
- Security level:** Select **No Security (No Authentication, No Encryption)**
- I/O Capabilities:** Set this to **No Input No Output**
- Pairing method:** Select **Just works**
- Bonding requirement:** Set this to **No Bonding**
- Encryption key size (bytes):** Leave this parameter to the default value of **16**

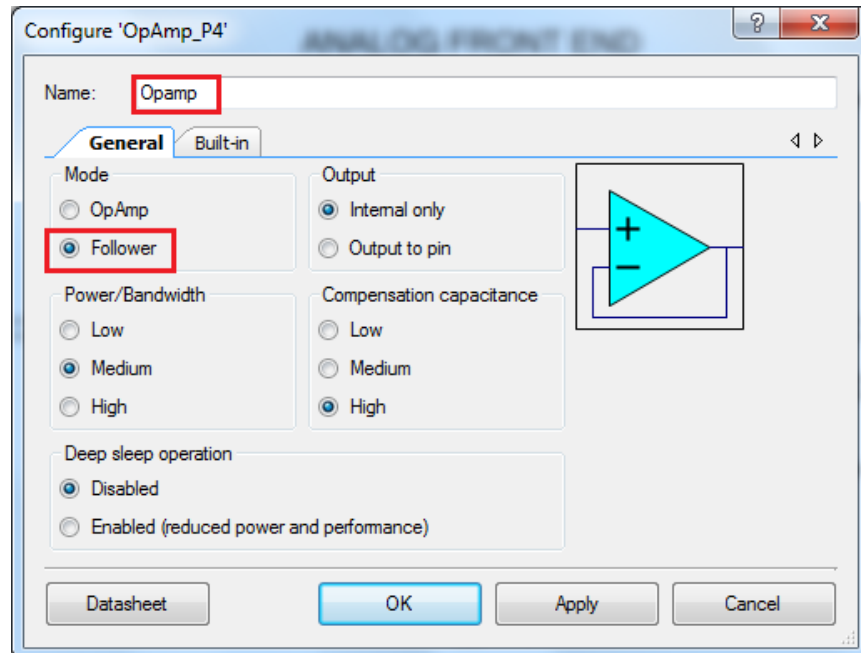
Figure 10: GAP Settings - Security



- Click **OK** to close the BLE configuration window.

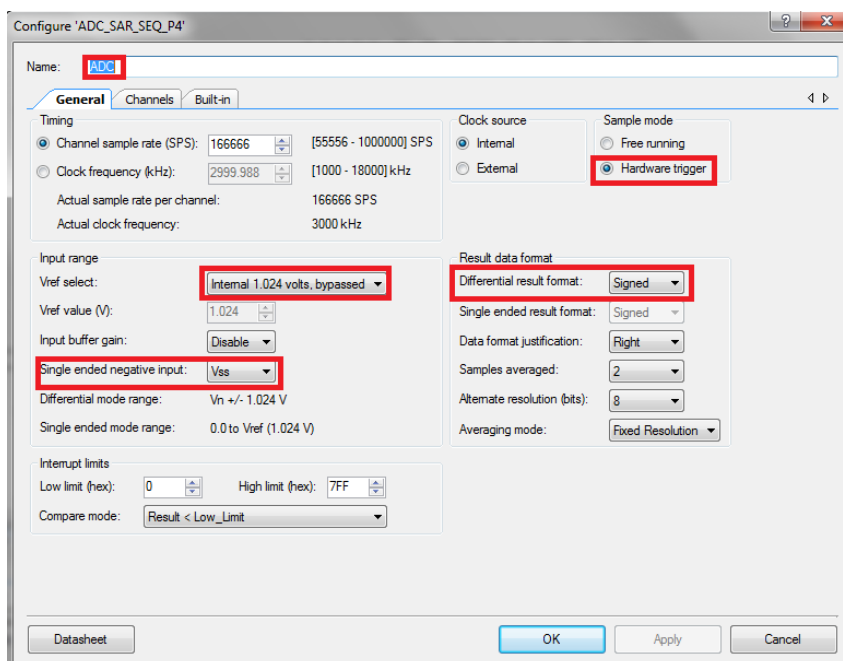
7. Select the **Analog Front End** sheet of the schematic.
8. Search for the **Opamp** Component in the **Component Catalog**, and drag and drop it onto the schematic. Double-click it configure. See [Figure 11](#).
9. Name the Component as **Opamp** and set the **Mode** to **Follower**.
10. Click **OK** to close the configuration window.

Figure 11: Opamp Component Configuration Tool



12. Search for the **Sequencing SAR ADC** Component in the **Component Catalog**, and drag and drop it on the **Analog Front End** sheet of the schematic. Configure the ADC by double-clicking it.
13. On the **General** tab of the ADC Component Configuration Tool, set the settings as shown in Figure 12. Note that an error is shown for the **Channel sample rate** until the **Channels** tab is configured in the next step.

Figure 12: ADC general Settings



Configure 'ADC\_SEQ\_P4'

Name: **ADC**

**General** Channels Built-in

Timing

- Channel sample rate (SPS): 166666 [55556 - 1000000] SPS
- Clock frequency (kHz): 2999.988 [1000 - 18000] kHz
- Actual sample rate per channel: 166666 SPS
- Actual clock frequency: 3000 kHz

Clock source: **Internal**

Sample mode: **Hardware trigger**

Input range

- Vref select: **Internal 1.024 volts, bypassed**
- Vref value (V): 1.024
- Input buffer gain: **Disable**
- Single ended negative input: **Vss**
- Differential mode range: Vn +/- 1.024 V
- Single ended mode range: 0.0 to Vref (1.024 V)

Result data format

- Differential result format: **Signed**
- Single ended result format: Signed
- Data format justification: **Right**
- Samples averaged: 2
- Alternate resolution (bits): 8
- Averaging mode: Fixed Resolution

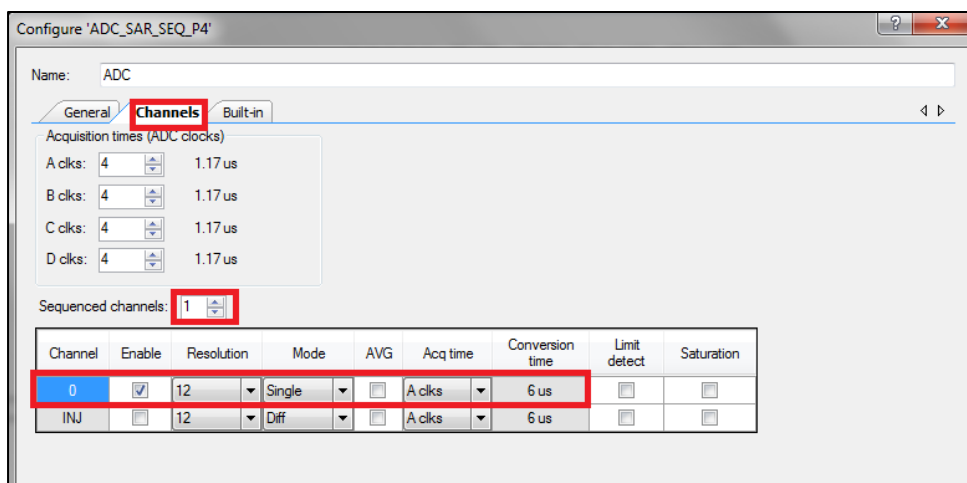
Interrupt limits

- Low limit (hex): 0 High limit (hex): 7FF
- Compare mode: Result < Low\_Limit

Datasheet OK Apply Cancel

14. Configure the settings in the **Channels** tab as shown in Figure 13.

Figure 13: ADC Channels Settings



Configure 'ADC\_SEQ\_P4'

Name: ADC

**General** **Channels** Built-in

Acquisition times (ADC clocks)

- A clks: 4 1.17 us
- B clks: 4 1.17 us
- C clks: 4 1.17 us
- D clks: 4 1.17 us

Sequenced channels: **1**

Channel	Enable	Resolution	Mode	AVG	Acq time	Conversion time	Limit detect	Saturation
0	<input checked="" type="checkbox"/>	12	Single	<input checked="" type="checkbox"/>	A clks	6 us	<input type="checkbox"/>	<input type="checkbox"/>
INJ	<input type="checkbox"/>	12	Diff	<input type="checkbox"/>	A clks	6 us	<input type="checkbox"/>	<input type="checkbox"/>

15. Click **OK** to close the ADC configuration window.

16. Add the **Logic Low '0'** Component to the schematic editor and connect its output to the **soc** input of the ADC Component.
17. Connect the **Heart\_Rate\_input** pin terminal to the **+** input of the Opamp.
18. Connect the output of the Opamp to the **+** input of the ADC. Your schematic sheets should now look like [Figure 14](#) and [Figure 15](#).

Figure 14: Analog Front End Sheet of the Schematic

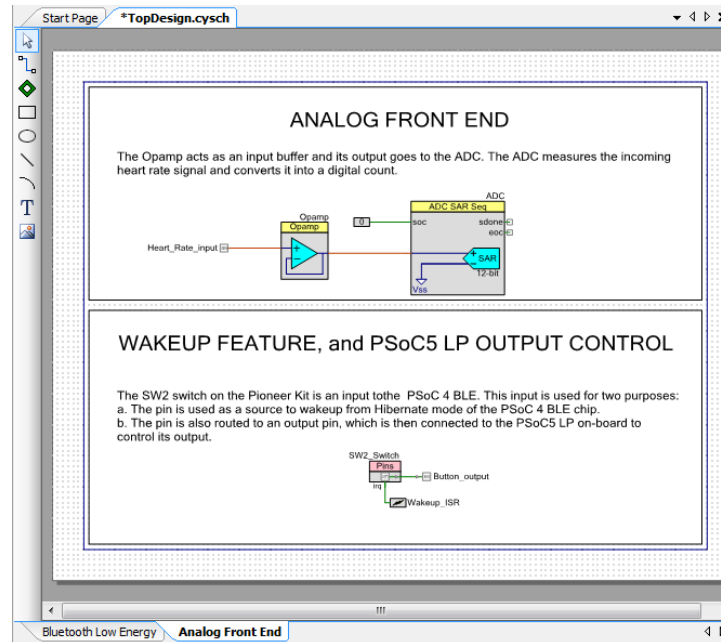
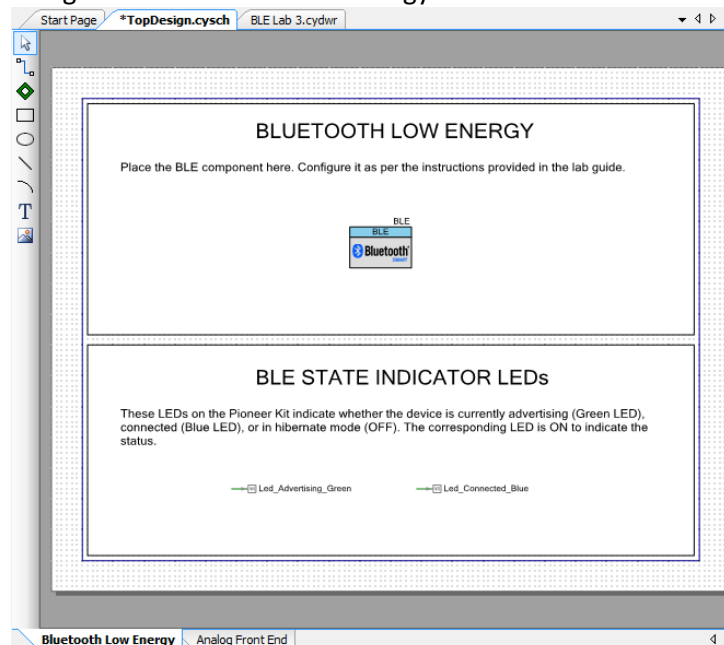


Figure 15: Bluetooth Low Energy Sheet of the Schematic



19. Click the menu item **Build -> Build BLE Lab 3** to generate the Component source code files.

### Review Firmware

The firmware for this lab on a high level can be categorized into the following sections:

1. **System Initialization** – When the device is reset or wakes up from the Hibernate mode, the firmware performs an initialization which includes the starting of the ADC and the Opamp and the enabling of global interrupts. The firmware then initializes the BLE Component, which registers the event handler to receive events for the Heart Rate Service.
2. **System Normal Operation** – In the system normal operation state, the firmware periodically calls `CyBle_ProcessEvents()` to process the BLE Stack-related operations and checks if the connection is established. If the connection is established, firmware measures the heart rate at regular intervals of time. If notifications are enabled, firmware sends the heart rate information to the GATT Client as notifications.
3. **System Low-power Operation** – The system operates in one of the three possible power modes:
  - a. **Sleep:** This mode is entered when the CPU is free but the BLESS (BLE Subsystem) is active and busy in data transmission or reception. In this scenario, the CPU enters the Sleep mode while the remaining chip is kept active for the normal BLE operation.
  - b. **Deep-Sleep:** The firmware continuously tries to put the BLESS into the Deep-Sleep mode. Once, the BLESS is successfully put into the Deep-Sleep mode, the device also transitions to the Deep-Sleep mode.

Note: Transitioning the device into the Deep-Sleep mode should happen immediately after the BLESS is put into Deep-Sleep mode. If this cannot be guaranteed, the firmware should disable the interrupt (to avoid servicing the ISR, i.e. the Interrupt Service Routine) and re-check if the BLESS is still in the Deep-Sleep mode or if in Active mode, and whether the ECO (External Crystal Oscillator) has stabilized. If the BLESS is in either of the two modes, then the device can safely enter the Deep-Sleep mode, else it has to wait till the Rx/Tx event is complete.
  - c. **Hibernate:** When the device gets disconnected or the advertising times out, it enters the Hibernate mode. After waking up from this mode, the firmware starts to execute from the beginning, although the SRAM contents are retained.
4. **Event Handler** – In the BLE Component, results of any operations performed on the BLE Stack are relayed to the firmware via a list of events. These events provide BLE interface status and data information. Events can be divided into the below two categories. Refer to the BLE Component datasheet for additional information.
  - a. **Common Events:** These are the general events generated because of operations performed on the GAP layer, the GATT layer and the L2CAP layer of the Stack. For example, the `CYBLE_EVT_STACK_ON` event is received when the BLE Stack is initialized and turned ON.
  - b. **Service Specific Events:** These are the events generated because of operation performed on the standard Services defined by the Bluetooth SIG. For example, the `CYBLE_EVT_HRSS_NOTIFICATION_ENABLED` event is received by the GATT Server when the GATT Client writes the Client Configuration Characteristic Descriptor (CCCD) to enable the notification for Heart Rate Measurement Characteristic.

Figure 16 shows the firmware flow of Lab 3 and Table 4 shows how this firmware is organized into different files in the project.

Figure 16: Firmware Flow

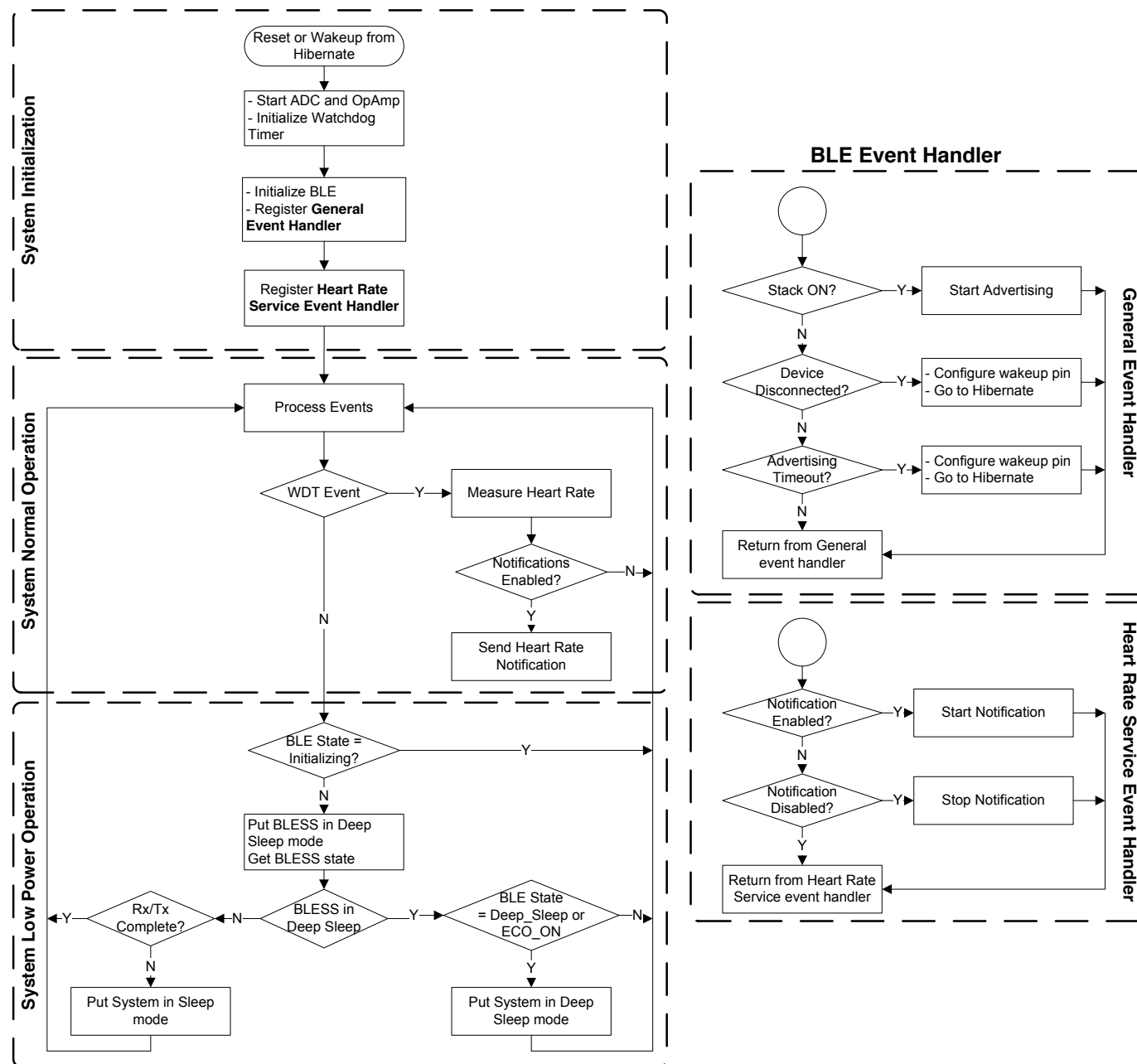




Table 4: Main Files Present in the Project

File name	Details
main.c	<p>This is the main firmware file. It initializes the system and runs the main loop, which includes low power implementation.</p> <p>This file has two functions:</p> <p><b>main()</b> – The main function</p> <p><b>InitializeSystem()</b> – Initializes all the blocks of the system</p>
BleProcessing.c	<p>This file handles the BLE specific functionality of the project. It handles the BLE events and HRS notifications. The file has these functions:</p> <p><b>GeneralEventHandler()</b> – Handles the general events for a BLE advertisement, connection, and disconnection. This function is a callback from the BLE Stack for general events.</p> <p><b>HrsEventHandler()</b> – Handles the HRS specific events for notification enable and notification disable. This function is a callback from the BLE Stack for HRS events.</p> <p><b>SendHeartRateOverBLE()</b> – Creates a Heart Rate Measurement Characteristic notification packet and sends it. This function is called from main once per second.</p>
HeartRateProcessing.c	<p>This file handles the heart rate measurement part of the project. It has one function:</p> <p><b>ProcessHeartRateSignal()</b> – Takes the ADC output and compares it to a threshold to identify R-peaks; calculates the system timestamp difference between two R-peaks to get the RR-interval; extrapolates to get the heart rate value in beats per minute. This function is called from main.</p>
WatchdogTimer.c	<p>This file handles the watchdog timer functionality and keeps track of the system time. It has these functions:</p> <p><b>WatchdogTimer_Start()</b> – Starts the watchdog timer (WDT0) with a 10 ms period and interrupt on match.</p> <p><b>WatchdogTimer_Isr()</b> – The ISR for the WDT; it increments the system timestamp variable by 10 ms. This function is a Callback from the watchdog timer.</p> <p><b>WatchdogTimer_GetTimestamp()</b> – Returns the current timestamp to the caller for any time-keeping purposes of the application.</p>
main.h	<p>This file defines a compile-time option <b>RGB_LED_IN_PROJECT</b> which enables the RGB LED usage. Keeping its value to '1' enables RGB LED drive and '0' turns off the LEDs.</p>

## Build and Program

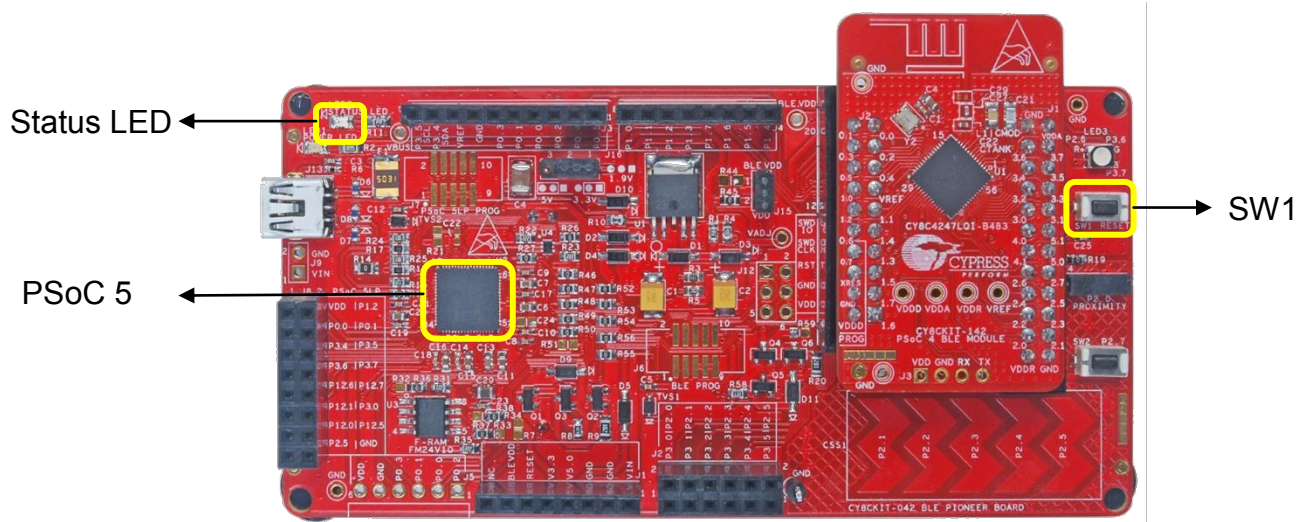
1. The firmware for this lab has been implemented as a part of the template project.
2. Build your project to generate the hex file and program the generate hex file to your kit.

## Bootloading PSoC 5

For simulating the heart rate signal, we use the PSoC 5 on the kit. A custom firmware file for the PSoC 5 is already available as part of this lab. This has to be bootloaded to the kit. Follow these steps for bootloading:

1. Remove the kit's USB connector from the PC if already connected.
2. While pressing the **SW1 (Reset)** switch, plug in the kit's USB connector to the PC. This puts the kit into the bootloader mode. See [Figure 17](#).

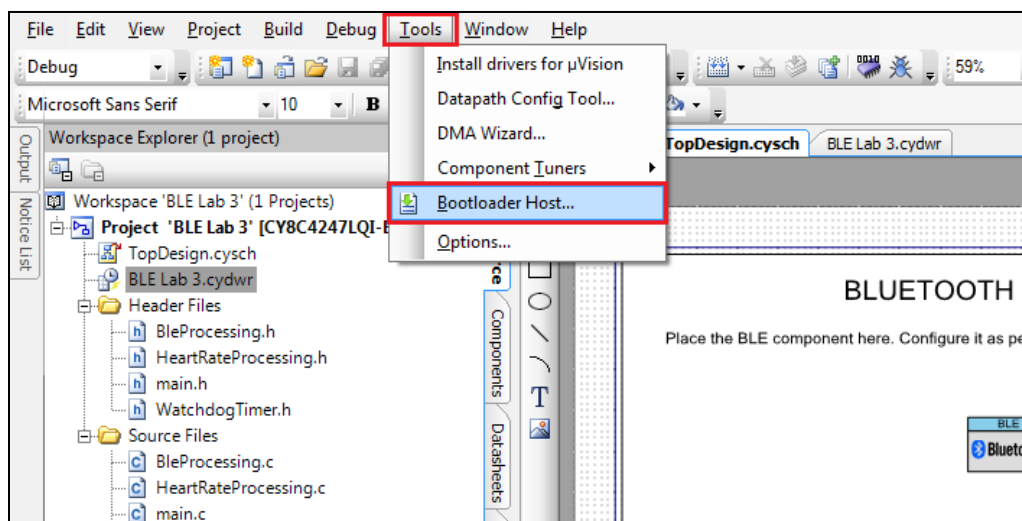
Figure 17: Bootloading the PSoC 5 Device



3. On entry to the bootloader mode, Status LED (LED 2) starts blinking at a frequency of 1 Hz.

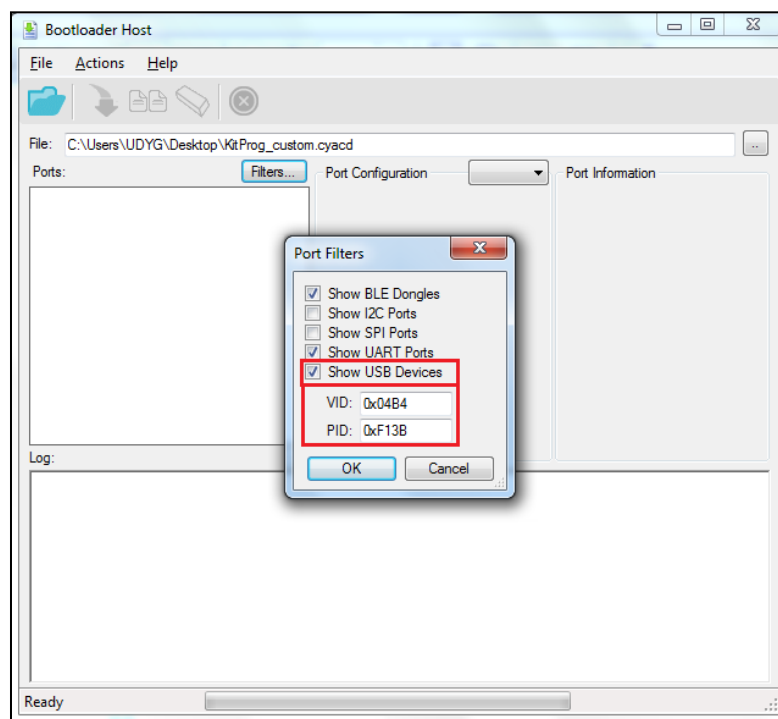
4. Launch the **Bootloader Host Tool**. To do this, you can use PSoC Creator. Click on the menu item **Tools -> Bootloader Host**. See [Figure 18](#).

Figure 18: Launching the Bootloader Host Tool from PSoC Creator



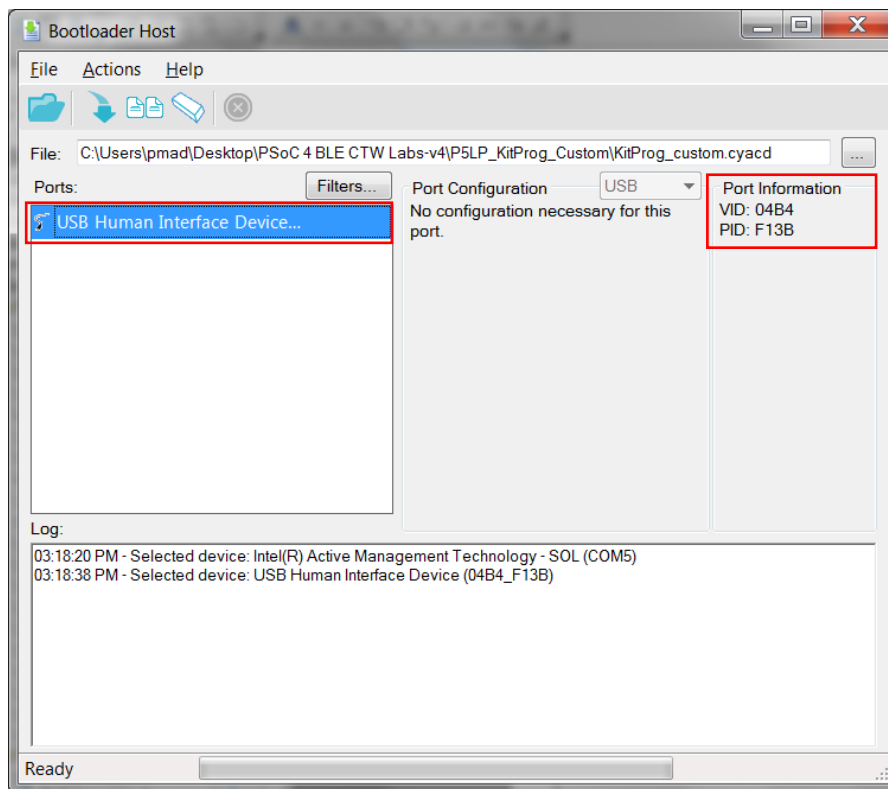
5. In the Bootloader Host Tool, click **Filters**. Ensure that the check box for **Show USB Devices** is enabled. Set the **VID** as **0x04B4**, **PID** as **0xF13B**, and click **OK**. See [Figure 19](#).

Figure 19: Adding a Device Filter in the Bootloader Host Tool



6. Select the **USB Human Interface Device (04B4\_F13B)** as shown in Figure 20.

Figure 20: Selecting the Filtered Device in the Bootloader Host Tool



7. In the Bootloader Host Tool, **Open** the bootloadable (\*.cyacd) file, available in the **BLE\_Labs -> P5LP\_KitProg\_Custom** folder. You can do this by selecting the **File > Open...** menu item and then selecting the provided **KitProg\_custom.cyacd** file.
8. Program the PSoC 5 on the kit. You can do this by selecting the **Actions > Program** menu item.
9. After the programming is complete, the log window displays **Programming Finished Successfully**.

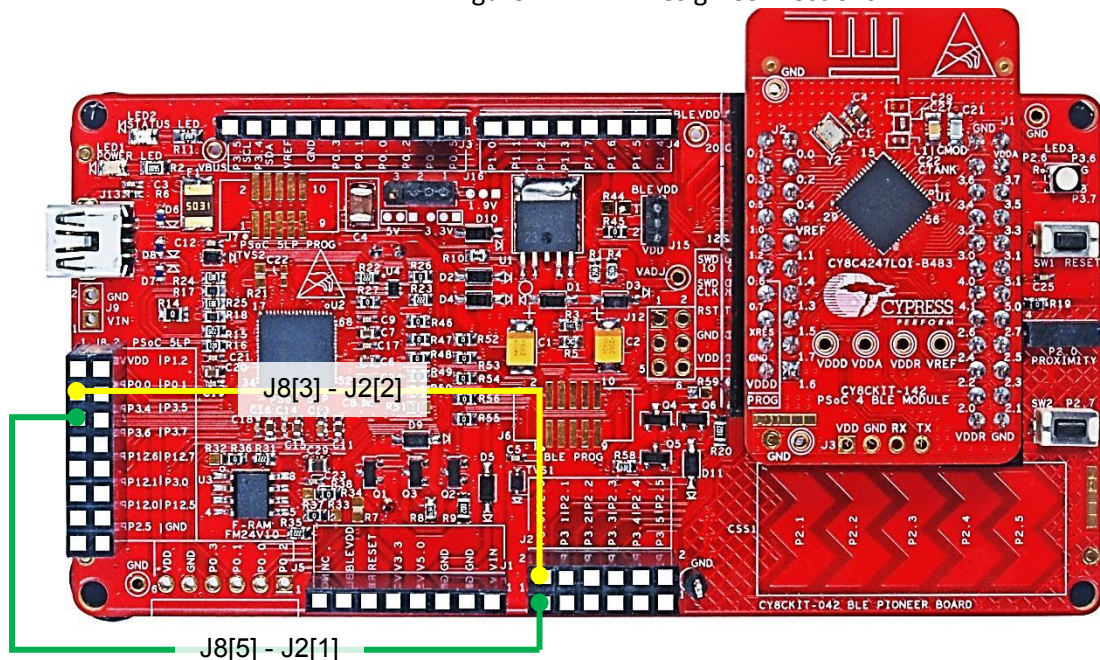
## Testing

It is time to test your application. Follow these steps:

1. PSoC 5 generates the heart rate signal on **P0.0**, with an expected value of around 115. Connect this pin (**Pin 3 on J8**) to **P2.0** of PSoC 4 BLE (**Pin 2 on J2**). See [Figure 21](#).
2. The generated heart rate signal can be changed to reflect different heart rate values within a range of 60 – 115 bpm. To check the different values, we will use the **SW2** switch on the kit. For this purpose, connect the PSoC 5 pin **P3.4** (**Pin 5 on J8**) to pin **P3.0** of PSoC 4 BLE (**Pin 1 on J2**). See [Figure 21](#).

Figure 21: Connecting the PSoC 5 Signals to PSoC 4 BLE on the Kit

Figure 22: HRM Design Connections



3. On the BLE Pioneer Kit, the RGB LED is used to indicate the current status of the device. Two of the three LEDs are used to signal different states, as explained in [Table 5](#).

Table 5: Device State Indicated by RGB LED on the Kit

RGB LED Color	Description
Green	Device is currently advertising and is ready to connect
Blue	Device is connected
None (LED OFF)	Device is disconnected

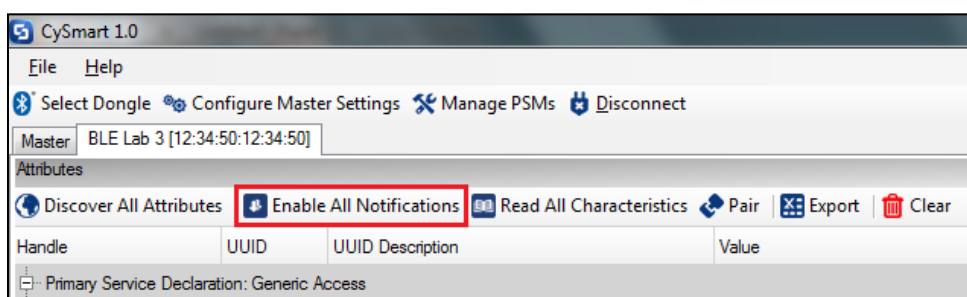
4. This lab can be tested with both the **CySmart iOS/Android Mobile App** as well as the **CySmart BLE Test and Debug Tool (Windows)**. You can choose either. To install the CySmart Mobile App on your iOS/Android device, search for **CySmart** on their respective app stores. Note, if the app does not show up on your phone's app store, it likely means your phone is not BLE-capable.



## Testing with CySmart BLE Test and Debug Tool

1. Open **CySmart 1.0** and connect the **BLE-USB Bridge** to it.
2. Make sure your device is advertising, and then click **Start Scan** to list all the available devices. If you do not see your device appear, press **SW2** on the BLE Pioneer Kit to exit from the low-power mode and restart advertising.
3. Connect to your device: Select the appropriate device name and click **Connect**.
4. Upon connection, a new tab opens in the tool. Click **Discover All Attributes** to list all the Services, Characteristics and Descriptors of your device.
5. Click **Enable All Notifications** on the top to enable Heart Rate Measurement Characteristic notifications. See [Figure 23](#).

Figure 23: Enable Notifications in CySmart



6. Observe that the value of the **Heart Rate Measurement** Characteristic is updated every second (this value is in hexadecimal), while the tool's log at the bottom shows new notification packets every second. See [Figure 24](#).

Figure 24: Heart Rate Data in CySmart

Primary Service Declaration: Heart Rate				
0x000C	0x2800	Primary Service Declaration	0D:18 (Heart Rate)	
Characteristic Declaration: Heart Rate Measurement				
0x000D	0x2803	Characteristic Declaration	10:0E:00:37:2A	
0x000E	0x2A37	Heart Rate Measurement	00:5C	0x10
0x000F	0x2902	Client Characteristic Configuration	01:00	

7. Press the **SW2** switch on the kit and observe that the heart rate number changes.
8. **Disconnect** the device and notice that the RGB LED turns off. At this point, the device has entered the Hibernate mode.
9. Press the **SW2** switch now to see that the Green LED turns on and the device starts advertising again.

## Testing with CySmart Mobile App

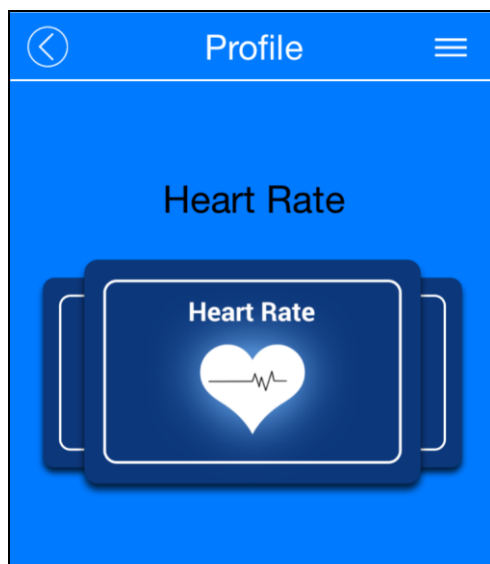
1. Open the **CySmart mobile app** on a BLE-enabled mobile device. If you do not have Bluetooth switched on already, the app asks you to do it.
2. Once Bluetooth is on, the app home screen lists the BLE devices nearby. Check that your device is on the list. Note, **swipe down** on this screen to refresh the list of available BLE devices. See [Figure 25](#).

Figure 25: CySmart iOS App Home Screen



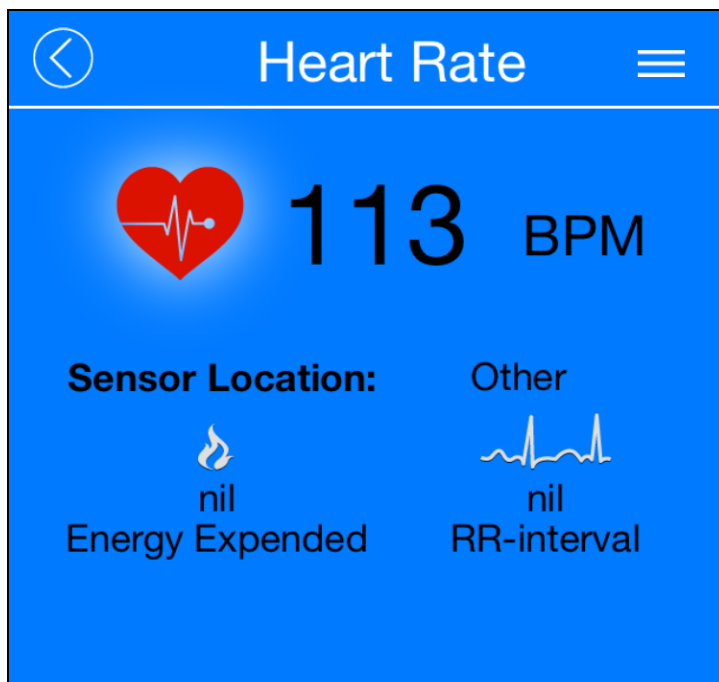
3. Tap your device name (BLE Lab 3) to go to the **Profile** screen. This screen shows the Services supported by your device. Tap on the **Heart Rate** Service now. See [Figure 26](#).

Figure 26: CySmart iOS Profile Page



- On the **Heart Rate Service** page you can see the current heart rate being transmitted by the sensor. See Figure 27.

Figure 27: CySmart iOS Heart Rate Service Page



- Press the **SW2** switch on the kit and observe that the heart rate number changes on the app.
- Disconnect** from the device and you will notice that the RGB LED turns off. At this point, the device has entered Hibernate mode. Hibernate is an ultra-low power mode from which the device can be woken up by a GPIO interrupt.
- Press the **SW2** switch now to see that the LED turns Green again and the device starts advertising afresh.

Note: If you want to measure power consumption in any of the various modes, you can attach a current probe at J15. Before measuring power, the value of RGB\_LED\_IN\_PROJECT in main.h should be changed to (0) and the device should be reprogrammed. Otherwise, the LED power is included in the measured power which dominates the total system power.

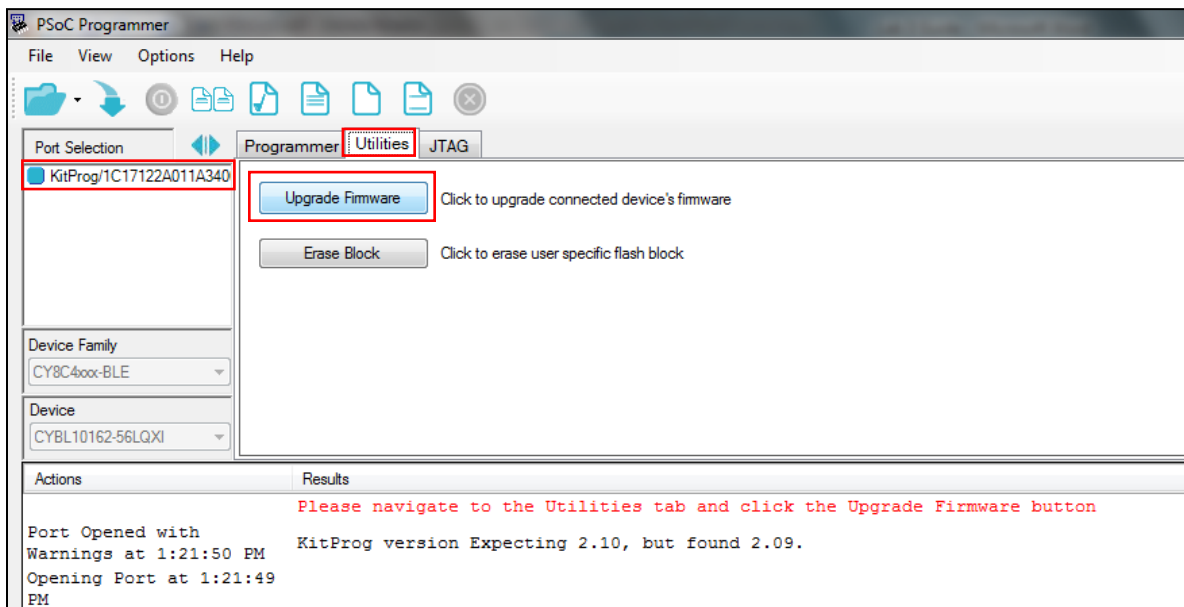


## Restoring PSoC 5

For restoring the default KitProg firmware on PSoC 5, follow these steps:

1. To restore the default firmware on PSoC 5, open **PSoC Programmer**, which is located in the **Cypress->PSoC Programmer 3.22** location in the Start menu.
2. When PSoC Programmer is opened, it detects the firmware installed on the PSoC 5 and reports it as out of date. It also instructs you to upgrade the firmware to get to the most recent official version.
3. To restore the default firmware, navigate to the **Utilities** tab and press the **Upgrade Firmware** button. See [Figure 28](#). The PSoC 5 is now programmed with the factory firmware, and no longer produces the heart rate signal. To bootload the custom firmware again, repeat steps 1 through 7 in the “Bootloading PSoC 5” section of this lab manual.

Figure 28: PSoC Programmer Firmware Update



**Congratulations, you have completed Lab 3!**

### *Additional Exercises*

1. Configure the Opamp, used as a follower, to work in Deep-Sleep mode with lower power settings.
2. Update the Connection Interval to 1 second from the PSoC 4 BLE device.  
Hint: Use the API function `CyBle_L2capLeConnectionParamUpdateRequest()` to update the connection parameters. The API function is available in the completed firmware as commented code.
3. Generate the heart rate pulses using a PWM Component inside the PSoC 4 BLE.  
Hints:
  - Don't forget to start the PWM Component using the `<component_name>_Start()` API.
  - As the TCPWM block is not active during the Deep-Sleep mode, comment out the code for putting the device into the Deep-Sleep mode.

### Document Revision History

Revision	By	Description
**	PMAD	Initial Release
*A	GUL	Edits for BLE terminology