

Instrumentation - computer Engg By Upendra Yadav,
unit - III [7 hrs] Date _____
Pekharia University Page _____

Electrical signal processing and data Acquisition

Basic op-amp characteristics-

- ① High voltage gain.
- ② High input impedance.
- ③ Low output impedance.
- ④ High common mode Rejection Ratio [CMRR].
- ⑤ Small signal bandwidth.
- ⑥ Low output voltage swing.
- ⑦ Perfect balanced.
- ⑧ High slew rate.

Instrumentation amplifier ⑨ -

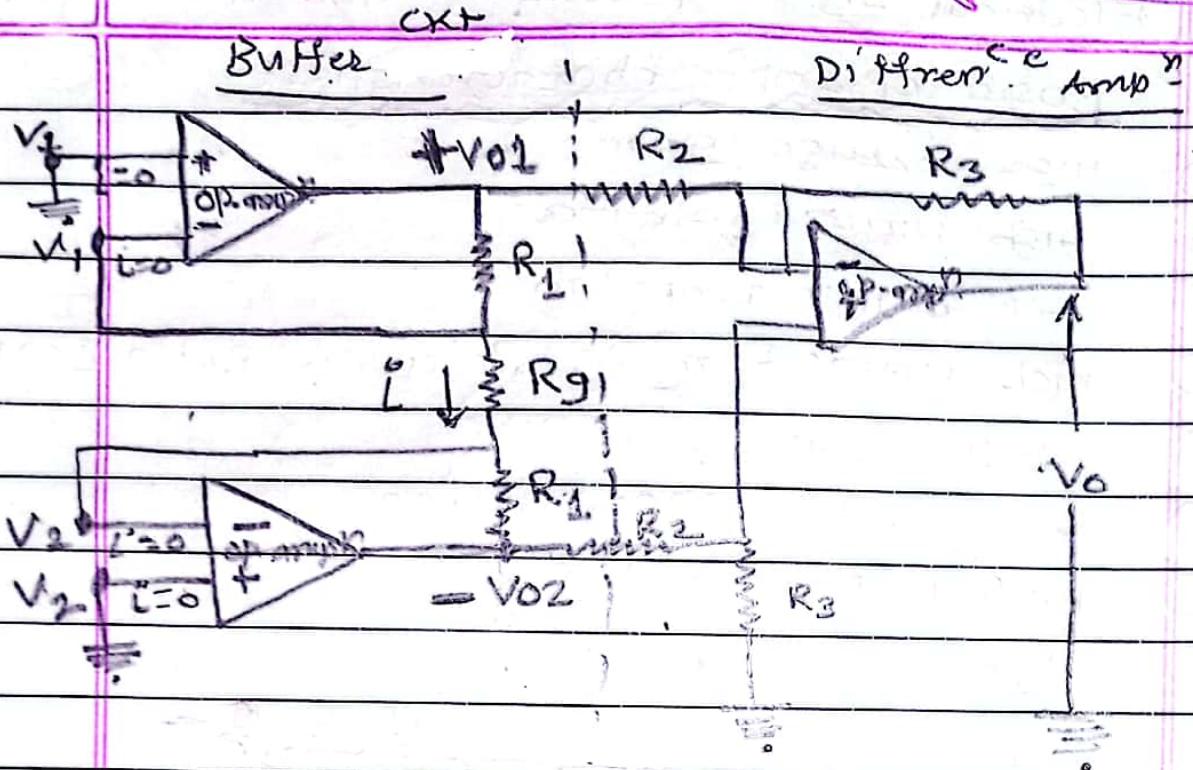
Instrumentation amplifier is a kind of differential amp.

- (a) Gain stability.
- (b) Input Impedance Matching.

For Design

- (c) To low d.c. offset.
- (d) Low drift.
- (e) Low noise.
- (f) Very high open loop gain.
- (g) very high open loop CMRR.
- (h) very high ^{input} impedance.

The ckt diagram of instrumentation amp



[fig-⑨ Instrumentational amp¹²]

$$V_o = \frac{R_3}{R_2} [V_{o2} - V_{o1}] \quad \text{--- (1)}$$

Let, assume,

$$V_1 > V_2$$

some current passing through

V_{o1} and V_{o2} path - I know, i current flowing through higher potential to lower potential

$$I = \frac{V_1 - V_2}{R_g} \quad \text{--- (2)}$$

Assume, V_{o1} is higher potential to lower potential at V_{o2}.

Applying the ohm's Law -

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$i = \frac{V_R}{R} = \frac{\text{Voltage difference}}{\text{Total resistance}}$ [Date] $V \cdot D$ [Page]

$$i = V_{OL} - V_{O2}$$

$$R_i + R_g + R_L$$

$$i = \frac{V_{OL} - V_{O2}}{2R_i + R_g}$$

$$\frac{V_1 - V_2}{R_g} = \frac{V_{OL} - V_{O2}}{2R_i + R_g}$$

$$\therefore V_{OL} - V_{O2} = \frac{2R_i + R_g}{R_g} [V_1 - V_2]$$

[Taking + sign common]

$$V_{O2} - V_{OL} = \left[1 + \frac{2R_i}{R_g} \right] [V_2 - V_1] \quad \text{--- (3)}$$

Substituting eqⁿ ③ in eqⁿ ①

$$\therefore V_O = \frac{R_3}{R_2} \left[1 + \frac{2R_i}{R_g} \right] [V_2 - V_1]$$

$$\frac{V_O}{V_2 - V_1} = \frac{R_3}{R_2} \left[1 + \frac{2R_i}{R_g} \right]$$

$$\text{Gain} = \frac{R_3}{R_2} \left[1 + \frac{2R_i}{R_g} \right] \leftarrow \text{output voltage}$$

Advantage of Instrumentation ampⁿ:

- ① Accurate and stable resistive feedback N/W to gives a desired gain.
- ② Due to closed loop configuration used, so that instrumentation ampⁿ can be used directly for amplifying the signal by

fixed amplification factor

- ③ The gain accuracy, gain stability and drift performance are normally specified by the manufacturer.
- ④ CMRR specified which is only applicable for very low source impedance.
- ⑤ Low DC offset, low noise, low drift, high open-loop gain, high slew rate, high input impedance.
- Signal amplification :-
- ① The signal amplifier is a circuit that used the electrical power to increase the amplitude of an incoming signal voltage or signal current output of this higher amplitude.
- ② The ideal signal ampⁿ creates an exact replica of original signal that is larger but identical in every other way. In practice, a perfect ampⁿ is not possible.
- ③ Signal amplifiers are an essential component of thousand of device. Like, Landline, cellular telephone, music and public address system, DAQ system, radio-freq-transmitter, Servometer controller.
- ④ In data[DAQ] system, signal amplifiers need to increase the

amplitude, from Transducer the output

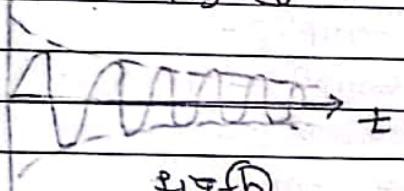
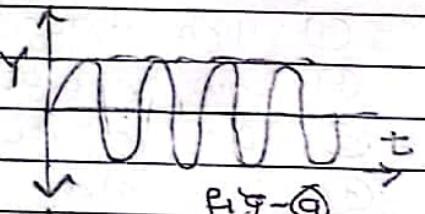
- Small signals, sent to ADC.
- Some common signal ampⁿ in data acquisition systems.
- Voltage ampⁿ :-
- (a) High voltage ampⁿ
 - (b) Low voltage ampⁿ
 - (c) D.C. voltage ampⁿ
 - (d) A.C. voltage ampⁿ
- Current ampⁿ :-
- (e) Charge ampⁿ
 - (f) Isolated ampⁿ
 - (g) Differential ampⁿ
- Strain gauge ampⁿ :-
- (h) Bridge ampⁿ
 - (i) Full-Bridge ampⁿ
 - (j) Half-Bridge ampⁿ
 - (k) Quarter-Bridge ampⁿ
- Thermocouple - amplifiers :-

Attenuation

Attenuation, the opposite of amplification is necessary when voltage to be digitized are beyond the ADC range. This form of signal conditioning decreases input signal amplitude.

so that conditioned signal is

within ADC range. Attenuation is typically necessary when measuring voltages that are more than 10V.



Attenuation is one of the techniques used in signal conditioning to scale down the amplitude or voltage level of a signal to ... match the input range or requirement of downstream components or systems.

Attenuation can be achieved using passive or active components within a signal conditioning ckt.

① Active attenuation -

② Passive attenuation -

③ Active attenuation - Active -

attenuation involves the use of

active components such as the op^m ampⁿ to actively amplify or attenuate the signal. In case of attenuation, an op^mampⁿ can be configured as an inverting amp^m or a voltage follower with appropriate gain settings to achieve the desired signal reduction.

Attenuation in signal condition-

-ing is often used to protect sensitive component from excessive signal levels, prevent signal clipping or saturation, or match signal level betwⁿ different

stages of the system. ie A/D converters where input voltage range is limited, attenuations can be applied to scale down the input signal to fit within the A/D range without losing important information.

passive attenuation - The passive

attenuation techniques typically involve the use of voltage divider.

The voltage divider ckt is commonly example of passive attenuation. Where output voltage is a fraction of the input voltage determined by the resistor values.

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(a) Attenuate the applied wave.

(b) Alter the d.c level of the applied wave.

(c) The wave shaping technique include clipping and clamping.

clippers :-

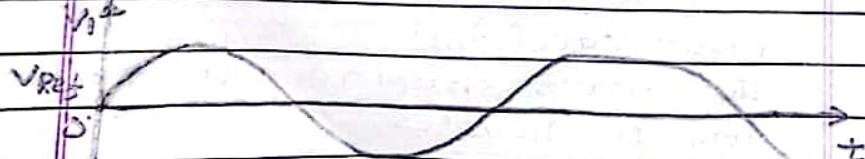
A clipper is an electronic ckt that produced an output by removing a part of the input above or below a refⁿ level. In other words clipper ckt is electronic ckt that limits or clips the amplitude of a waveform at certain level.

The clipper ckt is classified on the base of the clipping portion of the input:-

(a) positive clipper

(b) negative clipper

(c) positive clipper :- The positive clipper that clips only the positive portion of the input signal.



[fig-(a) sinusoidal input signal]

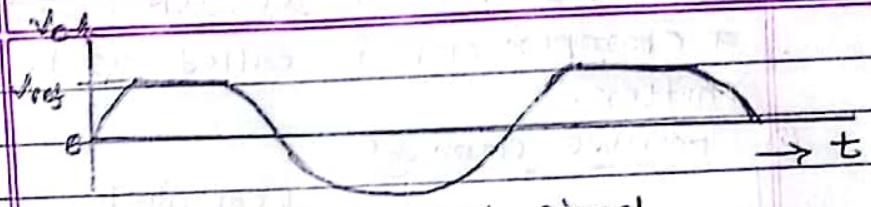


fig-(a) output signal

(b) Negative clipper :- A Negative clipper is a clipper that clips only the negative portion of the input signal.



fig-(a) sinusoidal Input signal

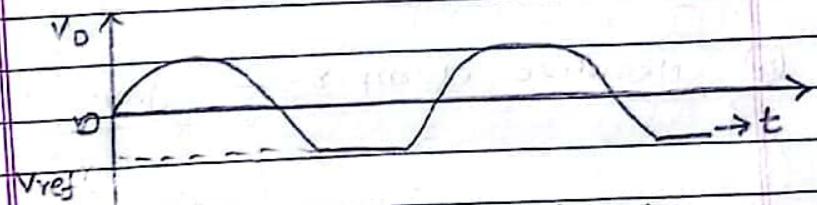


fig-(b) output signal

Clammer ckt :- A clammer ckt is a ckt that adds a D.C. level to an AC signal. Actually, the positive and negative peaks of the signals can be placed at desired levels using clamping

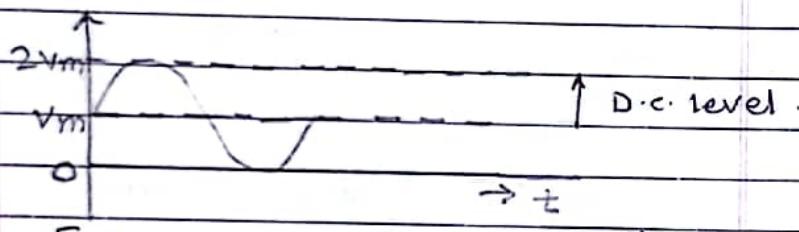
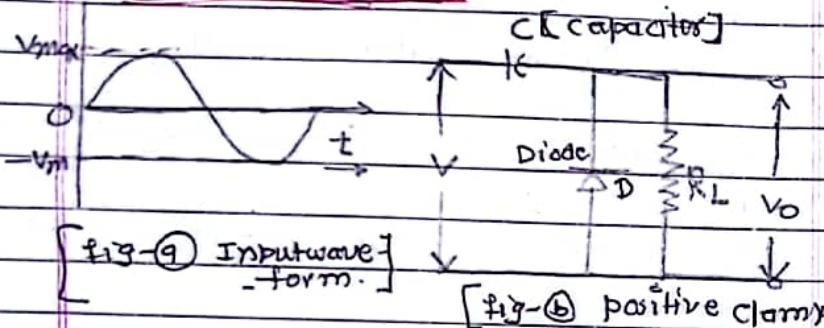
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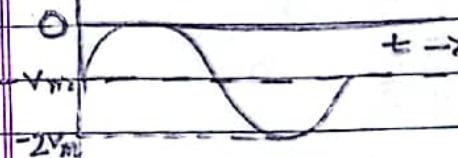
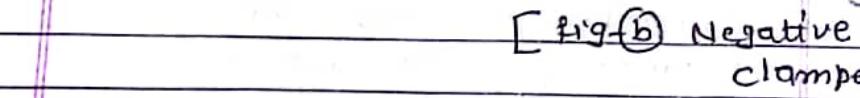
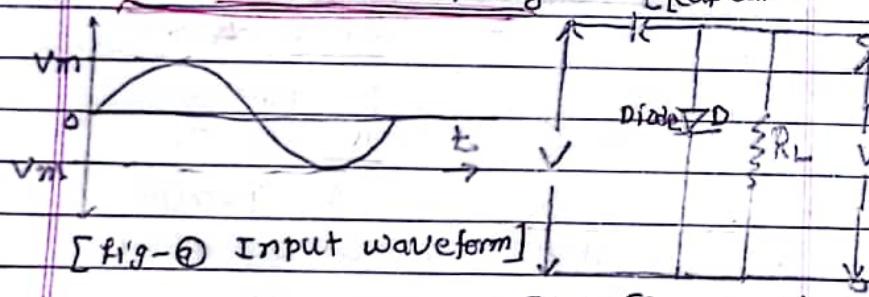
Circuits As D.C. level gets shifted

a clumper circuit is called as Level shifter.

(a) positive clumper:

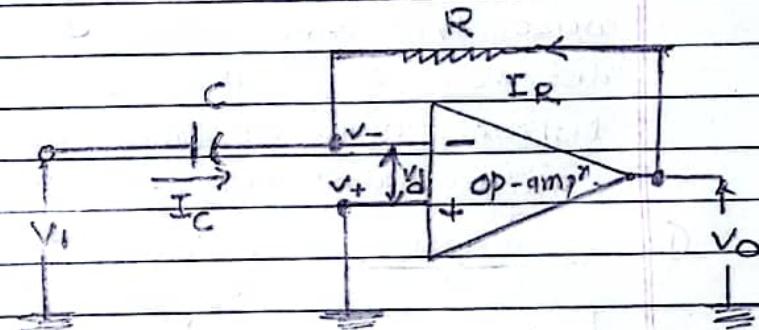


(b) Negative clumper:



[fig-(c) output waveform]

Differentiation :- It is function is to provide an output voltage proportional to the rate of change of input voltage. It is the inverse mathematical operation to that of an integrator. It also provides information about the instantaneous change in signal.



[fig-(d) differentiator ckt]

$$V_+ = 0, V_+ - V_- = 0, \\ V_- = 0$$

Applying KCL at node V_-

$$I_C + I_R = 0$$

$$C \frac{d[V_1]}{dt} + \frac{V_o}{R} = 0$$

$$C \frac{d[V_1]}{dt} = - \frac{V_o}{R}$$

$$\frac{V_o}{R} = - C \frac{d[V_1]}{dt}$$

$$V_o = - R C \frac{d[V_1]}{dt}$$

output voltage

The output voltage is equal to the differentiated input voltage. A differentiator is used in analog computer as it tends to amplify noise drift and other unwanted disturbance in the system.

Different types of Input signal in Differentiator ckt :-

(1) sine-wave e.g.-

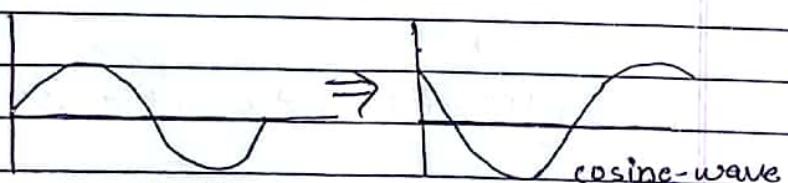


Fig-(a) Sine-wave
Input-signal

Fig-(b) Output signal

(2)



Fig-(a) Triangular-wave
Input-signal



Fig-(b) Rectangular
wave output
signal.

(3)

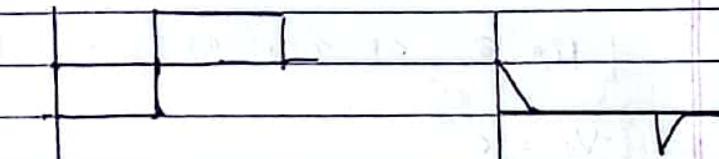


Fig-(a) Square-wave
Input-signal

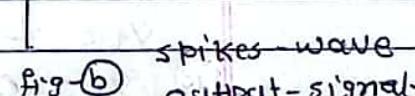
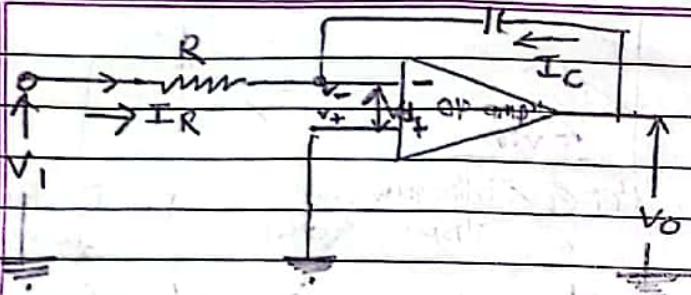


Fig-(b) Spikes wave
Output-signal.

Integrator :- An integrator is a ckt that performs a mathematical operation called integration. Integration is continuous addition. The most popular application of an integrator is to produce a ramp of output voltage which is linearly increasing or decreasing voltage. A ckt in which the output voltage is proportional to the integral of input voltage, shown in fig-(a) below.



[Fig-④ op-amp as Integrator]

$$V_+ = 0$$

$$V_+ - V_- = 0$$

$$V_- = 0$$

Applying the KCL at node V_-

$$I_R + I_C = 0$$

$$\frac{V_1}{R} + \frac{C dV_0}{dt} = 0$$

$$C \frac{dV_0}{dt} = -\frac{V_1}{R}$$

$$\frac{dV_0}{dt} = -\frac{V_1}{R_1 C}$$

Taking ∫ both sides

$$\int \frac{d(V_0)}{dt} dt = - \int \frac{V_1}{R_1 C} dt$$

$$V_0 = -\frac{1}{RC} \int V_1 dt$$

The convenient values of R_1 and C

are $M\Omega$ or μF change.

Let consider different types of input signal -

1) Let $V_{in} = V_m \sin \omega t$ [sin-wave]

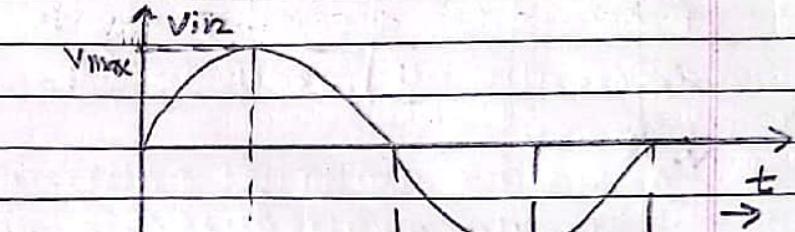


Fig-④

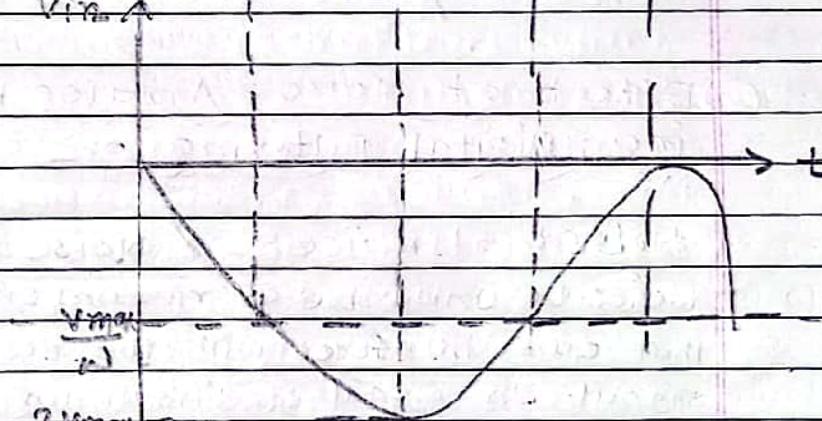


Fig-⑤

2) Let, $V_{in} = \text{step signal}$

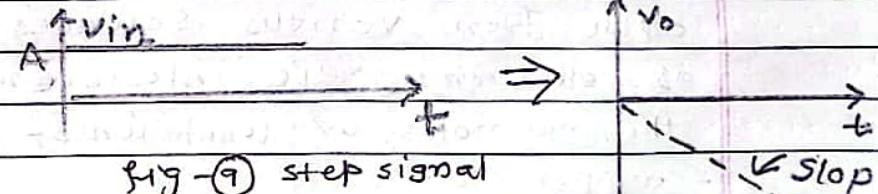
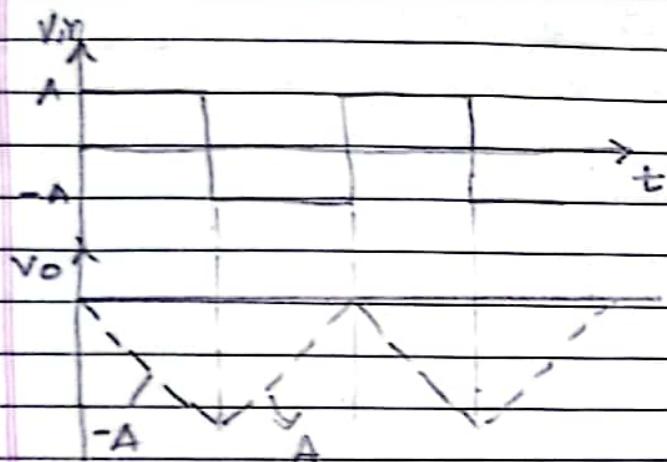


Fig-⑥ step signal

Fig-⑦ output signal

(3) V_{in} = square wave:-



Effect of Noise, Analog Filtering, Digital Filtering :-

Effect of Noise :- Noise refers to unwanted or random signals that can interfere with the desired signal. In signal conditioning, noise can degrade the quality of the signal and make it more difficult to extract useful information. It can be arise from various sources, such as electromagnetic interference, thermal noise or limitation of electronic components.

The effect of noise through the Filter Techniques. Filter helps to reduced the impact of noise on the signal and improve its quality for further processing.

Analog Filter :-

Analog filter involves the use of analog ckt. such as, Resistor, inductor and capacitor to modify the characteristics of the signal. Analog filters are commonly used in signal conditioning to attenuate unwanted frequencies, remove noise, or sharp the response of the signal.

Analog filter can be designed as low-pass, high-pass, Band-pass or Band-reject filter to selectively allow or block certain freq. component-s of the signal. The undesired freq. components, including noise, can be attenuated, improving the quality for subsequent processing stage.

Digital Filtering :-

Digital filtering involves processing the signal in the digital domain using digital signal processing techniques.

Digital filter are implemented through algorithms and computations performed by microprocessor, digital processor or dedicated hardware.

Advantage of digital Filter in signal conditioning :-

- ① They provide flexibility in terms of filter char & can easily modified.
- ② Digital filters can implement a wide range of filter types, including Finite impulse response Filter and Infinite impulse response filter.
- ③ Digital filtering allows precise control over filter parameters such as cutoff frequencies, stop band attenuation and filter order.
- ④ It enable efficient noise removal signal enhancement and freq. response shaping.
Overall noise, analog filtering and digital filtering all

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contribute to signal conditioning by reducing noise, improving signal quality and shaping the Freq. response as need.

Data Acquisition System

Data Acquisition system [DAS] :- Data acquisition systems may be defined as a system used for data processing, data conversion, data transmission and data storage. In other words, A data acquisition system is an information system that collect, stores and distributes of information. It is used in industrial and commercial electronics and environmental and scientific equipment to capture the electrical signals or environmental condition on a computer device. A data acquisition system is also known as a data logger.

These data acquisition system will perform the tasks such as conversion of data, storage of data, transmission of data processing of data for the purpose of monitoring, analyzing or controlling the system and processes.

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Data acquisition systems instrument

are the combinations of a number of data acquisition components which make complete system or self-contained instruments.

Types of Data Acquisition systems:

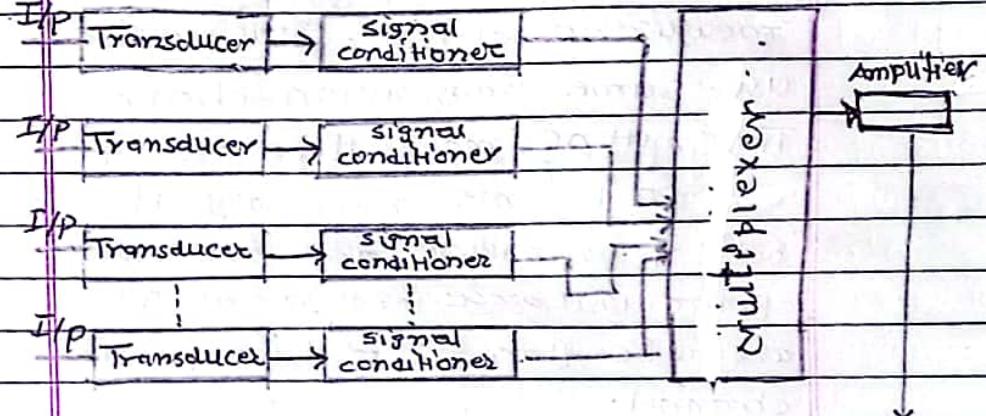
Data acquisition system is measurement system by which data is acquired economically and efficiently in desired form.

Data acquisition system can be classified into two types which is given below.

- (1) Analog Data Acquisition system.
- (2) Digital Data Acquisition system.
- (3) Analog Data Acquisition system.

The data acquisition systems, which can be operated with analog signal are known as Analog data acquisition system.

- (1) Transducer :-
- (2) Signal conditioner :-
- (3) Multiplexer :-
- (4) Amplifier :-
- (5) Analog Data :-

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[fig-① Analog data acquisition system]

- (1) Transducer :- It converts the physical quantities into electrical signals. one form of energy to another form of energy. ie ^{Non} Electrical signal to Electrical signals, such as voltage or current.
- (2) Signal conditioner :- It takes output from the transducer and make its into suitable form of condition and desired signal. Sending to multiplexer.
- (3) Multiplexer :- Multiplexer is the process of showing single channel with more than one input and multiplexer accept

multiple analog input and connects the sequentially to one measuring output. Multiplexing

use same transmission channel for transmitting more than one quantity.

it becomes necessary if distance

between transmitting and receiving point is large and many quantities are to be transmitted by separate channel.

(4) Amplifier :- An amplifier is increase the power of signal or Amplifier increases the amplitude of the signal on input terminals than output terminals. It produces the amplitude signal in output terminals. An amplifier act is power gain is greater than one.

(5) Analog Recorder/ Device :- It displays the input signal for monitoring purpose.

(a) Display - display the input analog data.

(b) Meter - Reads the analog input value.

① Recorder :- Recorder

records the input analog data permanently.

② Digital Data Acquisition System :-

The data Acquisition systems, which can be operated with digital signals are known as digital data Acquisition system. So that they use digital components for storing or displaying the informations.

Digital data Acquisition system [DAS] components :-

① Transducer :-

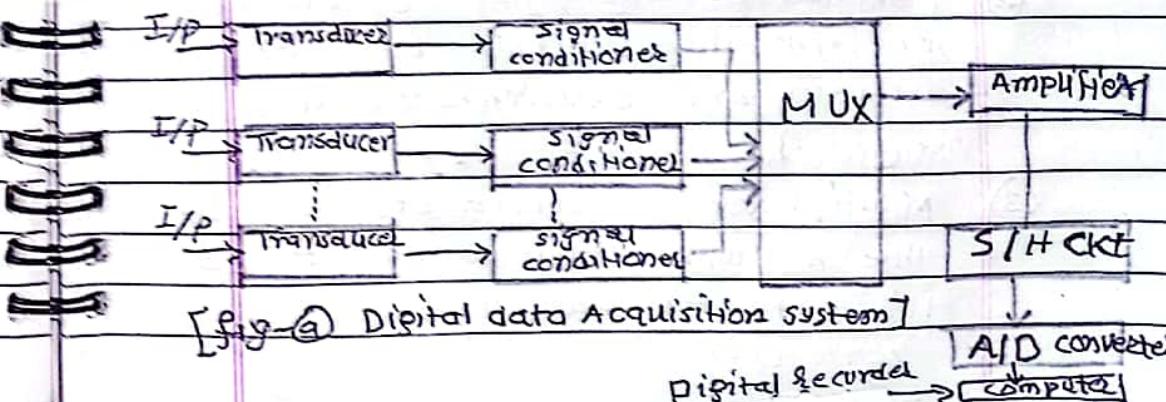
② Signal conditioner :-

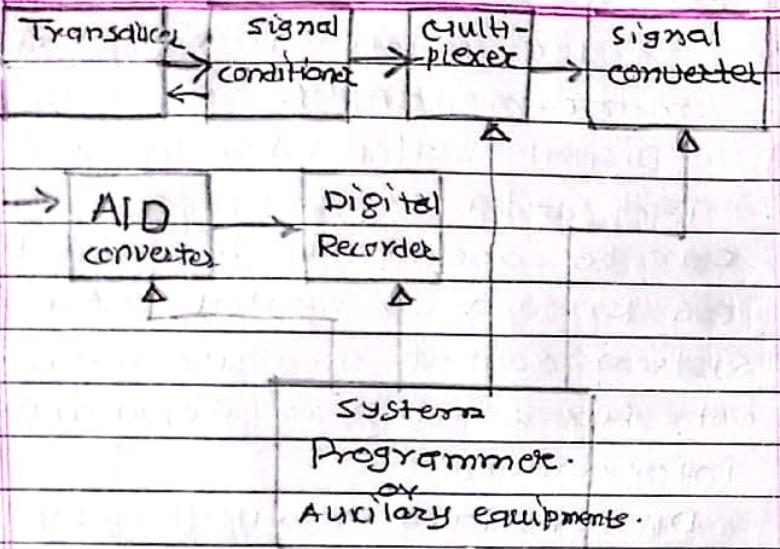
③ Multiplexer :-

④ Sample and Hold CKT :-

⑤ A/D converter :-

⑥ Digital Recorder / computer :-





[fig④] Modern digital data Acquisition system]

- ① Transducer :- It is converted Non-electrical quantities [signal] to Electrical signals or it can say physical quantities to electrical signal.
- ② Signal conditioner :- It is perform the function like amplification and selection of desired signal.
- ③ Multiplexer :- connects one of the multiple input to output so it's acts as parallel to serial converter.

④ Sample and Hold ckt [S/H ckt] :- It is usually used with an analog to digital converter to sample the input analog signal and hold the sampled signal, hence the name "sample and hold". In the sample and hold ckt, the analog signal is sampled for short interval of time. [to its us range]. After this, the sampled value is hold until the arrival of next input signal to be sampled. The duration of holding the sample will be usually milliseconds to few seconds.

The sampled value when provided to the ADC, it generates a discrete signal from an analog signal.

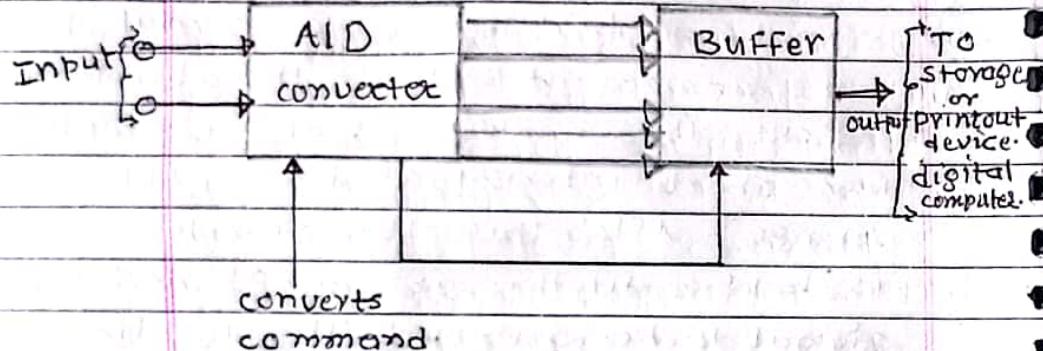
⑤ A/D converter :- It converts to analog input to digital outputs.

⑥ Digital Recorder/ computers :- It is used to record the data in digital format.

⑦ single channel data acquisition systems :-

The system consists of a "signal conditioner" followed by an analog to digital converter [A/D].

performing repetitive conversions at a free running, internally determined rate.



[fig-④ Single channel data - acquisition system]

The digital outputs from the buffer are further fed to a storage or printout device or to a digital computer for analysis.

Ex-Digital panel Meter [DPM].

A/D converter on the based on dual slope conversion techniques, useful for low freq. data.

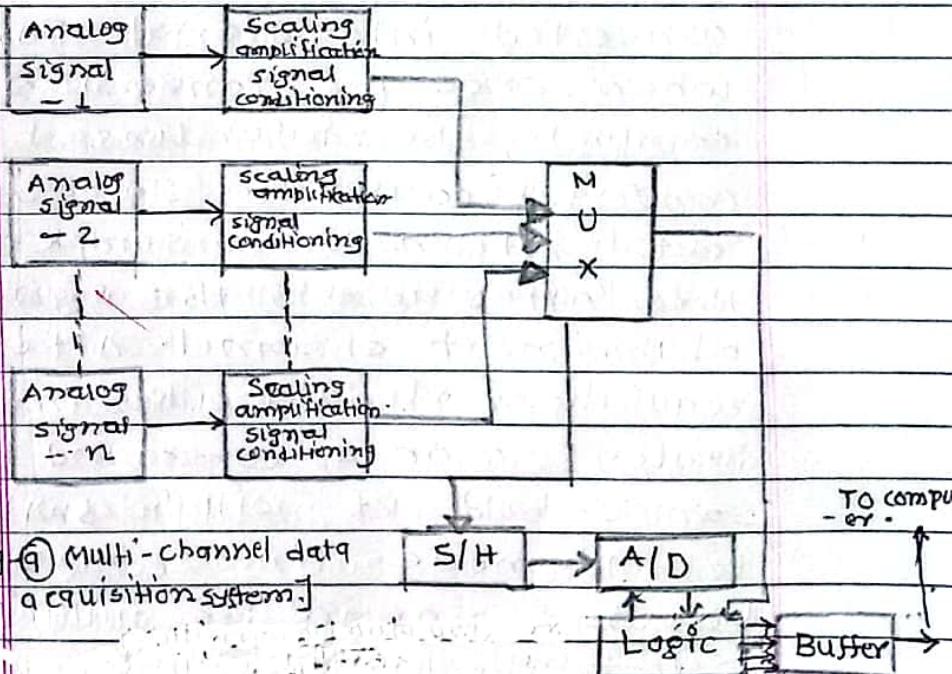
Successive approximation type A/D

converted also have high resolution and high speed. For signal levels which are low compared to input requirements, amplification may be

used in order to bring up the level of the input. Amplification is done by amplifiers by single ended input or differential input.

Multi-channel data acquisition systems

There will be many sub-systems in a data acquisition system can be time-shared by two or more input sources. Based on the derived properties of the multiplexed system a number of technique are employed for such time-shared measurements.



[fig-⑤ Multi-channel data acquisition system]

The output of Transducer given to signal conditioning ckt and from output of signal conditioning ckt signal goes to multiplexer.

The multiplexer output is converted into digital signals by A/D converter sequentially.

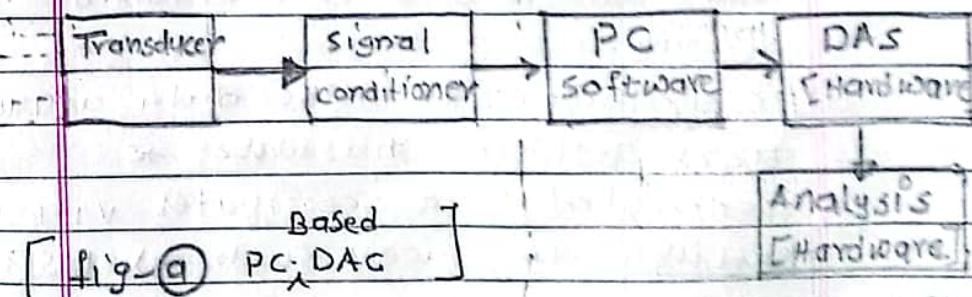
The multiplexer stores data say of the first channel in the sample hold ckt. It is then seeks the second channel. During the interval, the data of the first channel will be converted into digital form.

When once the conversion is complete, the status line from converter causes the sample-hold ckt return to the sample-mode. Then after accepts the signal of the next channel. After acquisition of data either immediately or on a command the sample-hold ckt will be switched to hold-mode. Now conversion begins and the multiplexer selects the next channel.

This method is slow, sample hold ckt or A/D converters are multiplexed. For faster operations, this method is less costly.

⑤ PC Based Data Acquisition System

The most visible trends can be seen as the effects of transition to PC-base DAS [Data Acquisition system]. Now, all that processing is being done inside computers, so instruments are interfaced to a computer with analysis being done through computer software. Thus we see a more software-defined approach to DAS, as well as the emergence of high-speed USB [Universal Serial Bus] enabled Data Acquisition system.



[fig-⑤ PC-DAS]

Data acquisition Hardware :-

The data acquisition hardware serves as the interface between physical signals and the computer. It typically consists of analog-to-digital converters [ADC] for converting analog signals to digital data. Digital-to-analog converters [DAC] for converting digital data into analog signals.

The various I/p or O/p channels for handling different types of signals [analog, digital, counter/timer etc].

The hardware may also include signal conditioning components like amplifiers, filter and isolation circuits to enhance the quality and reliability of the acquired signals.

PC Interface :- The data acquisition system hardware is connected to a computer via a suitable interface, such as USB, PCIe [peripheral component interconnect Express]

Ethernet or wireless connections - The

interface enables the transfer of acquired data between the hardware and computer.

Drive and software :- To communicate with the data acquisition hardware, the computer device drives and appropriate software. The device drivers establish necessary communication protocols and provide an interface for accessing and controlling the hardware from the software applications.

The software typically includes development tools and libraries that facilitate data acquisition, signal processing, and visualization and analysis.

Benefits of PC-Based DAs

- ① Versatility :-
- ② Processing power :-
- ③ Integration :-
- ④ Cost-Effectiveness :-
- ⑤ Versatility - Ability to utilize a wide range of software and hardware options.

② Processing computer - PCs provide ample computing power for real-time signal processing analysis and visualization.

③ Integration - P-C Based DAS system can easily integrate other software and hardware components making them suitable for complex measurement and control systems.

④ cost-effectiveness - It is more cost-effective.

Series and parallel Transmission

The process of sending data betⁿ two or more digital devices known as data transmission.

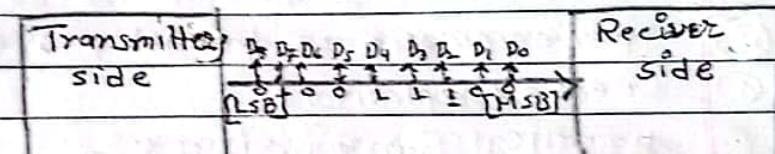
Data transmitted betⁿ digital devices using two methods -

① serial Transmission - [④ synchronous transmission]

② parallel Transmission - [④ Asynchronous transmission]

① serial Transmission - In serial transmission, data bit flows from one computer to

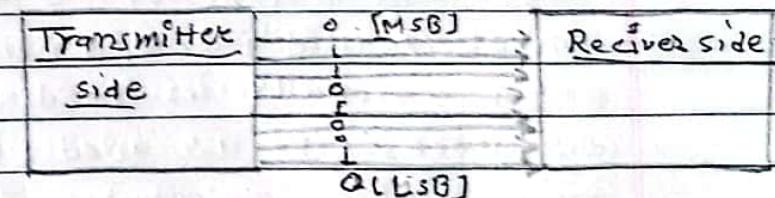
another computer in bi-direction. In this transmission, one bit flows at one-clock pulse. In series transmission, for 8-bits are transferred at a time having a start and stop bit.



[fig-a]

serial Transmission

② parallel Transmission - In parallel transmission, many bits are flow together simultaneously from one computer to another computer. parallel transmission is faster than serial transmission to transmit the bits. parallel transmission is used for short distance.



[fig-b] parallel Transmission

Difference b/w serial and parallel transmission :-

serial Transmission :-

- ① Bit Transmission :- only one bit is transferred at one pulse.
- ② cost efficient - ↑
- ③ performance is lower -
- ④ less complexity .

parallel Transmission :-

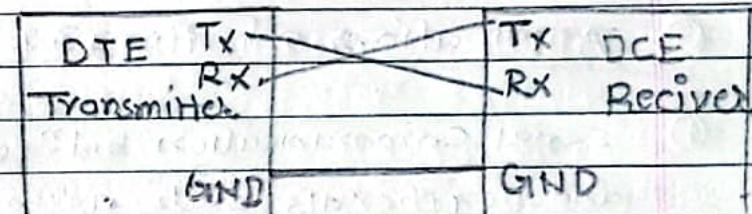
- ① Bit - Transmission :- 8-bits Transferred at one clock pulse .
- ② It is not cost efficient .
- ③ preferred only for short distance .
- ④ more complexity .

Features and application of RS 232

Cable :-

RS - 232 [Recommended standard - 232] It is type of serial communication used for transmission of data normally in medium or long distances , It is used for connecting computer and its peripheral device to allow serial data exchange b/w them . As its path data exchange b/w

Term . It is used to serial communications upto 50 feet with rate of 1.49 kbps . For EIA [Electronics Industry Association] defined , The RS 232 used for connecting DTE [Data Transmission Equipment] and DCE [Data communication Equipment]



RS - 232 Features :-

- ① RS - 232 uses Asynchronous communication so no clock is shared b/w PC and MODEM .
- ② Logic 0-pin is started voltage range high let assume +12 V , Logic 1 pin is started voltage low , let assume -12 V .
- ③ MAX 232 IC can easily be installed with microcontrollers .
- ④ Full-duplex interface of RS - 232 is very convenient .

⑤ Two-pin simplex RS-232 interface can also be established.

⑥ A max²³ data transfer speed of 19 kbps is possible through RS-232.

⑦ A max²³ current of 500 mA can be drawn from pins of RS-232.

Applications of RS-232 :-

① Serial communication b/w computers and peripherals such as modems, printers, barcode scanners and industrial control devices.

② config and programming devices including routers, switches, N/W devices and embedded systems.

③ As Data Acquisition and Instrumentation used for connecting data acquisition devices such as sensors, data loggers and measuring instruments to a computer or control system.

④ Industrial Automation and control to enable communication b/w Programmable logic controllers [PLCs]

Human-machine interfaces [HMIs] and other control devices.

Features and Application of IEEE 1248 B :-

IEEE-1248-2020 is titled "Standard for Analog to Digital converter [A/D] Testing - Methods and Metrics". This std. provided guidelines and methods for testing the performance of A/D converters used in various applications.

Features of IEEE 1248-2020 :-

① A/D converters Testing Method :- The various methods for evaluating the performance of A/D converters, including the testing of accuracy, linearity, resolution, dynamic range and important parameters.

② Test procedures :- It covers aspects such as test setup, test signals, measurement techniques and data analysis.

③ Performance Metrics :- ADC's ability to convert analog signals into digital representation accurately.

(4)

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Page _____

Reporting Requirements :- IEEE 1248-2020 specifies the information that should be included in test reports to ensure clear and consistent documentation of ADC performance.

Optical communication, Fibre optics, Electro-optic conversion Device :-

optical communication :- Optical fiber communication is method of communication in which signal is transmitted in the form of light and optical fiber is used as a medium of transmitting those light signals from one place to another place. The signal transmitted in optical fiber is converted from electrical signal to light signal from transmitting end to receiving end and it is converted back into the electrical

signal from light. The data sent in form of audio, video or Telemetry data for long distance or Local Area N/w.

- ① Transmitter side :-
- ② Optical Fiber cable :-
- ③ Receiver side :-

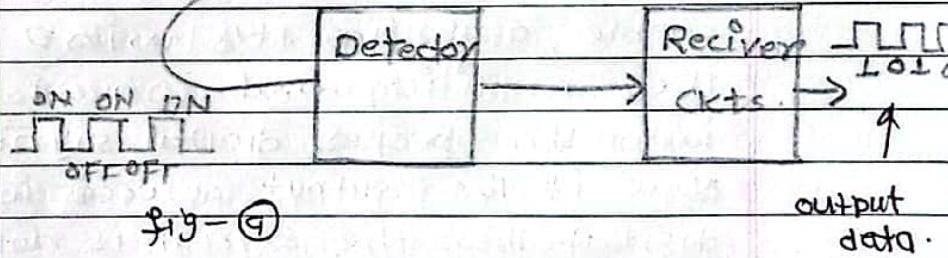
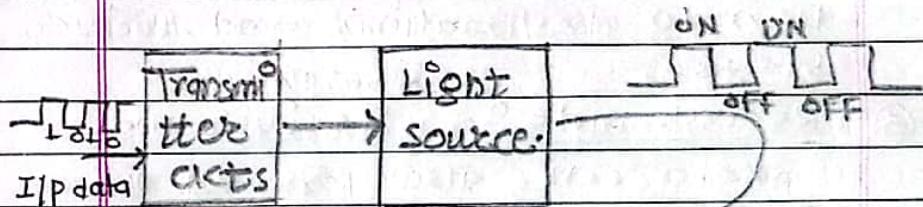


fig-④

① Transmitter side :- First, if data is analog then it sent to be converter ckt which converts the analog signals into digital pulses of 0, 1; 0, 1 and passed through a Light source Transmitter ckt. If the input signal is digital then it is directly sent to the Light source ckt.

which converts the signal
in the form of light wave.

② optical fiber cable :- The
light wave received from
the Transmitter ckt to the
fiber optic cable is now
transmitted from source loca-
tion to destination and receiver
blocks.

③ Receiver side :- Receiver side
photo cell, also known as
light detector, receives the
light waves from optical fiber
cable, amplifies it's using
the amplifier and converts it's
into the proper digital signal.
Now if the output source is
digital then the signal is not
changed. If the output source
need analog signal then the
digital pulses are converted into
analog signal by using conver-
ter or decoder ckt.

The whole process of
Transmitting an electrical signal

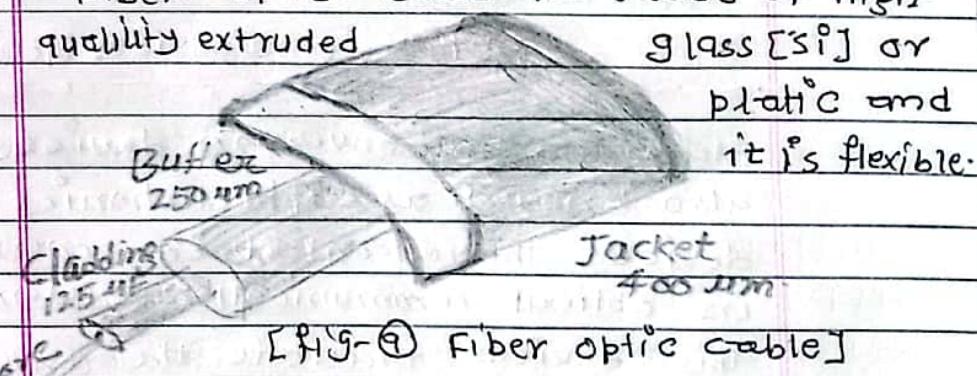
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From one point to others by

converting it's into light using
Fiber optic cable as Transmission
source is called Optical Fiber-
communication.

Fiber Optics :-

Fiber optics involves
Transmission of light signals through
optical fibers, which are typically
made of glass or plastic. These fibers have
the ability to guide light through multiple
reflections, allowing for efficient and high-
speed transmission of data
over long distances. The diameter of
fiber optic cable is made of high-
quality extruded



[Fig-④] Fiber optic cable]

Q. The diameter of fiber optic cable
is in betw 0.25 mm to 0.5 mm
[slightly thicker than a human hair]

Fiber optics :-

- ① Low loss :- Delta to be transmitted over long distance without significant degradation.
- ② Large Transmission capacity.
- ③ Easy Amplification.
- ④ High Bandwidth allowing for transmission of large amounts of data simultaneously.
- ⑤ Immunity to Electromagnetic interference - suitable use in environments with high electrical noise.
- ⑥ Light-weight and Flexible - make easy to install and for various applications.

Electro-optic conversion devices :-

Electro-optic conversion devices, also known as opto-electronics devices. The essential components in optical communication systems. These devices facilitate the conversion of electrical signals to optical signals and vice-versa.



Some commonly used electro-optic conversions devices include -

- ① **LEDs [Light emitting diodes] :-** LEDs are used as light sources in optical communication systems. They convert electrical signal to light signals [infrared or visible spectrum]
- ② **Laser Diodes :-** Laser diodes are another type of light source used in optical communications. They produce coherent & highly focused light, making them suitable for long-distance transmission.
- ③ **photo-diodes :-** photo-diodes are used to convert optical signal back into Electrical signals. They detect and convert light intensity variations into electrical current, allowing for signal detection and processing.
- ④ **modulators :-** Modulators are device that modulate the intensity, phase or frequency of optical signal in response to an electrical signal. These electro-optic conversion device enable the transmission, reception

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and processing of data in optical communication systems. They are integrated into transmitter, receivers and other system components to ensure efficient and reliable communication. Overall, fiber optic and electro-optic conversion device are crucial components in optical communication systems. High-speed, long-distance and secure data transmission in various applications.

EI Upendra Yadav
Jalpaiguri University

Unit - IV

[8 hrs]

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Wave Analyzers and Digital Instruments

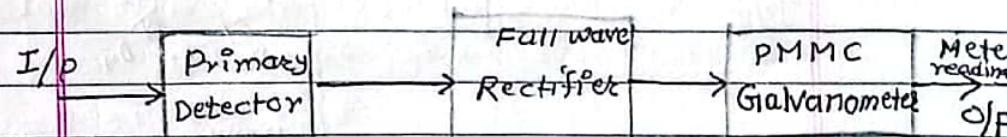
Wave Analyzer :- A wave analyzer is an instruments designed to measure the relative amplitude of signal-frequency components in a complex or distorted waveform. They provided a graphical representation of signal amplitudes versus frequency, known as a spectrum.

It is also called signal analyzer.

Basic-wave Analyzer :-

The Basic-wave analyzer mainly consists of three blocks

- ④ Primary detector :-
- ⑤ Full-wave rectifier :-
- ⑥ PMMC Galvanometer :-



[fig-④ Basic-wave analyzer]

- ④ Primary detector :- It consists of an LC circuit. We can adjust the value of L [inductor] and C [capacitor] in such way that it allows only the

desired harmonic frequency

component that is to be measured.

- (h) Full-wave Rectifiers - It is converted A.C input into D.C output.

- (i) PMMC Galvanometers - It shows the peak value of signal which is obtained at the output of full-wave rectifier.

Now, the corresponding components of above block-diagram, is the basic wave analyzer. So the CKT diagram of basic wave analyzer will shown belows.

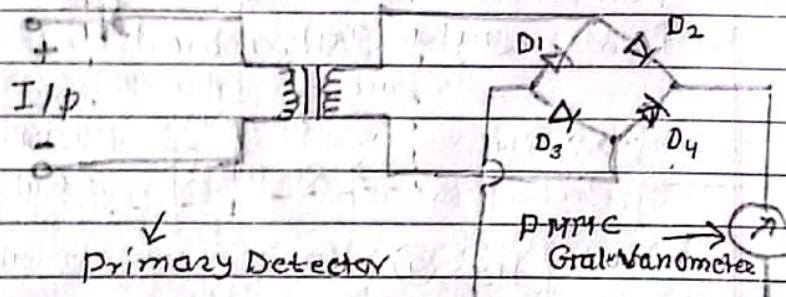


Fig - (a)

This basic wave analyzer can be used for analyzing each and every harmonic frequency

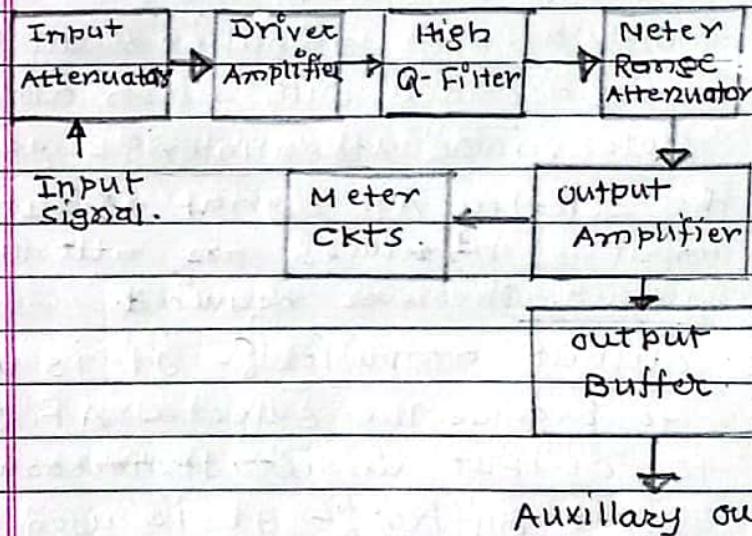
components of periodic signal.

TYPES OF WAVE ANALYZERS

There are two types of wave analyzers, which are given belows.

- ① Frequency selective wave-analyzer -
- ② Heterodyne wave analyzer -
[sub Heterodyne wave analyzer]
- ③ Frequency selective wave analyzer -

The wave analyzer, used for analyzing the signals are of 20 Hz to 20 kHz Audible frequency range



[Fig - (a) Freq. selective wave analyzer]

① Input Attenuator :- The AF signal, which is to be analyzed is applied to input attenuator. If signal amplitude is too large then it can be attenuated by input attenuator.

② Driver Amplifier :- It's amplifies the received signal when it's necessary.

③ High-Q Filter :- It is used to select the desired frequency and rejects unwanted frequencies. It consists of two RC sections and two filter amplifiers all this are cascaded with each other.

④ Meter range Attenuator :- It gets the selected AF signal as an input & produces an attenuated output, whenever required.

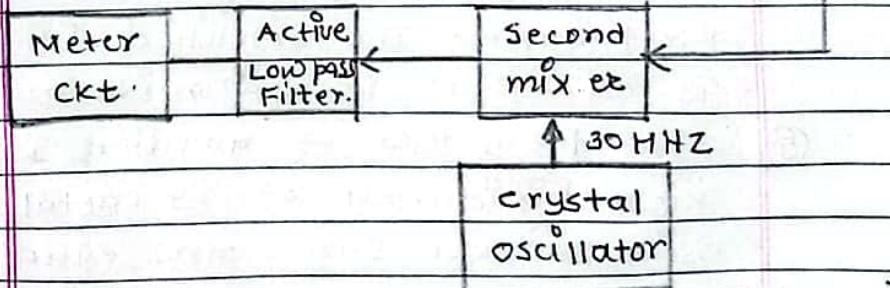
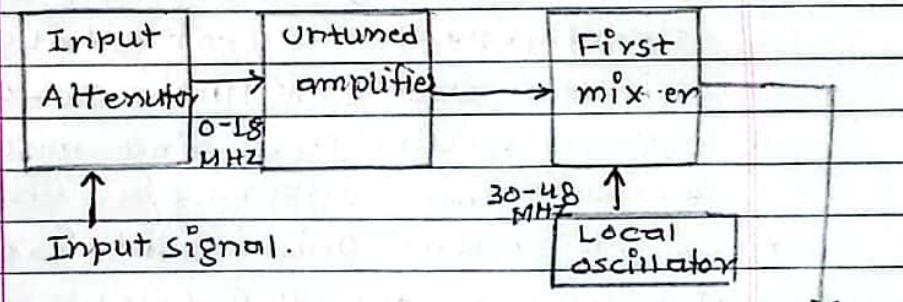
⑤ Output amplifier :- It is used to provide the selected AF signal to output if necessary.

⑥ Output Buffer :- It is used to provide the selected AF signal to output device.

⑦ Meter Ckt :- It displays the reading of selected AF signal. We can choose the meter reading in volt range or decible range.

Heterodyne wave analyzer :-

The wave analyzer, used to analyze the signals of RF range is called superheterodyne wave analyzer or heterodyne wave analyzer.



[fig-⑧ Heterodyne wave analyzer]

working of 1. heterodyne wave analyzer :-

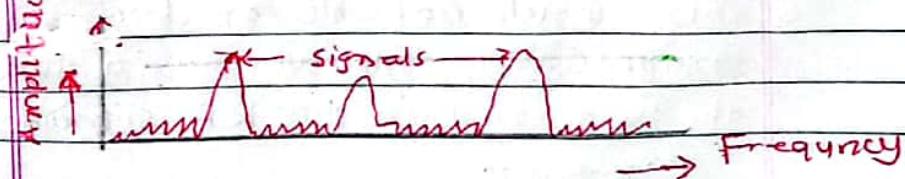
- ① The RF signal, which is given to input attenuator. If the signal amplitude is too large, then it can be attenuated by input attenuator.
- ② Untuned amplifier :- Amplifies the RF signal whenever necessary. It's applied to first mixer.
- ③ The frequency ranges of RF signal and the output of Local oscillator are 0-18 MHz & 30-48 MHz respectively. So First mixer produces an output which frequency of 30 MHz. This is difference of frequencies of the two signals that are applied to it.
- ④ IF amplifier :- The intermediate Frequency [IF] signal, The output of first mixer. The amplified IF signal is applied to second mixer.
- ⑤ The frequencies of amplified IF signal and output of crystal oscillator are same and equal to 30 MHz. So the second mixer

produced ^{an} output which the frequency is 0 MHz. This is difference of frequencies of two signals that are applied to it.

- ⑥ The cut off frequency of active low pass filter is chosen as 1500 Hz. Hence, this filter allows the output signal of second mixer.
- ⑦ Meter ckt display the reading of RF signal. We can choose the meter reading in volt range or decibel range.

Spectrum Analyzer :-

A Spectrum analyzer is a device that measures and displays signal amplitude as it varies by frequency within its frequency range. [Spectrum] : The frequency appears on the horizontal x-axis and the amplitude is displayed on the vertical y-axis. It looks like an oscilloscope and in fact some devices can function as either oscilloscope or spectrum analyzer.



or

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Spectrum analysis of signal.
Spectrum analysis is defined as study of energy distribution across the frequency spectrum of given electrical signal.

- 1) study gives information about band-widths, effects of different types of modulation and spurious signal generation.
2) knowledge of these quantities is useful in the design and testing of radio frequency and pulse circuitry.

Spectrum Analysis

1) Audio frequency analysis - [20Hz - 20KHZ]

2) Radio frequency analysis - [10MHz - 40GHz]

Spectrum analyzers are instruments which capable of portraying graphically amplitude as a function of frequency in the RF

Spectrum

Applications :-

- 1) Measurement of
2) Measurement of FM deviation.
3) Measurement of Frequency in pulse studies.

Types of Spectrum Analyzers :-

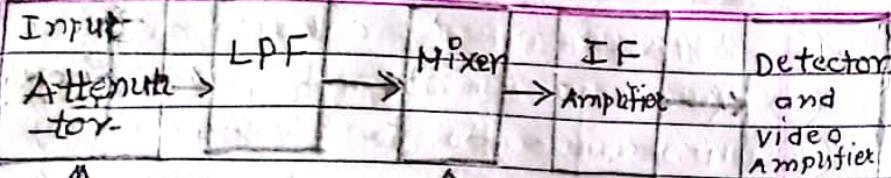
These instruments provide a display of frequency spectrum over a given frequency band. Spectrum analyzers use either a parallel Filter bank or swept frequency technique. So we can classified the spectrum analyzers into following two types :-

- ✓1) Filter Bank spectrum Analyzers -
✓2) swept-tuned or superhet erodyne spectrum Analyzer -

Basic Spectrum Analyzers using Receiver Design :-

The spectrum analyzers used for analyzing the signals are of RF scope is called superheterodyne spectrum analyzer.

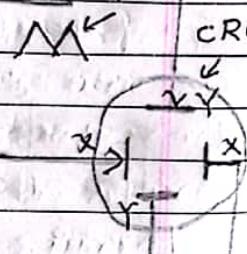
The block diagram is given in fig - ⑨ below.



[fig-6) superheterodyne spectrum analyzer.]

working principles:-

- ① The sweep gen. or sawtooth gen. provided the sawtooth voltage which drives the horizontal axis of CRO; and sawtooth voltage is freq. controlled element of the tuned oscillator. As oscillator sweeps from f_{\min} to f_{\max} of its freq. Band at linear recurring rate. It's beats with the frequency components of the input signal produced an IF.



② The RF signal, which is to be analyzed is applied to input attenuator if the signal amplitude is too large then it can attenuated by an input attenuator.

③ Low pass filter [LPF];- The LPF only sending the less than the cutoff frequency.

④ mixer :- Mixer get the inputs from LPF and voltage tuned oscillator and its produced output. which difference of frequency signals.

⑤ IF amplifier :- Amplifies, the IF signals. The output of mixer, The IF signal is applied to detector.

The output of detector is given to Y-axis of CRO that means vertical deflection plate of CRO; so that CRO displays the freq. Spectrum of RF signals on its CRT screen.

IRF Spectrum Analyzer :-

or

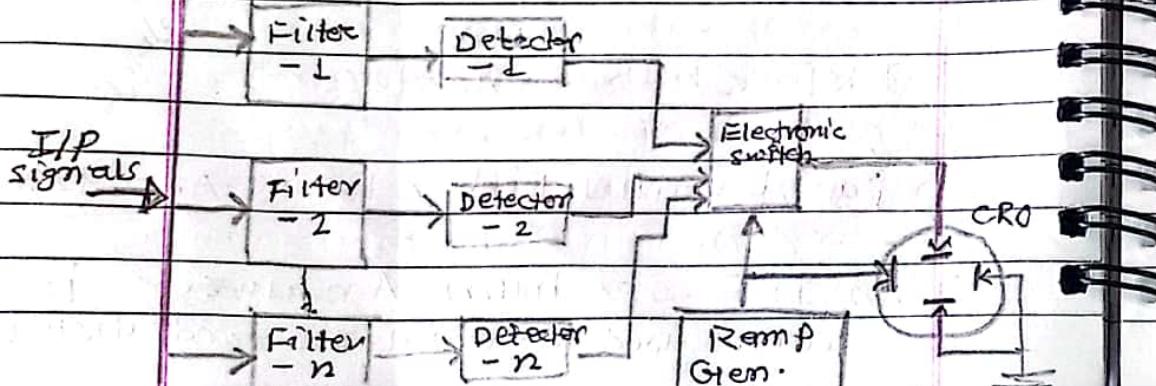
parallel Filter Bank Analyzer

An IRF spectrum Analyzer is device used to analyze and display

Date _____
Page _____

the frequency spectrum of intermediate freq. signals. It commonly used in communication system and electronic equipments for signal analysis and troubleshooting. In parallel filter bank analyzer, the frequency range is covered by a series of filters whose central frequencies and bandwidths are so selected that they overlap each other. The spectrum analyzer used for analyzing the signals are AF range is called filter bank spectrum analyzer or Real time spectrum analyzer because it shows any variation in all input frequencies.

The Block-diagram given below



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Page _____

[Fig-9] Parallel Filter Bank analyzers
working principles of parallel filter Bank analyzers :-

- (1) It is set of Band pass filters and each one is designed for allowing a specific band of frequencies. The output of each band pass filter given to detector.
- (2) All the detector output are connected to the Electronic switch. This electronic switch allows the detector outputs sequentially to the vertical deflection plate of CRO, so CRO displays the frequency spectrum of AF signal on its CRT screen.

Distortion Analyzer :-

Harmonic Distortion Analyzer :-

Fundamental suppression Type :-

Fundamental suppression Types :-

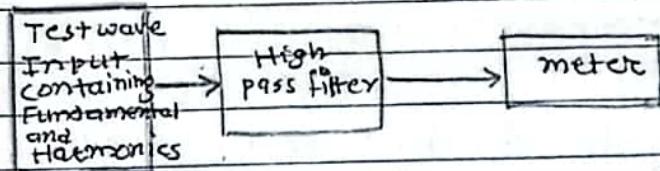
Distortion analyzer measures the total harmonic power present in the test wave rather than the distortion caused by each component

The simplest method of suppress the fundamental freq. by means of high pass filter whose cut-off freq. is a little above the fundamental frequency, Thus the high pass Filter allows only the harmonics to pass and the total harmonic distortion (THD) can measured. The most commonly used harmonic distortion analyzer based on fundamental suppression are as followings given below:-

- ① Using High pass Filter
- ② Resonance Bridge type
- ③ wien's Bridge type
- ④ Bridged-T Network Method
- ⑤ Using High pass Filter

A harmonic Distortion analyzer measured the total harmonic power present in the test wave not distortion caused by each individual components.

The simplest method is to suppress the fundamental frequency by means of high pass filter whose cutoff frequency, is little greater than fundamental frequency hence it falls in stop-band. This high pass filter allows the harmonics which are in pass band, hence the total harmonic distortion can measured.



[fig-⑥) using High pass filter]

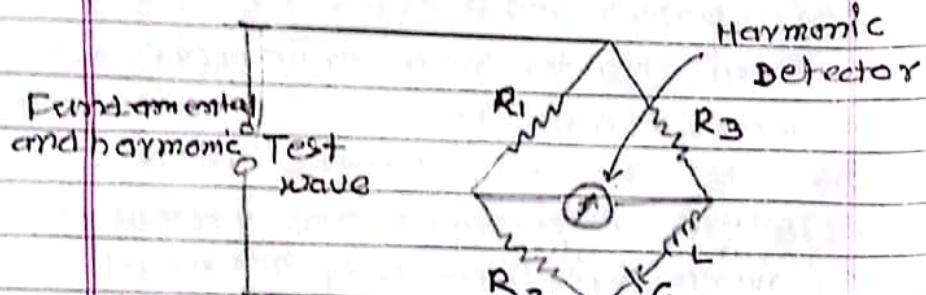
Gain↑

stop Band	pass Band
-----------	-----------

○ fc Frequency →

- ② Resonance Bridge type-
This Bridge ckt is balanced for the fundamental frequency i.e L and C are tuned to the fundamental frequency. The fundamental energy is dissipated in the bridge

ckt elements. The bridge is unbalanced for the harmonics and only harmonics passed through the bridge. Hence the harmonic power is available at the output terminals and can be measured on meter.



[Fig-6) Resonance Bridge ckt]

Two types measurements :-

- (1) Fixed fundamental frequency.
 - (2) Varying fundamental frequency.
- If L and C are fixed components then this method is suitable only when the test wave has fixed frequency.
 - If the fundamental frequency is changed, the bridge must be balanced again.

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Page _____

when a continuous adjustment of

the fundamental frequency is desired a wien bridge arrangement is used.

- Indicators can be thermocouples.
- The meter indicates the r.m.s. value of all harmonics.

(3) Wien's Bridge Method Type :-

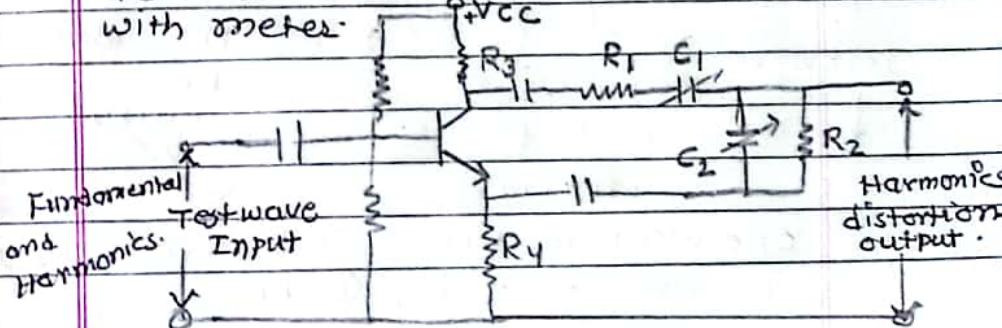
- The Bridge is balanced for Fundamental frequency. For balanced at the fundamental frequency

$$c_1 = c_2 = c$$

$$R_1 = R_2 = R$$

$$R_3 = 2R_4$$

The fundamental energy is dissipated in the bridge ckt elements. The bridge is unbalanced for the harmonics only the harmonic components reach the output terminals. The harmonic distortion output can be measured with meter.



[Fig-④] Wien's Bridge method]

④ Bridge T-Network Method

- L and C_s are turned to fundamental frequency and R is adjusted to bypass fundamental frequency. The tank CKT being tuned to the fundamental freq. the fundamental energy will circulate in the tank and passed by the resistance. only harmonic components will reach the output terminals and the distorted output can be measured by the meter. The Q of the resonant CKT must be at least 3-5

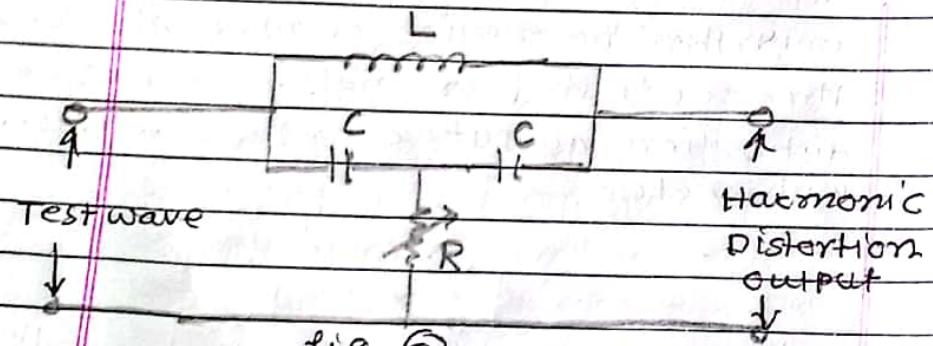


Fig-④

One way of using a bridge T-N/W is shown in Fig-⑤ below.

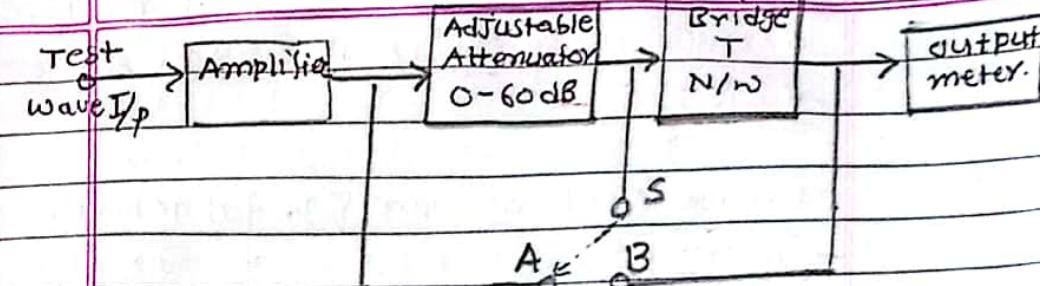


Fig-⑤ Harmonic Distortion Analyzer using Bridge T-N/W method]

The switch is first connected to point A so that the attenuator is excluded and the Bridge T-N/W is adjusted for suppression of the fundamental frequency i.e. minimum output. The minⁿ output indicates that the bridge T-Network is tuned to the fundamental frequency. The switch is next connected to terminals - B i.e. the Bridge T-N/W is excluded or bypassed. The attenuation is adjusted until the same reading is obtained on the meter. The attenuator reading indicates the total r.m.s distortion.

Measurements of frequency

and Time & Decimal count Assemblies

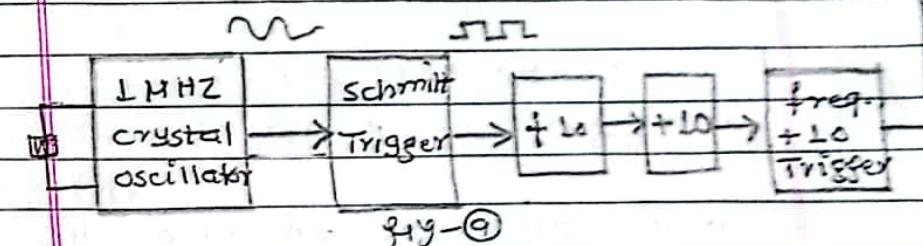
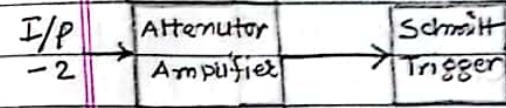
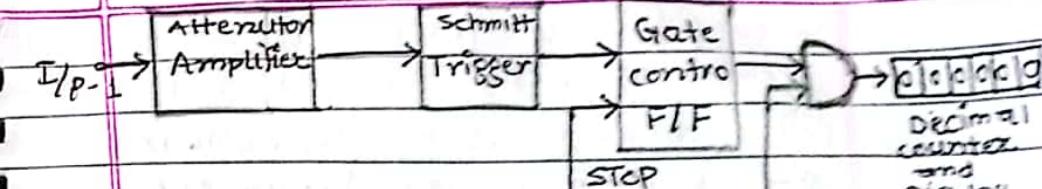
Measurement of Time / period Measure- ment

In some cases it is necessary for digital measurement of Time rather than Frequency.

This is especially true in the measurement of frequency in the low frequency range. To obtain good accuracy at low

frequency, we should measurements of the period, rather than make direct frequency measurements. The CKT used for measuring frequency can be used for measurement of time period if the counted signal and gating signal are interchanged.

The fig-@ given below.



The above fig-@ shows that the CKT for digital measurements of Time period. The gating signal is derived from the unknown input signal which now controls the enabling and disabling of the main gate. The number of pulses which occur during one period of the unknown signal are counted and displayed by the decade counting assemblies. The disadvantage of this system is measuring the low freq. range, The operation has to calculate the freq. From the

time by using eqn

$$f = \frac{1}{T}$$

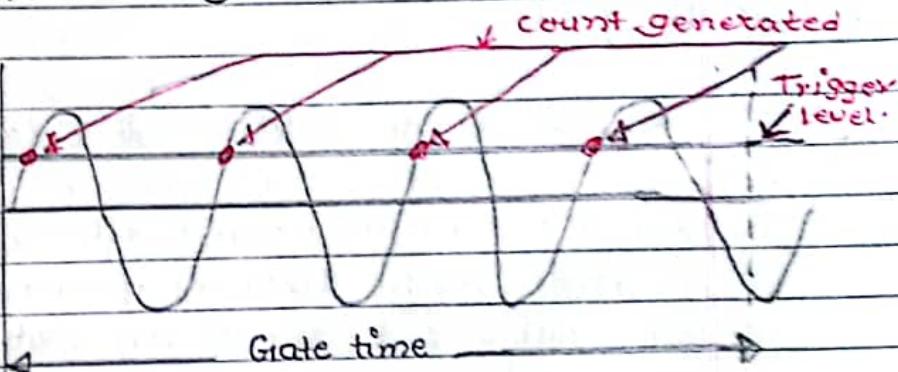
For example:- when measuring the period 60 Hz freq. the electronic counter might display 16.6673 ms

$$f = \frac{1}{T} = \frac{1}{16.6673 \times 10^{-3}} \\ = 59.9977 \text{ Hz.}$$

Frequency Counting

A freq. counter is an electronic instruments used to measure freq. and Time. freq. counters are used for a wide range of freq. and time measurements and display many digits of accuracy. freq. counter helps measure the time of repeated digital signals and the freq. correctly and associates with wide range of radio frequencies. In other words, These are essential instruments that count the number of cycle per seconds of an input signal.

These are used in electronics and Telecommunication industries to measure freq., Bandwidth, peak-to-peak voltage or current or rise time. Freq. counters count the pulses and Transfers them into Freq. counter when the number of pulses or events occur in a period and display it on the freq. range of vibrations. The counter then set to zero. freq. counter are often found in-built into other devices such as radio receivers, radar set, and Test equipments. It is device that is easy to use, measures the freq. accurately and display it digitally.



[Fig-④ Basic concept of a freq counter]

Block diagram and working

The freq. counter Block diagram contains input signal, input conditioning, and threshold AND gate, counter or latch, Latch, accurate timebase or clock, decade dividers, flip-Flop and display.

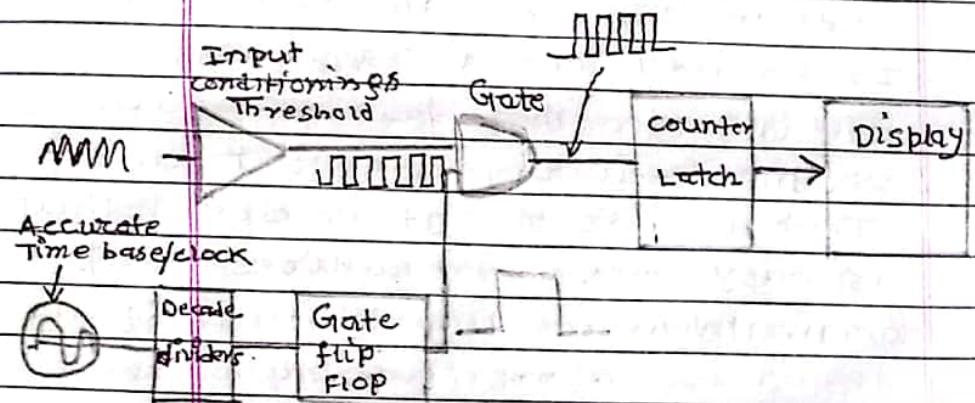


fig- ⑥ Freq. counter Block diagram]

- ① A Freq. counter measures a signal in the first split into the pulse setting. It operates by counting the number of times the signal passes

through the voltage point to a

trigger point in a duration.

- ② The Trigger of freq. counters start at zero crossing point automatically. It is a device that sets in a clock speed with pulse per unit cycle and the pulses present sent to the device for a limited time.

- ③ After this, vibrations / pulses apply in a definite interval of time, counts the pulse.

- ④ An electric counter does the whole process and the pulses are sent to the cycle to represent the unidentified signal and give it a value.

- ⑤ The freq. counter works on two modes to generate the pulses and -time delay.

- ⑥ whenever we talk about the working of the freq. counter, then the pulse in this device generates from the wave gen. or microcontrollers. The timer in this device figures as a counter.

- ⑦ It sets the count of the pulses

From high and low. The final count of the pulses takes place.

- ⑧ The device that converts the resultant by multiplying it by ten frequency cycles per seconds converts the value of the pulses in Hz. After the whole calculation inside the freq. counter, The freq. of the pulses becomes visible on the LCD or LED.

Types of Frequency counter :-

① Bench Freq. counter :-

② PXI Freq. Counter :-

③ Handheld Freq. counter :-

④ Panel Meter :-

① Bench freq. counter :-

Bench freq. counter is a type of device that useful in applying electronic test equipment. It measures the period and equal freq. precisely. This device is also known as for reducing measurement error due to temp drift.

This device used in electronic

Lab and electronic project, for measuring freq. of a periodic signals.

② PXI freq. counter :-

PXI freq. counter is helpful in the control system and track system for tests. It is measuring the freq. and phase of an input signal as per refⁿ signal. Its application is mainly in audio, video, and RF signal. For different application of the device is testing microwave ckt, wireless devices and antennas.

③ Handheld freq. counter :-

A handheld freq. counter helps measure the freq. of cycles per seconds of a periodic waveform in the signal. It provides precise measurement of output applications of this device to measures the radio-frequencies.

④ Panel Meter :- A panel meter

is type of freq. counter available in panel mount mode. Its applications in determining the freq. of audio and radio-signals.

Error - 1 :-

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Page _____

Counter Error and Signal

Related Errors -

- ① A counter error typically indicates a problem or discrepancy in the freq. or time measurement performed by the wave analyzer's counter.

The counter is responsible for counting the number of cycles or events within a given time period.

- ② A counter error might occur if there are issues with the counter circuitry, calibration or if the signal being measured is outside the counter's measurements range. In such cases, the accuracy and reliability of the freq. or time measurement may be compromised.

- ③ A signal-related error generally refers to an error or issue related to the input signal being analyzed by the wave analyzer.

This can include various factors, such as noise, distortion, interference or improper signal conditioning. Signal related errors can affect the accuracy of measurements and distort the waveform being analyzed and leading to incorrect or unreliable results.

Digital Voltmeter :-

Digital voltmeter displays the voltage reading of a circuit numerically which used to measure the electrical P.d betⁿ two points in the ckt. The block diagram of digital voltmeter given below.

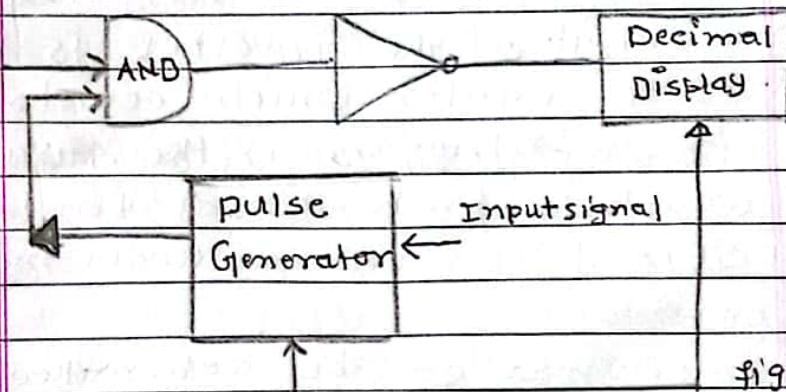


Fig - ④

Working principle of DVM :-

unknown voltage signal is fed to

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Page _____

the pulse gen. which generates a pulse whose width proportional to the input signals.

② output of pulse gen. is Fed to one-Leg of the AND gate.

③ The input signal to other Leg of the gate is a train of pulse.

④ output of AND gate is positive triggered train of duration same as the width of the pulse generated by the pulse generator.

⑤ This positive Triggered train is Fed to the inverter which converts it into a negative triggered train.

⑥ output of the inverter is fed to a counter which counts the no's of triggers in the duration which is proportional to the input signal ie voltage under measurements.

⑦ counter can be calibrated to indicate the voltage (V) directly.

The working of digital voltmeter that it is nothing but analog to digital converter which converts analog signal into a train of pulses. The digital voltmeter to A/D conversion methods.



Fig-④

Digital voltmeters can be classified into following methods which are given below:-

- ① Ramp type digital voltmeter :-
- ② Integrating type digital voltmeter :-
- ③ Servo potentiometer type digital voltmeter :-
- ④ Successive Approximation type digital voltmeter :-

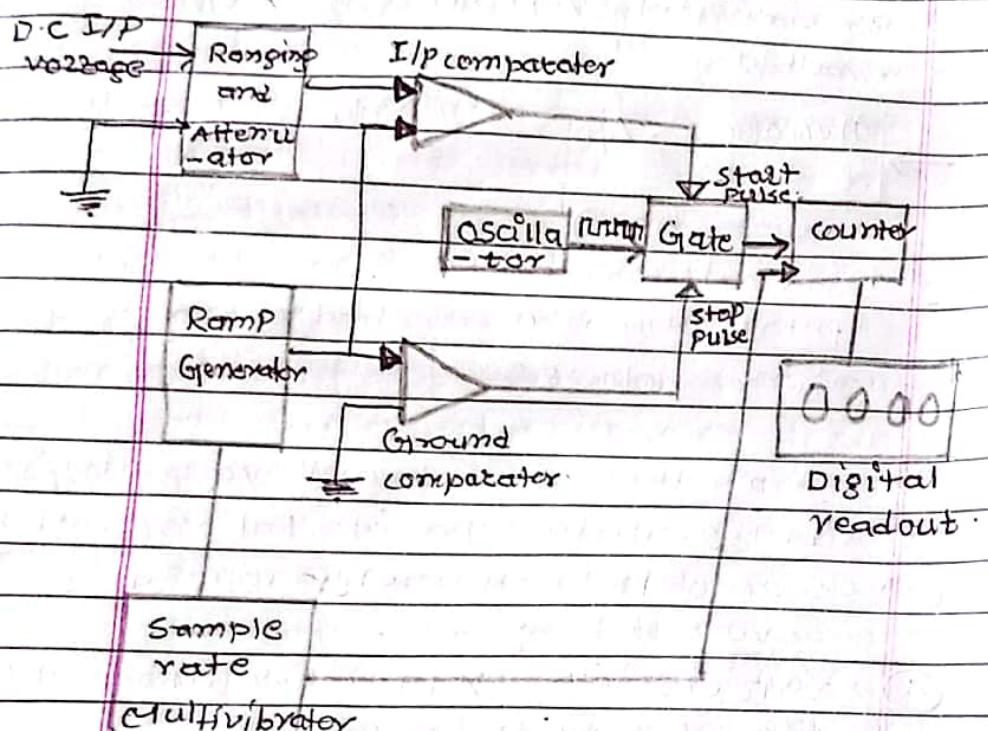
① Ramp type digital voltmeter :-

The Ramp type DVM, the operation basically depends on the measurements of time. The time which the ramp voltage takes to change from the level of the

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Input voltage to that 0(zero) voltage or vice-versa.

The block diagram and operating principle of a ramp-type DVM :-



[fig-⑥] Ramp-type DVM] .

It shows the "voltage-to-time" conversion using gated clock pulses. At starting of cycle, a ramp voltage is initiated; this voltage positive going to negative going. The negative going

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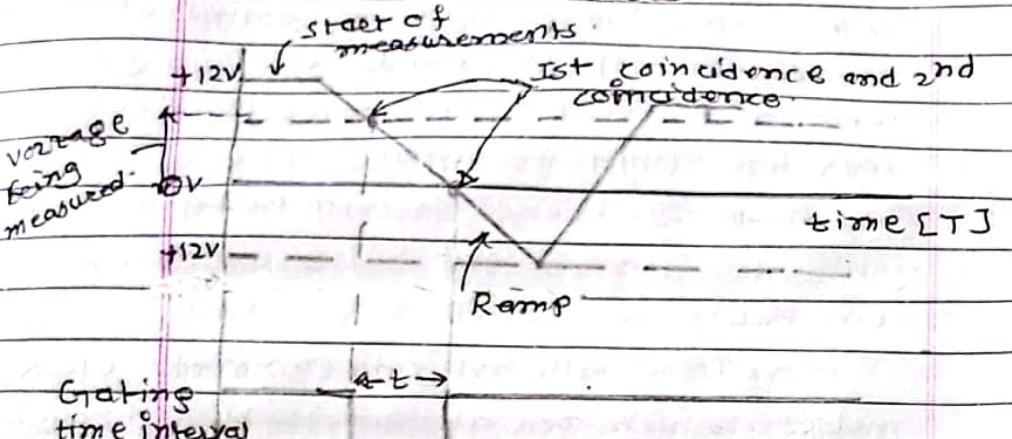
ramp continuously compared with unknown input voltage. At the instant that ramp voltage equals the unknown voltage, a coincidence ckt or comparator generates a pulse which opens a gate. the ramp voltage continues to decrease with time until it finally reaches 0 V [ground pt.] and second comparator generates an output pulse which closes the gate.

An oscillator generates clock pulses which are allowed to pass through the gate to a number of decade counting unit (DCUs). which totalise the number of pulses passed through the gate. The decimal number, displayed by indicator tubes associated with the DCUs is a measure of the magnitude of the input voltage.

The sample-rate multivibrator [MV] determine the rate at which the measurements cycle are initiated. The sample-rate ckt provided an initiating pulse for the ramp generator to start its next ramp voltage. At the same time a reset pulse is generated which return all

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DCUs to their zero state, by removing the & display momentarily from the indicator tubes.



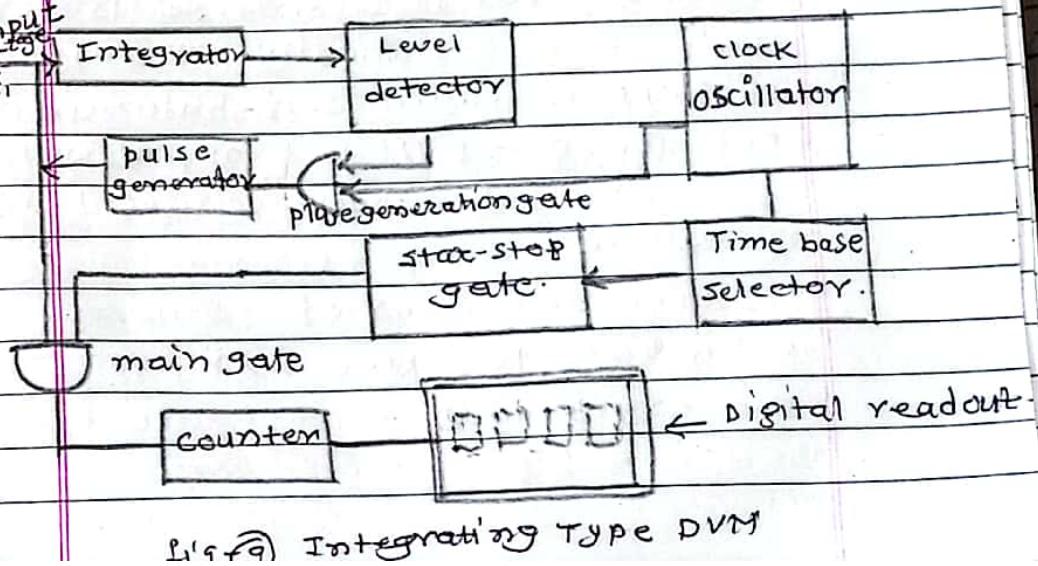
clock pulse
to
counter

[fig-④ voltage to time conversion using gate clock pulse.]

② Integrating Type Digital Voltmeter ② - [voltage-to-freq. conversion]

This voltmeter indicates the true avg. value of the unknown voltage over a fixed measuring

period. It employs an "integration technique" which uses a voltage-to-freq (V/F) conversion. The V/F converter function as a feedback control system which governs the rate of pulse generation in proportion to magnitude of the input voltage. In voltage to freq. conversion Technique, a train of pulses is generated. The freq. of pulse depends on the voltage being measured. These pulse are counted that appears in a definite time interval. After all, the freq. of pulses is a function of input voltage, the number of pulses is an indication of the input voltage.

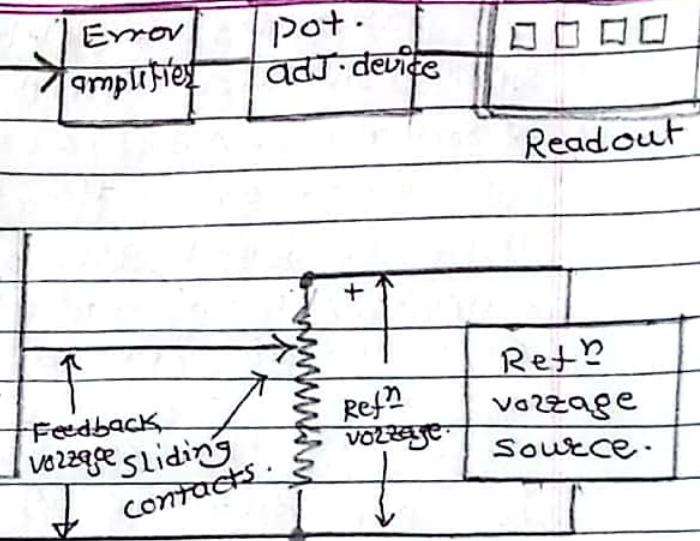


③ Servo potentiometer type DVM

A potentiometer type DVM employs voltage comparison technique. In this DVM the unknown voltage is compared with refⁿ voltage whose value is fixed by setting of the calibrated pot. The pot. setting is changed to obtain balanced. When null conditions are obtained the value of the unknown voltage, is indicated by the dial setting of the potentiometer in pot-type DVM.

The balanced is not obtained manually but is arrived at automatically. This type of DVM is called self-balancing pot. The pot. DVM is provided with readout which displays ^{the} voltage being measured.

Block diagram of servo potentiometer type DVM is given below.



[fig-@ servo.pot. type DVM]

④ Successive-Approximation Type DVM

A digital to Analog [D/A] converter is used to provide the estimate. The "equal to or greater than or less than" decision is made by the comparator. The D/A converter provides the estimate and is compared to the input signal. The special shift register called a successive-approximation register [SAR] is used to control the D/A

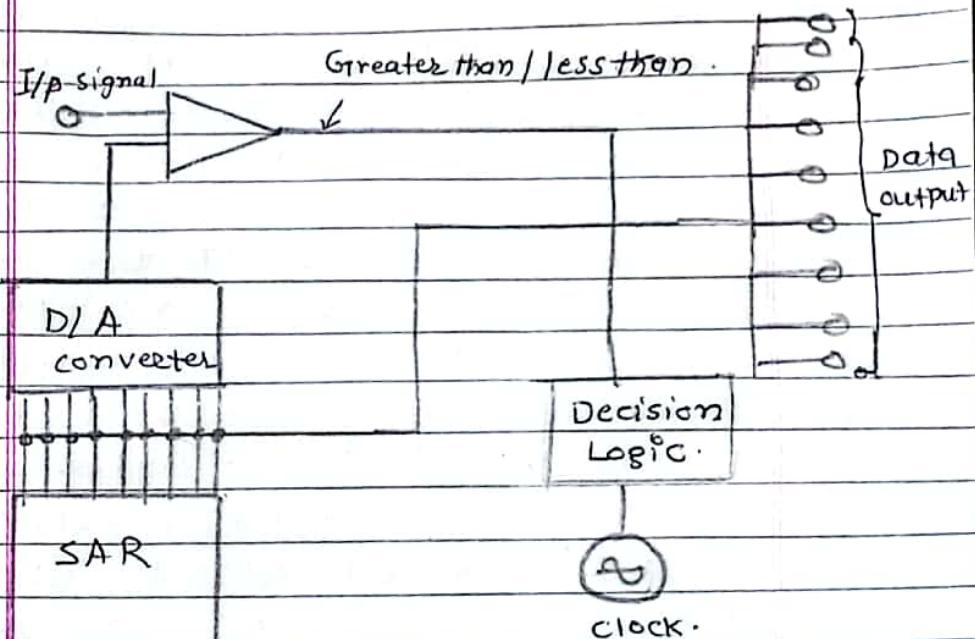
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converter and consequently the estimates. At the beginning of the conversion all the outputs from the SAR are at logic zero. If estimate is greater than the input, the comparator output is high and the first SAR output reverse state and second output changes logic 'one', if the comparator output is low, indicating that the estimate is lower than the input signal the first - output remains in the logic one state and second output assumes the logic one state. This continues to all the states until the conversion is complete.

The above sequence of events are performed electronically. For an N-bit conversion after N clocks, the actual value of the input is known. The least significant bit is the state of the comparator. In some

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- system an additional clock is employed to store the last bit in SAR and thus $N+1$ clocks are required for a conversion.



[fig-④] successive-approximation DVM

vector voltmeter - A vector voltmeter is specialized instruments used for measuring complex voltage quantities particularly in RF [Radio frequency] and

and microwave systems.

It is designed to measure both the magnitude and phase i.e. of complex voltage or vector quantity like a std. voltmeter that measures only the amplitude of voltage signal, a vector voltmeter provides additional information about the phase or angle of the voltage.

This is particularly useful in applications where the phase relationship bet" multiple voltage signal is critical, such as in RF signal analysis.

NW analysis or impedance measurements.

The vector voltmeter typically consists of two channels, an in-phase channel and quadrature channel. The phase channel measure the real or resistive component of voltage.

while quadrature channel measure

the Imaginary or reactive component of the voltage.

By processing the measurements from both channels,

the vector voltmeter can calculate the magnitude and phase angle of complex voltage. It can display these values in various formats, such as polar coordinates [magnitude and angle] or rectangular coordinates [real or imaginary components].

vector voltmeter are commonly used in RF and micro-wave Engg for task like impedance matching, Network analysis, phase measurements and evaluating the performance of RF system.

Digital Multimeter [DMM] :-

A digital multimeter [DMM] is a test tool which measure

two or more electrical components like voltage (V), current [amp] and resistance [Ω or ohm]. It is std. tool for Electrical and Electronic measurement systems.

Digital multimeter combine the testing capability of single-task meters - The voltage (V) measure for Volts, ammeter (amp) and ohmmeter [ohms]. Advanced options also available in DMM.

DMM include components:-

- ① Display :-
- ② Buttons :-
- ③ Dial [Rotary switch] :-
- ④ Input Jacks :-

① Display :- Read out the value.

② Buttons :- selects the various functions.

③ Dial [Rotary switch] :- For selecting primary

Measurement values [Volts,amps, ohms].

④ Input Jacks :- where the test leads are inserted. Two leads [one for Red [Positive] other for Black [Negative]] that plug into the DMM. The probe tips on each lead are used for testing Ckt.

Computer Based Digital Instruments

IEEE-488 (GPIB) instruments

Now a days automatic test equipments is one of the leading method for testing electronic equipments in factory productions and troubleshooting situations. The basic method is to be use a programmable digital computer to control a bank of test instruments. The bank of instruments can be configured for a purpose or for general use.

The IEEE-488 bus or General purpose interface bus [GPIB] is a tool that is based on the

IEEE [Institution of Electrical
and Electronics Engineers] specifica-
tion.

The digital signals on
IEEE-488 bus are generally
similar to TTL [Transistor -
Transistor Logic] ie logic
Low is less than 0.8 V and
logic High is greater than 2.0 V.

The digital signal can be
connected to the instruments through
a multiconductor cable upto 20
meters in length provided that an
instruments load is placed every
2-meter.

Most IEEE-488 bus/GPIB
systems operate unrestricted to
250 bytes per seconds or
faster with some restrictions.

There are two basic config-
uration for the IEEE-488/GPIB
system.

- ① Linear -
- ② Star -

① Linear - In this type of
configurations :

a tap-off to next instrumen-
ts is taken from the previous one
in series.

② Star - In this case, the instru-
ments are connected from a
central point.

The Basic structure of IEEE-488
/ GPIB system. The figure indicates
the following four different devices

- ① computer
- ② Frequency counter
- ③ signal generator
- ④ digital multimeter

The IEEE-488 / GPIB system itself
consists of three major buses -

① General Interface Management [GIM]
bus - This bus co-ordinates the whole
system and ensures an orderly
flow of data over data I/O/p
[DIO] bus.

② Data I/O [DIO] bus - This bus

is a bi-directional 8-bit data bus that carries data interface message and device dependent message betⁿ the controllers, talkers and listeners. This bus sends asynchronously in byte-serial format.

(3) Data Byte Transfer [DBT]

bus^o - This bus controls the sending of data along the DIO bus.

- The signal defined for above three buses are implemented as conductors in a system interface cable.
- Each IEEE - 488/GPIB system [ie computer, freq- counter, signal generator and digital multimeter] are categorised as

① controller^o-

② Listener^o-

③ Talker^o-

④ controller^o - It is function

is to communicate device addresses and other info to buses to instruments in the system.

⑤ Listener^o - It is function to receive commands from the other instruments [usually the controller] when the correct address is placed on the bus. The listener acts on the message received but does not send back any data to the controller.

⑥ Talker^o - It is function to respond to the message sent to it by the controller.

• VXI bus is another widely growing platform for instrumentation systems.

The VXI back plane include the 32-bit VME data bus high performance instrumentation buses for precision timing and synchronisation betⁿ instrument components.

Block diagram is given below:-

Talker
Listener
Talker and Listener computer

Talker
Freq. counter

Listener
Signal generator

Talker
Listener
Digital multimeter

General Interface management

Data-Byte Transfer control Bus

Data Input output Buses
Basic structure of IEEE - 488 / GPIB system

[Fig @]

Unit - VI

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[5 hrs]

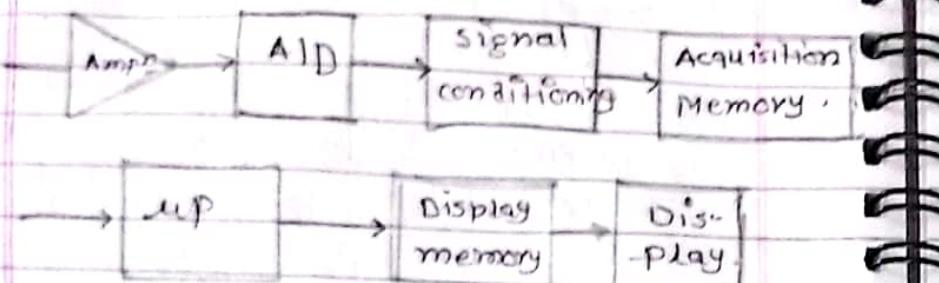
Recorders, Displays and Storage Devices

Fundamentals of cathode ray oscilloscope

An oscilloscope is a laboratory instrument commonly used to display and analyze the waveform of electronic signals. In effect, the device draws a graph of the instantaneous signal voltage as a function of time. Engineers use oscilloscopes to measure phenomena and solve the measurements challenges quickly and accurately to verify their designs or confirm that a sensor is working properly. There are three primary oscilloscope systems which are given below.

- Vertical system.
- Horizontal system.
- Trigger system.

All these systems provide information about the electrical signal. So the oscilloscope can accurately reconstruct it. The oscilloscope Block diagrams given below.



[fig-@ Block-diagram of oscilloscope]

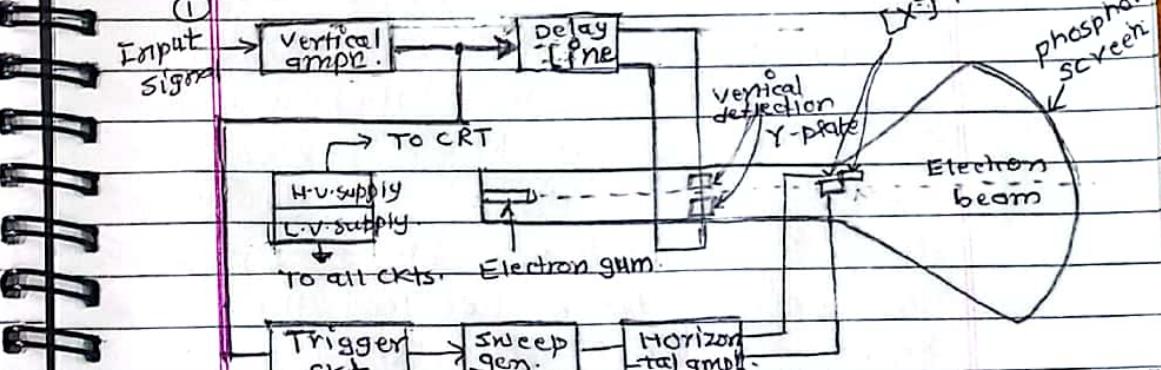
The first stage attenuates or amplifies the signal voltage in order to optimize the amplitude of the signal. This is known as a vertical system since it depends on the vertical scale control. where A/D converter used to sample the signal voltage and converted it in a digital format value. The horizontal system, which contains a sample clock, gives each voltage sample a precise time [Horizontal coordinates]. The sample clock drives the A/D converter its digital output is stored in the acquisition memory as records points. The trigger system detects

a user-specified condition in the incoming signal stream and applies it as a time reference in the waveform record. The event that met the trigger criteria is displayed.

Block diagram of oscilloscope :-

Block diagram of general-purpose oscilloscope [CRO]. It consists of the following major systems:-

- ① Cathode Ray tube [CRT] :-
- ② Vertical amplifiers :-
- ③ Horizontal amplifiers :-
- ④ Sweep and Time base generator :-
- ⑤ Trigger circuit :-
- ⑥ High and Low voltage supply :-
- ⑦ Delay line :-



[fig-@ Block-diagram of a general purpose CRT]

① cathode ray tube [CRT] :-

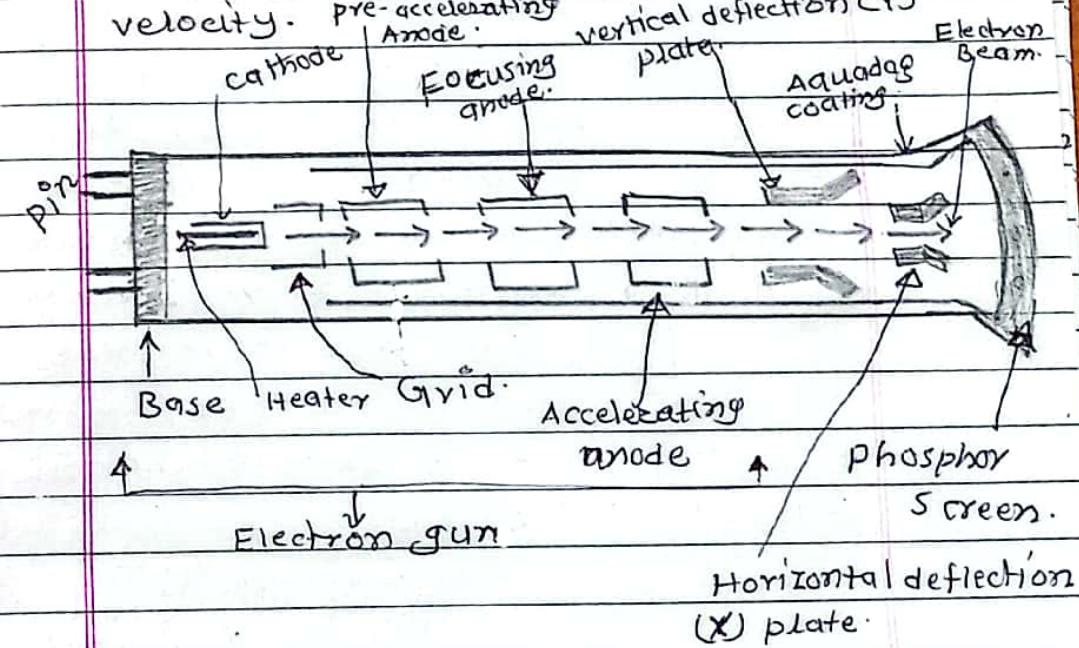
A cathode ray oscilloscope consists of a cathode ray tube [CRT] which is heart of the oscilloscope and some additional circuit. It tries to operate the CRT. The main part of CRT are

- ④ Electron gun assembly:-
- ⑤ Deflection plate assembly:-
- ⑥ Fluorescent screen:-
- ⑦ Glass envelope:-

The electron gun assembly produces a sharply focussed beam of electrons which accelerated to high velocity. This focussed beam of electrons strikes the fluorescent screen with sufficient energy to cause a luminous spot on the screen.

After leaving the electron gun, the electron beam passes through two pairs of electrostatic deflection plates. Voltage applied to these plates deflect the beam. Voltage applied to one pair of plates move the beam

vertically up and down and the voltage applied to the other pair of plates move the beam horizontally from one side to another. Focusing mode is used to focus the beam on the screen and the accelerating mode makes the electron beam to move with high velocity. pre-accelerating vertical deflection (Y) plate.



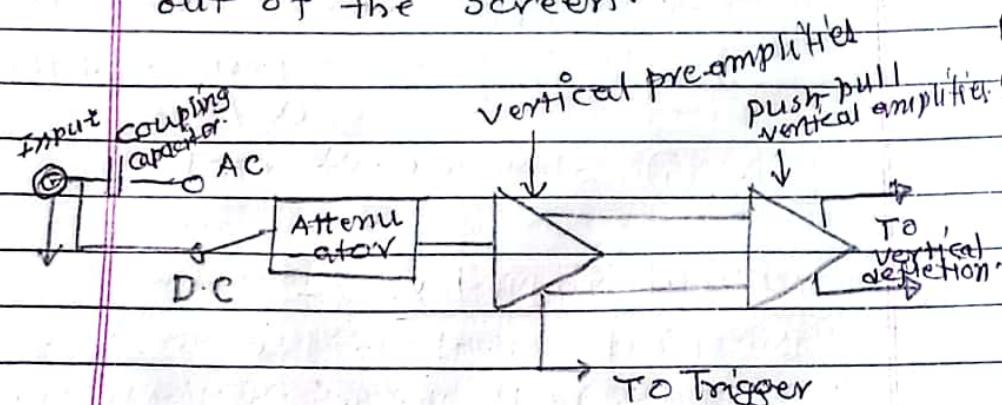
[fig-① structure of CRT.]

② vertical amplifiers :-

The input signal is fed to Y-deflection plate through a vertical amplifier.

The function of vertical ampⁿ is to amplify the input signal. It may consist of several stages in cascade and generally, operates with fixed gain. The output stage of this ampⁿ is invariably a push-pull amplifier. It is necessary to adjust the magnitude of the input signal so that a

large signal may not cause to the display to go out of screen. Therefore, the input signal is fed to the vertical amplifier through an attenuator. The attenuator is a network of resistances and capacitances and it reduces the input signal so that the display of large input signals may not go out of the screen.



[fig-⑨ Block diagram of CRT of vertical section of oscilloscope.]

The input converter feeds an input attenuator, after which follows the vertical amplifier. The input impedance of an oscilloscope is rather high being on the order of $1 \text{ M}\Omega$, which is desirable for measuring voltage in high impedance ckt. The attenuator set the sensitivity of the oscilloscope in the common 1-2-5 sequence i.e. the input attenuator could provide 10, 20, 100, 200 mv etc per centimetre. Practically, all oscilloscopes provided a switchable input coupling capacitor. This is provided so that measurements of A-C.

signals may be viewed in the presence of high D-C voltage by including the coupling capacitor. When D-C measurements are to be made, the capacitor may be removed. The value of the capacitor is chosen so that the frequency response of all the oscilloscope is preserved down to a few Hz.

The vertical ampⁿ is the principal factor in determining the sensitivity and Bandwidth of an oscilloscope.

The gain of the vertical ampⁿ

ampⁿ determines the smallest signal that the oscilloscope can satisfactorily reproduce on the CRT screen. The sensitivity of an oscilloscope is directly proportional to gain of the vertical ampⁿ that is as gain increase sensitivity increased which allows us to observe small ex-signal amplitude. The vertical sensitivity of an oscilloscope is deflection factor that can be selected with rotary switch. The band width of an oscilloscope is determine the range of frequencies that can be observed by the instruments.

③ Horizontal amplifiers

The function of the horizontal ampⁿ is to amplify the signal applied to the X-plate. The amplifier consists of several stages in the cascade.

For measurement of voltage and currents

the input to the horizontal ampⁿ is a sawtooth wave for frequency and phase measurements, the input is an external signal. The horizontal ampⁿ should be capable of amplifying both these inputs without distortion.

④ Time Base or sweep generator

If horizontal deflection plate x-plate are not energised, the electron beam would draw a vertical line on the screen. The function of time base generator is to drive the beam at a steady speed across the screen and when it is reached the right-hand side of the screen, the beam is made to fly back to the starting position on the left hand side of screen. The time base generator produce the saw-tooth wave which are given below, then of the same freq. as the input signal to the Y-plate. The horizontal deflection of the beam is proportional to the instantaneous voltage.

of saw-tooth wave. The beam sweeps at a uniform rate across the screen in horizontal direction. running to the starting position almost instantaneously and sweeping again.

The time base generator gives an output sweep signal of adjustable freq. so that the Y- input signals of a wide range of frequency may be displayed on the screen. No need of external input to the X- plates.

V_H

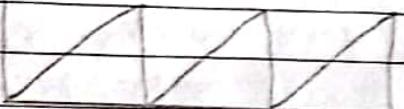


Fig-@ saw-tooth wave
four types of sweeps:-

- ① Free running sweep
- ② Triggered sweep
- ③ Driven sweep
- ④ Non-saw-tooth sweep.

Delay line -

It is used to delay the signal from some time in vertical section. comparing the vertical and horizontal deflection ckt in the oscilloscope block diagram - we observe that the deflection signals is initiated or triggered by a portion of the output signal applied to the vertical CRT plates. signal processing in the horizontal channel consists of generating and shaping a trigger pulse that starts the sweep generator, whose output is fed to horizontal deflection plate. This whole process takes time on the order of 80ns. To allow the operator to observe the leading edge of the signal waveform, the signal drive for the vertical CRT plates must therefore be delayed by atleast the same amount of time. This is function of time delay line.

CRO probes - The CRO probe performs the very important

Functions of connecting the test ckt to the oscilloscope with altering and loading. There are three different probes which are given below.

- (1) Direct Reading probe :-
- (2) ckt Isolation probe :-
- (3) Detector probe :-

(1) Direct Reading probe :-

This probe is the simplest all probes and it uses a shielded coaxial cable. It avoids stray pickups which may lead to problems when low level signals are being measured. It is usually used for low freq. and low impedance ckt. However in using the shielded probe,

(b) ckt Isolation probe :- ckt

Isolation probe is used in order to avoid the undesirable ckt loading effect of shielded probe. Isolation probe which is used alone

with the capacitive voltage divider, decreases the input capacitance and increases the input resistance of the oscilloscope. This way the loading effects are drastically reduced.

- (3) Detector probe :- - when analyzing the response to modulated signals in communication equipments like AM, FM and TV receivers, the detector probe functions to separate the lower freq. modulation components from higher freq carrier. The amplitude of modulator carrier is displayed on the oscilloscope by rectifying and bypassing action.

specification of an oscilloscope :-

- (1) Bandwidth :-
- (2) Sample Rate :-
- (3) Memory size/ Depth :-
- (4) Rise Time :-
- (5) channels :-
- (6) Trigger :-

① Bandwidth :- probably the most important specification of an oscilloscope is its bandwidth. As the freq of the signal gets closer to the maxm freq. that the oscilloscope can work with its accuracy drops.

② Sample rate :- The sample rate is the number of samples that the oscilloscope is capable of capturing per second. The higher sample rates require more and faster memory to store.

③ Memory size / Depth :- Very closely related to the oscilloscope sample rate is its memory size. It stores the waveform data in its memory.

④ Rise time :- The rise time of an oscilloscope describes the ability of the instrument to detect and

capture rapidly rising and falling signals.

⑤ Channels :- Each channel has a separate connector where you can attach a probe and through this probe to monitor a signal. Oscilloscopes having 2 or 4 - channels.

⑥ Trigger :- The trigger of an oscilloscope is fundamental to its operation. The trigger is the mechanism through which the oscilloscope can recognise a specific attribute of the input signal.

Oscilloscope Measurement Techniques

① Voltage Measurements :-

② Time Measurements :-

① Voltage Measurements :-

Voltage is the amount of electric potential, expressed in volts, usually one of these points is ground zero volts but not always. Voltages can be measured from peak-to-peak i.e., maxm point of a signal to its minm point.

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$$I = \frac{V \text{ [measured on CRO]}}{R}$$

① Peak to peak value $V_{p-p} = \frac{\text{volts}}{\text{div}} \times \text{no. of div.}$

② Amplitude (V_{max}) = $\frac{V_{p-p}}{2}$.

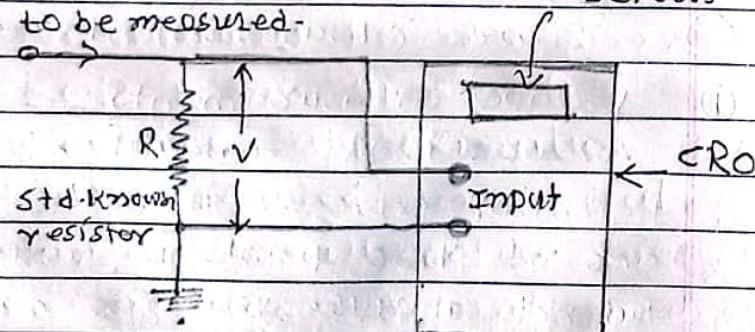
③ R.M.S. value [$V_{r.m.s.}$] $V_{r.m.s.} = \frac{V_{p-p}}{2\sqrt{2}}$

[For sinusoidal signals only]

2 Current Measurements

A CRO has very high input impedance and cannot be used for direct measurements of current, however the current can be measured in terms of voltage drop across a std. resistance.

Current to be measured -



[Fig-② current measurements.]

For measurement of high freq. currents, the shunt should be non-inductive.

Special oscilloscopes -

① Digital storage oscilloscopes :-

② Sampling oscilloscopes :-

① Digital storage oscilloscopes :-

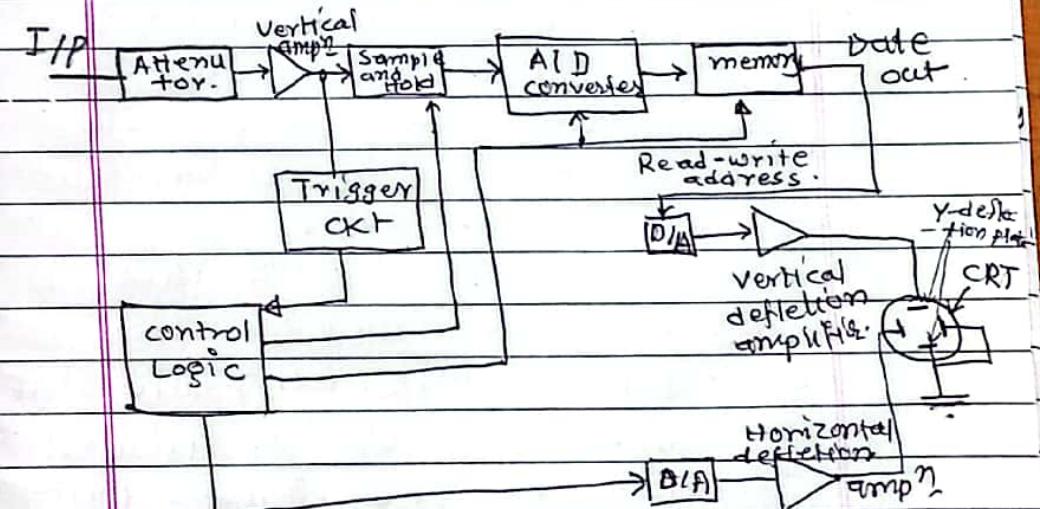
The digital storage oscilloscopes is a superior method of trace storage. In this technique, the waveform to be stored is digitized, stored in a digital memory and retrieved for display on the storage oscilloscope. The stored display can be displayed indefinitely as long as power is applied to the memory, which can be supplied with small battery.

The block diagram of a digital storage oscilloscope is given below.

The input is amplified and attenuated with input amplifiers. The digital storage oscilloscope uses the same types of input circuitry and oscilloscope probes as conventional oscilloscopes, and many digital storage oscilloscopes can operate in a conventional mode bypassing the digitizing and storing feature. The output of the input signal amplifier feeds an A/D converter. The main measurement of A/D converter is its speed, while accuracy and resolution are of secondary importance.

The digitized output is in the binary form and not BCD form. Generally, successive approximations type of ADC is used in digital storage oscilloscope. Digitizing the analog signal means to take samples of the input signal at periodic interval of time. The sampling

rate should be greater than twice the higher freq. present in the input signal so that Sampling theory is satisfied. The selection of the sampling rate and memory size depends on the type of waveform being recorded. An analog signal is sampled at suitable rates. A continuous storage oscilloscope consists of a feature called Pretrigger view. This indicates that the events that occurred before the trigger input signal was applied, are displayed. This selection is a % selection. This mode is useful when failure occurs.



[fig-④ Digital storage oscilloscope]

The digital storage oscilloscope

has the following three modes of operation.

- ① Roll mode:-
- ② Store mode:-
- ③ Hold or Save mode:-

Advantage

- ① Infinite storage time.
- ② Easy to operate.
- ③ Signal processing is possible.
- ④ It is capable of displaying X-Y plots, P-V diagram and B-H curve.
- ⑤ Cursor measurements is possible.

Applications :-

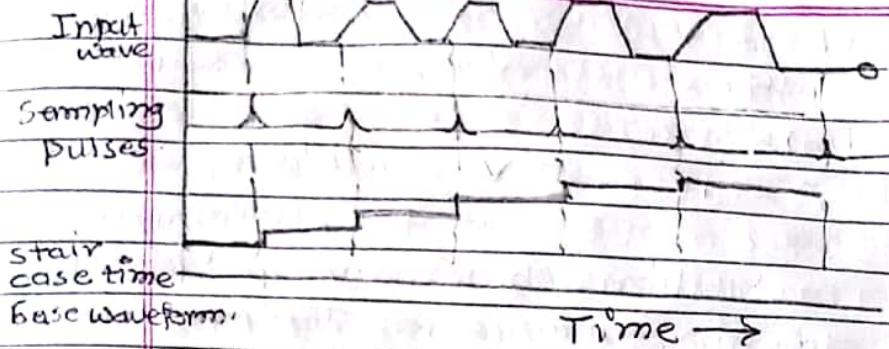
- ① It can be used to measure AC as well as DC voltages and currents. freq. time period, time interval betⁿ two signals etc. inductance, capacitance.
- ② It is used to give the visual representation for a target such as aeroplane, ship.
- ③ In medical fields, it is used to display cardiogram that are useful for diagnosis of heart.

of patient.

- ④ It is used to observe the radiation pattern generated by transmitting antenna.
- ⑤ Analyse TV waveform.
- ⑥ It can be used to determine the modulation charⁿ and detect the standing waves in transmission lines.

Sampling oscilloscope :-

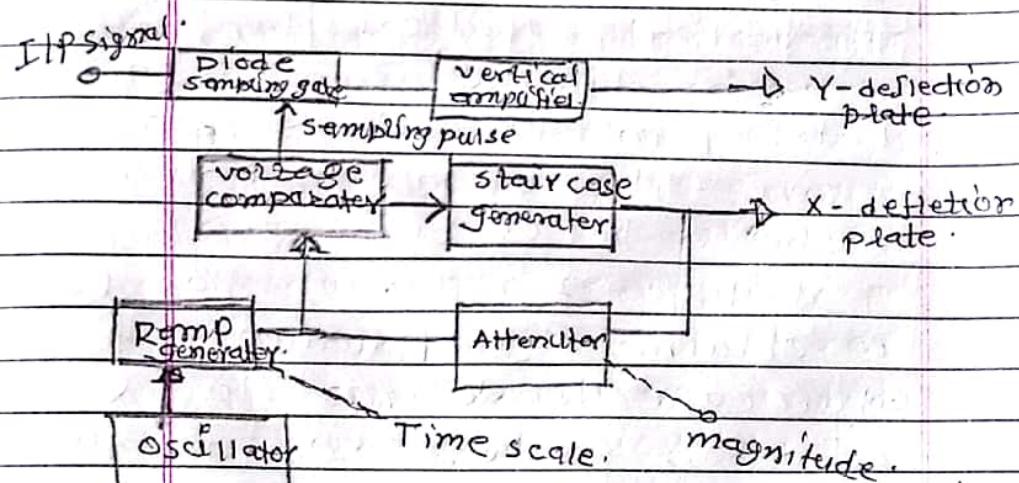
sampling technique have to be employed to obtain suitable display and CRO employing such sampling methods are called sampling oscilloscopes. The display is made up from as many 1000 dots of luminescence. The vertical deflection for each dot is obtained from progressively later points in each successive cycles of input waveform as shown fig-@ below. The horizontal deflection of the electron beam is obtained by application of staircase waveform to x-deflection plate shown in display of dots.



[fig-④ Sampling principle].



[fig-⑤] Display of dots.



[fig-⑥] Trigger [fig-⑦] Block diagram of sampling oscilloscope

The input is applied to the diode sampling gate. As the start of each sampling cycle a trigger input pulse is generated which activates the blocking oscillator. The oscillator output is given to the ramp gen. which generates the linear ramp signal, since the sampling must be synchronized with the input signal freq. the signal is delayed in the vertical amplifiers. The staircase generator produces a staircase waveform which is applied to an attenuator. The attenuator controls the magnitude of the staircase signal and then it is applied to a voltage comparator.

Another input to the voltage comparator is the output of the ramp generator. The voltage comparator compares the two signals and produces the output pulse when the two voltages are equal. This is nothing but a sampling pulse which applied to sampling gate through the gate control circuitry.

This pulse opens the diode gate and the sample is taken in. This sampled signal is then applied to the vertical amplifier and the Y-deflecting plate. The output of the staircase generator is also applied to the X-deflecting plate. Now during each step of staircase the spot moves on the screen. The comparator output advances the staircase output through one step. After the certain number of pulses about 100 or so the staircase generator reset. The smallest the size of the steps of the staircase generator, Large is the number of samples and higher is the resolution of image. The sampling oscilloscope responds and stores rapid bits information and displays them continuously.

It is this quality of sampling oscilloscopes which has limited sensitivity, Bandwidth and small display size. The sampling method transforms the input signal into lower freq. range, where low freq circuitry is then capable of producing an effective display.

Advantages :-

- ① The display produced is clear
- ② High speed electrical signal produced.
- ③ Very high freq performance can be achieved.
- ④ To controlling the size of step of the staircase Generator the number of samples and hence the resolution can be controlled.
- ⑤ The sampling technique allows the design of the oscilloscope with wide bandwidth, high sensitivity even for low duty cycle pulses.

Limitations :-

The sampling oscilloscope cannot used to display the Transient waveforms.

Recorders :-

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A Recorder records electrical and Non-electrical quantities as a function of time. The record may show how one variable varies with respect to another or how the input signal varies with time.

The record serves the following objectives :-

- (1) It preserves the details of measurements at a particular time.
- (2) It provides at a glance the overall picture of the performance of unit.
- (3) It provides immediate reflection on the actions taken by the operator.

Types of Recorders :-

- (1) Analog Recorders :-
- (2) Graphic Recorders :-
- (3) Galvanometer Type
- (4) Null Type
- (5) Potentiometric Recorders

Bridge Recorders.

- (d) LVDT Recorders.
- (e) circular chart Recorders :-
- (f) X-Y Recorders.

Magnetic Type Recorders :-

oscillographic Recorders :-

- (g) Digital Recorders :-
- (h) Incremental Digital Recorders :-
- (i) synchronous Digital Recorders :-

Strip chart Recorders :-

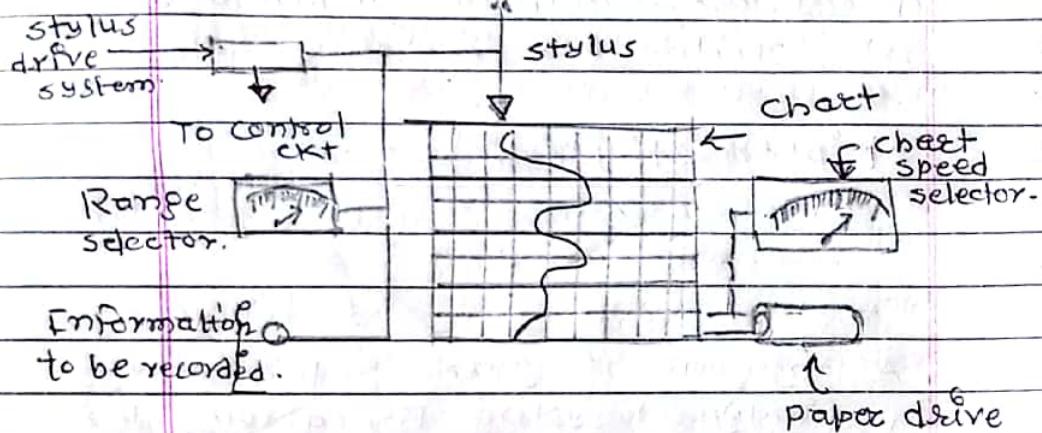
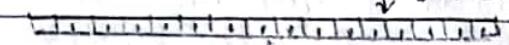
The basic construction features of strip chart Recorder.

A strip chart consist of following:- A long roll of graph paper moving vertically. A system for driving the paper at some selected speed. A stylus for marking paper on the moving graph paper. [Most recorders use a pointer attached to the stylus which pointer moves over a calibrated scale thus showing instantaneous value of the quantity being measured].

A stylus driving system which

moves the stylus in nearly exact replica or analog of quantity being measured. [A spring wound mechanism may be used but in most of recorders a synchronous motor is used for driving the paper].

↓ [Indication scale]



[fig-④ strip - chart Recorders]

marking mechanisms :- The most commonly used mechanisms employed for marking marks on the paper are:

- ① marking with ink filled stylus
- ② marking with heated stylus.
- ③ chopper bar.

Electric stylus marking

- ④ Electric stylus marking
 - ⑤ Electrostatic stylus.
 - ⑥ Optical marking method.
- Tracing system :- For producing graphic representations two types of tracing system are used.
- ① Curvilinear systems.
 - ② Rectilinear systems.

There are various kinds of strip chart recorders. According to working principle, These are mainly divided into two categories which are given below:-

- ① Galvanometric Type :-
- ② Potentiometric Type :-
- ③ Galvanometric Type :-

This type of recorder operates on the deflection principle. The deflection is produced by a galvanometer [D'Arsonval] which produces torque on account of a current passing through its coil. This current is proportional to the quality being measured. These recorders can work on ranges from a few mA to several mA or from a few mV to several mV.

The moving galvanometric type

recorder is comparatively inexpensive instruments having narrow bandwidths of 0 to 10 Hz. It has a sensitivity of 0.4 mV/mm or from a chart of 100 mm width a full scale deflection of 40 mV obtained. Linearamps are used for measurement of smaller voltages. This type of recorder is not useful for recording fast variations in either current or voltage or power.

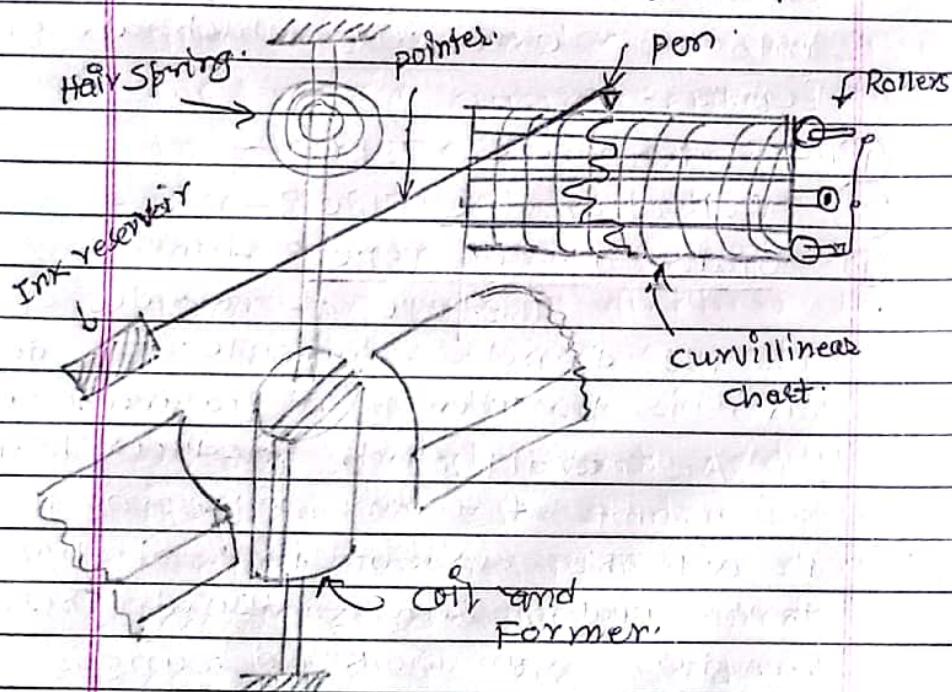


Fig-② Galvanometer-type recorder

Some recorders contain a timing mechanism that prints a series of small dots along the edge of the paper chart as the paper moves through the recorder. This time marker produces one mark per second. These types of recorders are mostly used as optical recorders, and contain Light source provided by either an ultra violet or Tungsten Lamp. A small mirror is connected to the Galvanometer movement and light beam is focused on the mirror. The beam reflected from the mirror is focused into a spot on a light sensitive paper. As the current passes through the coil the mirror deflects. The movement of the light beam is affected by the deflection of the small mirror and the spot on the paper also varies for the same reason, thus tracing the waveform on the paper.

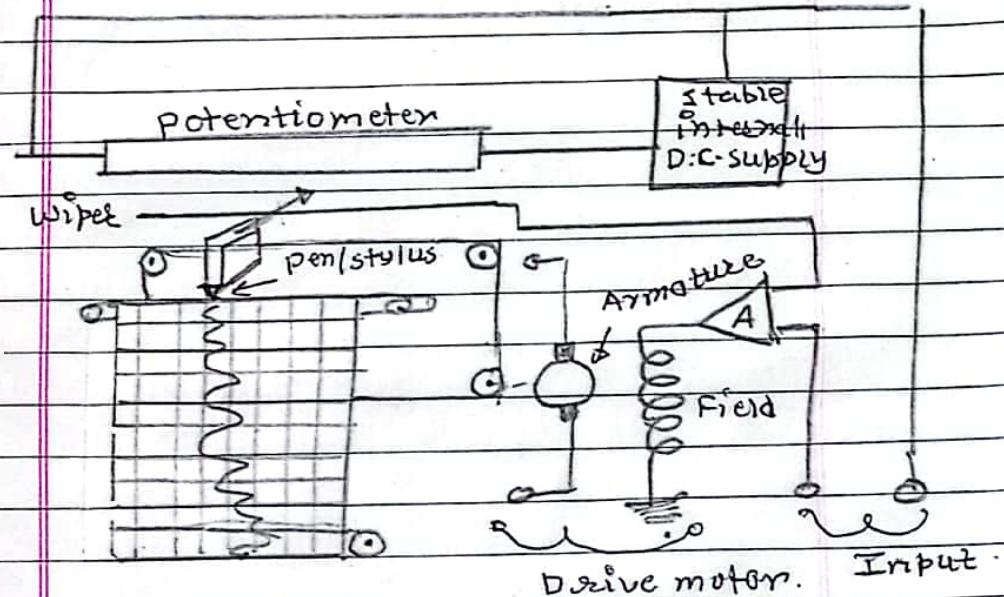
2 Potentiometer Recorders - A potentiometer recorder is used

where input signal to the recorder is a D.C. voltage. This potentiometer can be converted to recorder by attaching a stylus to the sliding contact. This stylus moves over paper and thus records the quantity under measurements.

During the operation of the recorder, the error signal is amplified and subsequently the field coil of D.C. motor is energised. The error current either flows in clockwise direction or anti-clockwise direction depending on the value of the voltage. The motor turns in such a direction that it reduces error signal to achieve balanced condition. As error signal starts reducing, the motor slows down and stops completely when error becomes zero. Thus balanced condition is achieved.

The pen which is used for marking waveform is

mechanically coupled to a wiper which in turn is mechanically coupled to the armature of D.C. motor. Thus, when wiper moves according to the error signal, so pen also moves in the same direction. As a result, the pen records the input signals variations moving across the paper.



[fig-④ potentiometer Recorders]

such recorders have very high Input impedance.

infinitely at balance conditions.

and high sensitivity of the order of $4V/rms$ with an error of less than $\pm 0.25\%$ with Bandwidth of 0.8 Hz.

The most common application of potentiometric recorder is for recording and control of process temp. self-balancing pot. are widely used in industry.

Magnetic Type and Disc Recorders

Magnetic type recorders are used for high frequency signal recording. In this recorder, the data is recorded in a way that it can be reproduced in electrical form any time. Also main advantage of these recorders is that the recorded data can be replayed for almost infinite times. Because of good higher freq. response.

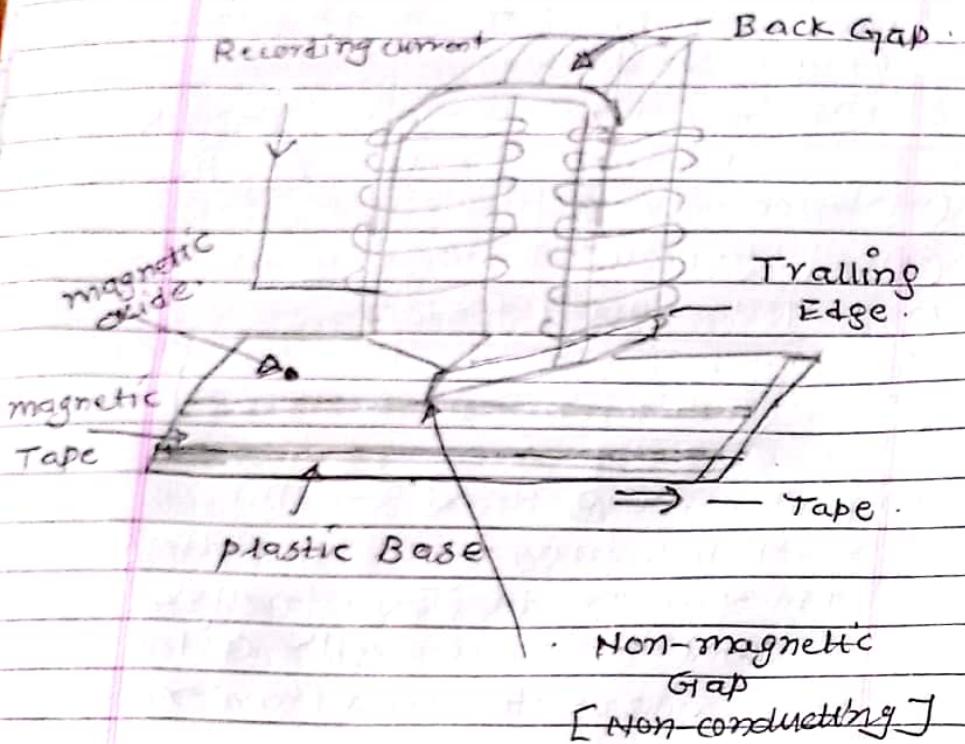
Basic components of Tape Recorder

magnetic Tape Recorder components :-

- ① Recording Head :-
- ② Magnetic Tape :-
- ③ Reproducing Head :-
- ④ Tape Transport Mechanism :-
- ⑤ Conditioning Device :-

① Recording Head :- The construction of the magnetic recording head is similar to Transformer having a toroidal core with coil where the current used for recording

is passed through coil wound around magnetic core. It produced magnetic flux. When the tape is passing the head, the flux produced due to recording current get linked with iron oxide particles on the magnetic tape and these particles get magnetized.



This ... magnetization particle remain as it is, even though the magnetic tape leaves the gap. The actual recording takes place at the trailing edge of the air gap. Any signal is recorded in the form of the patterns. These magnetic patterns are dispersed anywhere along the length of magnetic tape in accordance with variations in recording current w.r.t time.

② **Magnetic Tape** - The magnetic tape is made of thin sheet of tough and dimensionally stable plastic ribbon and is wound around a reel. This tape is transferred from one reel to another. When tape passes across air gap magnetic pattern is created in accordance with variation of recording current. To reproduce this pattern, the same tape with some recorded pattern is passed across the another magnetic head in which

Voltage is induced. This voltage induced is in accordance with the magnetic pattern.

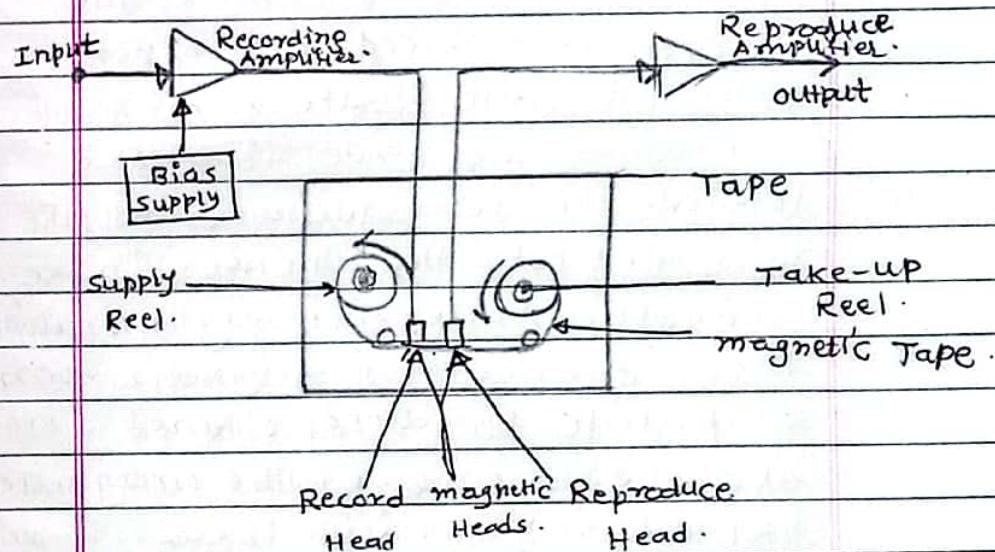
- ③ Reproducing Head :- The use of Reproducing head is to get the recorded data played back. The reproducing head direct detects the magnetic pattern recorded on the tape. The head converts the magnetic pattern back to the original electrical signal. In appearance, both recording and reproducing heads are very much similar.

④ Tape Transport mechanism :-

The tape Transport mechanism moves the magnetic tape along the recording head or reproducing head with constant speed. The magnetic tape is wound on reel. There are two reels; one called

as supply & other is called as take-up reel. Both the reels rotate in same direction.

The Transportation of the tape is done by using supply reel and take-up reel. The fast winding of the tape or the reversing of the tape is done by using special arrangements. The rollers are used to drive and guide the tape.



- ⑤ Conditioning Device :- These devices consist of amplifiers and filters to modify signal to be recorded.

Principle of Tape Recorders :-

when magnetic tape is passing through a recording head, the signal to be recorded appears some magnetic pattern on the tape. This magnetic pattern is in accordance with variation of original recording currents.

The recorded signal can be reproduced back by passing the same tape through a reproducing head where the voltage is induced corresponding to the magnetic pattern on the tape.

When the tape is passed through the reproducing head, the head detects the changes in the magnetic pattern i.e. magnetization. The change in magnetization of particle produced change in the reluctance of the magnetic ckt's of reproducing head, inducing the voltage in its winding.

The induced voltage depends on the directions of magnetization and its magnitude on the tape.

The e.m.f induced is proportional to the rate of change of magnitude of magnetization

$$e = N \frac{di}{dt}$$

where,

N = No. of turns of winding on reproducing head.

Suppose signal is recorded is $V_m \sin \omega t$. Thus, the current in the recording head and flux induced will be proportional to this voltage. It is given by

$$e = k_1 V_m \sin \omega t$$

where

$$k_1 = \text{constant.}$$

Above the pattern of flux is recorded on the tape. Now, when this tape is passed through the reproducing head, above pattern is regenerated by inducing voltage in the reproducing head winding. It is given by

$$e = k_1 U V_m \sin \omega t$$

Thus the reproducing signal is equal to derivative of input signal & it is proportional to flux recorded & freq. of recorded signal.

Application of magnetic Tape

Recorders :-

- ① Recording of stress, vibration and analysis of noise.
- ② Communication and spying.
- ③ Data recording and analysis on missiles, aircraft and satellite.

Indicators and Display Devices

- Nixie, LED, LCD and seven segment and Dot matrix Display.

Nixie Tube :- A Nixie tube or cold cathod display is an electronic device used for displaying numerals or other information using glow discharge. Nixie tubes are display device that were popular in mid of 20th century. They use neon-gas-filled tubes with cathodes shaped like numerals or symbols. The tube filled with a gas with low pressure,

mostly neon and a small amount of argon, in a penning mixture. When voltage is applied, a specific Cathode light up, displaying the corresponding character. Nixie tube have a vintage aesthetic and emit a warm orange glow. They are often used in retro-style clocks and other decorative applications. It's operation does not depend on thermionic emission of electrons from a heated cathod. It is hence a cold-cathod tube and is a variant of the neon lamp. Such tubes rarely exceed 40°C [104°F] even under the most severe of operating conditions in a room at ambient temp. Vacuum fluorescent displays from the same era use completely different technology.

They have heated cathod together with a control grid and shaped phosphor anode. Nixies have no heater or control grid, typically a single anode and shaped bare metal cathodes.

Data logger

Data loggers are stand-alone device that can record information electronically from internal or external sensors or other equipment that provided digital or serial output.

Features of Data Loggers :-

(1) stand-alone operation :-

Most data loggers are normally configured with a PC, some models can be configured from the front panel provided by the manufacturer. Once the data loggers are configured they don't need the PC to operate.

(b) support for multiple sensor type :-

Data loggers often have universal input type which accept input from common sensors like Thermocouple, RTD, humidity, voltage

(3) Local data storage :-

All the data loggers have local data storage and internal memory unit. So all the measured data stored within the logger for later transfer to PC.

④ Automatic's Data collection:

Data loggers are designed to collect data at regular interval , 24 hours a day and 365 days a year . If necessary, and the collection made is often configurable. Data logging and recording are both analog terms in fields of measurements.

Data logging is basically measuring and recording of any physical phenomena or electrical parameter over a period of time . The physical phenomena can be Temp ; strain, displacement, flow, pressure, voltage, current resistance, power and many parameters etc.

The data loggers converts the signal to help of A/D converters .

Basic component of data Loggers :-

- ① Hard components like sensors signal conditioning and A/D converters etc.
- ② Long term data storage , on PC.
- ③ software for collecting data analyzing and viewing.

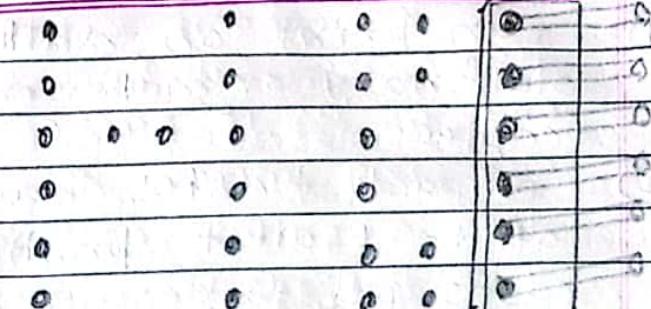
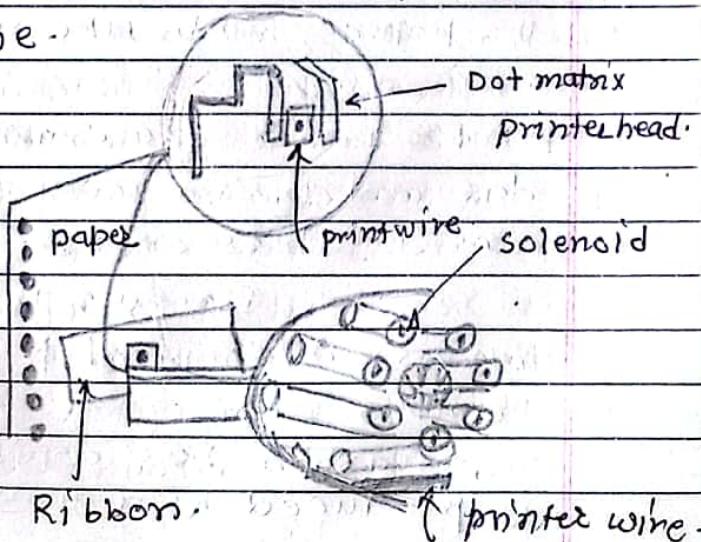
Dot Matrix :-

A Dot matrix printer or Impact matrix printer refer to a type of computer printer with a print head that runs back and fourth on the page and prints by impact, striking an ink-soaked cloth ribbon against the paper , much like a typewriter. Unlike a typewriter or daisy wheel printer letters are drawn out of a dot matrix . Varied fonts and arbitrary graphics can be produced .

Because the printing involves mechanical pressure , these printers can create carbon copies and carbon less copies . Each dot is produced by a tiny metal rod ,

also called a 'wire' or 'pin'

which less copies. Each dot is produced by a tiny metal rod also called wire or pin. which is driven forward by the power of a tiny electromagnet or solenoid. Either directly or through small levers [paws]. Facing the ribbon and the paper is a small guide plate pierced with holes to serve as guides for pins. The moving portion of the printer is called the print head and when running the printer as text device it generally prints one line of text at a time.



Most dot matrix printers have a single vertical line of dot making equipment on their print heads. Others have a few interleaved rows in order to improve dot density.

These mice can be highly durable, but eventually wear out. Ink invades the guide plate of the print heads causing grit to adhere to it. This grit slowly causes the channels in the guide plate to wear from circles into ovals or slots, providing less and less accurate guidance to the printing wires.

Disadvantages:-

- ① Impact printers are usually noisy.

② They can only low resolution graphics with limited color performance.

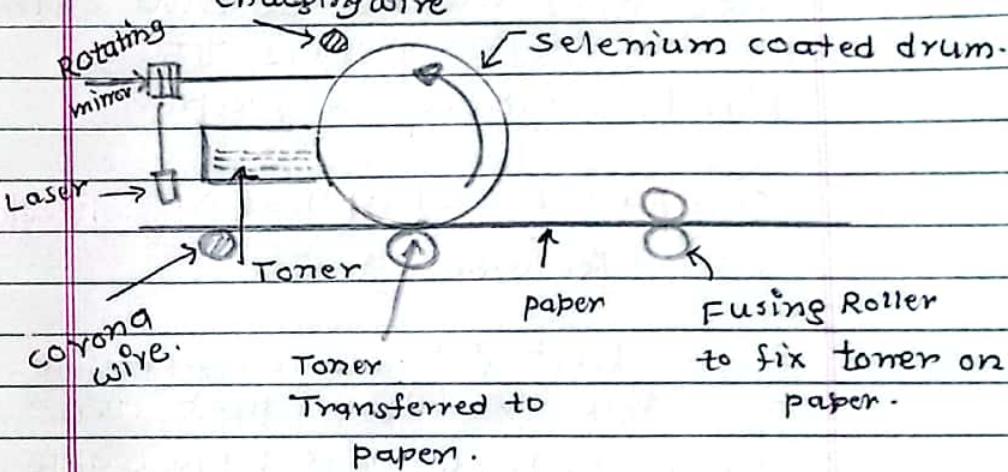
Advantage:

- ① can print on multi-part stationary or make carbon copies.
- ② Impact printers have one of the lowest printing costs per page.
- ③ They are able to use continuous paper rather than requiring individual sheets.
- ④ The ink ribbon also does not easily dry out.

Laser printer :-

It has a photosensitive drum which is coated with selenium based light sensitive material. The selenium, in the dark, has a high resistance and consequently becomes charged as it passes close to the charging wire; this is a wire at high voltage and off which charge leaks.

A light beam is made to scan along the length of the drum by a small rotating eight-sided mirror. When light strikes the selenium its resistance drops and it can no longer remain charged. By controlling the bright of the beam of light the point on the drum can be discharged or left charged.



[Fig-④] Laser printer's basic Elements

As the drum passes the toner reservoir, the charged areas attract particles of toner which thus stick to the areas that have not been exposed to light.

and do not stick on the areas that have been exposed to light. The paper is given a charge as it passes another charging wire, the so called corona wire, so that as it passes close to drum it attracts the toner off the drum. A hot fusing roller is then to melt the toner particles so that, after passing between rollers, they finely adhere to paper.

Compact Disc / Optical Disc

Recorders :-

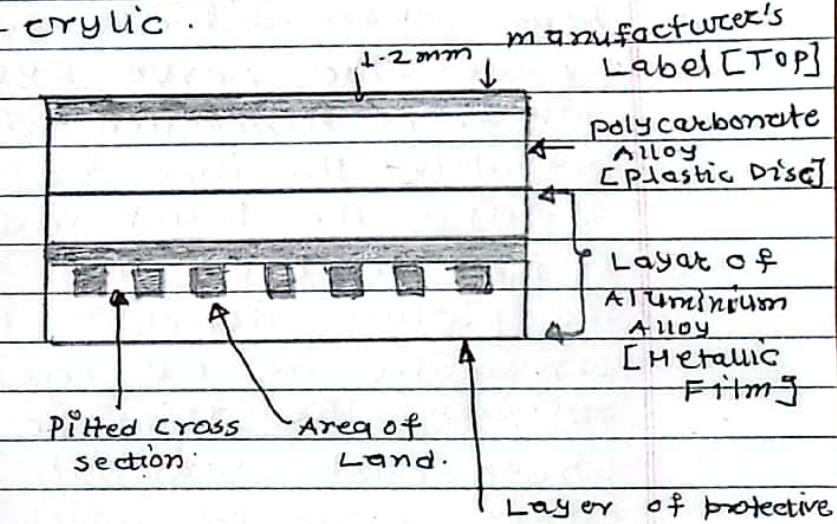
A compact disc is a portable storage medium that can record, store and play back audio, video and other data in digital form. As the results, the signals captured are a replica of the original audio stream. Text, picture images

Date _____
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audio, video and software are all

stored on compact discs. It is made up of three layers.

- ① Transparent substrate with polycarbonate wafer [plastic disc] makes up this layer.
- ② Thin metallic layer coating of aluminium alloy is applied to the wafer base.
- ③ Outer layer of protective acrylic.



[Fig-4) Layout CD-ROM Disc]

[playing the surface of CD].

working principle :- The using a

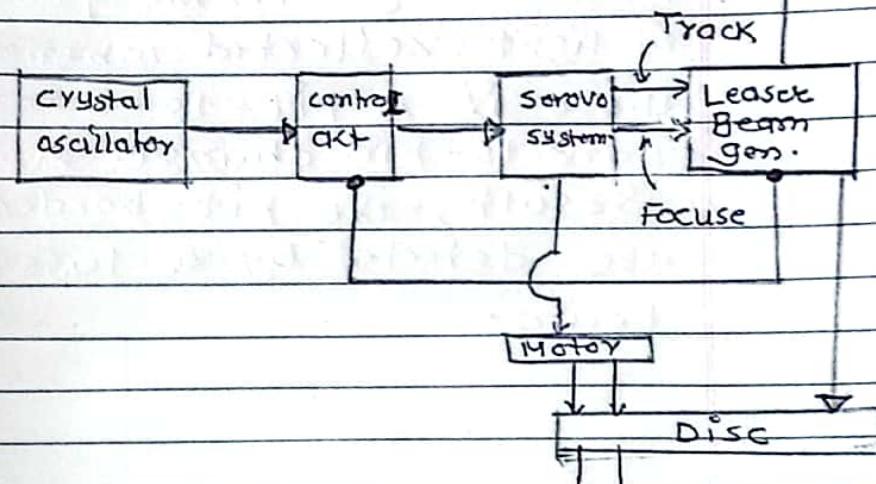
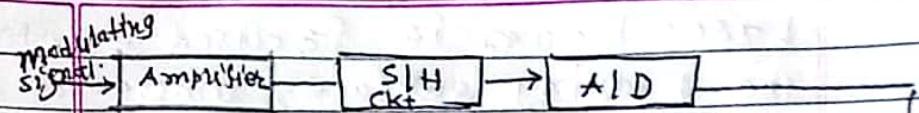
Sample and Hold CKT and an

ADC the signal to be recorded on CD is first amplified and then transformed into digital signal. The output of the ADC is also used by the Laser Beam Generator. The control CKT and the servo system are both controlled by the signal from the crystal oscillator and Laser beam generator.

The servo system, which is controlled by a motor regulates the disc rotation as well as the track and focus of the Laser beam generator.

The picture depicts a block schematic of CD recording system. The unexposed photoresist materials is chemically removed after leaving a helical pattern across the glass disc's surface.

This becomes the glass master - production CDs



[Fig-① Block diagram of CD Recording]

The data retrieval system is made up the the phase listed below:

- ① servomechanism which spins the CD.
- ② A laser head that moves in a radial pattern. The Laser head can both emit and detect a 70 nm laser beam.

When the disc spins, the

Laser beam is focused into the playing surface, where it is reflected by the lands and scattered by the pits resulting in a change in the quality of light reflected whenever there is a pit-to-land or land-to-pit change. As a result, the pit borders are detected by a laser beam: