## **POKHARA UNIVERSITY**

Level: Bachelor Semester: Spring Year : 2019 Programme: BE Full Marks: 100 Course: Embedded System Pass Marks: 45 Time : 3hrs. Candidates are required to give their answers in their own words as far as practicable. The figures in the margin indicate full marks. Attempt all the questions. List and define the three main characteristics of embedded system that 7 distinguish such systems from other computing system. State the differences between combinational and sequential circuits? b) 8 Describe sequential circuit design process with a suitable example. Define datapath and control units with its sub operations. a) 7 Design a custom single-purpose processor to calculate LCM (least 8 common multiple) between two integers. Why is cache memory needed? Explain the principle of operation of a) 8 EPROM with necessary illustrations. Describe how the memory modules can be composed. Use a suitable b) 7 example describing all the three cases. What do you understand by interrupt handler? Explain three method a) 8 by which RTOS handles interrupt. What do you mean by kernel? Describe the types of RT kernel. 7 b) a) Define Debugger, Downloader and Cross-Assembler. 7 Write an assembly language program for 8051 micro controller to 8 b) display the number from 0 to 9 in a seven segment display so that the number is updated in every 1 sec. User internal timer of the 8051 to generate 1 sec delay. Show necessary connection diagram and calculation. Write a VHDL program for which output will be 1 when the sequence 8 1101 is detected considering overlapping condition. Write a program in VHDL implement 8:1 multiplexer. Write short notes on: (Any two) 2×5 a) Cross compiler and Native Compiler b) DMA

1.

2.

3.

4.

5.

7.

c) Types of memory