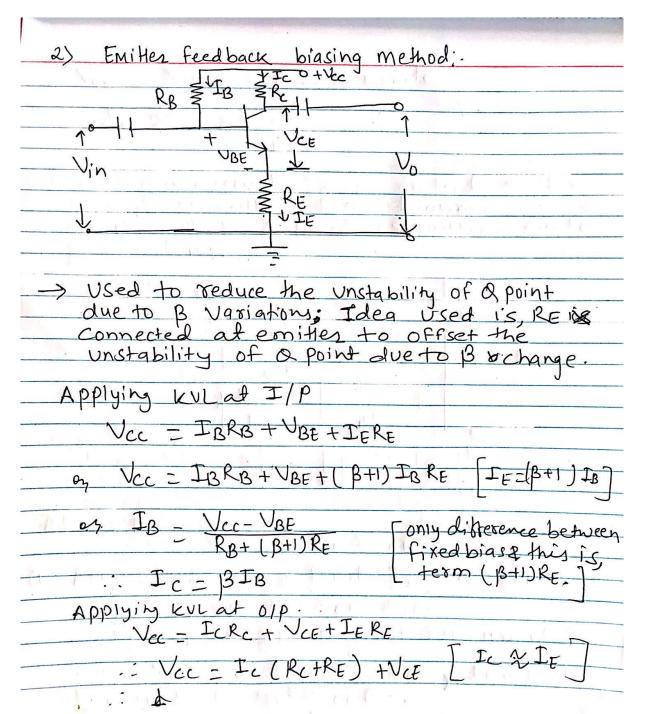
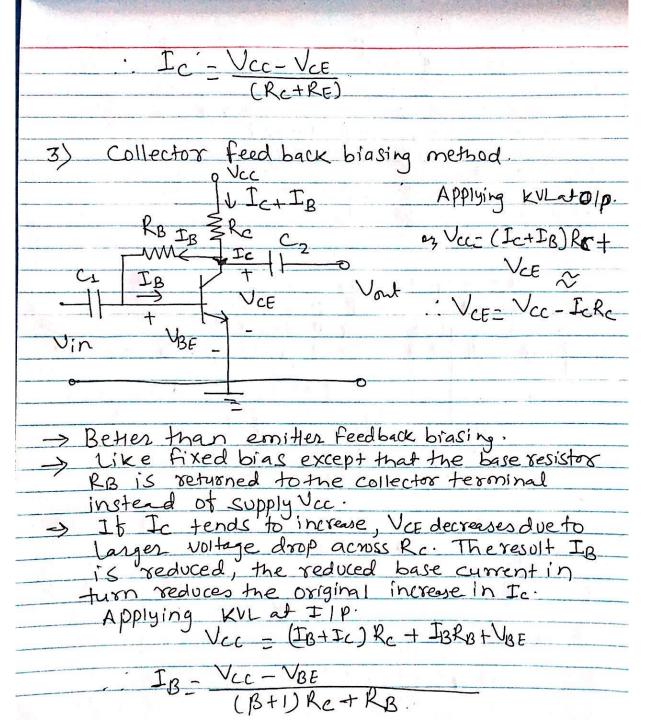
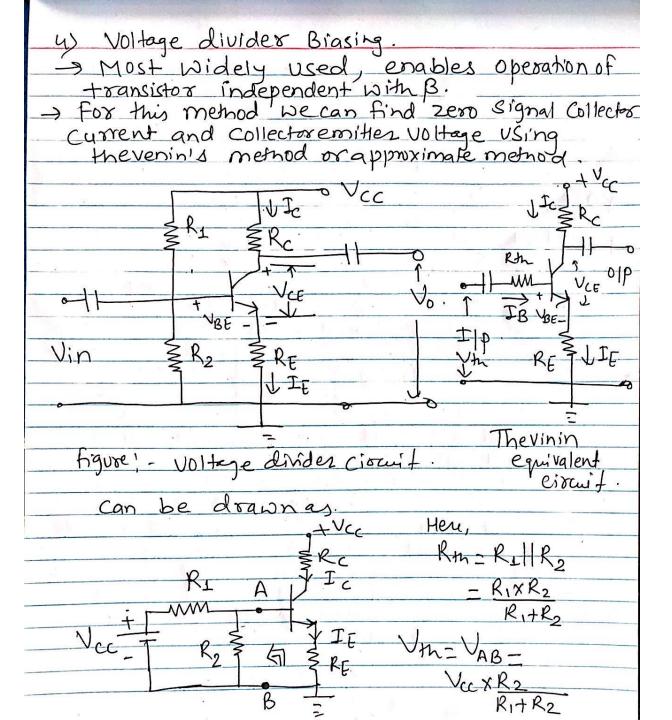
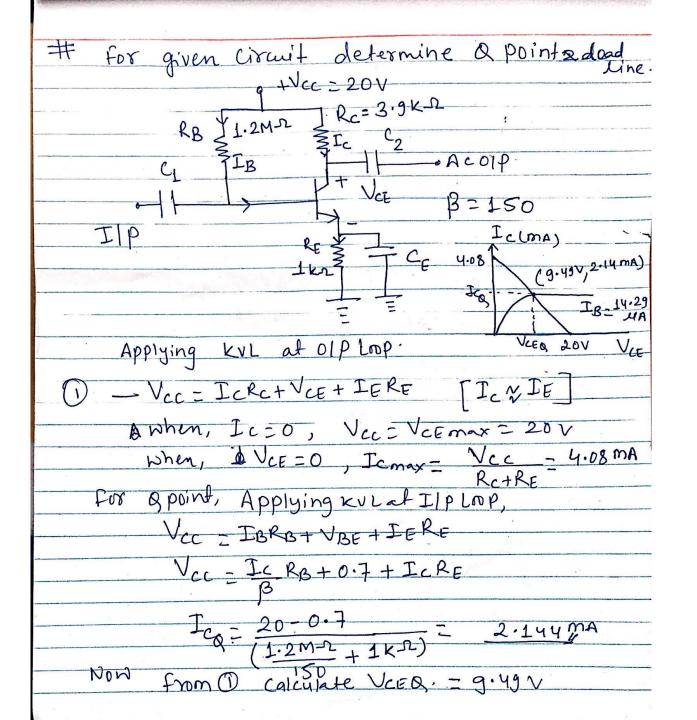
Transistor Biasing Methods continue.....

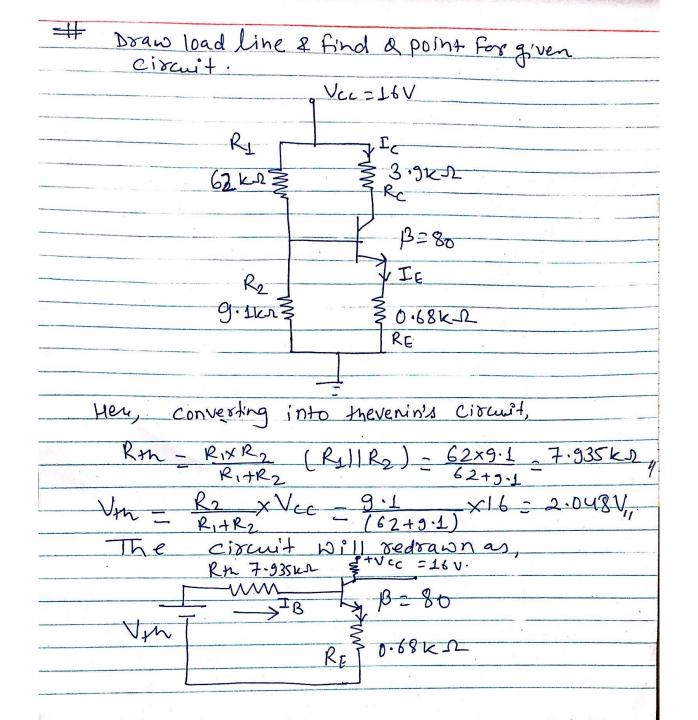






Applying KULat IIP	1
Vm = IBRm + VBE + IERE	
IB - Vtn-VBE Rtn + RE(B+1)	
Ic-BIB	1
NOW, Applying KVL at 0/P 150P.	
Vcc = IcRc + VcE + IERE	
VCE = VCC - ICERC+RE)	· [: Icx Le
	· · · · · · · · · · · · · · · · · · ·





Applying KUL at Olp.	
VCC - ICRC + VCE + IERE (ICXIE)	
1 Ic=0, Vcc=VcEmax= 16V	
@ VCE=0, Ic= Vcc = 16 = 3.49mA. mx (RC+RE) = (3.9+0.68)	
NOW, Applying KULat IIP.	
Vm = IBRm + VBE + IERE	
IB - Vm - VBE 2.048-0.7 [Rm+(1+B)RE] 7.935×103+180+1) 0.68×103	
: IB = 21.392.MA	
NOW, Ica = BJB = 80×21·392×1000 = 1·71 mA,	
Vera = Vcc - Iche (Ic+ IB) RE	
- 16- 1.71×3.9K-2 (1.71+21.392×10-3)×0.68	
$= 16 - 1.71 \times 3.9 \times 2.2.2.2.2.2.2.2.2.2.2.2.2.2.2.2.2.2.2$	
1 Ic(mA)	
Jcmax 3.94 (8.15V, 1.71MA)	
IB = 21.35MA	
282	
VCE V	
16V-Vermax	

