## POKHARA UNIVERSITY

Semester: Fall

Year

: 2023

Level: Bachelor

Programme: BE Full Marks: 100 Course: Microprocessor and ALP (New) Pass Marks: 45 :3hrs. Time Candidates are required to give their answers in their own words as far as practicable. The figures in the margin indicate full marks. Attempt all the questions. 1. Define microprocessor. Differentiate between Von Newmann and 7 Harvard architecture along with its block diagram. Draw the functional block diagram of intel 8085 microprocessor and 8 explain each block. Draw and explain a well-labelled timing diagram of the instruction LXI 8 2. a) B, D050H and calculate the time required to execute the instruction if frequency = 4Mhz. Write an ALP in 8085 to transfer 10 bytes of data of memory address 7 b) starting from 5000H to 6000H. Comment in each line indicate full marks. What are the major changes and advantages of 8086 over 8085 8 3. a) microprocessors? Explain addressing modes of 8086. OR What are the types of FLAGS available in 8086 microprocessors? Show 7 the Flag position and explain in brief with examples. Write a 8086 program to find square root of a number. b) What are the assembler directives of 8086? List out some common 4. a) 7 directives that are used while programming. What are Macros? Explain DOS interrupt functions. 8 Design an address decoding circuit to interface 2KB RAM, 4 KB ROM b) and 8 KB RAM with initial address 2000H. 7 5. Define parallel interface. Explain 8255 PPI. a) Draw and explain the architecture of 8251 USART along with its pins. 8 b) What is the difference between polled and Vectored interrupt handling 7 6. technique? Explain with necessary diagram. 8 What are the sources of interrupt? Explain interrupt vector Table. b)

2×5

- a) Microcontroller and applications
- b) Synchronous and Asynchronous Bus
- c) 8259 PIC

