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# Kahan summation algorithm

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In numerical analysis, the **Kahan summation algorithm** (also known as **compensated summation** <sup>[1]</sup>) significantly reduces the numerical error in the total obtained by adding a sequence of finite precision floating point numbers, compared to the obvious approach. This is done by keeping a separate *running compensation* (a variable to accumulate small errors).

In particular, simply summing n numbers in sequence has a worst-case error that grows proportional to n, and a root mean square error that grows as  $\sqrt{n}$  for random inputs (the roundoff errors form a random walk). With compensated summation, the worst-case error bound is independent of n, so a large number of values can be summed with an error that only depends on the floating-point precision.

The algorithm is attributed to William Kahan.<sup>[3]</sup> Similar, earlier techniques are, for example, Bresenham's line algorithm, keeping track of the accumulated error in integer operations (although first documented around the same time<sup>[4]</sup>) and the Delta-sigma modulation<sup>[5]</sup> (integrating, not just summing the error).

## 

## The algorithm [edit]

In pseudocode, the algorithm is:

```
function KahanSum(input)
   var sum = 0.0
    var c = 0.0
                                 // A running compensation for lost low-order bits.
    for i = 1 to input.length do
        var y = input[i] - c
                                 // So far, so good: c is zero.
        var t = sum + y
                                 // Alas, sum is big, y small, so low-order digits
of y are lost.
                                 // (t - sum) recovers the high-order part of y;
        c = (t - sum) - y
subtracting y recovers - (low part of y)
                                 // Algebraically, c should always be zero. Beware
       sum = t
overly-aggressive optimizing compilers!
                                 // Next time around, the lost low part will be
added to y in a fresh attempt.
   return sum
```

#### Worked example [edit]

This example will be given in decimal. Computers typically use binary arithmetic, but the principle being illustrated is the same. Suppose we are using six-digit decimal floating point arithmetic, *sum* has attained the value 10000.0, and the next two values of *input(i)* are 3.14159 and 2.71828. The exact result is 10005.85987, which rounds to 10005.9. With a plain summation, each incoming value would be aligned with *sum* and many low order digits lost (by truncation or rounding.) The first result, after rounding, would be 10003.1. The second result would be 10005.81828 before rounding, and 10005.8 after rounding. This is not correct.

However, with compensated summation, we get the correct rounded result of 10005.9.

Assume that c has the initial value zero.

```
y = 3.14159 - 0
t = 10000.0 + 3.14159
= 10003.1
c = (10003.1 - 10000.0) - 3.14159
= 3.10000 - 3.14159
Original full y.
= -.0415900
arithmetic.
sum = 10003.1
y = input[i] - c
Many digits have been lost!
This must be evaluated as written!
The assimilated part of y recovered, vs. the original full y.
Trailing zeros shown because this is six-digit arithmetic.
Sum in the six is six-digit from y and y is a six-digit arithmetic.
Thus, few digits from y input(y) met those of y in the six is y.
```

The sum is so large that only the high-order digits of the input numbers are being accumulated. But on the next step, c gives the error.

```
y = 2.71828 - -.0415900
                                   The shortfall from the previous stage gets
included.
   = 2.75987
                                  It is of a size similar to y: most digits meet.
 t = 10003.1 + 2.75987
                                  But few meet the digits of sum.
   = 10005.85987, rounds to 10005.9
  c = (10005.9 - 10003.1) - 2.75987 This extracts whatever went in.
   = 2.80000 - 2.75987
                                  In this case, too much.
   = .040130
                                   But no matter, the excess would be subtracted
off next time.
sum = 10005.9
                                   Exact result is 10005.85987, this is correctly
rounded to 6 digits.
```

So the summation is performed with two accumulators: *sum* holds the sum, and *c* accumulates the parts not assimilated into *sum*, to nudge the low-order part of *sum* the next time around. Thus the summation proceeds with "guard digits" in *c* which is better than not having any but is not as good as performing the calculations with double the precision of the input. However, simply increasing the precision of the calculations is not practical in general; if *input* is already double precision, few systems supply quadruple precision and if they did, *input* could then be quadruple precision.

#### Accuracy [edit]

A careful analysis of the errors in compensated summation is needed to appreciate its accuracy characteristics. While it is more accurate than naive summation, it can still give large relative errors for ill-conditioned sums.

Suppose that one is summing n values  $x_i$ , for i=1,...,n. The exact sum is:

$$S_n = \sum_{i=1}^n x_i$$
 (computed with infinite precision)

With compensated summation, one instead obtains  $S_n+E_n$ , where the error  $E_n$  is bounded above by:[2]

$$|E_n| \le \left[2\varepsilon + O(n\varepsilon^2)\right] \sum_{i=1}^n |x_i|$$

where  $\epsilon$  is the machine precision of the arithmetic being employed (e.g.  $\epsilon \approx 10^{-16}$  for IEEE standard double precision floating point). Usually, the quantity of interest is the relative error  $|E_n|/|S_n|$ , which is therefore bounded above by:

$$\frac{|E_n|}{|S_n|} \le \left[2\varepsilon + O(n\varepsilon^2)\right] \frac{\sum_{i=1}^n |x_i|}{\left|\sum_{i=1}^n x_i\right|}.$$

In the expression for the relative error bound, the fraction  $\Sigma |x_i|/|\Sigma x_j|$  is the condition number of the summation problem. Essentially, the condition number represents the *intrinsic* sensitivity of the summation problem to errors, regardless of how it is computed. [6] The relative error bound of *every* (backwards stable) summation method by a fixed algorithm in fixed precision (i.e. not those that use arbitrary precision arithmetic, nor algorithms whose memory and time requirements change based on the data), is proportional to this condition number. An *ill-conditioned* summation problem is one in which this ratio is large, and in this case even compensated summation can have a large relative error. For example, if the summands  $x_i$  are uncorrelated random numbers with zero mean, the sum is a random walk and the condition number will grow proportional to  $\sqrt{n}$ . On the other hand, for random inputs with nonzero mean the condition number asymptotes to a finite constant as  $n \to \infty$ . If the inputs are all non-negative, then the condition number is 1.

Given a condition number, the relative error of compensated summation is effectively independent of n. In

principle, there is the  $O(n\epsilon^2)$  that grows linearly with n, but in practice this term is effectively zero: since the final result is rounded to a precision  $\epsilon$ , the  $n\epsilon^2$  term rounds to zero unless n is roughly  $1/\epsilon$  or larger. [2] In double precision, this corresponds to an n of roughly  $10^{16}$ , much larger than most sums. So, for a fixed condition number, the errors of compensated summation are effectively  $O(\epsilon)$ , independent of n.

In comparison, the relative error bound for naive summation (simply adding the numbers in sequence, rounding at each step) grows as  $O(\varepsilon n)$  multiplied by the condition number. This worst-case error is rarely observed in practice, however, because it only occurs if the rounding errors are all in the same direction. In practice, it is much more likely that the rounding errors have a random sign, with zero mean, so that they form a random walk; in this case, naive summation has a root mean square relative error that grows as  $O(\varepsilon \sqrt{n})$  multiplied by the condition number. This is still much worse than compensated summation, however. Note, however, that if the sum can be performed in twice the precision, then  $\varepsilon$  is replaced by  $\varepsilon^2$  and naive summation has a worst-case error comparable to the  $O(n\varepsilon^2)$  term in compensated summation at the original precision.

By the same token, the  $\Sigma |x_i|$  that appears in  $E_n$  above is a worst-case bound that occurs only if all the rounding errors have the same sign (and are of maximum possible magnitude). [2] In practice, it is more likely that the errors have random sign, in which case terms in  $\Sigma |x_i|$  are replaced by a random walk—in this case, even for random inputs with zero mean, the error  $E_n$  grows only as  $O(\varepsilon \sqrt{n})$  (ignoring the  $n\varepsilon^2$  term), the same rate the sum  $S_n$  grows, canceling the  $\sqrt{n}$  factors when the relative error is computed. So, even for asymptotically ill-conditioned sums, the relative error for compensated summation can often be much smaller than a worst-case analysis might suggest.

#### Alternatives [edit]

Although Kahan's algorithm achieves O(1) error growth for summing n numbers, only slightly worse  $O(\log n)$  growth can be achieved by pairwise summation: one recursively divides the set of numbers into two halves, sums each half, and then adds the two sums. [2] This has the advantage of requiring the same number of arithmetic operations as the naive summation (unlike Kahan's algorithm, which requires four times the arithmetic and has a latency of four times a simple summation) and can be calculated in parallel. The base case of the recursion could in principle be the sum of only one (or zero) numbers, but to amortize the overhead of recursion one would normally use a larger base case. The equivalent of pairwise summation is used in many fast Fourier transform (FFT) algorithms, and is responsible for the logarithmic growth of roundoff errors in those FFTs. [8] In practice, with roundoff errors of random signs, the root mean square errors of pairwise summation actually grow as  $O(\sqrt{\log n})$ . [7]

Another alternative is to use arbitrary precision arithmetic, which in principle need no rounding at all with a cost of much greater computational effort. A way of performing exactly rounded sums using arbitrary precision is to extended adaptively using multiple floating-point components. This will minimize computational cost in common cases where high precision is not needed. [9][10] Another method that uses only integer arithmetic, but a large accumulator was described by Kirchner and Kulisch; a hardware implementation was described by Müller, Rüb and Rülling. [112]

## Computer languages [edit]

In principle, a sufficiently aggressive optimizing compiler could destroy the effectiveness of Kahan summation: for example, if the compiler simplified expressions according to the associativity rules of real arithmetic, it might "simplify" the second step in the sequence t = sum + y; c = (t - sum) - y; to (sum + y) - sum) - y; then to c = 0; eliminating the error compensation. [13] In practice, many compilers do not use associativity rules (which are only approximate in floating-point arithmetic) in simplifications unless explicitly directed to do so by compiler options enabling "unsafe" optimizations, [14][15][16][17] although the Intel C++ Compiler is one example that allows associativity-based transformations by default. [18] The original K&R C version of the C programming language allowed the compiler to re-order floating-point expressions according to real-arithmetic associativity rules, but the subsequent ANSI C standard prohibited re-ordering in order to make C better suited for numerical applications (and more similar to Fortran, which also prohibits re-ordering), [19] although in practice compiler options can re-enable re-ordering as mentioned above.

In general, built-in "sum" functions in computer languages typically provide no guarantees that a particular summation algorithm will be employed, much less Kahan summation. [citation needed] The BLAS standard for linear algebra subroutines explicitly avoids mandating any particular computational order of operations for performance reasons, [20] and BLAS implementations typically do not use Kahan summation.

The standard library of the Python computer language specifies an fsum & function for exactly rounded

summation, using the Shewchuk algorithm [10] to track multiple partial sums.

### See also [edit]

• Algorithms for calculating variance, which includes stable summation

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#### External links [edit]

• Floating-point Summation, Dr. Dobb's Journal September, 1996 ₺

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