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# Chaitin's algorithm

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**Chaitin's algorithm** is a bottom-up, [graph coloring register allocation algorithm](#) that uses cost/degree as its [spill metric](#). It is named after its designer, [Gregory Chaitin](#). Chaitin's algorithm was the first [register allocation](#) algorithm that made use of coloring of the [interference graph](#) for both register allocations and spilling.

Chaitin's algorithm was presented on the 1982 [SIGPLAN](#) Symposium on Compiler Construction, and published in the symposium proceedings. It was extension of an earlier 1981 paper on the use of graph coloring for register allocation. Chaitin's algorithm formed the basis of a large section of research into register allocators.

## References [\[edit\]](#)

- [Gregory Chaitin \*Register allocation and spilling via graph coloring\*](#)
- [Gregory Chaitin \*Register allocation and spilling via graph coloring\*](#) 

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