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"Ain't the world be so much better if we understood the second law of thermodynamics first"

## **Summary**\_

- Over fourteen years of experience implementing distributed, multi processor and multicore systems to perform complex tasks in a efficient, failsafe, high availability manner.
- Strong background in theoretical approaches to design and implementation of computer based systems in server based architectures and embedded systems.
- Languages: Scala, Java, C++, C, Closure/Lisp, Python, Shells in Unices
- Systems: Writing realtime OS systems, Linux OS
- Tools: Latex, Excel, Matlab, Hadoop, Cassandra, Redis, Messagge brokers, Web frameworks, MySql, PostGresSql, Unit and Integration test
  methodologies, Asynchronpus processing, PowerPC, Arm

## Education

Stanford University CA, USA

M.S. IN ELECTRICAL AND COMPUTER ENGINEERING

**IIT** Kharagpur, India

B.S. IN ELECTRICAL AND ELECTRONICS ENGINEERING

## **Experience**

Xad Inc San Francisco, CA

STAFF SOFTWARE ENGINEER IN BACKEND

Jul. 2015 - Current

- Implemented real time auction server in newer scala framework
- Architect engineer in cluster controller subsystem that provided distributed programming model with strong exception gurantees.

**Emc Inc**San Francisco, CA

STAFF SOFTWARE ENGINEER IN BACKEND

Jan. 2015 - July. 2015

• Data Science and Analytics. A short term work in breaking ground for incorporating recommneder system in storage usage analytics of content managemnt systems. Created experimental models and studied the suitablilty of approaches.

Synopsys Inc San Francisco, CA

STAFF SOFTWARE ENGINEER IN COMPILER OPTIMIZATION

Dec. 2012 - Dec. 2014

- Major contribution in feature enhancement and management of optimzer engine of synopsys integrated circuits compiler.
- · Recognized for new optimization goal implementation in the electromagnetic emission constraint optimization problem.

Nuance Inc San Francisco, CA

SOFTWARE ENGINEER IN MULTIMEDIA SERVING SYSTEMS

Dec. 2010 - Dec. 2012

 Major contribution to bringing up new packet voice distributed processing system offered as SAS. Involved in conceptual design of backend architecture of server systems like load balancing, failsafe architectures.C++, Java

**Ditech Inc**San Francisco, CA

SOFTWARE ENGINEER IN MULTIMEDIA EMBEDDED SYSTEMS

Apr. 2006 - Aug. 2008

Major contribution in design of packet multimedia processing and control system in multi array processor system with colocated cores. The
system needed to provide low latency and high bandwidth packet processing solutions to mobile phone networks.

Conexant Inc Redbanks, NJ

FIRMWARE ENGINEER IN MULTIMEDIA EMBEDDED SYSTEMS

Apr. 2004 - apr. 2006

• Major contribution in implementing signal processing modules in DSL modem processors. Usage of acquistion and statistical techniques to decode data in dsl networks

Ittiam systems Inc Bangalore, India

SOFTWARE ENGINEER IN MULTIMEDIA EMBEDDED SYSTEMS

Jul. 2001 - Mar. 2004

 Major contribution in design of real time scheduler and signal acquistion systems in PowerPC based architectures to implement telephony based modems.

JANUARY 20, 2017 NISH SINHA · RÉSUMÉ 1