Distillation Guided Residual Learning for Binary Convolutional Neural Networks

Jianming Ye, Shiliang Zhang, Jingdong Wang

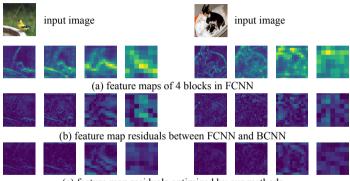
jmye@pku.edu.cn, slzhang.jdl@pku.edu.cn, jingdw@microsoft.com

Abstract. It is challenging to bridge the performance gap between Binary CNN (BCNN) and Floating point CNN (FCNN). We observe that, this performance gap leads to substantial residuals between intermediate feature maps of BCNN and FCNN. To minimize the performance gap, we enforce BCNN to produce similar intermediate feature maps with the ones of FCNN. This training strategy, i.e., optimizing each binary convolutional block with block-wise distillation loss derived from FCNN, leads to a more effective optimization to BCNN. It also motivates us to update the binary convolutional block architecture to facilitate the optimization of block-wise distillation loss. Specifically, a lightweight shortcut branch is inserted into each binary convolutional block to complement residuals at each block. Benefited from its Squeeze-and-Interaction (SI) structure, this shortcut branch introduces a fraction of parameters, e.g., 10\% overheads, but effectively complements the residuals. Extensive experiments on ImageNet demonstrate the superior performance of our method in both classification efficiency and accuracy, e.g., BCNN trained with our methods achieves the accuracy of 60.45% on ImageNet.

1 Introduction

Many milestone works [26,14,9] have been conducted to design deeper and more powerful Convolutional Neural Network (CNN) architectures. Thanks to those efforts, the performance of deep CNNs has been significantly boosted. Meanwhile, existing deep CNNs usually consist of millions of parameters and consume billions of Floating Point Operations Per second (FLOPs) for computation. Those properties limit their applications in scenarios with limited computation and memory capabilities. As there are growing demands to run vision tasks on portable devices, many research efforts [30,20,17,31] aim to reduce the space and computational complexities. One popular strategy is to convert Floating point CNNs (FCNNs) into Binary CNNs (BCNNs), where the binarization significantly improves the computation and memory efficiency.

As an early BCNN work, BNN [11] is proposed to train networks with weights and activations constrained to ± 1 . It is efficient but exhibits degraded performance on large dataset like ImageNet [2]. Many recent works like XNOR-Net [25], ABC-Net [16], Bi-Real Net [21], PCNN [5], BONNs [6] and CI-BCNN [28] have continuously boosted the performance of BCNN, e.g., CI-BCNN [28], achieves ImageNet classification accuracy of 59.9%, substantially better than the 51.2%



(c) feature map residuals optimized by our methods

Fig. 1: Visualization of intermediate feature maps from 4 convolutional blocks of ResNet18 [9] in (a) and feature map residuals between ResNet18 and baseline BCNN [1] in (b). (c) illustrates feature map residuals optimized by our methods.

of XNOR-Net [25]. However, there still exists a substantial performance gap between BCNNs and FCNNs, which easily achieve accuracy of 69% on ImageNet [2]. More detailed review to BCNNs will be given in Sec. 2.

Compared with FCNN, BCNN shows limited modeling capability because of its binary convolutional kernels. Meanwhile, BCNN training is not as efficient as the training of FCNN. For instance, it is difficult to implement gradient back prorogation with binary parameters. Therefore, BCNN training involves two sets of parameters [11], i.e., binarized parameters and floating point parameters, respectively. Binarized parameters are used for forward propagation and loss computation. Floating point parameters are used for loss back propagation and parameter updating. Inferior convolutional layers and training strategy result in substantially different network responses. As shown in Fig. 1 (a) and (b), FCNN and BCNN generate different intermediate feature maps for the same input. Such block-wise residuals may accumulate as the network goes deeper, resulting in substantial performance gap at the output block.

This work targets to study more efficient BCNN training strategies, as well as more effective BCNN architectures. Training BCNNs with back-propagation suffers from vanishing gradient and quantization error in parameter binarization. To alleviate those issues, we leverage intermediate feature maps of FCNN for BCNN training. This can be implemented by training each binary convolutional block with distillation loss. In other words, each BCNN block is supervised to produce similar outputs with its corresponding FCNN block. Existing methods mostly use FCNN for BCNN initialization. Compared with those works, blockwise distillation loss could better leverage FCNN in BCNN training and shows potential to eliminate aggregated residuals as network goes deeper.

Limited model capability of binary convolution hinders BCNN to simulate the behavior of FCNN. This makes it hard to optimize block-wise residuals as shown in Fig. 1. We propose to complement the residuals with additional shortcut branches, which are inserted into each binary convolutional block to enhance model capability. Compared with original feature maps, residual feature maps exhibit limited variance. Therefore, we manage to model them with a lightweight Squeeze-and-Interaction (SI) shortcut. Specifically, to compute a residual feature map with C channels, the squeeze module first computes a feature map with S,S < C channels, which is then feed into the interaction module to recover feature map with C channels. The parameter S is block-dependent and is automatically selected. SI shortcut introduces marginal parameter overheads, but facilitates optimization of block-wise distillation loss.

We conduct image classification experiments on two widely used datasets CIFAR-10 [13] and ImageNet [2]. Experimental results show that our distillation loss and SI shortcut effectively boost the BCNN performance. As illustrated in Fig. 1 (c), feature maps from BCNN optimized by our method are more similar to the ones by FCNN. It is also interesting to observe that, SI shortcuts only take about 10% parameter overheads, but substantially boosts the classification accuracy, e.g., we achieve 60.45% accuracy on ImageNet. We also make comparisons with other recent BCNNs, where our method achieves competitive performance, e.g., 2.13% better than the recent IR-Net [?] in accuracy on ImageNet.

Most of current works use FCNNs for BCNN initialization. To the best of our knowledge, this is an early work leveraging FCNNs for BCNN optimization through block-wise knowledge distillation. Another recent work [18] leverages FCNN in BCNN training through training GANs. Compared with this work, our model performs better and does not need to train GANs, hence could be easier to train and optimize. Our promising performance also thanks to the introduction of SI shortcuts, which complement the block-wise residuals, thus facilitate the BCNN optimization. Those novel components guarantee the competitive performance of our methods.

2 Related Work

Existing works on Binary Convolutional Neural Network (BCNN) can be roughly summarized into two categories according to their parameter overheads.

The first category improves the performance of BCNN by introducing new convolutional layers, loss functions [11,25,21,7,18,6,28], etc. BNN [11] achieves high classification accuracy on small datasets, like CIFAR-10 [13], but does not perform well on large-scale ImageNet [2]. XNOR-Net [25] boosts performance by introducing binary convolutional kernels with scalars. Bi-Real Net [21] further enhances performance by connecting real activations before sign function of the next block. RBCN [18] trains GANs to affiliate BCNN training. BONNs [6] develops a novel Bayesian learning algorithm. The recent CI-BCNN [28] mines channel-wise interactions through reinforcement learning.

The other category introduces more parameters [16,35,5,19,32], and achieves better performance. Related works include ABC-Net [16], GroupNet [35], PCNN [5], CBCN [19] and BENN [32]. Both ABC-Net [16] and GroupNet [35] fuse several binary convolutional layers to approximate one floating point convolutional layer.

PCNN [5] learns a set of diverse quantized kernels to improve the performance. CBCN [19] proposes a circulant binary convolution, which takes about 16 times more calculations than a simple BCNN. BENN [32] regards BCNNs as weak classifiers and uses AdaBoost [3] to ensemble these classifiers.

This work belongs to the first category. Our work differs with previous ones in that, it uses block-wise distillation loss to train each binary convolutional block. Some works leverage knowledge distillation to train quantized networks [23,24,34]. However, training binary networks with knowledge distillation is still under-explored. The reason could be that, the limited model capability of BCNN makes it hard to simulate the real-value network, leading to difficulty in training convergence. Our SI shortcut boosts the model capability and simplifies the block-wise distillation loss optimization. MoBiNet [7] adds parameter-free shortcuts between CNN blocks to prevent vanishing gradient and make convergence easier. Different from MoBiNet [7], we add SI shortcuts with learnable parameters inside CNN blocks to complement block-wise residuals. Our method also performs better than MoBiNet on ImageNet, e.g., 60.45% vs. 54.40%.

3 Problem Formulation

Convolutional block is the basis of feature learning in CNN. Each convolutional block in real-value network generally consists of convolutional kernels, activations functions, as well as Batch Normalization (BN) [12] layers, etc. Given an input image I, a feature O_N can be extracted from I by stacking N convolutional blocks. We denote the computation of O_N as,

$$O_N = \mathbf{R}_N(\dots \mathbf{R}_2(\mathbf{R}_1(I))\dots),\tag{1}$$

where $\mathbf{R}_i(\cdot)$ is the *i*-th convolutional block with real-value parameters. O_N can be used for classification [14,26,9], segmentation [22,33,10], or detection [4,8].

Our goal is to simulate the behavior of the real-value network using a binary network with similar structure, which can be denoted as, *i.e.*,

$$\bar{O}_N = \mathbf{B}_N(\dots \mathbf{B}_2(\mathbf{B}_1(I, \theta_1), \theta_2) \dots, \theta_N), \tag{2}$$

where \bar{O}_N represents the output of the N-th binary convolutional block. $\mathbf{B}_i(\cdot, \theta_i)$ denotes the *i*-th binary convolutional block and θ_i represents its binary parameters. Note that, because of BN [12] layers in $\mathbf{B}_i(\cdot, \theta_i)$, the produced feature \bar{O}_N can be a real-value tensor.

As discussed in many works [11,25,21], the binary network can be optimized by Back-Propagating (BP) training loss computed with \bar{O}_N to update each $\mathbf{B}_i(\cdot,\theta_i)$. This training can be achieved by maintaining real-value parameters θ_i^* , updating θ_i^* with BP loss, and binarizing θ_i^* to θ_i . Because of quantization errors and vanishing gradients, this training strategy is not efficient in optimizing $\mathbf{B}_i(\cdot,\theta_i)$, especially for convolutional blocks far from the output layer.

To seek a more efficient training strategy, we propose to supervise each binary convolution block with additional cues. Inspired by recent works on distillation learning [24,23,29], we leverage distillation loss derived from FCNN. In other words, each $\mathbf{B}_i(\cdot, \theta_i)$ is enforced to produce similar output with $\mathbf{R}_i(\cdot)$. This blockwise distillation loss trains binary convolution block in a more straightforward way. The distillation loss for the *i*-th convolutional block can be denoted as,

$$\mathcal{L}_i^D = \mathcal{D}(O_i, \bar{O}_i), \tag{3}$$

where $D(\cdot)$ computes distillation loss based on outputs from $\mathbf{R}_i(\cdot)$ and $\mathbf{B}_i(\cdot, \theta_i)$. Directly optimizing \mathcal{L}_i^D could be difficult because of the limited model capability of binary convolutional kernels. This leads to substantial residuals when comparing O_i and \bar{O}_i . To facilitate the optimization of distillation loss, we further introduce shortcut branches into $\mathbf{B}_i(\cdot, \theta_i)$ to complement residuals of feature maps. Introducing K branches updates the original $\mathbf{B}_i(\cdot, \theta_i)$ as $\bar{\mathbf{B}}_i(\cdot)$, *i.e.*,

$$\bar{\mathbf{B}}_{i}(\bar{O}_{N-1}) = \mathbf{B}_{i}(\bar{O}_{N-1}, \theta_{i}) + \sum_{k=1}^{K} \mathbf{b}_{i}(\bar{O}_{N-1}, \gamma_{i}^{(k)}), \tag{4}$$

where $\mathbf{b}_i(\cdot, \gamma_i^{(k)})$ denotes the k-th shortcut branch with binary parameters $\gamma_i^{(k)}$. As shown in Eq. (4), K branches are used to model the residuals between $\mathbf{R}_i(\cdot)$ and $\mathbf{B}_i(\cdot, \theta_i)$. With a properly trained $\mathbf{B}_i(\cdot, \theta_i)$, the residual would exhibit limited complexity and variation. This makes it possible to compress the K branches and limit their parameter overheads below a given threshold ϵ . We hence represent the overall training loss as.

$$\min_{\Theta} \mathcal{L} = \mathcal{L}^{BP} + \alpha \sum_{i=1:N} \mathcal{L}_i^D, \text{ subject to } \sum_{i=1:N} \sum_{k=1:K} |\gamma_i^{(k)}| < \epsilon,$$
 (5)

where Θ denotes the collection of parameters in binary network and \mathcal{L}^{BP} denotes the loss at output layer. ϵ denotes the limitation to memory overheads introduced by shortcut branches. α denotes the loss weight.

4 Implementation

This section first presents our main branch structure, then proceeds to introduce the Squeeze-and-Interaction (SI) shortcut and block-wise distillation loss.

4.1 Structure of Main Branch

The main branch implements the computations in Eq. (2). Fig. 2(a) illustrates the structure of a binary convolutional block in the main branch. It contains a sign function to convert a real-value tensor into a binary one, which is hence computed with a binary convolution kernel. The output is input into a Batch Normalization layer to produce a real-value tensor as the output.

Biased sign function: Sign function converts real-value tensors to binary ones and causes considerable quantization errors. Existing networks [11,21,25]

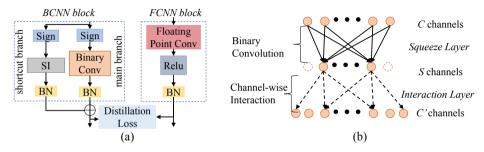


Fig. 2: Illustration of our convolutional block in (a) and Squeeze-and-Interaction (SI) shortcut in (b). Our convolution block consists of a main branch and K(K = 1) SI shortcut branches. SI computes a S-channel feature map with the squeeze layer, then generates a C'-channel output with the interaction layer, S < C'.

implement the sign function with a fixed threshold 0, *i.e.*, values larger than 0 are quantized to 1, otherwise -1. In order to decrease the quantization errors, we introduce a trainable threshold t to implement a biased sign function into each convolutional block. This biased sign function can be denoted as,

$$sign(x,t) = \begin{cases} -1 & \text{if } x \le t, \\ 1 & \text{if } x > t, \end{cases}$$
 (6)

where t is the trainable threshold. The effects of t will be discussed in Sec. 5.3.

Forward propagation: We follow the method in previous works [11] to implement the forward propagation of main branch. For the *i*-th convolutional block, the main branch first binarizes the input tensor \bar{O}_{i-1} with the sign function, Then, the binary convolution is computed with XNOR and bitcount operations. We represent the forward propagation of main branch in a binary convolutional block as,

$$\bar{O}_i = \text{BN}(\text{sign}(\bar{O}_{i-1}, t) \otimes \theta_i)), \theta_i = \text{sign}(\theta_i^*, 0), \tag{7}$$

where \otimes denotes convolution, θ_i^* is the floating point parameters used for loss back propagation and parameter updating, sign function converts θ_i^* into binary convolutional kernel θ_i . BN(·) is the Batch Normalization.

Compared with the convolutional block in FCNN, the one in BCNN omits the ReLU layer and accelerates the computation with binary convolutions. Our training stage learns proper t, θ_i^* , and BN parameters, to simulate the convolutional block in FCNN. We proceed to introduce the SI shortcut and block-wise distillation loss to facilitate the optimization.

4.2 Squeeze-and-Interaction Shortcut Branch

As shown in Fig. 2(a), a Squeeze-and-Interaction (SI) shortcut branch is trained to produce a residual feature map, which is hence fused with the feature map

from main branch. Computed based on a properly trained main branch, the target residual feature map would exhibit limited variances. This makes it possible to model residuals with a lightweight shortcut branch.

This intuition leads to the structure in Fig. 2(b). We take VGG-small network as an example. For an input tensor \bar{O}_{i-1} with C channels, the shortcut branch first converts it into a binary tensor with Eq. (6). A squeeze layer uses $S \times 3 \times 3 \times C$ sized binary convolutional kernel to produce a S (S < C') channel feature map. Then, this feature map is mapped to a C' channel feature map by interaction layer with sparse channel-wise connections.

Squeeze Layer: The squeeze layer is learned by firstly predicting a C' channel feature map with a $C' \times 3 \times 3 \times C$ sized binary convolutional kernel, then keeping S channels and discarding the others. There are many ways to select S channels, e.g., through random selection. We perform automatic selection by introducing a learnable C'-dim real-value weighting vector ω . ω is learned to weight the importance of each channel in an end to end training. The computation of a squeeze layer can be represented as,

$$O_{sqz}[c] = \mathrm{BConv}(O_{in})[c] \times \omega[c], c = 1 : C'$$
(8)

where $\mathrm{BConv}(\cdot)$ denotes the binary convolution with kernel size $C' \times 3 \times 3 \times C$. During end to end training, ω could be learned together with the BConv. The weight vector ω provides cues about importance of each channel, e.g., a channel c would be more important, if $|\omega[c]|$ is larger. We hence could select and keep important channels according to the absolute values in ω .

Suppose we introduce K shortcut branches into each block, to keep the overall memory overheads ration below a given threshold $\epsilon \in [0,1]$ compared to the main branch. We need to select $n = \epsilon \times \sum_{i=1:N} C_i'$ channels to keep and discard the others. This can be conducted by a block-wise selection, which selects top $C_i' \times \epsilon$ channels with largest weights from the i-th block. A global selection strategy can also be conducted by selecting the top $\epsilon \times \sum_{i=1:N} C_i'$ channels in the BCNN. Different selection strategies will be tested in Sec. 5.3.

Interaction Layer: After selecting S channels, the interaction layer introduces a channel-wise sparse interaction to generate a feature map with C' channels. This is achieved by learning a real-value sparse matrix T with size $S \times C'$. The computation of interaction layer can presented as,

$$O_{itr} = O_{sqz} \times T, \tag{9}$$

where O_{itr} is the C' channel output by interaction layer.

To ensure high computation efficiency in interaction layer, T is kept sparse. It is initialized as a sparse matrix with S non-zero elements, where T[i][j] = 1 only if i and j correspond to the same channel. After end-to-end training, small values in T are set to 0 to ensure high sparsity. After selecting S channels in Squeeze layer and fixing T, shortcut branch is fine-tuned to recover performance.

Discussions: As discussed above, S at each shortcut branch is determined by our channel selection with ω . This strategy selects different channels for different CNN blocks, e.g., more channels are kept for important blocks. Another way



Fig. 3: Illustration of our block-wise distillation loss, which is computed with channel-wise and spatial-wise max pooling.

is directly training the shortcut branches with a given S. Compared with the given S, our strategy has potential to achieve better performance with the same memory overheads. More discussions will be presented in Sec. 5.3.

4.3 Block-wise Distillation Loss

As shown in Fig. 2(a), to facilitate the optimization of BCNN block, we compute distillation loss by referring to both the spatial and channel differences. This differs with many existing methods that train CNN by computing distillation loss at the end of the network [24,23]. Some other works [29] compute the distillation loss on feature maps after spatial pooling. Only considering the spatial pooling fails to mine the channel-wise differences.

As shown in Fig. 3, for $W \times H \times C$ sized feature maps, we first compute $W \times H$ sized spatial maps and C-dim vectors with spatial and channel-wise pooling. Then, the distillation loss on the i-th convolutional block can be computed between BCNN and FCNN. The computation can be denoted as,

$$\mathcal{L}_{i}^{D} = \|\frac{\operatorname{SP}(O_{i})}{\|\operatorname{SP}(O_{i})\|_{2}} - \frac{\operatorname{SP}(\bar{O}_{i})}{\|\operatorname{SP}(\bar{O}_{i})\|_{2}}\|_{2} + \|\frac{\operatorname{CP}(O_{i})}{\|\operatorname{CP}(O_{i})\|_{2}} - \frac{\operatorname{CP}(\bar{O}_{i})}{\|\operatorname{CP}(\bar{O}_{i})\|_{2}}\|_{2}, \quad (10)$$

where $SP(\cdot)$ denotes spatial-wise max pooling and $CP(\cdot)$ denotes channel-wise max pooling. $||\cdot||_2$ computes the L_2 norm of a vector.

Compare to method [29], the proposed distillation loss considers extra channel-wise difference. Another work [34] computes distillation loss on the entire feature map by measuring the element-wise difference. Compared with [34], our method could be more robust by involving pooling strategies. Different distillation loss functions are tested in Sec. 5.3.

4.4 Training and Optimization

Overall Loss Function: For each convolution block in BCNN, the loss is composed by a block-wise distillation loss and the task specific loss back-propagated by its subsequent layers. Therefore, our method is general and shows potentials to train BCNNs for different vision tasks. We conduct experiments on image classification task. The overall loss function can be formulated as

$$\mathcal{L} = \mathcal{L}^{BP}(\bar{O}_N, y) + \alpha \sum_{i=1:N} \mathcal{L}_i^D(O_i, \bar{O}_i), \tag{11}$$

where y is the ground-truth image-label and \bar{O}_N is feature for classification. $L^{BP}(\cdot)$ computes the image classification loss at output layer. We use cross-entropy loss to implement $L^{BP}(\cdot)$. Loss weight α is set as 0.1 referring to [29].

Optimization: In forward-propagation shown in Eq. (7), the sign(x) function is not derivable at x = 0. We refer to the straight-through estimator [11] for network training. We use real-value parameters θ^* for back-propagation and network training. For the *i*-th convolutional block, derivatives of loss with respective to network parameters is computed as,

$$\frac{\partial \mathcal{L}}{\partial \theta_{i}^{*}} = \alpha \frac{\partial \mathcal{L}_{i}^{D}}{\partial \theta_{i}^{*}} + \frac{\partial \mathcal{L}^{BP}}{\partial \theta_{i}^{*}} = (\alpha \frac{\partial \mathcal{L}_{i}^{D}}{\partial \bar{O}_{i}} + \frac{\partial L^{BP}}{\partial \bar{O}_{i}}) \frac{\partial \bar{O}_{i}}{\partial \operatorname{sign}(\theta_{i}^{*}, 0)} \frac{\partial \operatorname{sign}(\theta_{i}^{*}, 0)}{\partial \theta_{i}^{*}} \\
= \frac{\partial (\alpha \mathcal{L}_{i}^{D} + \mathcal{L}^{BP})}{\partial \bar{O}_{i}} \frac{\partial \bar{O}_{i}}{\partial \operatorname{sign}(\theta_{i}^{*}, 0)} 1_{|\theta_{i}^{*}| < 1}.$$
(12)

For back-propagation, derivative of input of previous block is calculated as:

$$\frac{\partial(\alpha \mathcal{L}_{i}^{D} + \mathcal{L}^{BP})}{\partial \bar{O}_{i}} = \frac{\partial(\alpha \mathcal{L}_{i}^{D} + \mathcal{L}^{BP})}{\partial \operatorname{sign}(\bar{O}_{i}, b)} \frac{\partial \operatorname{sign}(\bar{O}_{i}, b)}{\partial \bar{O}_{i}} = \frac{\partial(\alpha \mathcal{L}_{i}^{D} + \mathcal{L}^{BP})}{\partial \operatorname{sign}(\bar{O}_{i}, b)} \mathbf{1}_{|\bar{O}_{i} - b| < 1} (13)$$

Since \bar{O}_i is the sum of the outputs from main branch and SI shortcut branches. The back-propagation functions of $\partial \mathcal{L}_i^D/\partial \gamma_i^{(k)}$ and $\partial \mathcal{L}^{BP}/\partial \gamma_i^{(k)}$, $k \in 1, 2...K$ share a similar formulation as $\partial \mathcal{L}_i^D/\partial \theta_i$ and $\partial \mathcal{L}^{BP}/\partial \theta_i$ respectively.

5 Experiments

5.1 Dataset

We conduct experiments on two widely used datasets to evaluate BCNN performance: CIFAR-10 [13] and ImageNet [2]. CIFAR-10 consists of $60k\ 32\times32$ sized images in 10 classes, including 50k training images and 10k test images. ImageNet is a large-scale dataset with 1,000 classes and 1.2 million training images, as well as 50k validation images. We use DGRL to represent our BCNN.

5.2 Implementation Details

We trains two BCNNs according to VGG-small and ResNet18, respectively. Note that, existing works [21,28] do not binarize the first conv layer, the last fc layer, as well as 1×1 convolutions in ResNet. We also follow this setting. The following parts present details for training of those two BCNN.

VGG-small: We conduct all experiments on CIFAR-10 using VGG-small [11]. We pad 4 pixels on each side of the input image and crop it randomly into the size of 32×32 . Then, all images are scaled into the range [-1,1]. We use the block structure as in XNOR-Net [25] as the main branch. The real-value VGG-small network proposed in [26] is used as the FCNN for BNN training. FCNN is trained on CIFAR-10. We regard each convolutional layer with a ReLU layer and BN layer as a convolutional block. This leads to 5 convolutional blocks and

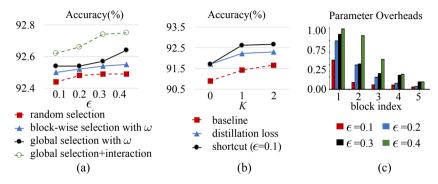


Fig. 4: Validity of channel selection in (a) and branch number K in (b). (c) shows parameter overheads in each block after global channel selection.

5 block-wise distillation loss computed. We train BCNN from scratch and follow the settings in XNOR-Net [25]. We set batch size as 128 and initial learning rate as 0.01. We train it for 320 epochs and reduce the learning rate by 0.1 at epoch 120, 200, 240 and 280, respectively.

ResNet18: On ImageNet, we build BCNN with ResNet18. For each input image, a 224×224 region is randomly cropped for training from the resized image whose short side is 256. We use the basic block in BinaryResNetE [1] to implement our main branch. The ResNet proposed in [9] is used as the corresponding FCNN, which is trained on ImageNet. ResNet18 has 4 convolutional blocks. Our binary ResNet18 hence has 4 binary convolutional block and is trained with 4 block-wise distillation loss. During training, we set the batch size as 1024 and the initial learning rate as 0.004. We train the BCNN from scratch for 120 epochs and reduce the learning rate by 0.1 at epoch 70, 90 and 110, respectively.

5.3 Ablation Study

This part first tests the validity of SI shortcuts, then analyzes the choosing of branch number K, as well as our training strategies. Experiments are conducted on CIFAR-10 with VGG-small.

Channel Selection in SI shortcut: Channel number S can be selected with different strategies, e.g., random selection, block-wise selection with ω , and global selection with ω . This part tests those strategies with different parameter overheads, i.e., ϵ and presents the comparison in Fig. 4(a). It is clear that, random selection gets the worst performance. This implies that, different channels present varied importance, thus should not be randomly selected. Layer-wise selection with ω outperforms the random selection, indicating the validity of weight vector ω . Global selection further outperforms block-wise selection. This shows that different CNN blocks present different importance. To maintain the same parameter overheads, more channels should be kept for important blocks.

Fig. 4(a) shows that, larger ϵ does not bring substantial performance boost over $\epsilon = 0.1$, indicating that the block-wise residuals can be effectively modeled

distillation	-	spatial	\mathcal{L}^D	\mathcal{L}^D	\mathcal{L}^D	\mathcal{L}^D
K	0	0	0	1	1	1
sign func.	t=0	t=0	t=0	t=0	learned t	learned t
step train	-	-	-	-	-	✓
accuracy(%)	90.90	91.32	91.71	92.25	92.41	92.62

Table 1: Validity of block-wise distillation loss \mathcal{L}^D , biased sign function, and step training strategy. K = 1 means adding a shortcut branch with $\epsilon = 0.1$.

with a lightweight shortcut. Fig. 4(a) also shows the performance after inserting the Interaction layer, which brings more substantial performance gains than larger ϵ , showing the importance of Interaction layer. According to Fig. 4(a), we fix $\epsilon = 0.1$ to ensure network compactness.

Shortcut Branch Number K: Each convolutional block allows to introduce K shortcut branches. Introducing more branches potentially helps to complement the block-wise residuals with FCNN. We hence test the parameter K and present the results in Fig. 4(b). We use XNOR-Net [25] as the baseline, and first insert shortcut branches having the same structure with main branch. Therefore, introducing 2 shortcut branches leads to 200% parameter overheads. As shown in Fig. 4(b), adding more branches to baseline boosts its performance. Meanwhile, block-wise distillation loss is also beneficial for performance boost. Fig. 4(b) also compares the performance of inserting SI branches which involve channel selection with parameter overhead $\epsilon = 0.1$. It can be observed, SI branch effectively drops the memory overheads, meanwhile boosts the performance. Also, channel selection with a fixed ϵ is not sensitive to a larger K. We hence fix K = 1 in following experiments.

Training Strategies: Table 1 further shows the validity of distillation loss, biased sign function, and step training strategy, which are valid in boosting BCNN performance. Distillation loss computed with spatial and channel pooling, *i.e.*, \mathcal{L}^D , outperforms the one computed only with spatial pooling. \mathcal{L}^D brings substantial performance gains over the baseline. Biased sign function with learned t outperforms the one with fixed t = 0. Our step training, *i.e.*, first train and fix main branch then train shortcut branch, further brings performance gains. Combination of those strategies boosts the accuracy from 90.9% to 92.62%. Note that, in each block we use addition to fuse features from main and shortcut branches. This simple fusion strategy fits well for our step training strategy.

Discussions: Fig. 4(c) analyzes parameter overheads introduced to each block by the global channel selection. With different ϵ , our method tends to keep more channels for shallow CNN blocks. This could be because shallow blocks are the basis for learning discriminative features. Well-trained shallow blocks also help to alleviate the accumulated residuals as network goes deeper. Fig. 5 shows statistics of feature map residuals on ImageNet, where both spatial and channel-wise residuals between BCNN and FCNN are illustrated. It is clear that, our BCNN, *i.e.*, DRGL, produces smaller residuals than baseline BCNN [1], indicating our BCNN could better simulate the responses in FCNN.

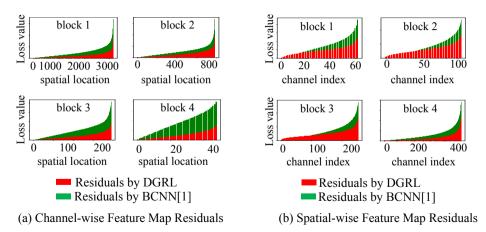


Fig. 5: Statistics of feature map residuals on ImageNet. Different blocks have different spatial sizes and channel numbers.

5.4 Comparison with Recent Works

Comparison on CIFAR-10: We compare with quantized networks including BWN [25], TWN [15], TBN [27], and BCNN including BNN [11], XNOR-Net [25], CI-BCNN [28]. The comparisons are summarized in Table 2 (a).

Table 2 (a) shows that our method achieves promising performance compared with quantized networks, which present higher computation and memory complexities. Only using main branch with K=0, our DGRL outperforms BWN [25] and TBN [27], and achieves comparable performance with TWN [15], e.g., our 92.29% vs. 92.56% of TWN with about 2% FLOPs of TWN. With a shortcut branch, DGRL achieves accuracy of 92.62%, which outperforms TWN [15].

DGRL also outperforms other binary networks in Table 2 (a). DGRL with $K{=}0$ share the same structure with baseline XNOR-Net [25]. Our training strategy makes DRGL outperforms the baseline by 2.39%, showing the effectiveness of our distillation loss. We also show the performance using the SI shortcut without distillation loss, which also outperforms XNOR-Net [25]. DGRL with $K{=}1$ and distillation loss further outperforms the recent CI-BCNN on CIFAR-10 with similar computational and memory complexities.

Comparison on ImageNet: We compare DGRL with quantized neural networks including BWN [25], DoReFa-Net [31], ABC-Net [16] with 3 bases, and binary neural networks including XNOR-Net [25], ABC-Net [16] with 1 base, Bi-Real-Net [21], BENN [32], PCNN [5], RBCN [18], BONNs [6], BinaryRes-NetE18 [1], CI-BCNN [28], MoBiNet-Mid [7]. Table 2 (b) shows the comparisons.

Comparisons with quantized networks in Table 2 (b) shows similar conclusion with that in Table 2 (a). DGRL (K=1) achieves comparable performance with BWN [25] using 20% of its FLOPs. DGRL outperforms the other three quantized networks in aspects of classification accuracy, FLOPs, and model size. For example, ABC-Net [16] involves 3 convolutional bases. BENN [32] ensem-

 1.48×10^{7}

14.84

Table 2: Comparison of with recent works. W, A denote the precision of network parameters and activations, respectively.

(a) comparison on offfitt to with vad smain.								
Methods	accuracy (%)	W (bits)	A (bits)	FLOPs	Size (Mbits)			
Full-precision	93.20	32	32	6.17×10^{8}	428.96			
BWN [25]	90.10	1	32	3.09×10^{8}	14.80			
TWN [15]	92.56	2	32	6.17×10^{8}	28.16			
TBN [27]	90.87	1	2	1.80×10^{7}	14.80			
BNN [11]	89.90	1	1	1.32×10^{7}	14.80			
XNOR-Net [25]	89.80	1	1	1.32×10^{7}	14.80			
CI-BCNN [28]	92.47	1	1	1.32×10^{7}	14.80			
DGRL $(K=0)$	92.29	1	1	1.32×10^{7}	14.80			
DGRL $(K=1)$ w/o \mathcal{L}^D	91.59	1	1	1.48×10^{7}	14.84			

(a) Comparison on CIFAR-10 with VGG-small.

(b) Comparison on ImageNet with ResNet18. † denotes further compressing 1×1 convolutions in SI branch with channel selection.

92.62

DGRL (K=1)

Methods	accuracy (%)	W (bits)	A (bits)	FLOPs	Size (Mbits)			
Full-precision	69.30	32	32	1.81×10^{9}	374.1			
BWN [25]	60.80	1	32	9.20×10^{8}	33.7			
DoReFa-Net [31]	59.20	1	4	4.91×10^{8}	182.72			
ABC-Net [16]	49.10	3	1	2.53×10^{8}	81.6			
BENN [32]	53.60	1	1	5.01×10^{8}	101.1			
XNOR-Net [25]	51.20	1	1	1.67×10^{8}	33.7			
ABC-Net [16]	42.70	1	1	1.05×10^{8}	27.2			
Bi-Real-Net [21]	56.40	1	1	1.63×10^{8}	33.6			
PCNN [5]	57.30	1	1	1.67×10^{8}	33.7			
RBCN [18]	59.50	1	1	1.67×10^{8}	33.7			
BONNs [6]	59.30	1	1	1.63×10^{8}	33.6			
BinaryResNetE18 [1]	58.10	1	1	1.63×10^{8}	33.6			
CI-BCNN [28]	59.90	1	1	1.63×10^{8}	33.6			
MoBiNet-Mid [7]	54.40	1	1	0.52×10^{8}	25.1			
DGRL $(K=0)$	59.50	1	1	1.63×10^{8}	33.6			
$DGRL(K=1)$ w/o \mathcal{L}^D	59.83	1	1	1.84×10^{8}	35.5			
DGRL $(K=1)$	60.45	1	1	1.84×10^{8}	35.5			
\mathbf{DGRL}^{\dagger} (K=1)	60.23	1	1	1.68×10^{8}	34.6			

bles outputs from 3 BCNNs. DRGL features more efficient design and better performance than those networks.

DGRL (K=1) consistently outperforms the other binary networks. DGRL with K=0 share identical structure with BinaryResNetE18 [1]. Our training strategy, e.g., block-wise distillation loss boosts the baseline performance by 1.40%. Adding one SI shortcut boosts the performance by 1.73%. Combining SI shortcut and distillation loss achieves the performance of 60.45%, outperforming the state-of-the art CI-BCNN [28] by 0.55%. RBCN [18] trains the BCNN with a FCNN by training GANs. Our method substantially outperforms RBCN, i.e., 60.45% vs. 59.5%. Our training strategy is also more straight-forward and efficient than training GANs in RBCN.

Efficiency and Memory Usage: Efficiency and memory usage are important for BCNNs. Table 2 compares the FLOPs and model size across different

	operation	conv0	block0	block1	block2	block3	fc	sum
	binary	-	7.2	6.3	6.3	6.3	-	26.1
main branch	float	118	0.0002	6.4	6.4	6.4	0.5	137
SI shortcut	binary	-	0.76	0.38	0.049	0.001	-	1.2
51 SHOFTCUT	float	_	0.0083	2.71	0.35	0.01	_	3.1

Table 3: FLOPs (in million) in main and SI shortcut branches in DGRL[†] (K=1) implemented with ResNet18. FLOPs is computed following [21].

networks. The model size and FLOPs are computed following [21], where FLOPs is computed as the amount of floating point multiplications plus 1/64 of the amount of 1-bit multiplication. Table 2 shows that DGRL achieves competitive performance with reasonably good computation and memory efficiency. Compared with full-precision network on CIFAR-10, our method achieves slightly lower accuracy, *i.e.*, 92.62% vs. 93.20%, but significantly saves FLOPs by about $40\times$, and saves memory by about $29\times$. Compared with quantized networks on ImageNet, DGRL shows substantial advantages in efficiency and compactness. It also outperforms other BCNN with comparable efficiency and compactness.

Table 2 shows that, the SI shortcut branch introduces marginal computational and parameter overheads, e.g., 5.7% and 12.9% in model size and FLOPs, respectively over the baseline in Table 2 (b). For our binary ResNet18, we can compress the 1×1 convolutions in SI shortcuts by discarding channels with small weights learned by Eq. (8). This operation further decreases the computations and memory overheads, while maintaining a similar performance, i.e., 60.23%.

To verify the effects of SI branches to BCNN inference speed, we test BCNNs with/without shortcut branches, and get similar average time to process one image: 2.063ms vs. 1.875ms on a 1080TI GPU using cuDNN 7.6.1 with batch size of 16. Thus, shortcut branch does not significantly slow down the speed. This is because main and shortcut branches are computed in parallel. The speed is decided by the slower one. Table 3 compares number of binary and floating operations in main and shortcut branches. It is clear that, shortcut branch needs less computations, thus is faster. Table 3 also shows that, the speed bottleneck of BCNN is the conv0, which is also not binarized in existing BCNNs [21,28].

6 Conclusion

This paper targets to learn a compact BCNN with guidance from FCNN. This is achieved by optimizing each binary convolutional block with block-wise distillation loss derived from FCNN, as well as updating binary convolutional block architecture. The block-wise distillation loss leads to a more effective optimization to BCNN. A lightweight shortcut branch with SI structure is inserted into each binary convolutional block to complement residuals at each block. Extensive experiments on CIFAR-10 and ImageNet demonstrate the superior performance of the proposed method in aspects of both classification accuracy and efficiency.

References

- Bethge, J., Yang, H., Bornstein, M., Meinel, C.: Back to simplicity: How to train accurate bnns from scratch? arXiv preprint arXiv:1906.08637 (2019)
- Deng, J., Dong, W., Socher, R., Li, L.J., Li, K., Fei-Fei, L.: Imagenet: A large-scale hierarchical image database. In: CVPR (2009)
- Freund, Y., Schapire, R.E.: A decision-theoretic generalization of on-line learning and an application to boosting. Journal of computer and system sciences 55(1), 119–139 (1997)
- 4. Girshick, R.: Fast r-cnn. In: ICCV (2015)
- Gu, J., Li, C., Zhang, B., Han, J., Cao, X., Liu, J., Doermann, D.: Projection convolutional neural networks for 1-bit cnns via discrete back propagation. In: AAAI (2019)
- Gu, J., Zhao, J., Jiang, X., Zhang, B., Liu, J., Guo, G., Ji, R.: Bayesian optimized 1-bit cnns. In: ICCV (2019)
- Hai Phan, Dang Huynh, Y.H.M.S.Z.S.: Mobinet: A mobile binary network for image classification. In: WACV (2020)
- 8. He, K., Gkioxari, G., Dollar, P., Girshick, R.: Mask r-cnn. In: ICCV (2017)
- 9. He, K., Zhang, X., Ren, S., Sun, J.: Deep residual learning for image recognition. In: CVPR (2016)
- Huang, Z., Wang, X., Huang, L., Huang, C., Wei, Y., Liu, W.: Ccnet: Criss-cross attention for semantic segmentation. In: ICCV (2019)
- Hubara, I., Courbariaux, M., Soudry, D., El-Yaniv, R., Bengio, Y.: Binarized neural networks. In: NeurIPS (2016)
- 12. Ioffe, S., Szegedy, C.: Batch normalization: Accelerating deep network training by reducing internal covariate shift. In: ICML (2015)
- 13. Krizhevsky, A.: Learning multiple layers of features from tiny images (2009)
- Krizhevsky, A., Sutskever, I., Hinton, G.E.: Imagenet classification with deep convolutional neural networks. In: NeurIPS (2012)
- 15. Li, F., Zhang, B., Liu, B.: Ternary weight networks. arXiv preprint arXiv:1605.04711 (2016)
- Lin, X., Zhao, C., Pan, W.: Towards accurate binary convolutional neural network. In: NeurIPS (2017)
- Liu, C., Chen, L.C., Schroff, F., Adam, H., Hua, W., Yuille, A.L., Fei-Fei, L.: Auto-deeplab: Hierarchical neural architecture search for semantic image segmentation. In: CVPR (2019)
- 18. Liu, C., Ding, W., Xia, X., Hu, Y., Zhang, B., Liu, J., Zhuang, B., Guo, G.: Rectified binary convolutional networks for enhancing the performance of 1-bit dcnns. In: IJCAI (2019)
- 19. Liu, C., Ding, W., Xia, X., Zhang, B., Gu, J., Liu, J., Ji, R., Doermann, D.: Circulant binary convolutional networks: Enhancing the performance of 1-bit dcnns with circulant back propagation. In: CVPR (2019)
- Liu, Z., Mu, H., Zhang, X., Guo, Z., Yang, X., Cheng, K.T., Sun, J.: Metapruning: Meta learning for automatic neural network channel pruning. In: ICCV (2019)
- 21. Liu, Z., Wu, B., Luo, W., Yang, X., Liu, W., Cheng, K.T.: Bi-real net: Enhancing the performance of 1-bit cnns with improved representational capability and advanced training algorithm. In: ECCV (2018)
- 22. Long, J., Shelhamer, E., Darrell, T.: Fully convolutional networks for semantic segmentation. In: CVPR (2015)

- Mishra, A., Marr, D.: Apprentice: Using knowledge distillation techniques to improve low-precision network accuracy. arXiv preprint arXiv:1711.05852 (2017)
- Polino, A., Pascanu, R., Alistarh, D.: Model compression via distillation and quantization. arXiv preprint arXiv:1802.05668 (2018)
- Rastegari, M., Ordonez, V., Redmon, J., Farhadi, A.: Xnor-net: Imagenet classification using binary convolutional neural networks. In: ECCV (2016)
- 26. Simonyan, K., Zisserman, A.: Very deep convolutional networks for large-scale image recognition. In: ICLR (2014)
- 27. Wan, D., Shen, F., Liu, L., Zhu, F., Qin, J., Shao, L., Tao Shen, H.: Tbn: Convolutional neural network with ternary inputs and binary weights. In: ECCV (2018)
- 28. Wang, Z., Lu, J., Tao, C., Zhou, J., Tian, Q.: Learning channel-wise interactions for binary convolutional neural networks. In: CVPR (2019)
- Zagoruyko, S., Komodakis, N.: Paying more attention to attention: Improving the performance of convolutional neural networks via attention transfer. In: ICLR (2017)
- 30. Zhang, X., Zhou, X., Lin, M., Sun, J.: Shufflenet: An extremely efficient convolutional neural network for mobile devices. In: CVPR (2018)
- 31. Zhou, S., Wu, Y., Ni, Z., Zhou, X., Wen, H., Zou, Y.: Dorefa-net: Training low bitwidth convolutional neural networks with low bitwidth gradients. arXiv preprint arXiv:1606.06160 (2016)
- 32. Zhu, S., Dong, X., Su, H.: Binary ensemble neural network: More bits per network or more networks per bit? In: CVPR (2019)
- 33. Zhu, Z., Xu, M., Bai, S., Huang, T., Bai, X.: Asymmetric non-local neural networks for semantic segmentation. In: ICCV (2019)
- Zhuang, B., Shen, C., Tan, M., Liu, L., Reid, I.: Towards effective low-bitwidth convolutional neural networks. In: CVPR (2018)
- 35. Zhuang, B., Shen, C., Tan, M., Liu, L., Reid, I.: Structured binary neural networks for accurate image classification and semantic segmentation. In: CVPR (2019)