## A Major Project Report

On

# ANALYZING MACHINE LEARNING ALGORITHMS FOR INTRUSION DETECTION IN IOT NETWORKS

Submitted in partial fulfilment of the requirements for the award of degree of

## **BACHELOR OF TECHNOLOGY**

IN

# COMPUTER SCIENCE AND ENGINEERING

**Under the Guidance of** 

Dr. C. ANJANAMMA

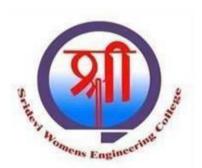
**Associate Professor** 

BY

MANKALA MANASA DEVI (21D21A05A5)

PATLOLLA NISHITHA (21D21A05B6)

SANIA MASROOR (21D21A05C4)



**Department of Computer Science and Engineering** 

# SRIDEVI WOMEN'S ENGINEERING COLLEGE

(An UGC Autonomous Institution)

(Estd. 2001 | Approved by AICTE& Govt. of TS | Affiliated to JNTUH Accredited by NBA and NAAC (A++) | Certified with ISO 9001:2015)

V.N. Pally, Gandipet, Hyderabad-75



#### **Department of Computer Science and Engineering**



## SRIDEVI WOMEN'S ENGINEERING COLLEGE

(An UGC Autonomous Institution)

(Estd. 2001 |Approved by AICTE & Govt. of TS |Affiliated to JNTUH Accredited by NBA and NAAC(A++) |Certified with ISO 9001:2015)

V.N. Pally, Gandipet, Hyderabad-75

2024-25



# **CERTIFICATE**

This is to certify that the MAJOR PROJECT report entitled "ANALYZING MACHINE LEARNING ALGORITHMS FOR INTRUSION DETECTION IN IOT NETWORKS" is being submitted by Mankala Manasa Devi (21D21A05A5), Patlolla Nishitha (21D21A05B6), Sania Masroor (21D21A05C4) in partial fulfillment for the award of degree of Bachelor of Technology in Computer Science and Engineering is a record of Bonafide work carried out by them.

INTERNAL GUIDE COORDINATOR

HEAD OF THE DEPARTMENT

Dr. C. ANJANAMMA

Dr. D. NAZIMODDIN

Dr. U. SRI LAKSHMI

**Associate Professor** 

**Associate Professor** 

**Professor** 

**EXTERNAL EXAMINER** 



DATE: 27-05-2025

#### **CERTIFICATE OF COMPLETION**

This is to certify that the following students MANKALA MANASA DEVI, PATLOLLA NISHITHA and SANIA MASROOR bearing with REG NO.: 21D21A05A5, 21D21A05B6 and 21D21A05C4 who are pursuing B.TECH (Computer Science & Engineering) at SRIDEVI WOMEN'S ENGINEERING COLLEGE, HYDERABAD, has successfully Completed their Mini Project Work at TRUPROJECTS EDUCATIONAL SERVICES PRIVATE LIMITED from 17th January, 2024 to 10th May, 2025 during the Major Project Work they worked on the Mini Project entitled ANALYZING MACHINE LEARNING ALGORITHMS FOR INTRUSION DETECTION IN 10T NETWORKS.

They were found punctual, hardworking and interested to learn the technologies during the Mini Project Work. They demonstrated good skills with self-motivate attitude towards learning.

Their association with the team was fruitful. We wish them all the best for future!

**Tru Projects** 

J. Mangi Kumar J MANOJ KUMAR

**Managing Director** 

RS BROTHERS BUILDING The Pavani Prestige 407, 4th floor, Ameerpet, Hyderabad, Telangana info@truprojects.in

+91 96761 90678

# **DECLARATION**

We, hereby declare that Major Project entitled "Analyzing Machine Learning Algorithms for Intrusion Detection in IoT Networks" is the work done during the period from 17<sup>th</sup> January, 2025 to 10<sup>th</sup> May, 2025 and is submitted in partial fulfillment of the requirements for the award of degree of Bachelor of Technology in Computer Science and Engineering from Jawaharlal Nehru Technological University, Hyderabad.

MANKALA MANASA DEVI (21D21A05A5)

PATLOLLA NISHITHA (21D21A05B6)

SANIA MASROOR (21D21A05C4)

# **ACKNOWLEDGEMENT**

We would like to express our sincere gratitude and indebtedness to our **Internal guide**, **Dr. C. ANJANAMMA**, **Associate Professor**, **Department of Computer Science and Engineering** for her valuable guidance, suggestions, and keen personal interest throughout the course of Major project and for her tireless patience in hearing all our seminars, minutely seeing all the reports and giving appropriate guidance and suggestions.

We are grateful for the support we received throughout the project and would like to express our sincere gratitude to our **Project Coordinator Dr. D. NAZIMODDIN, Associate Professor, Department of Computer Science and Engineering**, for his invaluable and timely coordination's throughout the Major Project work.

Our sincere gratitude goes out to **Dr. U. SRI LAKSHMI, Professor and Head of the Department, Department of Computer Science and Engineering**, for her insightful counsel and recommendations.

We also like to express our gratitude to **Dean Academics**, **Dr. K. SIVA NAGI REDDY**, **Professor**, **Sridevi Women's Engineering College**, for his valuable advice and insightful comments.

We are also extremely thankful to our **Principal Dr. A. NARMADA**, **Sridevi Women's Engineering College**, for her precious guidance and invaluable suggestions.

Finally, we would like to take this opportunity to thank all our faculty, family and friends for their support throughout this work. We also sincerely acknowledge and thank all those who gave directly or indirectly their support in completion of Major Project work.

MANKALA MANASA DEVI (21D21A05A5)

PATLOLLA NISHITHA (21D21A05B6)

SANIA MASROOR (21D21A05C4)

# LIST OF CONTENTS

S. NO.	TITLE	PAGE NO.
	TITLE PAGE	
	CERTIFICATE (COLLEGE)	ii
	CERTIFICATE(COMPANY)	iii
	DECLARATION	iv
	ACKNOWLEDGEMENT	v
	INDEX	vi
	LIST OF FIIGURES	viii
	ABSTRACT	ix
1	INTRODUCTION	1
	1.1 Purpose	1
	1.2 Scope	2
2	LITERATURE SURVEY	3
3	SYSTEM ANALYSIS	7
	3.1 Existing System	7
	3.2 Problem Statement	8
	3.3 Proposed System	8
4	SYSTEM REQUIREMENT SPECIFICATIONS	10
	4.1 Software Requirements	11
	4.2 Hardware Requirements	11
5	SYSTEM DESIGN	12
	5.1 System Architecture	12
	5.2 Modules	12
	5.3 Data Flow Diagram	16

	5.4 UML Diagrams	18
6	IMPLEMENTATION	26
	6.1 Sample Code	26
	6.2 Software Environment	35
7	SYSTEM TESTING	45
	7.1 Different Types of System Testing in Software Testing	45
	7.2 Test Cases	48
	7.3 Discussion of Results	50
8	CONCLUSION AND FUTURE ENHANCEMENT	56
	8.1 Conclusion	55
	8.2 Future Enhancement	55
9	REFERENCES	56

# LIST OF FIGURES

S.NO	FIGURE NO.	FIGURE NAME	PAGE NO.
1	5.1	System Architecture	12
2	5.3.1	Data Flow Diagram	17
3	5.4.1	Use Case Diagram	19
4	5.4.2	Class Diagram	20
5	5.4.3	Activity Diagram	21
6	5.4.4	Sequence Diagram	22
7	5.4.5	Collaboration Diagram	23
8	5.4.6	Component Diagram	24
9	5.4.7	Deployment Diagram	25
10	6.2.1	Anaconda for Windows	37
11	6.2.2	Anaconda Setup (Step 1)	38
12	6.2.3	Anaconda Setup (Step 2)	38
13	6.2.4	Anaconda Setup (Step 3)	39
14	6.2.5	Anaconda Setup (Step 4)	39
15	6.2.6	Anaconda Setup (Step 5)	40
16	6.2.7	Anaconda Setup (Step 6)	40
17	6.2.8	Anaconda Setup (Step 7)	41
18	6.2.9	Anaconda Setup (Step 8)	41
19	6.2.10	Anaconda Setup (Step 9)	42
20	6.2.11	Search Anaconda Prompt	43
21	7.3.1	Home Page	50
22	7.3.2	Registration Page	51
23	7.3.3	Login Page	52
24	7.3.4	Selection of Dataset	53
55	7.3.5	Output Page	54

# LIST OF TABLES

S.NO	TABLE NO.	TABLE NAME	PAGE NO
1	7.2	Test Cases	47
2	7.2.1	Test Case for User	47
		Registration	
3	7.2.2	Test Case for Admin Login	48
4	7.2.3	Test Case for Outcome	49

# **ABSTRACT**

This study presents a comprehensive analysis of machine learning algorithms in IoT Intrusion Detection Systems (IDS), focusing on critical aspects like accuracy, precision, recall, and F1 score. We examine the effect of data preparation methods—including normalization, outlier removal, standardization, and regularization—on model performance. The influence of dataset balancing, both balanced and unbalanced, is also assessed across four key IoT-focused datasets: CIC-IDS 2017, CIC-IoT-2023, IoT-ID20, and UNSW-NB15. To improve feature quality, ANOVA-based feature selection is applied. Eight algorithms, including Naive Bayes, K-Nearest Neighbors (KNN), AdaBoost, XGBoost, Support Vector Classifier (SVC), Logistic Regression, Decision Tree, and a Voting Classifier (comprising Boosted Decision Tree, Random Forest, and Bagging Extra Trees), are evaluated. Among these, the Voting Classifier ensemble demonstrated the highest performance in accuracy and overall classification metrics. This research highlights the potential of ensemble learning to optimize detection capabilities in IoT IDS, providing a robust framework for future developments in IoT security.