[This question paper contains 4 printed pages.]

Your Roll No

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Sr. No. of Question Paper: 1268 Unique Paper Code

: 32517916

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Name of the Paper

: Embedded Systems (DSE)

Name of the Course B.Sc.(H) Electronics

Semester VI

puration: 3 and 1/2 hours Maximum Marks: 75

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Instructions for Candidates

400

Write your Roll No. on the top immediately on receipt of this question paper.

There are seven questions in all, Attempt any Five 2. questions.

All questions carry equal marks. 2.

Q1(a) Differentiate between general-purpose computing system and embedded system in (6) terms of memory, performance, speed, power consumption, processing power, response time and execution.

What are the different types of embedded systems based on the performance of the (5) microcontroller? Briefly explain each one of them. (b)

It is said that Embedded Systems are Single functioned, Tightly Constrained and (4) (c) Reactive in Real Time. Explain each of these.

Q2 (a) Compare Von Neumann and Harward Architectures.

Draw and explain the Data Memory map (without extended I/O) and Program (5) (b)

What are the different reset sources in ATmega32 microcontroller? During Power- (6) (c) Up what are the conditions to be met for the CPU to function properly? Give three ways in which the reset pin can be connected.

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Q3 (a)	Which registers are allowed to be used as a pointer for Register Indirect Addressing Which registers are allowed to be used as a pointer for Register Indirect Addressing Which registers are allowed to be used as a pointer for Register Indirect Addressing Which registers are allowed to be used as a pointer for Register Indirect Addressing Which registers are allowed to be used as a pointer for Register Indirect Addressing Which registers are allowed to be used as a pointer for Register Indirect Addressing who we have they can be loaded which registers are allowed to be used as a pointer for Register Indirect Addressing who we have they can be loaded which registers are allowed to be used as a pointer for Register Indirect Addressing who we have they can be loaded which registers are allowed to be used as a pointer for Register Indirect Addressing who we have a loaded which registers are allowed to be used as a pointer for Register Indirect Addressing who we have a loaded which registers are allowed to be used as a pointer for Register Indirect Addressing who we have a loaded when accessing RAM? Give their names and show how they can be loaded as a pointer for Register Indirect Addressing who we have a loaded and the loaded as a pointer for Register Indirect Addressing who we have a loaded and the loaded and the loaded as a pointer for Register Indirect Addressing who we have a loaded and the loaded and the loaded as a pointer for Register Indirect Addressing who we have all the loaded and the load	(3)
	Which registers are allowed to be used their names and when accessing RAM? Give their names mode. Mode when accessing RAM? Give their names mode. Mode when accessing RAM? Give their names mode. If the current instruction and used in auto increment and pre-decrement mode. Differentiate between JMP, RJMP and LJMP instructions. If the current instruction change between JMP, RJMP and LJMP will the program execution change.	(6)
(b)	being executed is at ROM location oxzer	
	(i) JMP 0x12E4 (ii) RJMP 0x2E4 (iii) LJMPZ (Z = 0x12E4) (iii) LJMPZ (Z = 0x12E4)	
(c)	Explain Program Counter. What is the possible in AVR microcontrollers and why? Possible in AVR microcontrollers and why? A door sensor is connected to the port B pin 1, and an LED is connected to PORTC A door sensor is connected to the port B pin 1, and an LED is connected to poens, turn A door sensor is connected to the port B pin 1, and an LED is connected to poens, turn	(4)
Q4 (a)	A door sensor is connected to the port B pin 1, and an LED is connected to PORTC pin 7. Write an AVR program to monitor the door sensor and, when it opens, turn on the LED. Draw the circuit diagram.	·
(b)	8 LEDs are connected to Port B of the controller. Write an assembly language program which toggles all the LEDs after every 20ms. Assume clock frequency = 8 MHz.	
(c)	Assume clock frequency = 8 MHz. Assume clock frequency = 8 MHz. Give the details of the sequence of events that take place when an external interrupt flag is set, is recognized in an AVR microcontroller and corresponding interrupt flag is set. Explain the following interrupt related instructions - SEI, CLI, RETI	(6)
05(a)	What is PWM? Explain the difference between Fast PWM and Phase-corrected PWM with reference to Timer0.	(5)
(b) V	Why is precsaler used in timers? Servetal frequency is 6.144 MHz what shall be the Time Period of timer-clock if	(5)
A) abo	rescaler of 256 is used. Iso find the overflow time in normal mode for Timer 0, Timer 1 and Timer 2 with ove prescaler.	
(c) Pro	gram Timer0 to be an event counter. Use normal mode and display the binary of the on PORTC continuously on falling edge. Draw the circuit diagram.	(5)

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What is an Interrupt Service Routine? Is the memory space allocated sufficient for (5) What is an how and where is ISR written? ISR? If no, how and where is ISR written?

ISRA is the priority of INTO, INTO and INTO interrupts? Give their vector address (4)

What is the priority options for INTO interrupt. What is the programme options for INTO interrupt.

Assume that INTO and INTI are connected to two switches named S1 and S2. (6)

Assume that o initialize the interrupt and interrupt service routines (1993). Assume that III is a like the interrupt and interrupt service routines (ISR) in Write a program to initialize the content of PORTC increases. Write a program to make low, the content of PORTC increases by one; and which, whenever S1 goes low, the content of PORTC decreases by one while S2 goes low, the content of PORTC decreases by one while which, whenever so both the content of PORTC decreases by one while at the same whenever S2 goes low, the content of PORTA and sends it to BODA whenever 32 gues lovi, meads the data from PORTA and sends it to PORTB time the microcontroller reads the data from PORTA and sends it to PORTB

What is ADC? What is the relationship between Step Size and Reference Voltage?

What is ADC? What is the relationship between Step Size and Reference Voltage?

Write a program to initialize ADC to sample Ch2 in Single-ended configuration. Write a program to more voltage and Right-adjusted result. Assume clock Internal 2.56V reference voltage and Right-adjusted result.

If the system clock is 7.3728 MHz, write a subroutineto initialize USART as per (5)

following details -

Baud Rate - 9600bps

Data bits - 8

Parity - Even

What are the advantages of using serial communication? Explain Simplex, Half (5) Duplex and Full Duplex modes.