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## $Q\ 14)$ WAP to generate a SQUARE-WAVE of 1 KHz using SOD pin of 8085. Soln:

```
BACK: MVI A, 40H ; SIM Command = 0100 0000 SIM

CALL DLAY

MVI A, COH ; SIM Command = 1100 0000 SIM

CALL DLAY

JMP BACK
```

For a square wave of 1 KHz, the time period is 1 msec. Hence the required delay is of 0.5 msec.

## Assume 8085 is working at 3 MHZ

```
DLAY: MVI B, XXH ; 7 T-states ... ... Count is calculated later
BACK: DCR B
                            ; 4 T-states ... ... Decrement Count
            BACK
       JNZ
                            ; 10T (true) / 7T (false)
                            ; 10T-states
       RET
T_D = MT + [(Count)_d \times NT] - 3T
Here MT = Time outside the loop = 17T
       NT = Time inside the loop = 14T
T_D = 17T + [(Count)_d \times 14T] - 3T
Required T_D = 0.5 \text{ msec} = 0.5 \text{ x } 10^{-3} \text{ sec}
1T = 0.333 \, \mu sec = 0.333 \, x \, 10^{-6} \, sec
Substituting the above values we get:
0.5 \times 10^{-3} = 17 \times (0.333 \times 10^{-6}) + [(Count)_d \times 14 \times (0.333 \times 10^{-6})] - 3 \times (0.333 \times 10^{-6})
Count = 6AH
```

## Q 15) WAP to transfer the value 35H serially with one start bit "0" and one stop bit "1".

**Soln:** Serial communication happens bit by bit starting from the LSB.

As per the question, we need to send the start bit (0), then the data and finally the stop bit (1).

Hence a total of 10 bits will move out as follows:

```
1 0 1 0 1 1 0 0
                                          1
Start
         8-data bits in reverse order Stop
  MVI
       A, 40H
                 ; start bit (0)
  SIM
  MVI
       A, COH
                  ; send a "1"
  SIM
  MVI
       A, 40H
                  ; send a "0"
  SIM
  MVI A, COH
                  ; send a "1"
  SIM
                   ; send a "0"
  MVI
       A, 40H
  SIM
  MVI A, COH
                   ; send a "1"
  SIM
                   ; send a "1" again
  SIM
                   ; send a "0"
  MVI A, 40H
  SIM
                   ; send a "0" again
  STM
                   ; send a "1" as the stop bit
  MVI A, COH
  SIM
  RST1
```