## Power Line EMI sensing

Date: 21st Jan, 2014

#### Goal

#### To study conducted EMI in power lines

#### Specific points to focus:

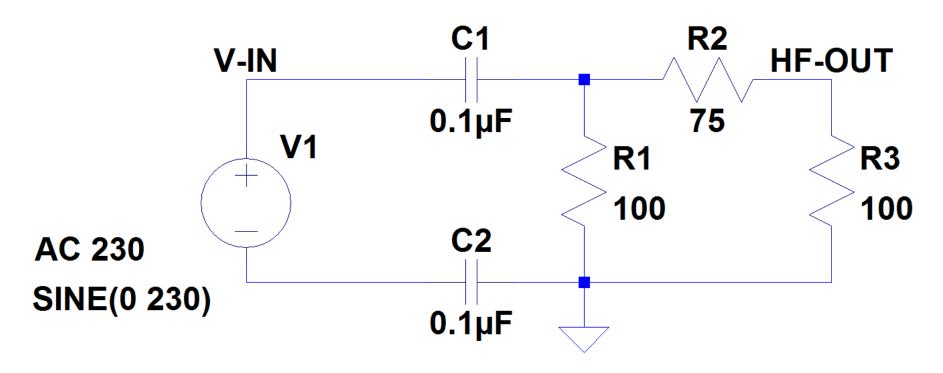
- 1. Effect of voltage fluctuation on CE.
- 2. Effect of building architecture on CE.
- 3. Analyze different CE signatures w/ and w/o load conditions.
- 4. CE signatures with different load settings on a single SMPS (EUT).
- 5. Difference in common mode CE and differential mode CE generated by appliances using CM and DM separation. [Added on 21/1/2014] [Imp.]

\* CE: Conducted EMI

## High Pass filter Circuit

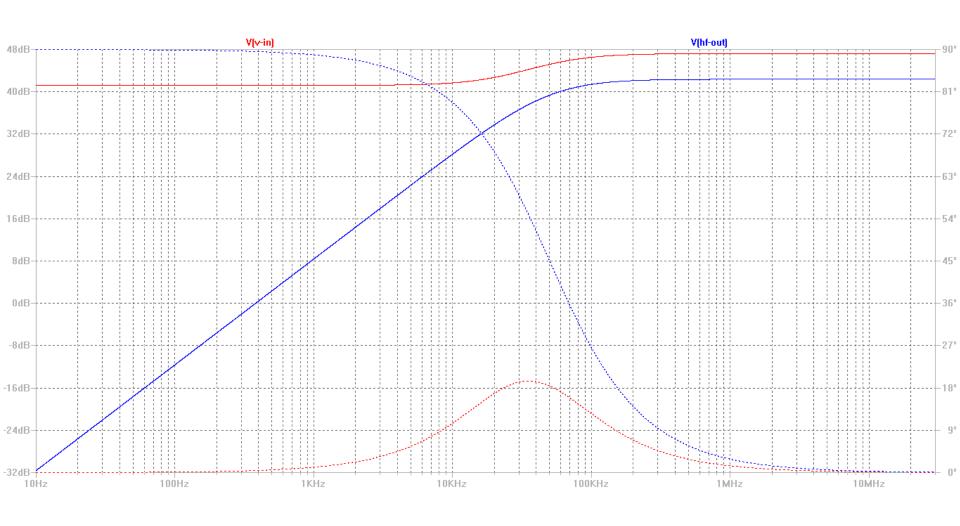
- Spec required :
  - 50Khz to 30Mhz flat range
  - Should be capable to withhold 250VAC
  - components should be selected to work in RF range
- Attenuator required at o/p to avoid high voltage signal from destroying analyzer front end.
- Output impedance should be 50 Ohm to match with Signal analyzer input port.

### Simulated Circuit HPF



.ac oct 1000 10Hz 3000000Hz

# Frequency response



# HPF Filter from Solar EMC (US)



URL: <a href="http://solar-emc.com/TEMP/7415-3.html">http://solar-emc.com/TEMP/7415-3.html</a>

## HPF filter from COM-Power



URL: <a href="http://www.com-power.com/transient limiters.html">http://www.com-power.com/transient limiters.html</a>

## EMI testing Facility in Delhi

STQC lab {Standardisation Testing and Quality Certification}

http://www.stqc.gov.in/content/emiemc-testing

Key Areas:

http://www.stqc.gov.in/testing-key-areas/160/333

Lab in Okhla, Delhi

ERTL(North) Delhi			
Head of Laboratory	M P Sharma (Sr. Director)	ERTL(North) S- Block, Okhla Industrial Area, Phase - II, New Delhi - 110020	Fax: 011 - 26384583

## EMI Testing GB pant College

 GB pant engineering college, Okhla (Delhi) is having good microwave, EMI/EMC testing labs.

#### Link:

<u>www.sltmicrowave.com/doc\_pdf/EMIEMC%20Test%20Facility.p</u> <u>df</u>

#### References

 Power Line Filter Design for Conducted Electromagnetic Interference Using Time-Domain Measurements (<u>Link to PDF</u>).

#### Conclusions

- Setup a test bed for CE testing without wasting much time on designing HPFs.
- Look for off the shelf components and facilities available nearby.
- Simulate a generic SMPS model for modeling noise from different appliances.
- Study 2-3 research papers and prepare presentation for weekly meeting. Document each and every discussion.

\*CE: Conducted EMI