

Mantosh Kumar

Hamilton – Ontario – Canada

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Summary

Software Developer with over 14 years of experience across multiple phases of the software development lifecycle. Strong competence in C, Python, Django, system design, and back-end development. No sponsor required for employment in the US, Canada, or India.

GitHub Profile: <https://github.com/mantoshkumar1>

Professional Experience

Nokia Corporation

Staff Test/Automation Engineer

Ottawa, Canada

Aug 2020–Present

- Designing and developing an automated testing framework for next-generation optical networking devices.
- Gathering test requirements directly from feature architects and formulating automatable test cases.
- Migrated legacy testing framework to the latest Infinera proprietary platform (PACE), fully integrated into CI/CD pipeline. Ensured no bugs and completed end-to-end in 5 months with 1000+ test scripts.
- Maintaining system integration across testing framework, CI/CD, and 24/7 test cycles.
- Mentoring team members, interviewing candidates for software dev positions, reviewing code, and collaborating with cross-functional teams and stakeholders.
- Technologies: Python, gRPC, YAML, Git

KI labs

Backend Engineer

Munich, Germany

May 2018–Nov 2019 (1 year 6 months)

- Built the MVP of a B2B chemical e-commerce platform — among the first in the European market.
- Developed RESTful APIs using Django; designed databases following n-tier architecture.
- Collaborated with front-end teams and product managers to deliver end-to-end features.
- Focused on code quality through unit tests and Cypress-based end-to-end automation.
- Automated CI/CD pipelines with Jenkins to streamline deployments.
- Website: <https://chemondis.com/marketplace/>
- Technologies: Python, Django, PostgreSQL, RESTful, Cypress

Siemens AG

Research and Development Intern

Munich, Germany

Feb 2017–Sept 2017 (7 months)

- Designed a RESTful control interface and real-time monitoring system for SDN/NFV-based industrial networks.
- Developed a proof of concept for intent specification and monitoring for time-sensitive applications.
- Website: www.virtuwind.eu
- Technologies: Java, Python, RESTCONF, SDN, Mininet, Yang, Maven, OSGi

Intel Corporation

Protocol Stack Engineer (Part-time)

Munich, Germany

Feb 2015–Feb 2017 (2 years)

- Contributed to Radio Resource Control (RRC) development and testing for Intel's XMM 7360 modem platform.
- Produced functional specs and implemented 3GPP-aligned features with Intel's internal requirements.
- Built Matlab tools to visualize OTA message traces and analyze modem performance metrics.
- Developed ENAS/UICC decoder modules and initiated automation framework for unit testing.
- Automated project status monitoring and collaborated closely with internal users for improvements.
- Languages: C++, Matlab, Python

Cisco Systems

LTE Engineer

Bangalore, India

Jan 2012–Sept 2014 (2 years 8 months)

- Contributed to the development of core network solutions for 2G/3G/LTE/Wi-Fi mobile networks.
- Built simulation tools to test and validate mobile network behavior, improving deployment efficiency.
- Collaborated with cross-functional teams and stakeholders to ensure successful delivery and support.
- Technologies: Python, Wireshark

Aricent Group*Software Engineer***Bangalore, India***Jan 2011–Jan 2012 (1 year)*

- Built a prototype of a location-enabled social networking application showcased at Mobile World Congress 2012.
- Created use cases and process flows to support business requirements and integrated social and location-based features.
- Technologies: Java, Web2.0, MySQL

Education

Technical University of Munich*M.Sc. in Computer Science (2.4/5, Best: 1)***Munich, Germany***Oct 2014–Oct 2017***West Bengal University of Technology***Bachelors in Computer Science (7.7/10, Best: 10)***Kolkata, India***July 2006–June 2010*

Technical Skills

Programming: C, C++, Java, Python, Matlab, Yang, Bash**Software Skills:** Flask, Django, SQL, PostgreSQL, RESTful**Tools:** Git, UML, Maven, Wireshark, LaTeX, Cypress**Platform/API:** Linux, SuperMUC, Repast HPC**Profiling:** Valgrind, Gprof, Perf, PAPI, VTune Amplifier XE, Scalasca

Awards and Recognition

- **Heroes of Tomorrow Award, Intel Corporation (Jan & Dec 2016):** Received twice in one year for outstanding contributions to the PS module of Intel XMM 7360 modem.
- **Graduate Aptitude Test in Engineering (GATE) - Computer Science:** National-level exam conducted by IISc and IITs for engineering graduates.
 - **GATE 2013:** Top **0.87%** (Out of 224160 applicants)
 - **GATE 2012:** Top **0.76%** (Out of 156780 applicants)