S.No	API Name	URL	Method	Payload	Response	Error

1	Login	/auth/login/	POST	{
				"email": "user@example.com",
				"password": "********"
				}
					{
					"accessToken": "JWT_ACCESS_TOKEN",
					"refreshToken": "JWT_REFRESH_TOKEN",
					"user": {
					"id": 1,
					"email": "user@example.com",
					"role": "admin"
					}
					}
						{
						"error": "Invalid credentials"
						}

2	Register	/auth/register/	POST	{
				"email": "user@example.com",
				"password": "********",
				"password2": "********",
				"full_name": "User Name",
				"role": "admin"
				}
					{
					"email": "Raju12@gmail.com",
					"full_name": "Raju123"
					}
						{
						"password": [
						"This password is too short. It must contain at least 8 characters."
						],
						"email": [
						"This field must be unique."
						]
						}

3	Logout	/auth/logout/	POST	{
				"refresh": "JWT_REFRESH_TOKEN"
				}
					{
					"message": "Logout successful"
					}	

4	Password Reset	/auth/forgot-password/	POST	{
				"email": "testuser@example.com",
				"password": "zxasqw123@Z",
				"password2": "zxasqw123@Z"
				}
					{
					"email": "testuser@example.com"
					}
						{
						"error": ""
						}

5	getUsers	/auth/users/	GET	-	[
					{
					"id": 1,
					"email": "sathish111@gmail.com",
					"role": [
					"Admin"
					],
					"full_name": "sathish"
					},,,,,,,
					
					]
						{
						"error": "Exception occurred While Deleting the User: 'AnonymousUser' object has no attribute 'email'"
						}

6	Delete user	auth/users/3/	Delete	-	"User deleted successfully."	{
						"error": "Exception occurred While Deleting the User: 'AnonymousUser' object has no attribute 'email'"
						}

7	Update User	/auth/users/3/	PUT	{
				"email": "testuser@example.com",
				"full_name": "New Name",
				"role": "Approver"
				}
					{
					"full_name": "New Name"
					}
						{
						"error": "Exception occurred While Updating the User: 'AnonymousUser' object has no attribute 'email'"
						}

PCB API						
8	Get PCB Specification	/right-draw/pcb-specification/{component_id}/	GET	queryparams= ?is_designer=1 or is_verifier: 1	[{ "category_id": 11, "category_name": "Category for B11", "subcategories": [{ "id": 134, "name": "Lumped Technology", "is_design_options_exists": true, "is_sub_2_categories_exists": false, "has_verifier_fields": true }, {}...] }]	

9	components	masters/components/	GET	-	[
					{
					"id": 1,
					"component_name": "B14",
					"description": "PC Boards"
					},
					{
					"id": 2,
					"component_name": "B11",
					"description": "Metal Cover"
					},
					{
					"id": 3,
					"component_name": "B17",
					"description": "Metal Shield for barrier between signal ports"
					},
					{
					"id": 4,
					"component_name": "B11 for B14C",
					"description": "METAL COVER for Thin Film Technology"
					}
					]

10	getComponent	masters/components/component_id/	GET		{
					"id": 1,
					"component_name": "B14",
					"description": "PC Boards"
					}

11	getSubCategoriesTwo	right-draw/sub-categories-two/sub_category_id/	GET		[
					{
					"id": 1,
					"sub_2_category_name": "Thread Size 0.250\""
					}
					]

12	getDesignOptions	right-draw/design-options/sub_category_id/	GET		[{ "design_option_id": 1, "desing_option_name": "Design with Fixed components" },,,,]

13	CheckTemplate	right-draw/check-template/	GET	/right-draw/check-template/?oppNumber=ap30p	{
					"designer_exists": false,
					"verifier_exists": false,
					"approver_exists": false
					}

14	design-rules	right-draw/design-rules/?design_option_ids=8,9	GET		[{ "id": 1, "design_doc": "D3-E002", "rule_number": "4.1.1", "parameter": "Following PTFE materials: OAK, NX9245, NX9250, NH9350 shall not be used in new designs without management approval", "min_value": "N/A", "max_value": "N/A", "nominal": "N/A", "comments": "INFO" },,,,,,]

15	cad-design-templates	right-draw/cad-design-templates/	POST	{
				"oppNumber": "apr30pcb",
				"opuNumber": "apr30pcb",
				"eduNumber": "apr30pcb",
				"modelName": "apr30pcb",
				"partNumber": "apr30pcb",
				"component": "1",
				"revisionNumber": "apr30pcb",
				"componentSpecifications": {
				"1": 111,
				"2": 121,
				"3": 9,
				"4": 55,
				"5": 64,
				"6": 78,
				"7": 104,
				"8": 110,
				"9": 130,
				"10": 89
				},
				"designOptions": [
				"1",
				"4"
				],
				"secondarySubLevel": {
				"121": "2"
				}
				}					

16	getcadDesignTemplate	right-draw/cad-design-templates/5/	GET	{
				"id": 5,
				"created_at": "2025-04-30T07:26:33.202391Z",
				"updated_at": "2025-04-30T07:26:33.202427Z",
				"opp_number": "apr30pcb",
				"opu_number": "apr30pcb",
				"edu_number": "apr30pcb",
				"model_name": "apr30pcb",
				"part_number": "apr30pcb",
				"revision_number": "apr30pcb",
				"pcb_specifications": {
				"1": 111,
				"2": 121,
				"3": 9,
				"4": 55,
				"5": 64,
				"6": 78,
				"7": 104,
				"8": 110,
				"9": 130,
				"10": 89
				},
				"smt_design_options": [
				"1",
				"4"
				],
				"secondary_sub_level": {
				"121": "2"
				},
				"remarks": null,
				"created_by": 5,
				"updated_by": 5,
				"component_Id": 1
				}					

17	user-templates	right-draw/user-templates/	GET		{
					"design_templates": [
					{
					"oppNumber": "apr30pcb",
					"opuNumber": "apr30pcb",
					"eduNumber": "apr30pcb",
					"modelName": "apr30pcb",
					"partNumber": "apr30pcb",
					"revisionNumber": "apr30pcb",
					"component": 1
					}
					],
					"verifier_templates": [],
					"approver_templates": []
					}				

18	verifier-fields	right-draw/verifier-fields/?component_id=1&categ	GET	component_id=1&category_id=1&sub_category	[{ "id": 1, "field_name": "Enter Copper Trace Clearance from PCB edge at top side in a DWG" },,,,,]				

19	verifier-templates	right-draw/verifier-templates/	POST	{
				"oppNumber": "apr30pcb",
				"opuNumber": "apr30pcb",
				"eduNumber": "apr30pcb",
				"modelName": "apr30pcb",
				"partNumber": "apr30pcb",
				"component": "1",
				"revisionNumber": "apr30pcb",
				"componentSpecifications": {
				"1": "111",
				"3": "1",
				"4": 0.01,
				"5": "64",
				"6": "77",
				"7": "95",
				"8": 0.4,
				"10": "86"
				},
				"verifierQueryData": {
				"1": 1,
				"2": 1,
				"3": 1,
				"4": 1,
				"5": 1,
				"6": 1,
				"7": 1,
				"8": 1,
				"9": 1,
				"10": 1,
				"11": 1,
				"12": 1,
				"13": 1,
				"14": 1,
				"15": 1,
				"16": 1,
				"17": 1,
				"18": 1,
				"19": 1,
				"20": 1
				}
				}
					{
					"template_id": 1,
					"res": {
					"opp_number": "apr30pcb",
					"opu_number": "apr30pcb",
					"edu_number": "apr30pcb",
					"model_name": "apr30pcb",
					"part_number": "apr30pcb",
					"revision_number": "apr30pcb",
					"component_id": "1",
					"verify_design_fields_data": [
					{
					"categor_id": 1,
					"name": "Category for B14",
					"selected_deviation_id": "111",
					"selected_deviation_name": "SMT",
					"is_deviated": false
					},
					{
					"categor_id": 3,
					"name": "Dielectric material",
					"selected_deviation_id": "1",
					"selected_deviation_name": "Alumina Ceramic",
					"is_deviated": true
					},
					{
					"categor_id": 4,
					"name": "Dielectric Thickness",
					"selected_deviation_id": "N/A",
					"selected_deviation_name": 0.01,
					"is_deviated": true
					},
					{
					"categor_id": 5,
					"name": "B14 Finish",
					"selected_deviation_id": "64",
					"selected_deviation_name": "ENIG",
					"is_deviated": false
					},
					{
					"categor_id": 6,
					"name": "Copper Thickness",
					"selected_deviation_id": "77",
					"selected_deviation_name": "1",
					"is_deviated": true
					},
					{
					"categor_id": 7,
					"name": "Second Dielectric Thickness",
					"selected_deviation_id": "95",
					"selected_deviation_name": "0",
					"is_deviated": true
					},
					{
					"categor_id": 8,
					"name": "B14 Size",
					"selected_deviation_id": "N/A",
					"selected_deviation_name": 0.4,
					"is_deviated": true
					},
					{
					"categor_id": 10,
					"name": "Number of Layers",
					"selected_deviation_id": "86",
					"selected_deviation_name": "2",
					"is_deviated": true
					}
					],
					"verified_query_data": [
					{
					"id": "1",
					"name": "Enter Copper Trace Clearance from PCB edge at top side in a DWG",
					"value": 1.0,
					"is_deviated": true
					},
					{
					"id": "2",
					"name": "Enter Copper Trace Clearance from PCB edge at bottom side in a DWG.",
					"value": 1.0,
					"is_deviated": true
					},
					{
					"id": "3",
					"name": "Enter Castellated lead PTH diameter in a DWG.",
					"value": 1.0,
					"is_deviated": true
					},
					{
					"id": "4",
					"name": "Enter Annular ring around Castellated lead in a DWG.",
					"value": 1.0,
					"is_deviated": true
					},
					{
					"id": "5",
					"name": "Enter minimum clearance between TWO exposed metallization pads in a DWG",
					"value": 1.0,
					"is_deviated": true			

20	verify-results	right-draw/verify-results/	POST	{
				"oppNumber": "apr30pcb",
				"opuNumber": "apr30pcb",
				"eduNumber": "apr30pcb",
				"modelName": "apr30pcb",
				"partNumber": "apr30pcb",
				"component": "1",
				"revisionNumber": "apr30pcb",
				"componentSpecifications": {
				"1": "111",
				"3": "1",
				"4": 0.01,
				"5": "64",
				"6": "77",
				"7": "95",
				"8": 0.4,
				"10": "86"
				},
				"verifierQueryData": {
				"1": 1,
				"2": 1,
				"3": 1,
				"4": 1,
				"5": 1,
				"6": 1,
				"7": 1,
				"8": 1,
				"9": 1,
				"10": 1,
				"11": 1,
				"12": 1,
				"13": 1,
				"14": 1,
				"15": 1,
				"16": 1,
				"17": 1,
				"18": 1,
				"19": 1,
				"20": 1
				}
				}
					{
					"res": {
					"opp_number": "apr30pcb",
					"opu_number": "apr30pcb",
					"edu_number": "apr30pcb",
					"model_name": "apr30pcb",
					"part_number": "apr30pcb",
					"revision_number": "apr30pcb",
					"component_id": 1,
					"verify_design_fields_data": [
					{
					"categor_id": 1,
					"name": "Category for B14",
					"selected_deviation_id": "111",
					"selected_deviation_name": "SMT",
					"is_deviated": false
					},
					{
					"categor_id": 3,
					"name": "Dielectric material",
					"selected_deviation_id": "1",
					"selected_deviation_name": "Alumina Ceramic",
					"is_deviated": true
					},
					{
					"categor_id": 4,
					"name": "Dielectric Thickness",
					"selected_deviation_id": "N/A",
					"selected_deviation_name": 0.01,
					"is_deviated": true
					},
					{
					"categor_id": 5,
					"name": "B14 Finish",
					"selected_deviation_id": "64",
					"selected_deviation_name": "ENIG",
					"is_deviated": false
					},
					{
					"categor_id": 6,
					"name": "Copper Thickness",
					"selected_deviation_id": "77",
					"selected_deviation_name": "1",
					"is_deviated": true
					},
					{
					"categor_id": 7,
					"name": "Second Dielectric Thickness",
					"selected_deviation_id": "95",
					"selected_deviation_name": "0",
					"is_deviated": true
					},
					{
					"categor_id": 8,
					"name": "B14 Size",
					"selected_deviation_id": "N/A",
					"selected_deviation_name": 0.4,
					"is_deviated": true
					},
					{
					"categor_id": 10,
					"name": "Number of Layers",
					"selected_deviation_id": "86",
					"selected_deviation_name": "2",
					"is_deviated": true
					}
					],
					"verified_query_data": [
					{
					"id": "1",
					"name": "Enter Copper Trace Clearance from PCB edge at top side in a DWG",
					"value": 1.0,
					"is_deviated": true
					},
					{
					"id": "2",
					"name": "Enter Copper Trace Clearance from PCB edge at bottom side in a DWG.",
					"value": 1.0,
					"is_deviated": true
					},
					{
					"id": "3",
					"name": "Enter Castellated lead PTH diameter in a DWG.",
					"value": 1.0,
					"is_deviated": true
					},
					{
					"id": "4",
					"name": "Enter Annular ring around Castellated lead in a DWG.",
					"value": 1.0,
					"is_deviated": true
					},
					{
					"id": "5",
					"name": "Enter minimum clearance between TWO exposed metallization pads in a DWG",
					"value": 1.0,
					"is_deviated": true
					},			