Appendix A. Hardware Drawings and Schematics

A.1 INTRODUCTION

This Appendix provides a layout drawing of the printed circuit board followed by schematics for the dsPICDEM™ 2 Development Board.

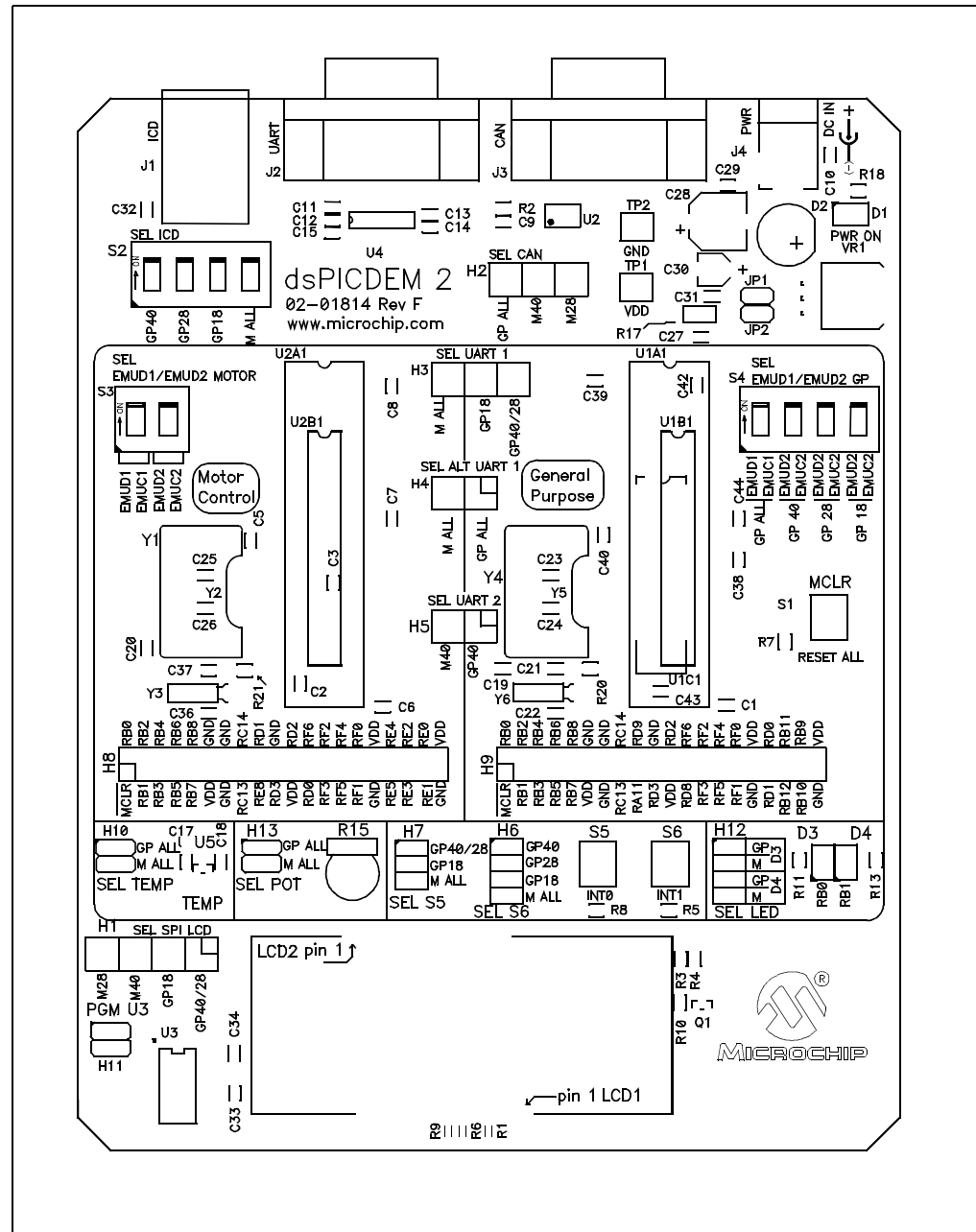**FIGURE A-1:** **dsPICDEM™ 2 DEVELOPMENT BOARD LAYOUT**

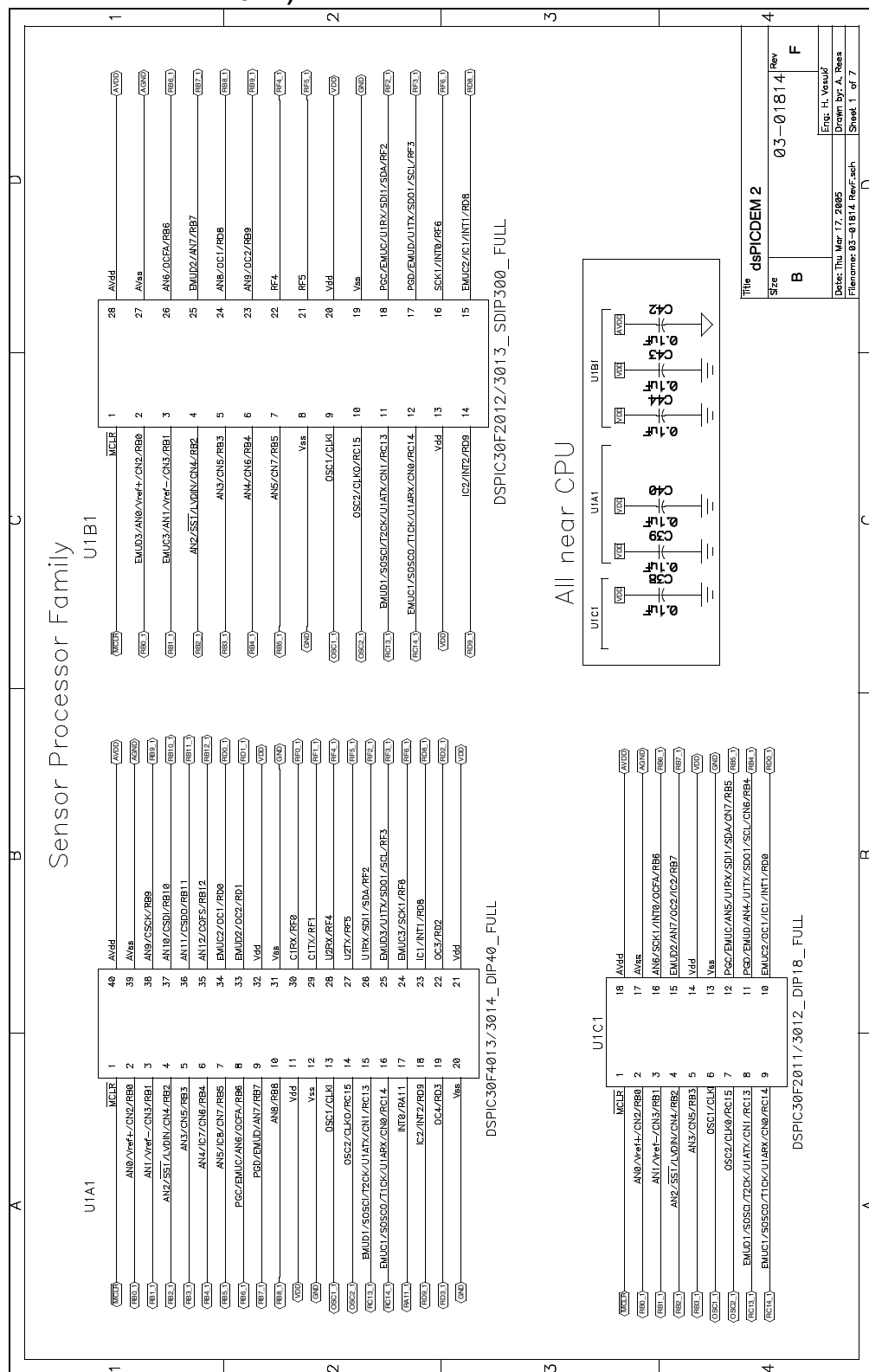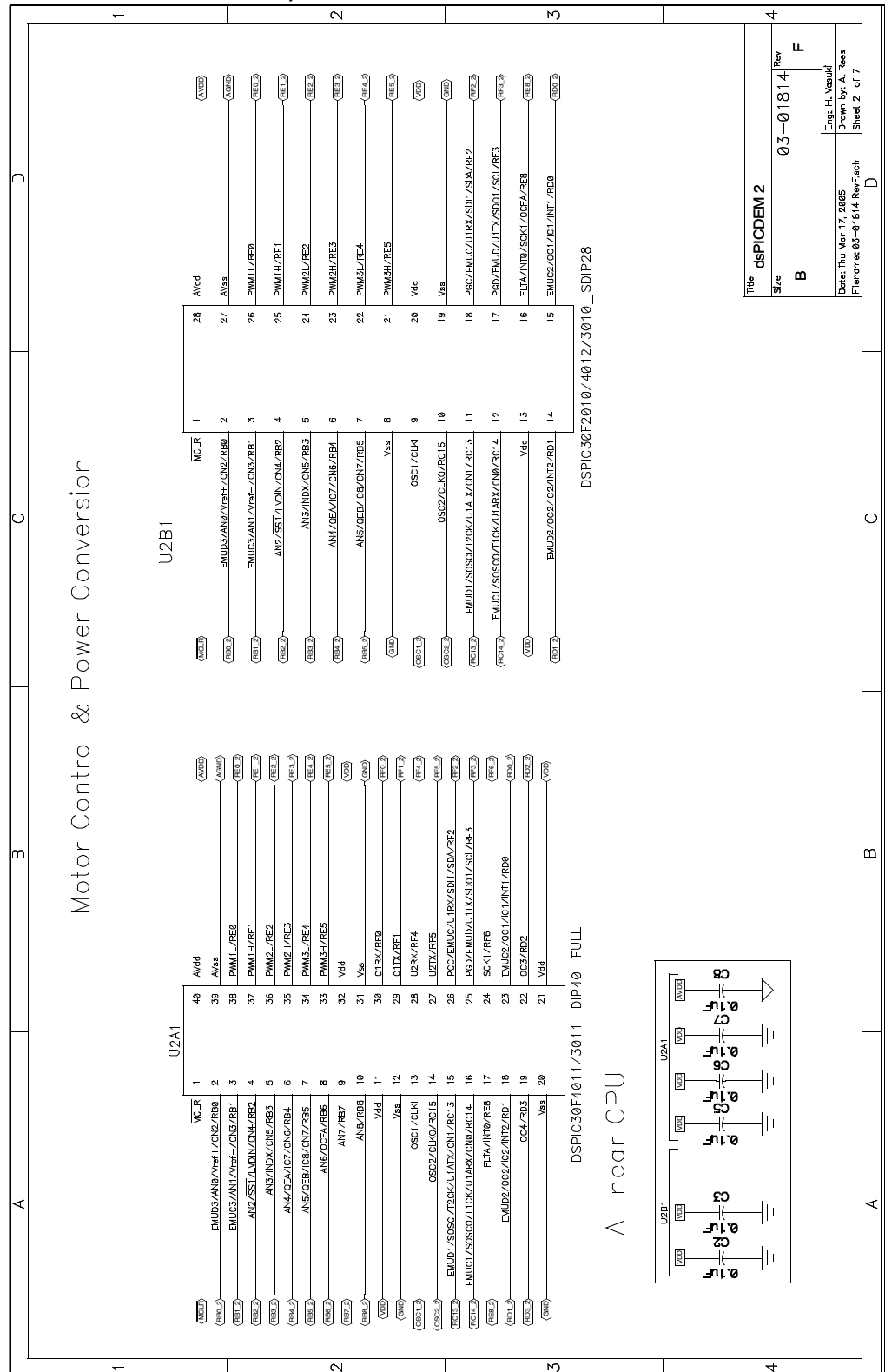**FIGURE A-2:** dsPICDEM™ 2 DEVELOPMENT BOARD SCHEMATIC (SHEET 1 OF 7)

FIGURE A-3: dsPICDEM™ 2 DEVELOPMENT BOARD SCHEMATIC (SHEET 2 OF 7)

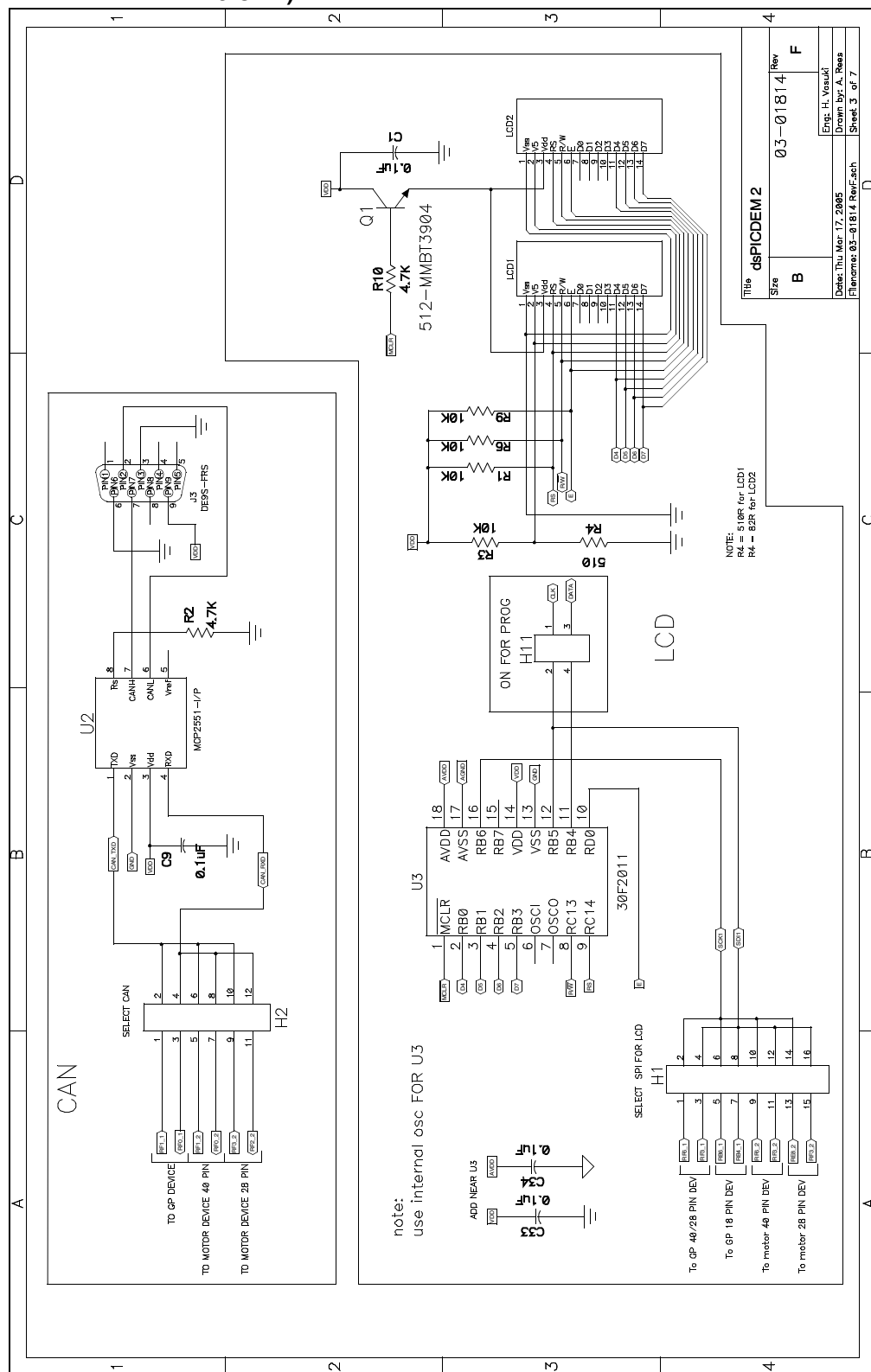**FIGURE A-4:** dsPICDEM™ 2 DEVELOPMENT BOARD SCHEMATIC (SHEET 3 OF 7)

FIGURE A-5: dsPICDEM™ 2 DEVELOPMENT BOARD SCHEMATIC (SHEET 4 OF 7)
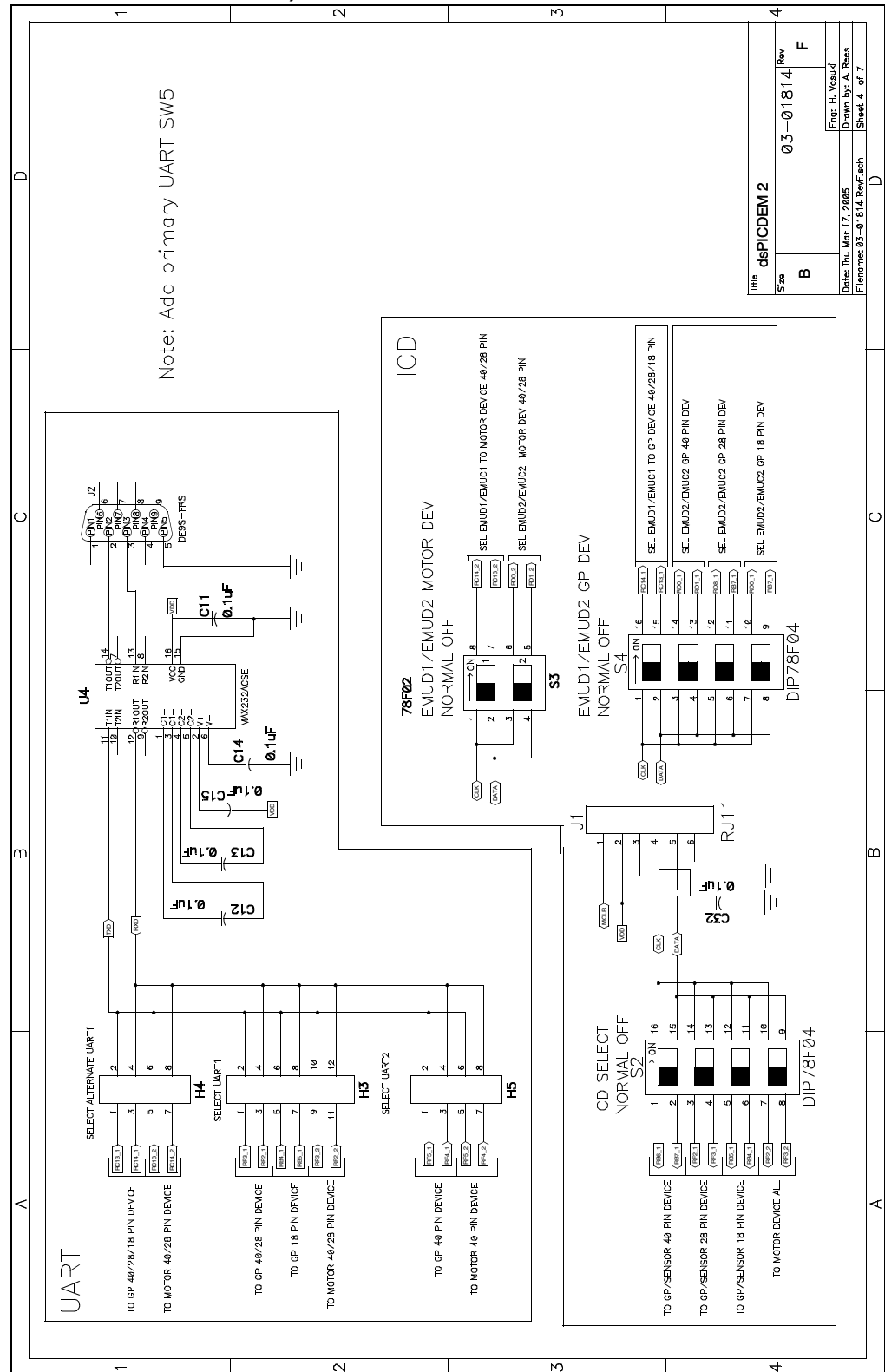
FIGURE A-6: dsPICDEM™ 2 DEVELOPMENT BOARD SCHEMATIC (SHEET 5 OF 7)
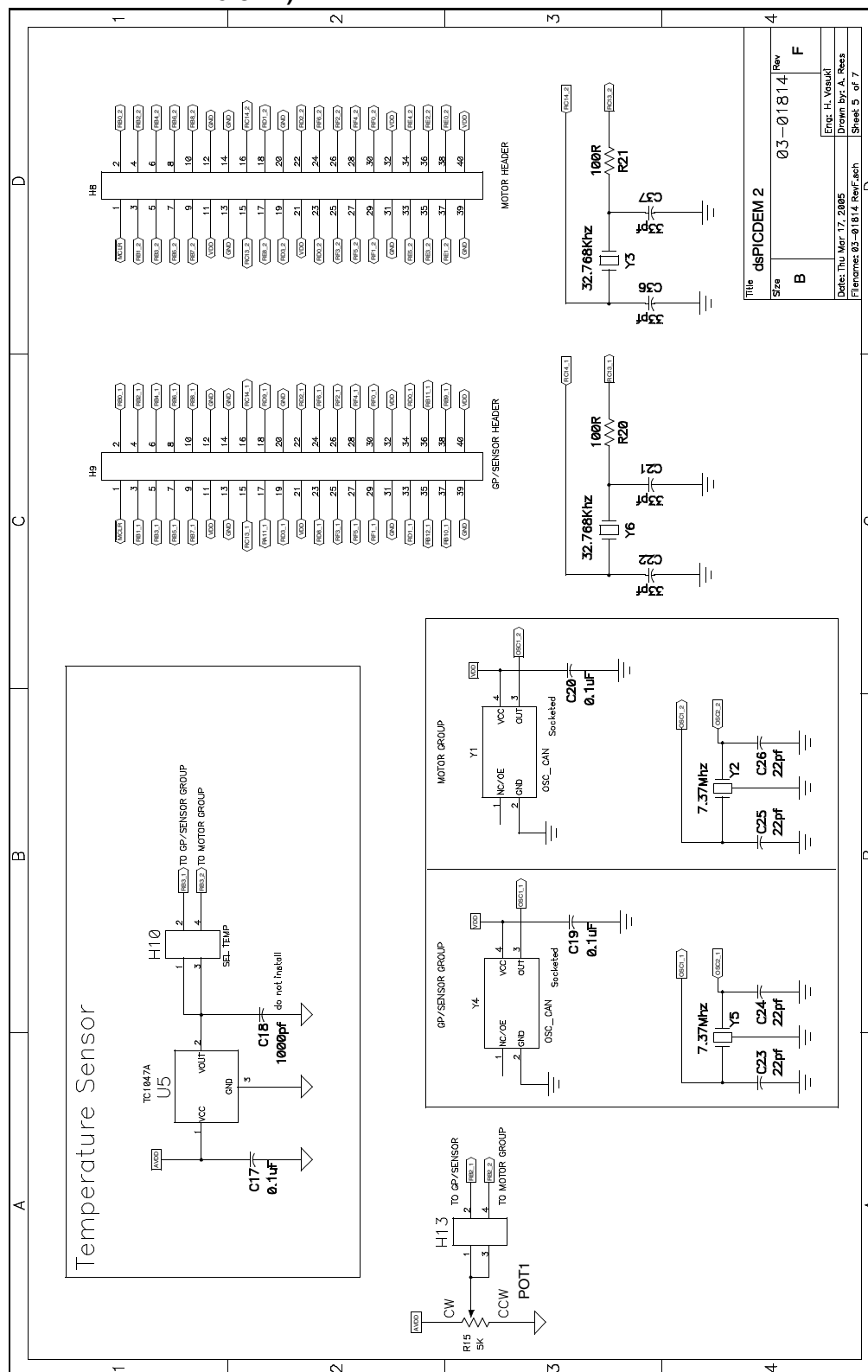
FIGURE A-7: dsPICDEM™ 2 DEVELOPMENT BOARD SCHEMATIC (SHEET 6 OF 7)
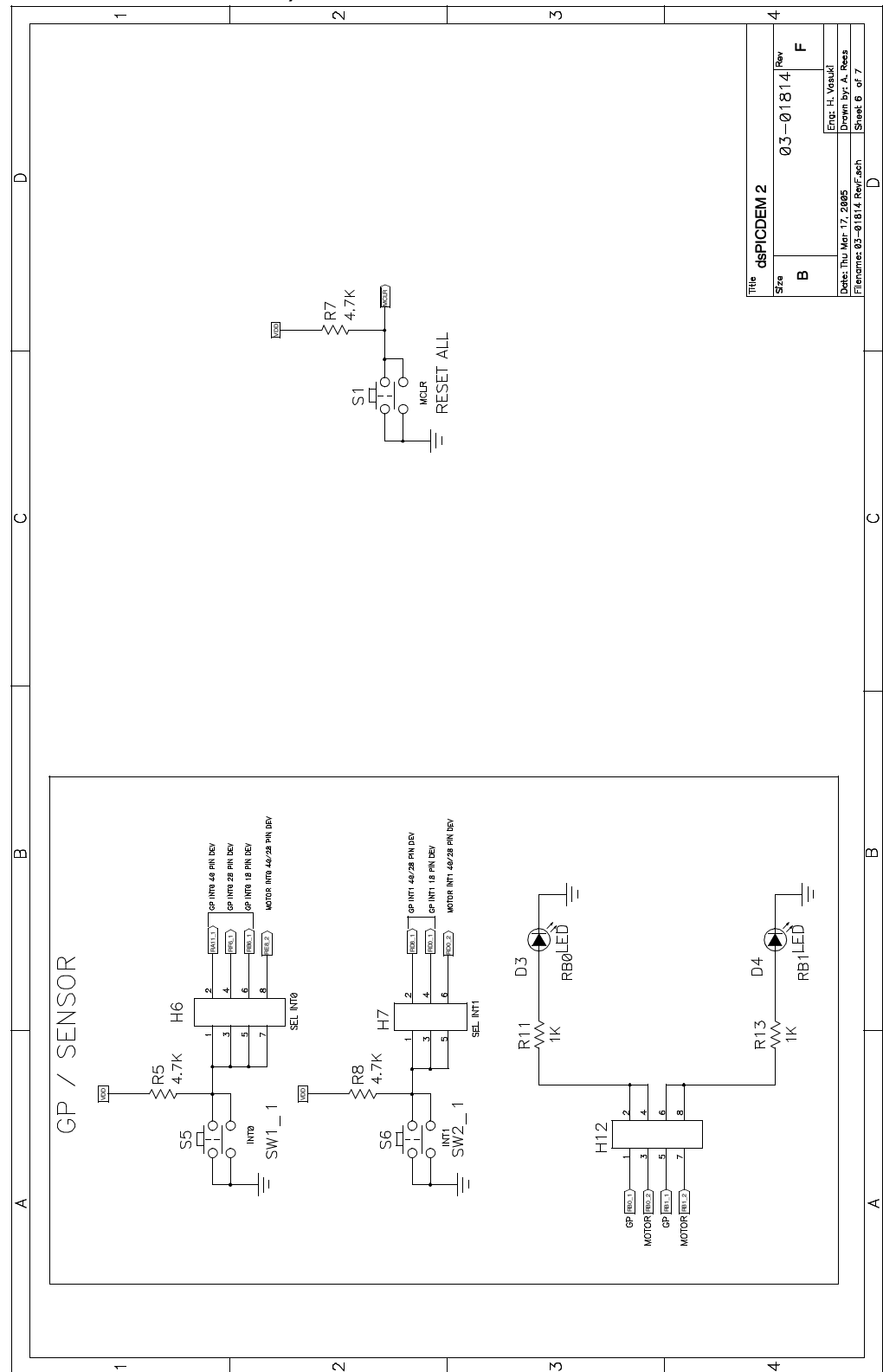
FIGURE A-8: **dsPICDEM™ 2 DEVELOPMENT BOARD SCHEMATIC (SHEET 7 OF 7)**