

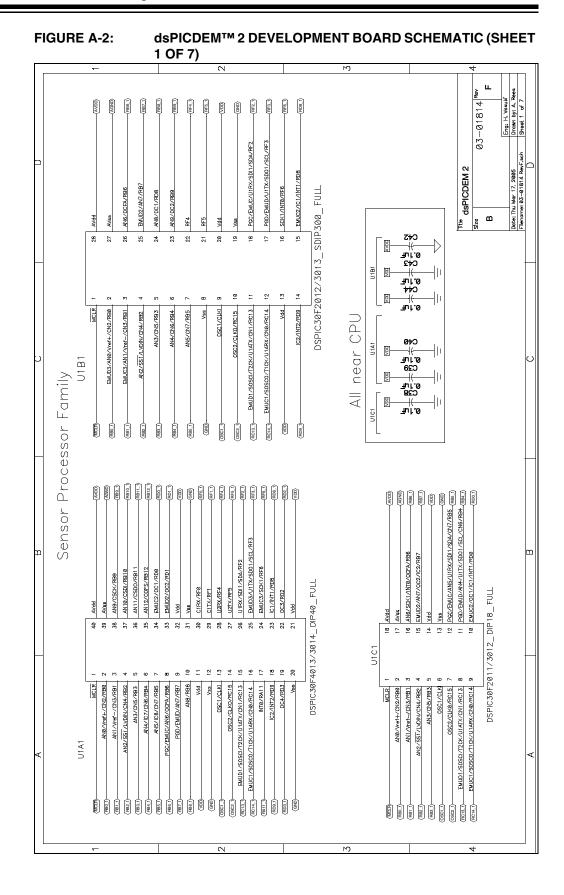
dsPICDEM™ 2.0 DEVELOPMENT BOARD USER'S GUIDE

Appendix A. Hardware Drawings and Schematics

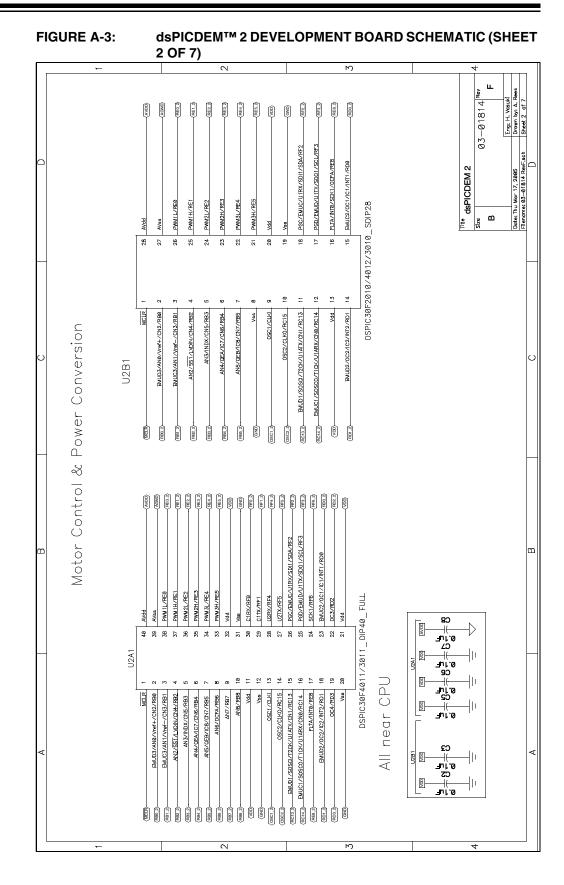
A.1 INTRODUCTION

This Appendix provides a layout drawing of the printed circuit board followed by schematics for the dsPICDEM™ 2 Development Board.

FIGURE A-1: dsPICDEM™ 2 DEVELOPMENT BOARD LAYOUT 를 **나** JART ₹ J4 <u>ခို </u> R18 J1 C29 C32 PWR ON VR1 GND TP1 dsPICDEM 2 H2 02-01814 Rev F VDD www.microchip.com R17 SEL UART 1 U1A SEL EMUD1/EMUD2 MOTOR 31 S4 EMUD1/EMUD2 GP C39 <u>8</u> <u>U181</u> EMUD1 EMUC1 EMUC2 EMUC2 General Motor Control Purpose è 9 g. ____ Y5 MCLR **S1** C24 R7 811 RESET ALL ||_{C2} C17 © C17 © CP ALL U55 M ALL [- - | I GP40/ GP18 M ALL ∭M ALL SEL TEMP GP18 SEL POT M ALL INT0 TEMP SEL S5 LCD2 pin 1 f PM N3 R4 PM SB R4 PM GP40/28 |]г_л 28.25 **C34** U3 | |MICROCHIP 11 –pin 1 LCD1 C33 21112



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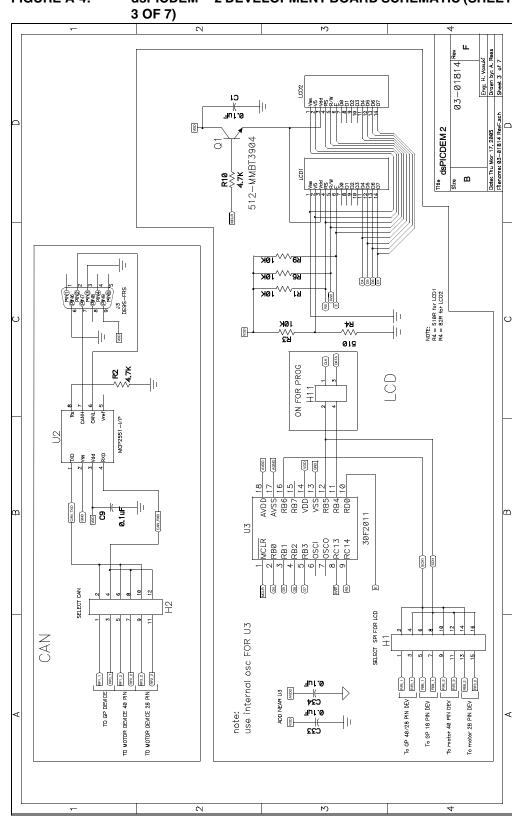
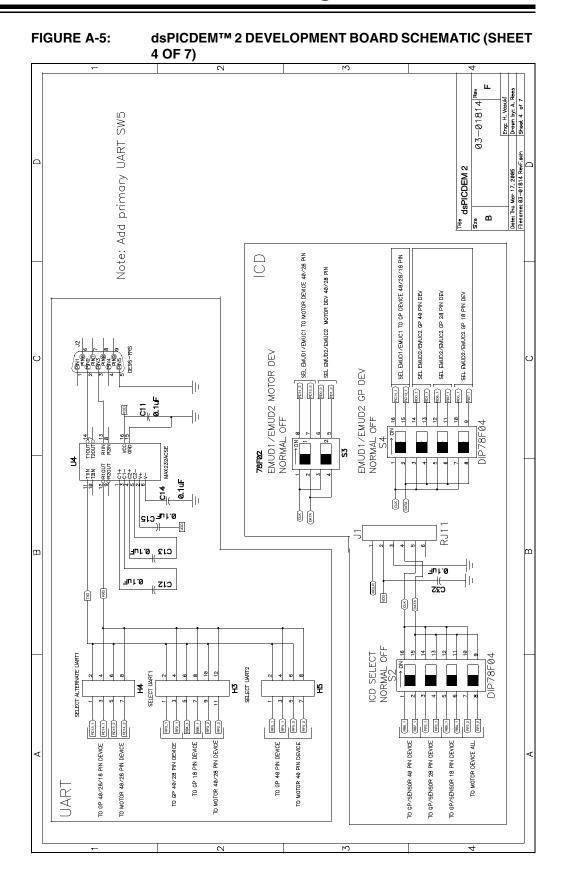
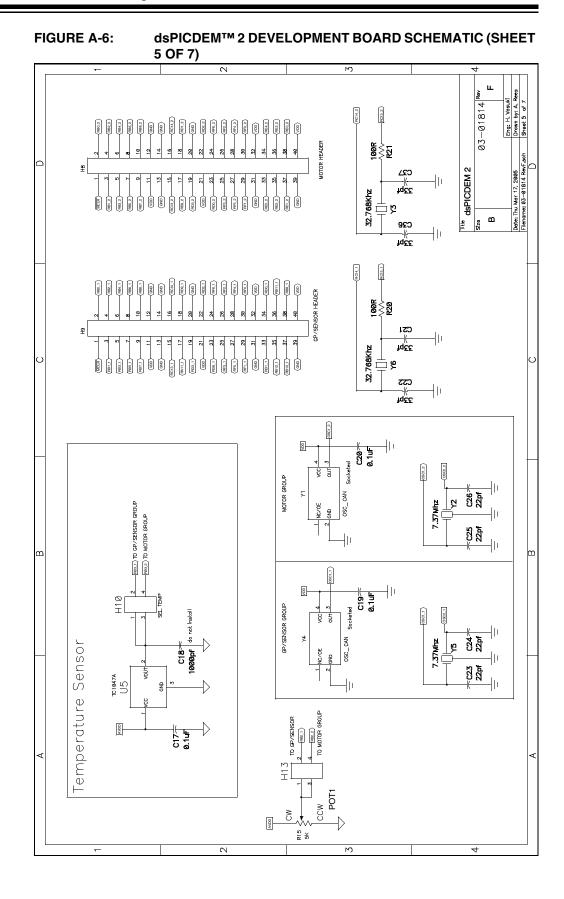


FIGURE A-4: dsPICDEM™ 2 DEVELOPMENT BOARD SCHEMATIC (SHEET



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FIGURE A-7: dsPICDEM™ 2 DEVELOPMENT BOARD SCHEMATIC (SHEET 6 OF 7) 03-01814 Rev Title dsPICDEM 2 \$R7 4.7K SENSOR \$R5 \$4.7K CP / 2

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