```
1 /**
    2 * @file
    3 * File:
                                                                                                    powerspy.h
    4 * Author:
                                                                                                    Manuel Federanko
    5 * Version:
                                                                                                    1.0
    6 * Comments:
    7 * Revision history:
    8 */
    9
 10 #include <xc.h>
 11 #include "types.h"
13 #ifndef POWERSPY H
14 #define __POWERSPY H
 16 #ifdef cplusplus
17 extern "C" {
18 #endif
20 #pragma config FOSC = INTOSC // Oscillator Selection (INTOSC oscillator: I/O f
21 #pragma config WDTE = OFF  // Watchdog Timer Enable (WDT disabled)
22 #pragma config PWRTE = OFF  // Power-up Timer Enable (PWRT disabled)
                                                                                                                            // MCLR Pin Function Select (MCLR/VPP pin function
23 #pragma config MCLRE = ON
24 #pragma config CP = OFF
                                                                                                                            // Flash Program Memory Code Protection (Program
25 #pragma config CPD = OFF  // Data Memory Code Protection (Data memory code 26 #pragma config BOREN = OFF  // Brown-out Reset Enable (Brown-out Reset disabled  // Clock Out Enable (CLKOUT function is disabled  // Clock Out Enable  // Clock Out Ena
28 #pragma config IESO = OFF // Internal/External Switchover (Internal/External 29 #pragma config FCMEN = OFF // Fail-Safe Clock Monitor Enable (Fail-Safe Clo
31
                                       // CONFIG2
32 #pragma config WRT = OFF
                                                                                                                           // Flash Memory Self-Write Protection (Write prot
33 #pragma config PLLEN = ON
33 #pragma config PLLEN = ON // PLL ENGDIE (TA LEE GEOLE),
34 #pragma config STVREN = OFF // Stack Overflow/Underflow Reset Enable (Stack Overflow)
35 #pragma config BORV = LO // Brown-out Reset Voltage Selection (Brown-out Foundation)
                                                                                                                                // PLL Enable (4x PLL disabled)
                                                                                                                              // Low-Voltage Programming Enable (High-voltage
36 #pragma config LVP = OFF
 37
 38
                                        //xc8 gives a warning when converting to lower data types
                                       //even when casting to the appropriate type
 40 #pragma warning push
41 #pragma warning disable 752
42 #pragma warning disable 520
43 #pragma warning pop
 44
                                      //print specific defines
 46 #define XTAL FREQ
                                                                                                                                 32000000
 47 #define RX
                                                                                                                                  RB1
 48 #define TX
                                                                                                                                  RB2
50 #define IN FREQ
                                                                                                                                  50
52 #define CURRENT VAL IN
                                                                                                                           RB5
53 #define CURRENT PHA IN
                                                                                                                                 RA0
54
55 #define VOLTAGE VAL IN
                                                                                                                                 RB4
 56 #define VOLTAGE PHA IN
                                                                                                                                  RA1
```

```
57
 58 #define DISPLAY_LAT RA3
59 #define DISPLAY_CLK RA4
 60 #define DISPLAY DATA
                                   RA7
 61
 62 #define SHIFT_DIR_MSBFIRST 1
 63 #define SHIFT_DIR_LSBFIRST
 65 #define STATUS LED
                       RA6
 66 #define BUTTON
                                   RB3
 68 #define PWM_OUT_GEN_VOLT RB0
69 #define PWM_IN_REF RA2
 71 #define SHIFT_REG_LEN
                                   7
 72 #define SHIFT_DELAY
                                   NOP();\
 73
                                    NOP();\
 74
                                    NOP();\
 75
                                    NOP();\
 76
                                     NOP();\
 77
                                    NOP();\
 78
                                    NOP();\
 79
                                    NOP();\
 80
                                    NOP();\
 81
                                    NOP();
 82
 83 #define RET OK
 84 #define RET NOK
                                    1
 85
                                    'C'
 86 #define K RAWCURRENT
87 #define K_OFFS
88 #define K_CURRENT
                                    '0'
 89 #define K_VOLTAGE
90 #define K_ANGLE
                                    'a'
 91 #define K_APPARENTEPOWER
                                    'A'
 92 #define K REALPOWER
                                    'r'
93 #define K_REACTIVEPOWER
                                    'R'
                                    's'
 94 #define K RAWVOLTAGE
 96 #define VOLT TO AMP FACT
                                   5
 97
                                   0b10000001
 98 #define NRMASK
 99 #define NR0
                                    0b10000001
100 #define NR1
                                   0b10111101
                                  0b10111101
0b00010011
0b00011001
0b01001001
0b01000001
0b10011101
101 #define NR2
102 #define NR3
103 #define NR4
104 #define NR5
105 #define NR6
106 #define NR7
107 #define NR8
                                   0b0000001
108 #define NR9
                                     0b00001001
109
110
        //shift 1 - 3
                                  111 #define BIGMASK
112 #define SMAMASK
113 #define MASK
                                     (BIGMASK | SMAMASK)
```

```
114
115 #define V
                                    0b11111111111011101011011111
116 #define WFIRST
                                    0b1110111101010101011111111
117 #define WSECOND
                                    0b111111001111111111111111111
118
119 #define AFIRST
                                    0b1110011101011111001011111
                                    0b111110101111111111111111111
120 #define ASECOND
121
122 #define RSECOND
                                    0b11111111111111111111111111111
123
124 #define UNIT NONE
                                   0xffffff
125 #define UNIT VA
                                    (V&ASECOND)
126 #define UNIT A
                                    AFIRST
127 #define UNIT W
                                   WFIRST
128 #define UNIT V
129 #define UNIT VR
                                    (V&RSECOND)
130
131 #define NNR0
                                    0b11111110111111000111100111
132 #define NNR1
                                    133 #define NNR2
                                    0b1111101101111100111100111
134 #define NNR3
                                    0b1111101101111110011100111
135 #define NNR4
                                    0b1111101001111111011110111
136 #define NNR5
                                    0b1111101001111110011101111
137 #define NNR6
                                    0b1111101001111100011101111
138 #define NNR7
                                    0b11111111111111111011100111
139 #define NNR8
                                    0b111110100111100011100111
140 #define NNR9
                                    0b11111010011111100111100111
142 #define WAIT TOH
                                   NOP();
143 #define WAIT TOL
                                    NOP();\
144
                                    NOP();\
145
                                    NOP();
146 #define WAIT T1H
                                   NOP();\
                                    NOP();\
147
148
                                    NOP(); \
                                    NOP();
150 #define WAIT T1L
                                    NOP();
152 #define LED LOWBIT
                                    STATUS_LED=1;\
153
154
                                    WAIT TOH\
155
                                    STATUS LED=0;\
156
                                    WAIT TOL\
157
                                    }
158 #define LED HIGHBIT
                                    { \
159
                                    STATUS LED=1;\
160
                                    WAIT T1H\
161
                                    STATUS LED=0;\
162
                                    WAIT T1L\
                                    (0xff>>3)
164 #define LED INTENSE
166 #define DMODE_NONE
167 #define DMODE CURRENT
                                   1
168 #define DMODE VOLTAGE
                                    2
169 #define DMODE ANGLE
170 #define DMODE APPARENT
```

```
171 #define DMODE_REAL
172 #define DMODE REACTIVE
                                    7
173 #define DMODE MAX
174
175 #define QUARTER ROTATION
                                       (100)
176 #define HALF ROTATION (QUARTER ROTATION<<1)
177 #define FULL ROTATION (QUARTER ROTATION<<2)
178 #define MIN SIN RES
                            (-100)
179 #define MAX SIN RES
                           (100)
180
181 #define getTime()
                                TMR 1
182
            /**
183
            * Prepares the ports of the processor.
184
            * No device may be turned on. They only "exception" to this rule is the
185
             * Display, which uses shift registers for storing it's information, it
186
            * is cleared after all other initialisation steps have been finished.
187
188
             * This Method also activates the pull up resistor and sets the operation
189
            * frequency to 32MHz.
            */
190
191
            void initPins();
192
            /**
193
            * Prepare the ADC module for operation. The positive reference is set
194
            * to Vdd, while the negative one is set to Vss. The conversion clock
195
            * speed is set to FOSC/64 since the SampleHold - Capacitor would otherwi
196
197
            * not be fully charged and unexpected results would be the consequence.
198
            */
199
            void initADC();
200
            /**
201
202
            * Prepares the Timer 2 as an refresh-rate generator. This functionality
203
            * is, as of now, not used and not vital to the operation of the device.
204
205
            void initTMR2();
206
            /**
207
             * Timer 1 is set up with a resolution of 250ns. It is used to measure th
208
209
             * phase delay between Current and Voltage.
210
211
            void initTMR1();
212
            /**
213
            * initializes both Buffers to 1.024Volts.
214
             * The first one is needed to measure Vdd with the ADC-Module,
215
216
            * the second one is used to provide the comparator with a voltage to
217
            * compare the Voltage against.
218
            void initFVR();
219
220
            /**
221
            * Prepares the PWM with Timer 4. This PWM is used to provide the second
222
223
             * reference to the second comparator, which is used for the current.
224
            * Since the voltage, representing the current is small, we need to have
             * a precise reference, this we used a PWM with a low-pass filter of
225
226
             * second order to create a direct current.
227
             * The Output is switched from RB3 to RB0.
```

```
*/
228
229
            void initPWMTMR4();
230
231
232
             * Sets up the Comparator 1 Module for measuring the phase of the Voltage
233
             * The Interrupt is set to fire on falling edges only.
234
             */
235
            void initCOMP1();
236
            /**
237
238
             * Sets up the Comparator 2 Module for measuring the phase of the Curren∜
239
             * The Interrupt is set to fire on falling edges only.
240
            void initCOMP2();
241
242
            /**
243
             * Configures the USART Module as asynchronous with an baud rate of 9600
244
245
             * and clears all previously received data.
             */
246
247
            void initBT();
248
            /**
249
250
             * Performs an AD-Conversion on the specified source. The Sources ANO to
251
             * AN11 are proportional to the source specified (setting src to 4 will
252
             * read from AN4). Also the source for the FVR-Buffer1 can be selected,
253
             * which is 0x1f;
254
             * @param src the source from which to convert
255
256
            void adc(const int8_t src);
257
258
259
             * This method is a placeholder method and was written, in case a Voltage
260
261
             * measurement was to be implemented. In it's current state it returns th
262
             * value of 230Volts.
             * @return the line voltage (about 230V in Europe)
263
264
             * /
265
            uint8 t readVoltage();
266
267
            /**
268
             * Measures the current which is currently flowing and returns it in mA
             * as Integer to provide an accurate result, without the implications of
269
             * using floats. The channel from which the measurement is taken is AN7.
270
271
             * @return the measured current in mAmps.
             */
272
273
            int24 t readCurrent();
274
            /**
275
276
             * Measures Vdd and returns it as an Integer in the range of 0 to 1023.
277
             * This is useful, because the calculation of the current is a lot easier
278
             * if first the conversion from all 10 bit values to more reasonable ones
279
             * is done and only then the currect value computed.
             * @return the supply voltage from 0 to 1023 where 0 = 0V and 1023 = 5V
280
281
             */
            uint16_t readVdd();
282
283
            /**
284
```

```
285
             * Shifts one byte of data into the shift registers with the least
286
             * significant bit first.
287
             * @param data the data to write into the shift register
             */
288
289
            void so(const uint8_t data);
290
            /**
291
292
             * Clears the display by writing 0xff into every shift register.
293
             * @param leng the number of registers
             */
294
295
            void clearDisplay(int8 t leng);
296
297
             * Sends one byte of colour information to the status led. Since
298
299
             * the colour depends on the write order this function does not specify
300
             * the colour of the LED.
             * @param the intensity of the colour
301
302
             */
303
            void sendColour(uint8_t);
304
            /**
305
306
             * Computes the time difference of the two times. Tm low is the time, whi
             * came chronologically before tm high. Since these values are the values
307
308
             * of Timer 1 at set time it could be, that tm_low>tm_high, if this is th
309
             * case the difference will be computed as follos: 0xffff - tm low + tm 1
310
             * otherwise the difference is simply tm high-tm low.
             * @param tm_low the chronologically first value
311
             * @param tm_high the chronologically second value
312
             * @return the time difference in 250nano seconds
313
             */
314
315
            uint16_t deltaT(uint16_t tm_low, uint16_t tm_high);
316
317
            /**
318
             * Reads the sine from the eeprom. It is important to note, that not
             * 360° represent a full rotation, but rather 400°. Since not every
319
             * value can be stored in the eeprom (it is also not needed) it reads
320
321
             * only the value from 0 to 100°.
             * @param z the angle in grad (not deg!)
322
323
             * @return the sine multiplied by 100
324
             */
325
            int8_t sin_(int8_t z);
326
            /**
327
328
            * Computes values of the sine, which are not covered by sin ().
             * @param z the angle in grad (not deg!)
329
330
             * @return the sine multiplied by 100
             */
331
            int8_t sin(int16_t z);
332
333
            /**
334
335
            * Behaves in the exactly same way as sin() but returns the cosine.
336
             * @param z the angle ing grad (not deg!)
             * @return the cosine multiplied by 100
337
338
             */
            int8 t cos(int16 t z);
339
340
            /**
341
```

```
342
            * Evaluates if the LED value can be rewritten (the LED needs a reset
343
             * time of 50us). If these 50us have passed since the last write to the
            * LED this method returns 1 otherwise 0.
344
345
            * @return a flag if the LED can be rewritten
346
            */
347
           uint8_t ledReset();
348
349
            /**
             * Writes a colour to the LED. The led is programmed with an rgb profile.
350
351
            * @param g the green colour intensity
352
            * @param r the red colour intensity
353
            * @param b the blue colour intensity
354
355
           void setLED(uint8_t g, uint8_t r, uint8_t b);
356
357
358
            * Writes a specified unit into the shift registers. Note, that all
359
            * following registers need to be filled, in order for these values to
360
            * appear in the correct register.
361
            * @param u the unit to write into the registers
362
            */
363
           void setUnit(uint24_t u);
364
365
            /**
366
            * Writes the Integer value into the registers. Typically setUnit() is
            * called prior to this function and only after this function has been
367
368
            * called the display will output reasonable values.
369
            * @param v
370
             */
371
           void setVal(int16_t v);
372
373
            * The interrupt service routine.
374
375
            * It handles incoming data and the phases of current and voltage.
376
            */
377
           void interrupt ISR();
378
379 #ifdef cplusplus
380 }
381 #endif
382
383 #endif
```