

# Roulette Display

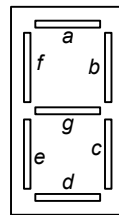
## General Description

The RD47seg accepts five lines of input data ( $d0..4$ ), that is stored in an internal register on every rising edge of the  $wr$  signal. The outputs of such register are decoded as show in the figure below. The decoded data is connected to the 7 output pins ( $a$  to  $g$ ) that drive the LEDs of the 7-segment display. The phase ( $ph$ ) input is used to reverse the function table phase.

## PIN Description

<b>d0</b>	1 •	24	<b>Vcc</b>
<b>d1</b>	2	23	nc
<b>d2</b>	3	22	<b>e</b>
<b>d3</b>	4	21	<b>f</b>
<b>d4</b>	5	20	<b>g</b>
nc	6	19	<b>d</b>
nc	7	18	<b>a</b>
nc	8	17	<b>b</b>
nc	9	16	<b>c</b>
<b>ph</b>	10	15	nc
<b>wr</b>	11	14	nc
<b>Gnd</b>	12	13	nc

## 7-Segment Display



00h	01h	02h	03h	04h	05h	06h	07h
08h	09h	0Ah	0Bh	0Ch	0Dh	0Eh	0Fh
10h	11h	12h	13h	14h	15h	16h	17h



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**RD47seg**