SUVANKAR BISWAS (07EE1039)

ACADEMIC QUALIFICATIONS

Year	Degree/Certificate	Institution	CGPA
2010	B.Tech(Hons), Electrical Engineering	IIT Kharagpur	8.72/10.00
2006	Class XII, ISC	Don Bosco School	96.25%
2004	Class X, ICSE	Don Bosco School	93.40%

ACADEMIC DISTINCTIONS

SCHOLASTIC ACHIEVEMENTS

- **All India Rank** of **1726** in the **IIT-JEE** amongst 3,00,000 candidates, 2007.
- **All India Rank** of **68** in the **West Bengal Joint Entrance Examination,** 2006**.**
- **State Rank** of **49 and All India Rank** of **3037 in AIEEE,** 2006**.**
- Secured a **branch change** to **B.Tech(Hons)** in **Electrical Engineering** on account of **CGPA 9.21/10.00** in 1st year **(top 5%** in institute**).**
- **Department Rank** of **6** out of 42 B.Tech students in department**.**
- Recipient of the prestigious **DAAD(**German Academic Exchange Service**) scholarship** for **summer internship, 2010.**
- Score of **1450** in the **GRE** (**800/800** in the **quantitative** portion)
- Score of **116/120** in **TOEFL**
- Recipient of **BSNL Scholarship** for **Meritorious Students** in **Engineering** from **2007 onwards.** It is given to students studying engineering at **National Institutes** of **importance** with **cgpa above 8.00**.

RESEARCH PROJECTS

- Design of **two-way** communication between two **Zigbee**-compliant **RF** devices **(B.Tech project-** in progress**).**
- Designed a **hand-held programmer** with USB and Serial Port communication for use with an **FPGA**. my first experience of **designing a PCB** for a simple application and this worked as a platform for my **BTP**.
- **Signal Parameter Estimation** of **Speech** Signals. On completion of the project I was awarded a grade of excellence by the concerned professor.
- Design of an **intelligent low-power** supply module for **Laptops**.

INTERNSHIPS

TU Munich, Germany **Summer Researcher** **May-July'10**

- **Designed** and **tested** an innovative **integer programming** method for **cost function minimization** for **Model Predictive Control** of Multi-Level Inverters.
- Analysed **simulation techniques** for **Space Vector Modulation** of Multi-Level Inverters.
- Did **assembly** and **test** of **IGBT Gate driver** circuits.

BHEL, Kolkata **Trainee Engineer(Power Systems Division)** **June'09**

- Understood the **principles** and **working** of a standard **thermal power plant**.
- Made a **critical appraisal** of the 6.6 kV **switchgear** in the **Budge Budge Generating Station** unit-3.
- Suggested **scopes** for the **improvement** for the **control scheme** for the switchgear.

LANGUAGES KNOWN: **English, German, Hindi, Bengali**.
OPERATING SYSTEMS KNOWN: **MS Windows, Mac OS, Linux, Solaris**.
PROGRAMMING LANGUAGES KNOWN: **C, C++, Java, MATLAB, Verilog, VHDL**.
ANALOG AND DIGITAL CAD TOOLS KNOWN: **Cadence, ORCAD, PSpice, Eagle**.

CO-CURRICULAR ACHIEVEMENTS

- Was awarded a **certificate of excellence('A' grade)** on successful completion of a first course in **VLSI Design** and **CAD**, **Advanced VLSI Design Lab**, IIT Kharagpur, **May'09**.
- Qualified for the **final round** of **Overnite'09**, an **IBM-ACPC** certified **programming contest** held in **Kshitij'09** and was among the **top 50** selected.
- Was awarded a **certificate of appreciation** from **Texas Instruments** for successfully conducting a **workshop** on **Digital Signal Processing.**

EXTRA-CURRICULAR ACHIEVEMENTS

- Was an integral part of the **Gold** winning **inter-hall Bengali Dramatics Team, Nehru Hall of Residence, in 2008-09.**
- Member of the **Silver-winning** inter-hall **Football Team, Nehru Hall of Residence, in 2008-09.**
- **Bronze-medal** winner in **Open IIT 'Whats the Good Word'** Competition**, 2010.**
- **NSS Volunteer** in **first** and **second year** and was **head** of **several local rural survey teams.**