BATTULA THARUN (07EE1027)

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ACADEMIC RECORD:

Class/Year	Degree/Certificate	Institute/School, City	Performance
B. Tech (Hons.)	4 th year undergraduate,7 th semester Department of Electrical Engineering	Indian Institute of Technology, Kharagpur	7.07 CGPA
Class XII 2007	Intermediate Public Examination, Board of Intermediate Education, Andhra Pradesh	Sri Chaitanya Junior Kalasala Kukatpally , Hyderabad	93.5%
Class X 2005	All India Secondary School Examination Certificate, Central Board of Secondary Education	Jawahar Navodaya Vidyalaya Karimnagar, Andhra Pradesh	89 %

Scholastic Achievements

- Secured All India IIT JEE Rank 649- out of 5 Lakh students attended, i.e., in the top 0.1%.
- Secured AIEEE Rank -891 out of about 6 Lakh students attended
- Obtained *Certificate of Merit* in National Mathematics Olympiad conducted by All India Schools Mathematics Teachers Association 2003.
- Is a recipient of Merit-cum-Means (MCM) Scholarship for good performance, from IIT Kharagpur.

SUMMER INTERNSHIP:

Name of the Organization	Project outline and work	
Place: Turin, Italy	 Designed and implemented Neuromuscular 	
Duration: 5th May to 18th July, 2010.	Electrical Stimulator for the generation of	
Professor: Dr.Roberto Merletti,	Electromyographic signals	
Department of Electronics	Implementation is done on Spartan 3AN FPGA	
Politecnico di Torino, Italia.	device.	
Institution:Laboratory of Engineering of Neuromuscular System (LISiN)	 VHDL language and Assembly languages are used for the design of waveform synthesizer and controller respectively. 	

ACADEMIC PROJECTS

B Tech Project

Project Title: Model Order Reduction using the Multi-objective optimization using in Genetic Algorithm.

Outline: Approximating higher order system to lower order system model with the objectives minimizing the error between given and reduced model for both time and frequency responses.

Software used: Matlab ,Control System Tool box, Genetic algorithm tool box.

Guide: Prof. Jayanta Pal, Department Of Electrical engineering.

Research experiences

- Second Year summer Training: Successfully completed summer training course on Integrated Circuit Design
 conducted by the Advanced VLSI Design Laboratory, IIT Kharagpur in the duration 4th May to 29th May 2009,
 under the supervision of Prof. Pallab Das Gupta, and acknowledged for Good Performance.
- [Completed] Designing Matlab Simulink model for Elevator Control System comprising the graphical user interface to reduce the jerks and process modeling in VHDL language.
- [Completed] Designing an embedded system Voice Operated Switch, for commands ON and OFF, by implementing filter banks in 8051 Micro controller as real time Signal Processing under the guidance of Prof.A.Routray.
- Designing the system model for adaptive control of variable speed wind turbine with the aim of maximizing the powerfrom generator.
- Worked as a member of KRAIG (Kharagpur Robotics and Artificial Intelligence Group), under the guidance of technical heads, Robotix team, IIT Kharagpur.

SKILL SET:

- Software Skills: C-language, Verilog, VHDL, Assembly language.
- Engineering Tools: MATLAB, Xilinx ISE, Cadence virtuoso, SPICE, Auto-CAD.
- Operating Systems : Windows, Linux, Mac OS
- Languages: English (Education), Telugu (Proficient), Hindi (Proficient).

INTERESTS:

- Interested in development works based on practical engineering situations (Mainly if topics related to Electrical and Electronics Engineering)
- Interest in Analysis and Application of concepts with modeling and simulation of real world processes (using C, MATLAB, Xilinx etc.,)
- My Hobbies are swimming, Hockey, Football etc...

EXTRA CURRICULAR ACTIVITIES:

- Gold Medalist in Inter Hall Field Hockey 2008.
- Achieved Gold Medal in Open IIT Hockey in 2008.
- Was active member of National Service Scheme for two years.
- Taken part in various Hall cultural activities like Inter Hall Choreography, Illumination and Rangoli

DECLARATION:

I consider myself to be a hard-working, work devoted, punctual and balanced individual capable of sustained commitment. I can fully dedicate myself to the tasks and work assigned to me and come up with better results. I vouch that the information furnished above are true to my knowledge.