

LRU replacement

order - 4, 3, 25, 8, 19, 6, ^H25, ^H8, 16, 35, 45, 22, ^H8, 3, ^H16, ^H25, 7

0	4 45
1	3 22
2	25
3	8
4	19 3
5	6 7
6	16
7	35

∴ cache block 5 has memory block 7

Fully Associative Cache Replacement

order - 4, 3, 25, 8, 19, 6, ^H25, ^H8, 16, 35, 45, 22, ^H8, 3, ^H16, 25, 7

0	4 45
1	3 22
2	25 3
3	8 25
4	19 7
5	6
6	16
7	35

Hence 7 memory block will be placed in 4th cache block