

1. Description

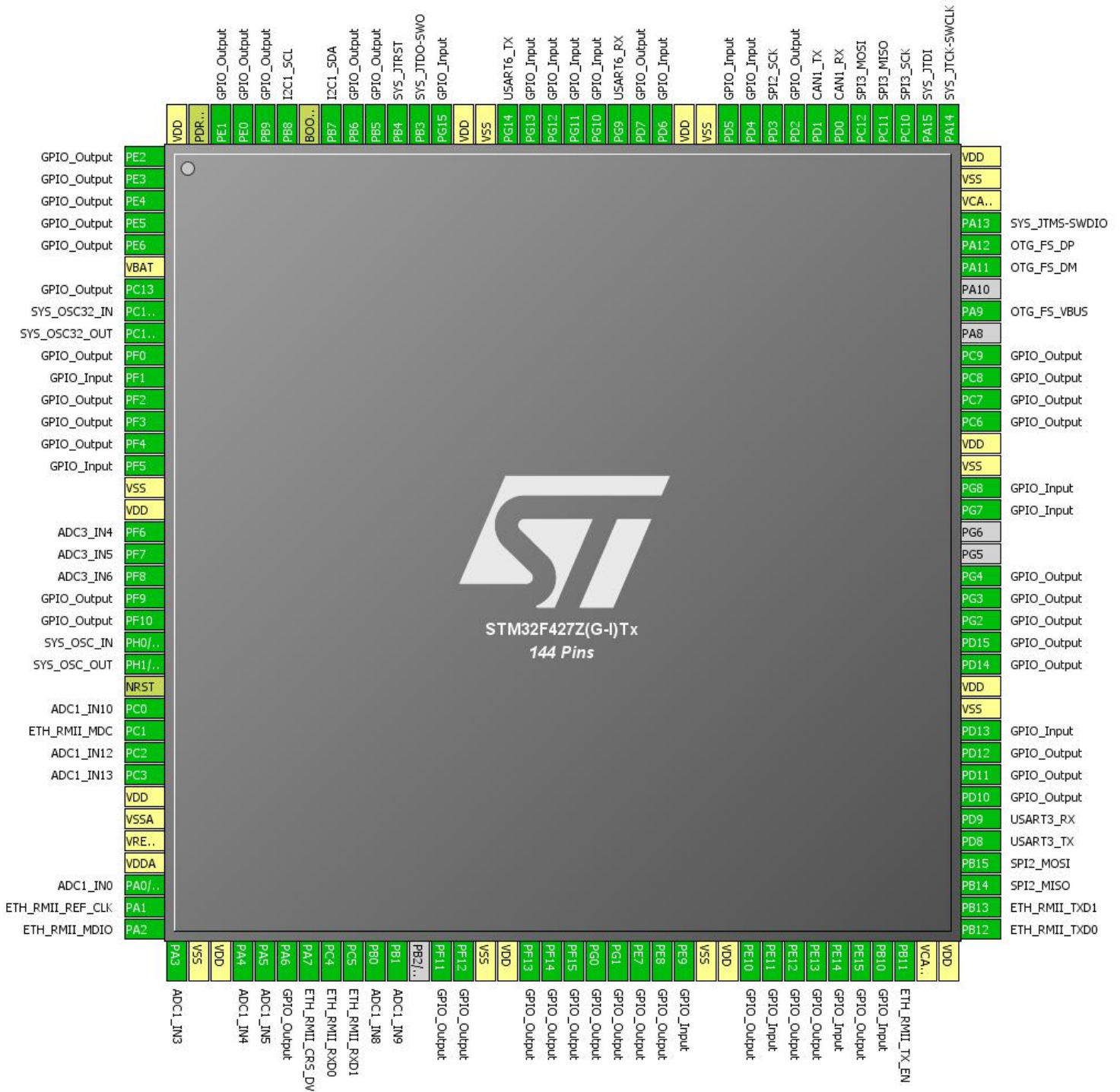
1.1. Project

Project Name	FEC2 All Pins
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1.2. MCU

MCU family	STM32F4
MCU name	STM32F427Z(G-I)Tx
MCU Package	LQFP144
MCU Pin number	144

2. Pinout Configuration



3. Peripherals Configuration

Peripheral	Mode	Remap	Fonction	Pin
ADC1	IN0	0	ADC1_IN0	PA0/WKUP
	IN3	0	ADC1_IN3	PA3
	IN4	0	ADC1_IN4	PA4
	IN5	0	ADC1_IN5	PA5
	IN8	0	ADC1_IN8	PB0
	IN9	0	ADC1_IN9	PB1
	IN10	0	ADC1_IN10	PC0
	IN12	0	ADC1_IN12	PC2
	IN13	0	ADC1_IN13	PC3
ADC3	IN4	0	ADC3_IN4	PF6
	IN5	0	ADC3_IN5	PF7
	IN6	0	ADC3_IN6	PF8
CAN1	CAN	0	CAN1_RX	PD0
			CAN1_TX	PD1
ETH_RMII	RMII	0	ETH_RMII_CRSDV	PA7
			ETH_RMII_MDC	PC1
			ETH_RMII_MDIO	PA2
			ETH_RMII_REF_CLK	PA1
			ETH_RMII_RXD0	PC4
			ETH_RMII_RXD1	PC5
			ETH_RMII_TXD0	PB12
			ETH_RMII_TXD1	PB13
			ETH_RMII_TX_EN	PB11
I2C1	I2C	0	I2C1_SCL	PB8
			I2C1_SDA	PB7
OTG_FS	Host-Only	0	OTG_FS_DM	PA11
			OTG_FS_DP	PA12
			OTG_FS_VBUS	PA9
SPI2	Full-Duplex-Master	0	SPI2_MISO	PB14
			SPI2_MOSI	PB15
			SPI2_SCK	PD3
SPI3	Full-Duplex-Master	0	SPI3_MISO	PC11
			SPI3_MOSI	PC12
			SPI3_SCK	PC10
SYS	JTAG(5-pin)	0	SYS_JTCK-SWCLK	PA14
			SYS_JTDI	PA15
			SYS_JTDO-SWO	PB3
			SYS_JTMS-SWDIO	PA13
			SYS_JTRST	PB4

FEC2 All Pins Project

Peripheral	Mode	Remap	Fonction	Pin
	HSE-External-Oscillator	0	SYS_OSC_IN	PH0/OSC_IN
			SYS_OSC_OUT	PH1/OSC_OUT
	LSE-External-Oscillator	0	SYS_OSC32_IN	PC14/OSC32_IN
			SYS_OSC32_OUT	PC15/OSC32_OUT
USART3	Asynchronous	0	USART3_RX	PD9
			USART3_TX	PD8
USART6	Asynchronous	0	USART6_RX	PG9
			USART6_TX	PG14

4. Pins Configuration

Pins	Pos	Functions
PE2 *	1	GPIO_Output
PE3 *	2	GPIO_Output
PE4 *	3	GPIO_Output
PE5 *	4	GPIO_Output
PE6 *	5	GPIO_Output
PC13 *	7	GPIO_Output
PC14/OSC32_IN	8	SYS_OSC32_IN
PC15/OSC32_OUT	9	SYS_OSC32_OUT
PF0 *	10	GPIO_Output
PF1 *	11	GPIO_Input
PF2 *	12	GPIO_Output
PF3 *	13	GPIO_Output
PF4 *	14	GPIO_Output
PF5 *	15	GPIO_Input
PF6	18	ADC3_IN4
PF7	19	ADC3_IN5
PF8	20	ADC3_IN6
PF9 *	21	GPIO_Output
PF10 *	22	GPIO_Output
PH0/OSC_IN	23	SYS_OSC_IN
PH1/OSC_OUT	24	SYS_OSC_OUT
PC0	26	ADC1_IN10
PC1	27	ETH_RMII_MDC
PC2	28	ADC1_IN12
PC3	29	ADC1_IN13
PA0/WKUP	34	ADC1_IN0
PA1	35	ETH_RMII_REF_CLK
PA2	36	ETH_RMII_MDIO
PA3	37	ADC1_IN3
PA4	40	ADC1_IN4
PA5	41	ADC1_IN5
PA6 *	42	GPIO_Output
PA7	43	ETH_RMII_CRS_DV
PC4	44	ETH_RMII_RXD0
PC5	45	ETH_RMII_RXD1
PB0	46	ADC1_IN8
PB1	47	ADC1_IN9
PF11 *	49	GPIO_Output
PF12 *	50	GPIO_Output

FEC2 All Pins Project

Pins	Pos	Functions
PF13 *	53	GPIO_Output
PF14 *	54	GPIO_Output
PF15 *	55	GPIO_Output
PG0 *	56	GPIO_Output
PG1 *	57	GPIO_Output
PE7 *	58	GPIO_Output
PE8 *	59	GPIO_Output
PE9 *	60	GPIO_Input
PE10 *	63	GPIO_Output
PE11 *	64	GPIO_Input
PE12 *	65	GPIO_Output
PE13 *	66	GPIO_Output
PE14 *	67	GPIO_Input
PE15 *	68	GPIO_Output
PB10 *	69	GPIO_Input
PB11	70	ETH_RMII_TX_EN
PB12	73	ETH_RMII_TXD0
PB13	74	ETH_RMII_TXD1
PB14	75	SPI2_MISO
PB15	76	SPI2_MOSI
PD8	77	USART3_TX
PD9	78	USART3_RX
PD10 *	79	GPIO_Output
PD11 *	80	GPIO_Output
PD12 *	81	GPIO_Output
PD13 *	82	GPIO_Input
PD14 *	85	GPIO_Output
PD15 *	86	GPIO_Output
PG2 *	87	GPIO_Output
PG3 *	88	GPIO_Output
PG4 *	89	GPIO_Output
PG7 *	92	GPIO_Input
PG8 *	93	GPIO_Input
PC6 *	96	GPIO_Output
PC7 *	97	GPIO_Output
PC8 *	98	GPIO_Output
PC9 *	99	GPIO_Output
PA9	101	OTG_FS_VBUS
PA11	103	OTG_FS_DM
PA12	104	OTG_FS_DP
PA13	105	SYS_JTMS-SWDIO
PA14	109	SYS_JTCK-SWCLK
PA15	110	SYS_JTDI

FEC2 All Pins Project

Pins	Pos	Functions
PC10	111	SPI3_SCK
PC11	112	SPI3_MISO
PC12	113	SPI3_MOSI
PD0	114	CAN1_RX
PD1	115	CAN1_TX
PD2 *	116	GPIO_Output
PD3	117	SPI2_SCK
PD4 *	118	GPIO_Input
PD5 *	119	GPIO_Input
PD6 *	122	GPIO_Input
PD7 *	123	GPIO_Output
PG9	124	USART6_RX
PG10 *	125	GPIO_Input
PG11 *	126	GPIO_Input
PG12 *	127	GPIO_Input
PG13 *	128	GPIO_Input
PG14	129	USART6_TX
PG15 *	132	GPIO_Input
PB3	133	SYS_JTDO-SWO
PB4	134	SYS_JTRST
PB5 *	135	GPIO_Output
PB6 *	136	GPIO_Output
PB7	137	I2C1_SDA
PB8	139	I2C1_SCL
PB9 *	140	GPIO_Output
PE0 *	141	GPIO_Output
PE1 *	142	GPIO_Output

* The pin is affected with an I/O function