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| **Func. Group** | **Nick**  **Name** | **Port/Bit** | **An/**  **Dig** | **Dir.** | **Configured As** | **Pin #** | **Rem.** |
| **SSR** | SSR\_1-2 | PE0-1 | Dig | Out | Output Mode: Push-Pull;  Speed 50MHz;  NO\_PULL; | 141-142 |  |
| SSR\_3-7 | PG4-PG8 | 89-93 |  |
| SSR\_8-10 | PE7-PE9 | 58-60 |  |
| SSR\_11-16 | PE10-PE15 | 63-68 |  |
| **SSR\_b** | SSR\_b\_1-6 | PF0-PF5 | Dig | Out | Output Mode: Push-Pull;  Speed 50MHz;  NO\_PULL; | 10-15 |  |
| SSR\_b\_7-11 | PF6-PF10 | 18-22 |  |
| SSR\_b\_12-13 | PF11-PF12 | 49-50 |  |
| SSR\_b\_14-16 | PF13-PF16 | 53-55 |  |
| **SSR\_c** | SSR\_c\_1-6 | PD0-PD5 | Dig | Out | Output Mode: Push-Pull;  Speed 50MHz;  NO\_PULL; | 114-119 |  |
| SSR\_c\_7-8 | PD6-PD7 | 122-123 |  |
| SSR\_c\_9-14 | PD8-PD13 | 77-82 |  |
| SSR\_c\_15-16 | PD14-PD15 | 85-86 |  |
| **LEDs** | LED\_A- LED\_D | PG9-PG12 | Dig | Out | -||- | 124-127 |  |
| **USART1** | USART1\_TX | PA9 | AF | Out |  | 101 |  |
| USART1\_RX | PA10 | In |  | 102 |  |
| **USART3** | USART3\_TX | PC10 | AF | Out |  | 111 |  |
| USART3\_RX | PC11 | In |  | 112 |  |
| **USART6** | USART6\_TX | PC6 | AF | Out |  | 96 |  |
| USART6\_RX | PC7 | In |  | 97 |  |
| Modebus\_2  HalfDuplexCtrl | RE | PA0 | Dig | Out | Output Mode: Push-Pull;  Speed 50MHz;  NO\_PULL; | 34 |  |
| DE | PG15 | 132 |
| Modebus\_1  HalfDuplexCtrl | RE2 | PC8 | 98 |  |
| DE2 | PC9 | 99 |
|  | ETH\_MDC | PC1 |  |  |  |  |  |
|  | ETH\_MDIO | PA2 |  |  |  |  |  |
|  | ETH\_RMII\_REF\_CLK | PA1 |  |  |  |  |  |
|  | ETH\_RMII\_CRS\_DV | PA7 |  |  |  |  |  |
|  | MCO1 | PA8 |  |  |  |  |  |
|  | ETH\_RMII\_TX\_EN | PB11 |  |  |  |  |  |
|  | ETH\_MII\_COL | PA3 |  |  |  |  |  |
|  | ETH\_RMII\_RXD0 | PC4 |  |  |  |  |  |
|  | ETH\_RMII\_RXD1 | PC5 |  |  |  |  |  |
|  | ETH\_RMII\_TXD0 | PG13 |  |  |  |  |  |
|  | ETH\_RMII\_TXD1 | PG14 |  |  |  |  |  |
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