

# SARA-G3 series

## GSM/GPRS modules

### Data Sheet

#### Abstract

Technical data sheet describing SARA-G3 series GSM/GPRS cellular modules.

These modules are complete and cost efficient solutions offering two-band or quad-band GSM/GPRS voice and/or data transmission technology in a compact form factor.



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#### This document applies to the following products:

Product name	Type number	Firmware version	SDN / IN / PCN
SARA-G300	SARA-G300-00S-00	08.58	GSM.G2-TN-13007
SARA-G310	SARA-G310-00S-00	08.58	GSM.G2-TN-13007
SARA-G340	SARA-G340-00S-00	08.49	UBX-14000382
	SARA-G340-01S-00	08.64	UBX-14015742
SARA-G350	SARA-G350-00S-00	08.49	GSM.G2-TN-13002
	SARA-G350-01S-00	08.64	UBX-14015742
	SARA-G350-01B-00	08.64	TBD
SARA-G350 ATEX	SARA-G350-00X-00	08.49	GSM.G2-TN-13002

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# 1 Functional description

## 1.1 Overview

SARA-G3 series modules are versatile 2.5G GSM/GPRS cellular modules in a miniature LGA (Land Grid Array) form factor. Featuring low power consumption, the SARA-G3 series combines baseband, RF transceiver, power management unit, and power amplifier in a single, easy-to-integrate solution.

SARA-G3 modules provide a fully qualified and certified solution, reducing cost and enabling short time to market. These modules are ideally suited for M2M and automotive applications such as: Automatic Meter Reading (AMR), Remote Monitoring Automation and Control (RMAC), surveillance and security, eCall, road pricing, asset tracking, fleet management, anti theft systems and Point of Sales (PoS) terminals.

SARA-G340 (dual-band) and SARA-G350 (quad-band) are full feature GSM/GPRS cellular modules with a comprehensive feature set including an extensive set of internet protocols. The modules also provide fully integrated access to u-blox GNSS positioning chips and modules, with embedded A-GPS (AssistNow Online and AssistNow Offline) functionality. Any host processor connected to the cellular module through a single serial port can control both the cellular module and the positioning chip / module.

SARA-G310 (quad-band) and SARA-G300 (dual-band) are GSM/GPRS cellular modules targeted for high volume cost-sensitive applications, providing GSM/GPRS functionalities with a reduced set of additional features to minimize the customer's total cost of ownership.

The SARA-G3 series' compact form factor and LGA pads allow fully automated assembly with standard pick & place and reflow soldering equipment for cost-efficient, high-volume production.

## 1.2 Product features

Module	Data Rate	Bands	Interfaces	Audio	Functions
	GPRS multi-slot class 10	GSM/GPRS 4-band GSM/GPRS 2-band 900/1800	UART SPI USB DDC for u-blox GNSS receivers GPIO	Analog Audio Digital Audio	Network indication Antenna detection Jamming detection Embedded TCP/UDP FTP, HTTP, SMTP SSL GNSS via Modem AssistNow software FW update over AT (FOAT) FW update over the air (FOTA) In-band modem CellLocate® Low power idle-mode ATEX certification
<b>SARA-G300</b>	•	•	2		• E
<b>SARA-G310</b>	•	•	2		• E
<b>SARA-G340</b>	•	•	2 • 4	• •	• • • • • F • • • • A • • •
<b>SARA-G350</b>	•	•	2 • 4	• •	• • • • • F • • • • A • • •
<b>SARA-G350 ATEX</b>	•	•	2 • 4	• •	• • • • • • • • • A • • • •

A = available upon request

E = 32 kHz signal at EXT32K input pin is required for low power idle-mode

F = Supported by '01' FW versions onwards

**Table 1: SARA-G3 series – summary of modules' main features<sup>1</sup>**

<sup>1</sup> SARA-G350 ATEX modules provide the same feature set as the SARA-G350 modules, plus the certification for use in potentially explosive atmospheres. Unless otherwise specified, SARA-G350 refers to all SARA-G350 ATEX modules and SARA-G350 modules.

The diagram illustrates the architecture of a mobile phone system, showing the flow of signals and power between various components. The components are represented by gray boxes, and their interconnections are shown by lines with arrows indicating the direction of flow.

**Components and Connections:**

- ANT (Antenna):** Connected to the **Switch** component.
- Switch:** A component that routes signals between the antenna and the **PA** (Power Amplifier) and the **RF Transceiver**.
- PA (Power Amplifier):** A component that amplifies signals for transmission over the antenna.
- SAW Filter (Surface Acoustic Wave Filter):** A filter that filters signals between the **Switch** and the **RF Transceiver**.
- RF Transceiver:** A component that handles radio frequency signals, connected to the **Baseband** and the **SAW Filter**.
- Baseband:** A component that handles baseband signals, connected to the **RF Transceiver** and the **Memory**.
- Memory:** A component that stores data, connected to the **Baseband** and the **Power Management** block.
- Power Management:** A block that manages power, connected to the **Memory** and the **Baseband**.

**External Connections:**

- 26 MHz:** A clock signal input to the **RF Transceiver**.
- 32 kHz:** A clock signal input to the **Baseband**.
- Power-On, Reset, SIM card:** Control signals connected to the **Baseband**.
- UART, Auxiliary UART:** Data communication signals connected to the **Baseband**.
- VCC (Supply), V\_BCKP (RTC), V\_INT (I/O):** Power and control signals connected to the **Power Management** block.

The diagram illustrates the internal architecture of the RF front end and baseband IC. Key components and their connections include:

- External Connections:**
  - ANT:** Antenna connection to the Switch.
  - 26 MHz and 32.768 kHz:** Crystal oscillators connected to the RF Transceiver.
  - Power-On, Reset, SIM card, SIM card detection, UART, Auxiliary UART, DDC (for GPS/GNSS), Analog Audio, Digital Audio, GPIO, Antenna detection:** Various control and data signals connected to the Baseband.
  - VCC (Supply), V\_BCKP (RTC), V\_INT (I/O):** Power and I/O signals connected to the Power Management block.
- Internal Components:**
  - Switch:** Connects the ANT to the PA or the SAW Filter.
  - PA (Power Amplifier):** Amplifies signals for transmission.
  - SAW Filter (Surface Acoustic Wave Filter):** Filters signals between the PA and the RF Transceiver.
  - RF Transceiver:** Handles RF communication, connected to the 26 MHz and 32.768 kHz oscillators.
  - Memory:** Connected to the Baseband for data storage.
  - Baseband:** The central processing unit for RF signals, connected to the RF Transceiver, Memory, and various external interfaces.
  - Power Management:** Manages power supply and I/O signals.

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## 1.4 Product description

Item	SARA-G300	SARA-G310	SARA-G340	SARA-G350
GSM/GPRS Protocol Stack	3GPP Release 99	3GPP Release 99	3GPP Release 99	3GPP Release 99
Mobile Station Class	Class B <sup>2</sup>	Class B <sup>2</sup>	Class B <sup>2</sup>	Class B <sup>2</sup>
GSM/GPRS Bands	E-GSM 900 MHz DCS 1800 MHz	GSM 850 MHz E-GSM 900 MHz DCS 1800 MHz PCS 1900 MHz	E-GSM 900 MHz DCS 1800 MHz	GSM 850 MHz E-GSM 900 MHz DCS 1800 MHz PCS 1900 MHz
GSM/GPRS Power Class	Class 4 (33 dBm) for 900 band Class 1 (30 dBm) for 1800 band	Class 4 (33 dBm) for 850/900 bands Class 1 (30 dBm) for 1800/1900 bands	Class 4 (33 dBm) for 900 band Class 1 (30 dBm) for 1800 band	Class 4 (33 dBm) for 850/900 bands Class 1 (30 dBm) for 1800/1900 bands
Packet Switched Data Rate	GPRS multi-slot class 10 <sup>3</sup> Coding scheme CS1-CS4 Up to 85.6 kb/s DL <sup>4</sup> Up to 42.8 kb/s UL <sup>4</sup>	GPRS multi-slot class 10 <sup>3</sup> Coding scheme CS1-CS4 Up to 85.6 kb/s DL <sup>4</sup> Up to 42.8 kb/s UL <sup>4</sup>	GPRS multi-slot class 10 <sup>3</sup> Coding scheme CS1-CS4 Up to 85.6 kb/s DL <sup>4</sup> Up to 42.8 kb/s UL <sup>4</sup>	GPRS multi-slot class 10 <sup>3</sup> Coding scheme CS1-CS4 Up to 85.6 kb/s DL <sup>4</sup> Up to 42.8 kb/s UL <sup>4</sup>
Circuit Switched Data Rate	Up to 9.6 kb/s DL/UL <sup>4</sup> Transparent mode Non-transparent mode	Up to 9.6 kb/s DL/UL <sup>4</sup> Transparent mode Non-transparent mode	Up to 9.6 kb/s DL/UL <sup>4</sup> Transparent mode Non-transparent mode	Up to 9.6 kb/s DL/UL <sup>4</sup> Transparent mode Non-transparent mode
Network Operation Modes	I to III	I to III	I to III	I to III

**Table 2: SARA-G3 series GSM/GPRS characteristics summary**

The network automatically configures the channel encoding used by the module, depending on conditions and the quality of the radio link between cell phone and base station. If the channel is very noisy, the network may use the most robust coding scheme (CS-1) to ensure higher reliability. If the channel provides good conditions, the network can use the least robust but fastest coding scheme (CS-4) to obtain optimum speed.

Basic features	Supplementary services	Short Message Service (SMS)
Display of Called Number <sup>5</sup>	Call Hold/Resume (HOLD) <sup>5</sup>	Text and PDU mode supported
Indication of Call Progress Signals <sup>5</sup>	Call Waiting (CW) <sup>5</sup>	Mobile-Originating SMS (MO SMS)
Country/PLMN Indication	Call Forwarding (CFU, CFB, CFNRy, CFNRc) <sup>5</sup>	Mobile-Terminating SMS (MT SMS)
Country/PLMN Selection	Call Barring (BAOC, BOIC, BOIC-exHC, BAIC, BIC_Roam) <sup>5</sup>	SMS indication and acknowledgement
International Access Function <sup>5</sup>	Call Deflection (CD) <sup>5</sup>	SMS Cell Broadcast (CBS)
Service Indicator	Explicit Call Transfer (ECT) <sup>5</sup>	SMS during circuit-switched calls <sup>5</sup>
Emergency Calls Capabilities <sup>5</sup>	Multi-Party (MTPY) <sup>5</sup>	SMS over CSD
Dual Tone Multi Frequency (DTMF) <sup>5</sup>	Calling Line Identification Presentation (CLIP) <sup>5</sup>	SMS over PSD
Subscription Identity Management	Calling Line Identification Restriction (CLIR) <sup>5</sup>	SMS storage on SIM
Service Provider Indication	Connected Line Identification Presentation (CoLP) <sup>5</sup>	SMS storage on module memory <sup>5</sup>
Abbreviated Dialing <sup>5</sup>	Connected Line Identification Restriction (CoLR) <sup>5</sup>	Concatenated SMS
Fixed Number Dialing <sup>5</sup>	Unstructured Supplementary Services Data (USSD)	
Barring of Dialed Numbers <sup>5</sup>	Advice of Charge Charging (AoCC, AoCI) <sup>5</sup>	
SIM Application Toolkit <sup>5</sup>	Calling Name Presentation (CNAP) <sup>5</sup>	
ME-SIM lock	Network Identify and Time Zone (NITZ)	

**Table 3: SARA-G3 series mobile stations: basic features, supplementary services, and SMS service summary<sup>6</sup>**

<sup>2</sup> Device can be attached to both GPRS and GSM services (i.e. Packet Switch and Circuit Switch mode) using one service at a time.

<sup>3</sup> GPRS multi-slot class 10 implies a maximum of 4 slots in DL (reception) and 2 slots in UL (transmission) with 5 slots in total.

<sup>4</sup> The maximum bit rate of the module depends on the current network settings.

<sup>5</sup> Supported only by SARA-G340 and SARA-G350 modules

<sup>6</sup> These functionalities are supported via AT commands (for more details see the u-blox AT Commands Manual [1]).

## 1.5 AT command support

The module supports AT commands according to 3GPP standards: TS 27.007 [7], 27.005 [8], 27.010 [9], and the u-blox AT command extension.



For the complete list of the supported AT commands and their syntax, refer to the document u-blox AT Commands Manual [1].

RIL (Radio Interface Layer) drivers for Windows CE, Windows EC and Android are available for SARA-G340 / SARA-G350 modules. The RIL drivers are compatible with the following deliveries:

- Android 2.3 (Gingerbread)
- Android 4.3 (Jelly Bean)
- Windows Embedded CE 6.0
- Windows Embedded Compact 7


## 1.6 Supported features

Table 4 describes the main features supported by SARA-G3 modules. For more details, refer to the SARA-G3 / SARA-U2 Series System Integration Manual [2] and u-blox AT commands manual [1].

Feature	Module	Description
Network Indication	SARA-G340 / SARA-G350	GPIO configured to indicate the network status: registered home network, registered roaming, voice or data call enabled, no service. The network indication feature can be enabled through a custom AT command (refer to the u-blox AT commands manual [1], +UGPIOC AT command).
Antenna Detection	SARA-G340 / SARA-G350	The <b>ANT_DET</b> pin provides antenna presence detection capability, evaluating the resistance from the <b>ANT</b> pin to GND by means of an external antenna detection circuit implemented on the application board. The antenna detection feature can be enabled through a custom AT command (refer to the u-blox AT commands manual [1], +UANTR AT command).
Jamming detection	SARA-G340 / SARA-G350	Detects “artificial” interference that obscures the operator’s carriers providing access to the GSM service and reports the start and stop of such conditions to the application processor (AP). The AP can react appropriately, e.g. by switching off the radio transceiver to reduce power consumption and monitoring the environment at constant periods. The jamming detection feature can be enabled and configured through a custom AT command (refer to u-blox AT commands manual [1], +UCD AT command).
Embedded TCP/IP and UDP/IP	SARA-G340 / SARA-G350	Embedded TCP/IP and UDP/IP stack including direct link mode for TCP and UDP sockets. The sockets can be configured in Direct Link mode to establish a transparent end-to-end communication with an already connected TCP or UDP socket via serial interface.
FTP, FTPS <sup>7</sup>	SARA-G340 / SARA-G350	File Transfer Protocol as well as Secure File Transfer Protocol (SSL encryption of FTP control channel) functionalities are supported via AT commands.
HTTP, HTTPS <sup>7</sup>	SARA-G340 / SARA-G350	Hyper-Text Transfer Protocol as well as Secure Hyper-Text Transfer Protocol (SSL encryption) functionalities are supported via AT commands. HEAD, GET, POST, DELETE and PUT operations are available. Up to 4 client contexts can be simultaneously used.
GPS/GNSS via Modem	SARA-G340 / SARA-G350	Full access to u-blox positioning chips and modules is available through a dedicated DDC (I <sup>2</sup> C) interface. A single serial port from any host processor can control both the cellular module and the positioning chips / modules. For more details refer to the GNSS Implementation Application Note [3].

<sup>7</sup> FTPS and HTTPS supported by SARA-G340-01S / SARA-G350-01S onwards



Feature	Module	Description
AssistNow Software	SARA-G340 / SARA-G350	Embedded AssistNow Online and AssistNow Offline clients to provide full developed to provide better GNSS performance and faster Time-to-First-Fix. The clients can be enabled / disabled with an AT command.
CellLocate®	SARA-G340 / SARA-G350	Enables the estimation of device position based on the parameters of the mobile network cells visible to the specific device based on the CellLocate® database: <ul style="list-style-type: none"> <li>Normal scan: parameters of the visible home network cells are only sent</li> <li>Deep scan: parameters of all surrounding cells of all mobile operators are sent</li> </ul> CellLocate® is implemented using a set of AT commands for CellLocate® service configuration and position request.
Hybrid Positioning	SARA-G340 / SARA-G350	Provides the module's current position using a u-blox positioning chip or module or the estimated position from CellLocate®, depending on which positioning method provides the best and fastest solution according to the user configuration. Hybrid positioning is implemented through a set of AT commands that allow the configuration and the position request.
Firmware update Over AT commands (FOAT)	All	Firmware module upgrade over the UART interface using AT command.
In-Band modem	SARA-G340 / SARA-G350	In-Band modem solution for eCall and ERA-GLONASS emergency call applications over cellular networks implemented according to the 3GPP TS 26.267 specification [10]. When activated, the in-vehicle eCall / ERA-GLONASS system (IVS) creates an emergency call carrying both voice and data (including vehicle position data) directly to the nearest Public Safety Answering Point (PSAP) to determine whether rescue services should be dispatched to the known position.
Smart Temperature Supervisor	SARA-G340 / SARA-G350	Constant monitoring of the module board temperature: <ul style="list-style-type: none"> <li>Warning notification when the temperature approaches an upper or lower predefined threshold</li> <li>Shutdown notified and forced when the temperature value is outside the specified range (shutdown suspended in case of an emergency call in progress)</li> </ul> The Smart Temperature Supervisor feature can be enabled or disabled through an AT command (refer to u-blox AT commands manual [1], +USTS AT command)
		 The sensor measures board temperature inside the shields, which can differ from ambient temperature.
Power saving	All	The power saving configuration is by default disabled, but it can be configured using an AT command. When power saving is enabled, the module automatically enters the low power idle-mode whenever possible, reducing current consumption. During idle-mode, the module processor core runs with the RTC 32 kHz reference clock, which is generated by: <ul style="list-style-type: none"> <li>The internal 32 kHz oscillator, in case of SARA-G340 and SARA-G350 modules</li> <li>The 32 kHz signal provided at the <b>EXT32K</b> input pin, in case of SARA-G300 and SARA-G310 modules</li> </ul> For more details, refer to the SARA-G3 / SARA-U2 Series System Integration Manual [2] and to the u-blox AT commands manual [1], +UPSV AT command.

**Table 4: SARA-G3 series' main supported features**


u-blox is extremely mindful of user privacy. When a position is sent to the CellLocate® server, u-blox is unable to track the SIM used or the specific device.

## 2 Interfaces

### 2.1 Power management

#### 2.1.1 Module supply input (VCC)

SARA-G3 modules must be supplied through the **VCC** pins by a DC power supply. Voltages must be stable: during operation, the current drawn from **VCC** can vary by some order of magnitude, especially due to the surging consumption profile of the GSM system (described in the SARA-G3 / SARA-U2 Series System Integration Manual [2]). It is important that the system power supply circuit is able to support peak power.

#### 2.1.2 RTC supply input/output (V\_BCKP)

**V\_BCKP** is the Real Time Clock (RTC) supply of SARA-G3 modules. When **VCC** voltage is within the valid operating range, the internal Power Management Unit (PMU) supplies the RTC and the same supply voltage is available on **V\_BCKP** pin. If the **VCC** voltage is under the minimum operating limit (e.g. during not powered mode), the **V\_BCKP** pin can externally supply the RTC.

#### 2.1.3 Digital I/O interfaces supply output (V\_INT)

SARA-G3 modules provide an internally generated supply rail output for digital interfaces (**V\_INT**). This can be used in place of an external discrete regulator to supply pull-up resistors on the DDC interface. This optimizes the bill of material for various applications, e.g. with u-blox positioning modules operating at 1.8 V.

### 2.2 Antenna

#### 2.2.1 Antenna RF interface (ANT)

The **ANT** pin has an impedance of 50  $\Omega$  and provides the RF antenna interface of SARA-G3 modules.

#### 2.2.2 Antenna detection (ANT\_DET)

The **ANT\_DET** pin is an Analog to Digital Converter (ADC) input provided only by SARA-G340 and SARA-G350 modules to sense the antenna presence (as optional feature), evaluating the resistance from the **ANT** pin to GND by means of an external antenna detection circuit implemented on the application board. (For more details, see SARA-G3 / SARA-U2 Series System Integration Manual [2] and u-blox AT Commands Manual [1], +UANTR.)

### 2.3 System functions

#### 2.3.1 Module power-on (PWR\_ON)

SARA-G3 modules can be switched on in one of the following ways:

- Rising edge on the **VCC** pin to a valid voltage for module supply, i.e. applying module supply
- Low level on the **PWR\_ON** input pin, i.e. forcing the pin (normally high with external pull-up) to a low level for a valid time period. The **PWR\_ON** pin requires an external pull-up resistor to set its value to logic high and may not be left floating
- RTC alarm, i.e. pre-programmed scheduled time (refer to u-blox AT Commands Manual [1], AT+CALA)



SARA-G300 and SARA-G310 cannot be switched on with RTC alarm.

### 2.3.2 Module power-off

SARA-G3 modules can be properly switched off, with storage of current settings and network detach, by:

- AT+CPWROFF command

An under-voltage shutdown occurs on SARA-G3 modules when the **VCC** supply drops below the extended operating range minimum limit, but in this case it is not possible to perform the storing of the current parameter settings in the module's non-volatile memory as well as the proper network detach.

An over-temperature or an under-temperature shutdown occurs on SARA-G3 modules when the temperature measured within the cellular module reaches the dangerous area, if the optional Smart Temperature Supervisor feature is enabled and configured by the dedicated AT command. For more details refer to SARA-G3 / SARA-U2 Series System Integration Manual [2] and u-blox AT commands manual [1], +USTS AT command.

### 2.3.3 Module reset (RESET\_N)

SARA-G3 modules can be reset in one of these ways:

- Low level on the **RESET\_N** input pin, normally high with internal pull-up. This causes an "external" or "hardware" reset of the module. The current parameter settings are not saved in the module's non-volatile memory and a proper network detach is not performed.
- AT+CFUN command (refer to u-blox AT Commands Manual [1]). This causes an "internal" or "software" reset of the module. The current parameter settings are saved in the module's non-volatile memory and a proper network detach is performed.

### 2.3.4 External 32 kHz signal input (EXT32K)

SARA-G300 and SARA-G310 provide the **EXT32K** input pin that must be fed by a proper 32 kHz signal to furnish the reference clock for the RTC, allowing very low power idle-mode and RTC functions support. (See section 4.2.9 for detailed electrical specifications for the 32 kHz signal.)

SARA-G340 and SARA-G350 do not provide the **EXT32K** input pin. An internal 32 kHz oscillator automatically generates the reference clock for the RTC, allowing very low power idle-mode and RTC functions support.

### 2.3.5 Internal 32 kHz signal output (32K\_OUT)

SARA-G300 and SARA-G310 provide the **32K\_OUT** output pin which gives a 32 kHz reference signal suitable only to feed the **EXT32K** input pin of SARA-G300 / SARA-G310 modules, furnishing the reference clock for the RTC, allowing low power idle-mode and RTC functions support with modules switched on.

SARA-G340 and SARA-G350 do not provide the **32K\_OUT** output; there is no **EXT32K** input on these modules.

## 2.4 SIM

### 2.4.1 (U)SIM interface

A (U)SIM card interface is available via the **VSIM**, **SIM\_IO**, **SIM\_CLK**, **SIM\_RST** pins of SARA-G3 series modules: the high-speed SIM/ME interface is implemented as well as the automatic detection of the required SIM supporting voltage.

Both 1.8 V and 3 V SIM types are supported: activation and deactivation with automatic voltage switch from 1.8 V to 3 V are implemented, according to ISO-IEC 7816-3 Specifications. The SIM driver supports the PPS procedure for baud-rate selection, according to the values proposed by the SIM Card.

### 2.4.2 SIM card detection (SIM\_DET)



Not supported by SARA-G300-00S and SARA-G310-00S modules.

The **SIM\_DET** pin of SARA-G3 modules is a digital input provided to sense the SIM card presence (as an optional feature), when it is properly connected to the mechanical switch of the SIM card holder (for more details refer to the SARA-G3 / SARA-U2 Series System Integration Manual [2]).

## 2.5 Serial interfaces

SARA-G3 modules provide the following serial communication interfaces:

- UART interface: 9-wire unbalanced 1.8 V asynchronous serial interface available on SARA-G3 modules for AT commands interface, data communication, FW upgrades by means of the FOAT feature
- Auxiliary UART interface: 3-wire unbalanced 1.8 V asynchronous serial interface available on SARA-G3 modules for FW upgrades by means of the u-blox EasyFlash tool and for Trace log capture (debug purpose)
- DDC interface: I<sup>2</sup>C compatible interface available only on SARA-G340 / SARA-G350 modules for the communication with u-blox GPS/GNSS positioning chips / modules

### 2.5.1 Asynchronous serial interface (UART)

The UART interface is a 9-wire unbalanced 1.8 V asynchronous serial interface provided on all SARA-G3 modules for communication with an application processor: AT commands interface, data communication, FW upgrades by means of the FOAT feature.

UART characteristics are:

- Complete serial port with RS-232 functionality conforming to the ITU-T V.24 Recommendation [6], with CMOS compatible signal levels (0 V for low data bit or ON state and 1.8 V for high data bit or OFF state)
- Data lines (**RXD** as output, **TXD** as input), hardware flow control lines (**CTS** as output, **RTS** as input), modem status and control lines (**DTR** as input, **DSR** as output, **DCD** as output, **RI** as output) are provided
- Hardware flow control (default value), software flow control, or none flow control are supported
- Power saving indication available on the hardware flow control output (**CTS** line): the line is driven to the OFF state when the module is not ready to accept data signals
- 1200, 2400, 4800, 9600, 19200, 38400, 57600, 115200, 230400 b/s baud rates are supported for the AT interface; 1200 and 230400 b/s are available in conjunction only with autobauding
- Auto baud rate detection (autobauding) is the default configuration
- Frame format can be: 8N2 (8 data bits, No parity, 2 stop bits) or 8E1 (8 data bits, even parity, 1 stop bit) or 8O1 (8 data bits, odd parity, 1 stop bit) or 8N1 (8 data bits, No parity, 1 stop bit) or 7E1 (7 data bits, even parity, 1 stop bit) or 7O1 (7 data bits, odd parity, 1 stop bit)
- Default frame configuration is 8N1 where there are eight (8), no (N) parity bit, and one (1) stop bit
- Automatic frame recognition is supported: this feature is enabled in conjunction with the auto baud rate detection only

#### 2.5.1.1 Multiplexer protocol

SARA-G3 module has a software layer with MUX functionality, 3GPP TS 27.010 [9], available on the UART physical link.

This is a data link protocol (layer 2 of OSI model) which uses HDLC-like framing and operates between the module (DCE) and the application processor (DTE), and allows a number of simultaneous sessions over the physical link (UART): the user can concurrently use AT command interface on one MUX channel and data communication on another MUX channel.

SARA-G340 and SARA-G350 modules define the following virtual channels:

- Channel 0: control channel
- Channel 1 – 5: AT commands / data connection
- Channel 6: GNSS tunneling

SARA-G300 and SARA-G310 modules define the following virtual channels:

- Channel 0: control channel
- Channel 1 – 2: AT commands / data connection

For more details, refer to the Mux Implementation Application Note [4].

## 2.5.2 Auxiliary asynchronous serial interface (AUX UART)

The auxiliary UART interface is a 3-wire unbalanced 1.8 V asynchronous serial interface available on all SARA-G3 modules for firmware upgrade by means of the u-blox EasyFlash tool and for Trace log capture (debug purpose). The AT commands interface is not available on the auxiliary UART interface.

Auxiliary UART characteristics are:

- Only the **RXD\_AUX** data output and the **TXD\_AUX** data input are provided, with CMOS compatible signal levels (0 V for low data bit or ON state and 1.8 V for high data bit or OFF state)
- 115200, 230400, 460800, 921600 b/s baud rates are supported for the firmware upgrade by means of the u-blox EasyFlash tool and for the Trace log capture (debug purpose)
- Frame format configuration is 8N1 (8 data bits, No parity, 1 stop bit)

## 2.5.3 DDC (I<sup>2</sup>C compatible) interface

SARA-G340 and SARA-G350 modules provide an I<sup>2</sup>C compatible DDC interface on the **SCL** and **SDA** pins exclusively for the communication with u-blox GNSS positioning chips / modules.

## 2.6 Audio

SARA-G340 and SARA-G350 modules provide analog and digital audio interfaces:

- Analog audio input:
  - Differential analog audio input (**MIC\_P**, **MIC\_N**) shared for all the analog audio path modes: the pins can be connected to the output of an external analog audio device or can be connected to an external microphone by means of a simple circuit implemented on the application board (for more details refer to the SARA-G3 / SARA-U2 Series System Integration Manual [2])
  - Supply output for an external microphone (**MIC\_BIAS**): the pin can provide the bias to an external microphone by means of a simple circuit implemented on the application board (for more details refer to the SARA-G3 / SARA-U2 Series System Integration Manual [2])
  - Local ground for the external microphone (**MIC\_GND**): the pin can provide the reference for the differential analog audio input as sense ground line for the external microphone circuit
- Analog audio output:
  - Differential audio output (**SPK\_P**, **SPK\_N**) shared for all the analog audio path modes: the pins can be connected to the input of an external analog audio device or can be connected to an external speaker
- I<sup>2</sup>S digital audio interface:
  - 4-wire digital audio interface (**I2S\_TXD**, **I2S\_RXD**, **I2S\_CLK**, **I2S\_WA**) that can be configured by AT command in PCM or in normal I<sup>2</sup>S mode (for more details refer to u-blox AT Commands Manual [1] and SARA-G3 / SARA-U2 Series System Integration Manual [2])

## 2.7 GPIO

SARA-G340 and SARA-G350 modules provide 4 GPIO pins (**GPIO1-GPIO4**) that can be configured for general purpose input/output, or to provide custom functions via u-blox AT commands (for further details, refer to the SARA-G3 / SARA-U2 Series System Integration Manual [2] and to u-blox AT Commands Manual [1], +UGPIOC, +UGPIOR, +UGPIOW, +UGPS, +UGPRF).

Table 5 summarizes the custom functions available on the GPIO pins of SARA-G340 / SARA-G350 modules.

Function	Description	Default GPIO	Configurable GPIOs
GNSS supply enable	Enable/disable the supply of u-blox positioning chips and modules connected to cellular module	GPIO2	GPIO1, GPIO2, GPIO3, GPIO4
GNSS data ready	Sense when u-blox positioning chip / module connected to cellular module is ready for sending data by the DDC (I <sup>2</sup> C)	GPIO3	GPIO3
GNSS RTC sharing	RTC (Real Time Clock) synchronization signal to u-blox positioning chips and modules connected to cellular module	GPIO4	GPIO4
Network status indication	Indicates network status: registered home network, registered roaming, data transmission, no service	--	GPIO1, GPIO2, GPIO3, GPIO4
GSM Tx-burst indication	Indicates when a GSM Tx burst/slot occurs	--	GPIO1
General purpose input	Input to sense high or low digital level	--	GPIO1, GPIO2, GPIO3, GPIO4
General purpose output	Output to set the high or the low digital level	--	GPIO1, GPIO2, GPIO3, GPIO4
Pad disabled	Tri-state with an internal active pull-down enabled	GPIO1	GPIO1, GPIO2, GPIO3, GPIO4

**Table 5: GPIO custom functions configuration**

## 3 Pin definition

### 3.1 Pin assignment

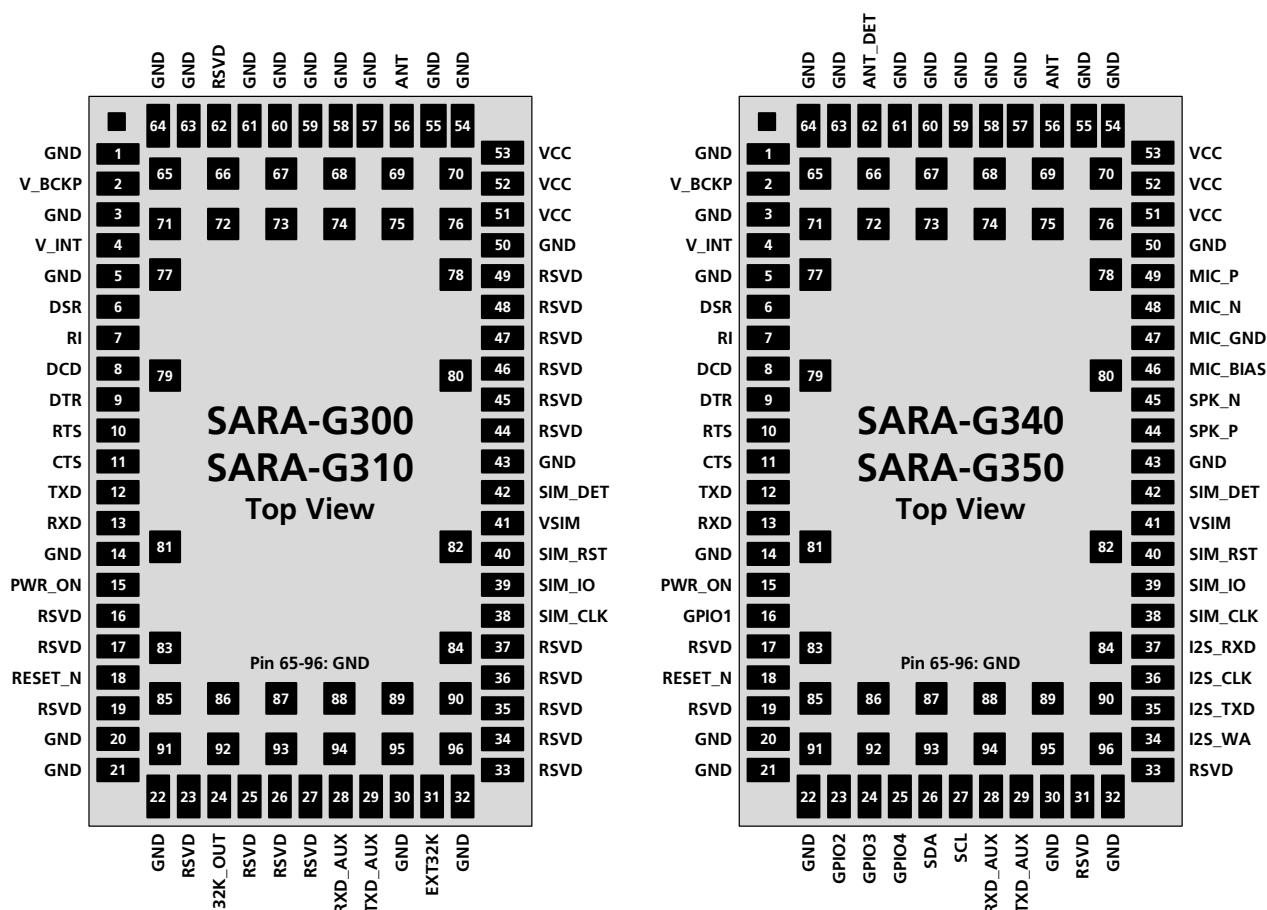


Figure 3: SARA-G3 series modules – pin assignments

No	Module	Name	Power domain	I/O	Description	Remarks
1	All	GND	-	N/A	Ground	All the GND pads must be connected to ground.
2	All	V_BCKP	-	I/O	Real Time Clock supply input/output	V_BCKP = 2.3 V (typical) generated by the module to supply the Real Time Clock when VCC supply voltage is within valid operating range. See section 4.2.3 for detailed electrical specs.
3	All	GND	-	N/A	Ground	All the GND pads must be connected to ground.
4	All	V_INT	-	O	Digital I/O Interfaces supply output	V_INT = 1.8V (typical) generated by the module when it is switched-on. See section 4.2.3 for detailed electrical specs.
5	All	GND	-	N/A	Ground	All the GND pads must be connected to ground.
6	All	DSR	GDI	O	UART data set ready	Circuit 107 (DSR) in ITU-T V.24. Output driver class A. PU/PD class a. Value at reset: T/PU. See section 4.2.11 for detailed electrical specs.
7	All	RI	GDI	O	UART ring indicator	Circuit 125 (RI) in ITU-T V.24. Output driver class A. PU/PD class b. Value at reset: T/PU. See section 4.2.11 for detailed electrical specs.
8	All	DCD	GDI	O	UART data carrier detect	Circuit 109 (DCD) in ITU-T V.24. Output driver class A. PU/PD class a. Value at reset: T/PU. See section 4.2.11 for detailed electrical specs.
9	All	DTR	GDI	I	UART data terminal ready	Circuit 108/2 (DTR) in ITU-T V.24. Internal active pull-up to V_INT enabled. PU/PD class b. Value at reset: T/PU. See section 4.2.11 for detailed electrical specs.
10	All	RTS	GDI	I	UART ready to send	Circuit 105 (RTS) in ITU-T V.24. Internal active pull-up to V_INT enabled. PU/PD class c. Value at reset: T/PU. See section 4.2.11 for detailed electrical specs.
11	All	CTS	GDI	O	UART clear to send	Circuit 106 (CTS) in ITU-T V.24. Output driver class A. PU/PD class c. Value at reset: T/PD. See section 4.2.11 for detailed electrical specs.
12	All	TXD	GDI	I	UART transmitted data	Circuit 103 (TxD) in ITU-T V.24. Internal active pull-up to V_INT enabled. PU/PD class a. Value at reset: T/PU. See section 4.2.11 for detailed electrical specs.
13	All	RXD	GDI	O	UART received data	Circuit 104 (Rx) in ITU-T V.24. Output driver class B. PU/PD class a. Value at reset: T/PU. See section 4.2.11 for detailed electrical specs.
14	All	GND	-	N/A	Ground	All the GND pads must be connected to ground.
15	All	PWR_ON	POS	I	Power-on input	The PWR_ON pin has high input impedance: don't leave it floating in noisy environment (an external pull-up resistor is required). See section 4.2.7 for detailed electrical specs.
16	SARA-G350 SARA-G340	GPIO1	GDI	I/O	GPIO	Configurable GPIO: refer to Table 5 for the available custom functions. Output driver class A. PU/PD class b. Value at reset: T/PD. See section 4.2.11 for detailed electrical specs.
	SARA-G310 SARA-G300	RSVD	-	N/A	RESERVED pin	Pin disabled. Leave unconnected.
17	All	RSVD	-	N/A	RESERVED pin	Leave unconnected.



No	Module	Name	Power domain	I/O	Description	Remarks
18	All	RESET_N	ERS	I	Reset signal	A series Schottky diode is integrated in the module as protection, and then an internal 10 kΩ pull-up resistor to V_INT is provided. See section 4.2.8 for detailed electrical specs. For more details regarding module reset, see [2].
19	All	RSVD	-	N/A	RESERVED pin	Leave unconnected.
20	All	GND	-	N/A	Ground	All the GND pads must be connected to ground.
21	All	GND	-	N/A	Ground	All the GND pads must be connected to ground.
22	All	GND	-	N/A	Ground	All the GND pads must be connected to ground.
23	SARA-G350 SARA-G340	GPIO2	GDI	I/O	GPIO	By default, the pin is configured to provide the GNSS supply enable function. Output driver class A. PU/PD class b. Value at reset: T/PD. See section 4.2.11 for detailed electrical specs.
	SARA-G310 SARA-G300	RSVD	-	N/A	RESERVED pin	Pin disabled. Leave unconnected.
24	SARA-G350 SARA-G340	GPIO3	GDI	I/O	GPIO	By default, the pin is configured to provide the GNSS data ready function. Output driver class B. PU/PD class c. Value at reset: T/PD. See section 4.2.11 for detailed electrical specs.
	SARA-G310 SARA-G300	32K_OUT	-	O	32 kHz output	32 kHz output suitable only to feed the EXT32K input giving the RTC reference clock, allowing low power idle-mode and RTC functions support.
25	SARA-G350 SARA-G340	GPIO4	GDI	I/O	GPIO	By default, the pin is configured to provide the GNSS RTC sharing function. Output driver class A. PU/PD class b. Value at reset: T/PD. See section 4.2.11 for detailed electrical specs.
	SARA-G310 SARA-G300	RSVD	-	N/A	RESERVED pin	Pin disabled. Leave unconnected.
26	SARA-G350 SARA-G340	SDA	DDC	I/O	I <sup>2</sup> C bus data line	Fixed open drain. No internal pull-up. Value at reset: T. See section 4.2.12 for detailed electrical specs.
	SARA-G310 SARA-G300	RSVD	-	N/A	RESERVED pin	Pin disabled. Leave unconnected.
27	SARA-G350 SARA-G340	SCL	DDC	O	I <sup>2</sup> C bus clock line	Fixed open drain. No internal pull-up. Value at reset: T. See section 4.2.12 for detailed electrical specs.
	SARA-G310 SARA-G300	RSVD	-	N/A	RESERVED pin	Pin disabled. Leave unconnected.
28	All	RXD_AUX	GDI	O	Auxiliary UART received data	Circuit 104 (RxD) in ITU-T V.24. Output driver class B. PU/PD class a. Value at reset: T/PU. See section 4.2.11 for detailed electrical specs.
29	All	TXD_AUX	GDI	I	Auxiliary UART transmitted data	Circuit 103 (TxD) in ITU-T V.24. Internal active pull-up to V_INT enabled. PU/PD class a. Value at reset: T/PU. See section 4.2.11 for detailed electrical specs.
30	All	GND	-	N/A	Ground	All the GND pads must be connected to ground.
31	SARA-G350 SARA-G340	RSVD	-	N/A	RESERVED pin	Internally not connected. Leave unconnected.
	SARA-G310 SARA-G300	EXT32K	-	I	32 kHz input	Input for RTC reference clock, needed to enter the low power idle-mode and provide RTC functions. See section 4.2.9 for detailed electrical specs.
32	All	GND	-	N/A	Ground	All the GND pads must be connected to ground.
33	All	RSVD	-	N/A	RESERVED pin	This pin must be connected to GND.

No	Module	Name	Power domain	I/O	Description	Remarks
34	SARA-G350 SARA-G340	I2S_WA	GDI	O	I <sup>2</sup> S word alignment	Output driver class A. PU/PD class c. Value at reset: T/PU. See section 4.2.11 for detailed electrical specs.
	SARA-G310 SARA-G300	RSVD	-	N/A	RESERVED pin	Pin disabled. Leave unconnected.
35	SARA-G350 SARA-G340	I2S_TXD	GDI	O	I <sup>2</sup> S transmit data	Output driver class B. PU/PD class c. Value at reset: T/PD. See section 4.2.11 for detailed electrical specs.
	SARA-G310 SARA-G300	RSVD	-	N/A	RESERVED pin	Pin disabled. Leave unconnected.
36	SARA-G350 SARA-G340	I2S_CLK	GDI	O	I <sup>2</sup> S clock	Output driver class B. PU/PD class a. Value at reset: T/PU. See section 4.2.11 for detailed electrical specs.
	SARA-G310 SARA-G300	RSVD	-	N/A	RESERVED pin	Pin disabled. Leave unconnected.
37	SARA-G350 SARA-G340	I2S_RXD	GDI	I	I <sup>2</sup> S receive data	Internal active pull-down enabled. PU/PD class a. Value at reset: T/PD. See section 4.2.11 for detailed electrical specs.
	SARA-G310 SARA-G300	RSVD	-	N/A	RESERVED pin	Pin disabled. Leave unconnected.
38	All	SIM_CLK	SIM	O	SIM clock	Value at reset: L. See section 4.2.10 for detailed electrical specs.
39	All	SIM_IO	SIM	I/O	SIM data	Internal 4.7k pull-up to VSIM. Value at reset: L. See section 4.2.10 for detailed electrical specs.
40	All	SIM_RST	SIM	O	SIM reset	Value at reset: L. See section 4.2.10 for detailed electrical specs.
41	All	VSIM	-	O	SIM supply output	VSIM = 1.80 V typical if SIM card = 1.8V type. VSIM = 2.85 V typical if SIM card = 3.0V type. See section 4.2.3 for detailed electrical specs.
42	All	SIM_DET	GDI	I	SIM detection	SIM card presence detection function. PU/PD class a. Value at reset: T/PU. See section 4.2.11 for detailed electrical specs.
43	All	GND	-	N/A	Ground	All the GND pads must be connected to ground.
44	SARA-G350 SARA-G340	SPK_P	AUDIO	O	Differential analog audio output (positive)	Differential analog audio output shared for all the analog audio path modes. See section 4.2.13 for detailed electrical specs.
	SARA-G310 SARA-G300	RSVD	-	N/A	RESERVED pin	Leave unconnected.
45	SARA-G350 SARA-G340	SPK_N	AUDIO	O	Differential analog audio output (negative)	Differential analog audio output shared for all the analog audio path modes. See section 4.2.13 for detailed electrical specs.
	SARA-G310 SARA-G300	RSVD	-	N/A	RESERVED pin	Leave unconnected.
46	SARA-G350 SARA-G340	MIC_BIAS	AUDIO	O	Microphone supply output	Supply output for the external microphone. See section 4.2.13 for detailed electrical specs.
	SARA-G310 SARA-G300	RSVD	-	N/A	RESERVED pin	Leave unconnected.
47	SARA-G350 SARA-G340	MIC_GND	AUDIO	I	Microphone analog reference	Local ground for the external microphone (reference for the differential analog audio input). See section 4.2.13 for detailed electrical specs.
	SARA-G310 SARA-G300	RSVD	-	N/A	RESERVED pin	Leave unconnected.
48	SARA-G350 SARA-G340	MIC_N	AUDIO	I	Differential analog audio input (negative)	Differential analog audio input shared for all the analog audio path modes. See section 4.2.13 for detailed electrical specs.
	SARA-G310 SARA-G300	RSVD	-	N/A	RESERVED pin	Leave unconnected.

No	Module	Name	Power domain	I/O	Description	Remarks
49	SARA-G350 SARA-G340	MIC_P	AUDIO	I	Differential analog audio input (positive)	Differential analog audio input shared for all the analog audio path modes. See section 4.2.13 for detailed electrical specs.
	SARA-G310 SARA-G300	RSVD	-	N/A	RESERVED pin	Leave unconnected.
50	All	GND	-	N/A	Ground	All the GND pads must be connected to ground.
51	All	VCC	-	I	Module supply input	All VCC pins must be connected to external supply See section 4.2.3, 4.2.4 for detailed electrical spec.
52	All	VCC	-	I	Module supply input	All VCC pins must be connected to external supply See section 4.2.3, 4.2.4 for detailed electrical spec.
53	All	VCC	-	I	Module supply input	All VCC pins must be connected to external supply See section 4.2.3, 4.2.4 for detailed electrical spec.
54	All	GND	-	N/A	Ground	All the GND pads must be connected to ground.
55	All	GND	-	N/A	Ground	All the GND pads must be connected to ground.
56	All	ANT	-	I/O	RF antenna	50 $\Omega$ nominal impedance See section 4.2.5 for detailed electrical specs.
57	All	GND	-	N/A	Ground	All the GND pads must be connected to ground.
58	All	GND	-	N/A	Ground	All the GND pads must be connected to ground.
59	All	GND	-	N/A	Ground	All the GND pads must be connected to ground.
60	All	GND	-	N/A	Ground	All the GND pads must be connected to ground.
61	All	GND	-	N/A	Ground	All the GND pads must be connected to ground.
62	SARA-G350 SARA-G340	ANT_DET	ADC	I	Antenna detection	Antenna presence detection function. See section 4.2.6 for detailed electrical specs.
	SARA-G310 SARA-G300	RSVD	-	N/A	RESERVED pin	Leave unconnected.
63	All	GND	-	N/A	Ground	All the GND pads must be connected to ground.
64	All	GND	-	N/A	Ground	All the GND pads must be connected to ground.
65-96	All	GND	-	N/A	Ground	All the GND pads must be connected to ground.

**Table 6: SARA-G3 series module pin-out**


For more information about pin-out, refer to SARA-G3 / SARA-U2 Series System Integration Manual [2].



For an explanation of abbreviations and terms used, see Appendix A.

## 4 Electrical specifications

**Stressing the device above one or more of the ratings listed in the Absolute Maximum Rating section may cause permanent damage. These are stress ratings only. Operating the module at these or at any conditions other than those specified in the Operating Conditions sections (chapter 4.2) of the specification should be avoided. Exposure to Absolute Maximum Rating conditions for extended periods may affect device reliability.**

Operating conditions ranges define those limits within which the functionality of the device is guaranteed.  
 Where application information is given, it is advisory only and does not form part of the specification.

### 4.1 Absolute maximum rating

Limiting values given below are in accordance with the Absolute Maximum Rating System (IEC 134).

Symbol	Description	Condition	Min	Max	Unit
VCC	Module supply voltage	Input DC voltage at VCC pins	-0.15	6.0	V
V_BCKP	RTC supply voltage	Input DC voltage at V_BCKP pin	-0.15	2.5	V
GDI	Generic digital interfaces	Input DC voltage at Generic digital interfaces pins	-0.15	2.2	V
DDC	DDC interface	Input DC voltage at DDC interface pins	-0.15	3.6	V
SIM	SIM interface	Input DC voltage at SIM interface pins	-0.30	3.6	V
ERS	External reset signal	Input DC voltage at RESET_N pin	-0.15	6.0	V
POS	Power-on signal	Input DC voltage at PWR_ON pin	-0.15	6.0	V
AUDIO	Audio pins	Input DC voltage at Audio pins	-0.15	3.0	V
ADC	Antenna detection pin	Input DC voltage at ANT_DET pin	-0.15	3.0	V
P_ANT	Antenna power	Input RF power at ANT pin		-8	dBm
Rho_ANT	Antenna ruggedness	Output RF load mismatch ruggedness at ANT pin		20:1	VSWR
Tstg	Storage temperature range		-40	+85	°C

Table 7: Absolute maximum ratings

**The product is not protected against overvoltage or reversed voltages. If necessary, voltage spikes exceeding the power supply voltage specification, given in table above, must be limited to values within the specified boundaries by using appropriate protection devices.**

#### 4.1.1 Maximum ESD

Parameter	Min	Typical	Max	Unit	Remarks
ESD sensitivity for all pins except ANT pin			1000	V	Human Body Model according to JESD22-A114
ESD sensitivity for ANT pin			1000	V	Human Body Model according to JESD22-A114
ESD immunity for ANT pin			4000	V	Contact Discharge according to IEC 61000-4-2
			8000	V	Air Discharge according to IEC 61000-4-2

Table 8: Maximum ESD ratings

**u-blox cellular modules are Electrostatic Sensitive Devices (ESD) and require special precautions when handling. See Section 7.4 for ESD handling instructions.**

## 4.2 Operating conditions



Unless otherwise specified, all operating condition specifications are at an ambient temperature of 25°C.



**Operation beyond the operating conditions is not recommended and extended exposure beyond them may affect device reliability**

### 4.2.1 Operating temperature range

Symbol	Parameter	Module	Min	Typical	Max	Units	Remarks
Topr	Operating temperature range	All	-40	+25	+85	°C	
		All	-30		+85	°C	Normal operating range see chapter 4.2.1.1
		All	-40		+85	°C	Extended operating range see chapter 4.2.1.2

Table 9: Environmental conditions

#### 4.2.1.1 Normal operating temperature range

SARA-G3 series modules are fully functional and meet 3GPP specifications across the specified temperature range.

#### 4.2.1.2 Extended operating temperature range

SARA-G3 series modules are fully functional across the specified temperature range. Occasional deviations from the 3GPP specifications may occur.

### 4.2.2 Module thermal resistance

Symbol	Parameter	Min	Typical	Max	Unit	Remarks
R <sub>th,M-A</sub>	Module-to-Ambient thermal resistance		10.6		°C/W	Thermal resistance from the module internal temperature sensor to the ambient, with the module mounted on a 79 mm x 62 mm x 1.41 mm 4-Layers PCB with a high coverage of copper in still air conditions
R <sub>th,M-C</sub>	Module-to-Case thermal resistance		1.7		°C/W	Thermal resistance from the module internal temperature sensor to the module case, evaluated as the thermal resistance from the module internal temperature sensor to the ambient, with the module mounted on a 79 mm x 62 mm x 1.41 mm 4-Layers PCB with a high coverage of copper, with a robust aluminum heat-sink on the back of the application board, with forced air ventilation.

Table 10: SARA-G3 series modules thermal resistance

### 4.2.3 Supply/Power pins

Symbol	Parameter	Min	Typical	Max	Unit
VCC	Module supply normal operating voltage <sup>8</sup>	3.35	3.80	4.50	V
	Module supply extended operating voltage <sup>9</sup>	3.00		4.50	V
ICC_PEAK <sup>10</sup>	Module Supply Peak Current: peak of current consumption through the VCC pads during a GSM 1-slot transmit burst, with a matched antenna		1.50	1.60	A
	Module Supply Peak Current: peak of current consumption through the VCC pads during a GSM 1-slot transmit burst, with a mismatched antenna			1.90	A
V_BCKP	Real Time Clock Supply input voltage	1.00	2.30	2.40	V
I_BCKP	Real Time Clock Supply average current consumption, at V_BCKP = 2.3 V		2.00		µA

**Table 11: Input characteristics of Supply/Power pins**

Symbol	Parameter	Min	Typical	Max	Unit
VSIM	SIM Supply	1.76	1.80	1.84	V
		2.78	2.85	2.91	V
V_BCKP	Real Time Clock Supply output voltage	2.19	2.30	2.42	V
I_BCKP	Real Time Clock Supply output current capability			2	mA
V_INT	Digital I/O Interfaces supply output voltage	1.75	1.80	1.85	V
V_INT_RIPPLE	Digital I/O Interfaces supply output peak-to-peak voltage ripple during active or connected mode			15	mV
	Digital I/O Interfaces supply output peak-to-peak voltage ripple during low power idle mode with power saving enabled			90	mV
I_INT	Digital I/O Interfaces supply output current capability			70	mA

**Table 12: Output characteristics of Supply/Power pins**

<sup>8</sup> Input voltage at VCC must be above the normal operating range minimum limit to switch-on module. Complete functionality of the module is only guaranteed within the specified range.

<sup>9</sup> Occasional deviations from 3GPP specifications may occur. Ensure that VCC voltage never drops below extended operating range minimum limit during module operation. Module may switch off when VCC voltage drops below extended operating range minimum limit.

<sup>10</sup> Use this figure to dimension maximum current capability of power supply.

#### 4.2.4 Current consumption

Mode	Module	Band	Condition	Min	Typ	Max	Unit	
Power Off Mode	All	-	Module is switched off by AT+CPWROFF	36	40		μA	
Cyclic Idle/Active-Mode (Power Saving enabled by AT+UPSV, Module registered with network)	SARA-G300 SARA-G310	All	DRX = 9 <sup>11</sup> AT+UPSV=2 EXT32K fed by external 32.768 kHz signal	0.6	0.8		mA	
			DRX = 9 <sup>11</sup> AT+UPSV=2 EXT32K fed by 32K_OUT output signal	4.2	4.4		mA	
			DRX = 5 <sup>12</sup> AT+UPSV=1 EXT32K fed by external 32.768 kHz signal	0.9	1.1		mA	
			DRX = 5 <sup>12</sup> AT+UPSV=1 EXT32K fed by 32K_OUT output signal	4.5	4.7		mA	
	SARA-G340 SARA-G350	All	DRX = 9 <sup>11</sup> AT+UPSV=2	0.6	0.8		mA	
			DRX = 5 <sup>12</sup> AT+UPSV=1	0.9	1.1		mA	
	Active-Mode (Power Saving disabled by AT+UPSV, Module registered with network)	SARA-G300 SARA-G310	All	DRX = 5 <sup>12</sup> AT+UPSV=0 (default) EXT32K fed by external 32.768 kHz signal	4.2	4.4		mA
				DRX = 5 <sup>12</sup> AT+UPSV=0 (default) EXT32K fed by 32K_OUT output signal	4.5	4.7		mA
DRX = 5 <sup>12</sup> AT+UPSV=0 (default) EXT32K not fed				18.0	18.2		mA	
SARA-G340-00S SARA-G350-00S SARA-G350-00X		All	DRX = 5 <sup>12</sup> AT+UPSV=0 (default)	18.2	18.4		mA	
SARA-G340-01S SARA-G350-01S SARA-G350-01B		All	DRX = 5 <sup>12</sup> AT+UPSV=0 (default)	2.9	3.1		mA	
GSM Connected Mode (Call enabled)	All	850, 900	1 Tx + 1 Rx slot (voice or data call up to 21.4 kb/s UL, 21.4 kb/s DL) Maximum Tx power (32.2 dBm typ.)	200	250		mA	
		1800, 1900	1 Tx + 1 Rx slot (voice or data call up to 21.4 kb/s UL, 21.4 kb/s DL) Maximum Tx power (29.2 dBm typ.)	150	200		mA	
GPRS Connected Mode (Call enabled)	All	850, 900	2 Tx + 3 Rx slots (GPRS data call up to 42.8 kb/s UL, 64.2 kb/s DL) Maximum Tx power (30.5 dBm typ.)	300	350		mA	
		1800, 1900	2 Tx + 3 Rx slots (GPRS data call with up to 42.8 kb/s UL, 64.2 kb/s DL) Maximum Tx power (27.5 dBm typ.)	210	260		mA	

Table 13: VCC current consumption<sup>13</sup>

<sup>11</sup> Module is registered with the network, with a paging period of 2.12 s (2G network DRX setting = 9), with none neighbour cell.

<sup>12</sup> Module is registered with the network, with a paging period of 1.18 s (2G network DRX setting = 5), with 16 neighbour cells.

<sup>13</sup> Averaged values for module current consumption through **VCC** pins in the listed modes/conditions.

## 4.2.5 RF performance

Parameter		Min	Max	Unit	Remarks
Frequency range GSM 850	Uplink	824	849	MHz	Module transmit
	Downlink	869	894	MHz	Module receive
Frequency range E-GSM 900	Uplink	880	915	MHz	Module transmit
	Downlink	925	960	MHz	Module receive
Frequency range DCS 1800	Uplink	1710	1785	MHz	Module transmit
	Downlink	1805	1880	MHz	Module receive
Frequency range PCS 1900	Uplink	1850	1910	MHz	Module transmit
	Downlink	1930	1990	MHz	Module receive

**Table 14: Operating RF frequency bands**

Parameter	Min	Typical	Max	Unit	Remarks
Receiver input sensitivity GSM 850		-109		dBm	Downlink RF level @ BER Class II < 2.4 %
Receiver input sensitivity E-GSM 900		-109		dBm	Downlink RF level @ BER Class II < 2.4 %
Receiver input sensitivity DCS 1800		-109		dBm	Downlink RF level @ BER Class II < 2.4 %
Receiver input sensitivity PCS 1900		-109		dBm	Downlink RF level @ BER Class II < 2.4 %

Condition: 50  $\Omega$  source

**Table 15: Receiver sensitivity performance**

Parameter	Min	Typical	Max	Unit	Remarks
Maximum output power GSM 850 / E-GSM 900		32.2		dBm	Uplink burst RF power for GSM or GPRS 1-slot TCH at maximum output power control level (PCL 5 or Gamma 3)
		30.5		dBm	Uplink burst RF power for GPRS 2-slot TCH at maximum output power control level (Gamma 3)
Maximum output power DCS 1800 / PCS 1900		29.2		dBm	Uplink burst RF power for GSM or GPRS 1-slot TCH at maximum output power control level (PCL 0 or Gamma 3)
		27.5		dBm	Uplink burst RF power for GPRS 2-slot TCH at maximum output power control level (Gamma 3)

Condition: 50  $\Omega$  output load

**Table 16: Transmitter maximum output power**

## 4.2.6 ANT\_DET pin

Pin Name	Parameter	Min	Typical	Max	Unit	Remarks
ANT_DET	Output DC current pulse value		20		$\mu$ A	
	Output DC current pulse time length	3.7	5.4	8.1	ms	

**Table 17: ANT\_DET pin characteristics**



### 4.2.7 PWR\_ON pin

Pin Name	Parameter	Min	Typical	Max	Unit	Remarks
PWR_ON	Internal supply for Power-On Input Signal	2.19	2.30	2.42	V	Real Time Clock supply (V_BCKP)
	L-level input	-0.10		0.65	V	High input impedance (no internal pull-up)
	H-level input	2.00		4.50	V	High input impedance (no internal pull-up)
	L-level input current		-8		μA	
	PWR_ON low time to switch-on the module	5			ms	

Table 18: PWR\_ON pin characteristics

### 4.2.8 RESET\_N pin

Pin Name	Parameter	Min	Typical	Max	Unit	Remarks
RESET_N	Internal supply for External Reset Signal	1.75	1.80	1.85	V	Digital I/O Interfaces supply (V_INT)
	L-level input	-0.10		0.15	V	A series Schottky diode is integrated in the module as protection: the module senses a low level when the RESET_N pin is forced low.
	H-level input	1.40		4.50	V	A series Schottky diode is integrated in the module as protection: the module senses a low level when the RESET_N pin is forced low.
	Pull-up resistance		10		kΩ	Internal pull-up to V_INT after the internal series Schottky diode
	L-level input current		-180		μA	
	RESET_N low time to perform a proper reset	50			ms	SARA-G340, SARA-G350
	RESET_N low time to perform a proper reset	3000			ms	SARA-G300, SARA-G310

Table 19: RESET\_N pin characteristics

### 4.2.9 EXT32K pin

Pin Name	Parameter	Min	Typical	Max	Unit	Remarks
EXT32K	L-level input	-0.25		0.20	V	
	H-level input	V_BCKP – 0.20		V_BCKP + 0.25	V	V_BCKP = 2.3 V (typical) generated by the internal LDO linear regulator when valid VCC supply is present.
	Input clock frequency	32.765	32.768	32.771	kHz	Frequency tolerance: +/- 100 ppm

Table 20: EXT32K pin characteristics

## 4.2.10 SIM pins

Parameter	Min	Typical	Max	Unit	Remarks
Low-level input			0.37	V	VSIM = 1.80 V
			0.60	V	VSIM = 2.85 V
High-level input	1.22		3.30	V	VSIM = 1.80 V
	1.95		3.30	V	VSIM = 2.85 V
Low-level output		0.00	0.35	V	VSIM = 1.80 V, Max value at $I_{OL} = +1.0$ mA
		0.00	0.35	V	VSIM = 2.85 V, Max value at $I_{OL} = +1.0$ mA
High-level output	1.45	1.80		V	VSIM = 1.80 V, Min value at $I_{OH} = -1.0$ mA
	2.50	2.85		V	VSIM = 2.85 V, Min value at $I_{OH} = -1.0$ mA
Input/Output leakage current			0.7	μA	$0.2\text{ V} < V_{IN} < 3.3\text{ V}$
Internal pull-up resistor on SIM_IO to VSIM			4.7	kΩ	
Clock frequency on SIM_CLK			3.25	MHz	

Table 21: SIM pins characteristics

## 4.2.11 Generic Digital Interfaces pins

Parameter	Min	Typical	Max	Unit	Remarks
Internal supply for GDI domain	1.75	1.80	1.85	V	Digital I/O Interfaces supply ( $V_{INT}$ )
Input characteristic: L-level input	-0.20		0.35	V	
Input characteristic: H-level input	1.30		1.95	V	
Output characteristics: L-level output		0.00	0.20	V	Max value at $I_{OL} = +3.0$ mA for Driver Class A
		0.00	0.35	V	Max value at $I_{OL} = +6.0$ mA for Driver Class A
		0.00	0.20	V	Max value at $I_{OL} = +2.5$ mA for Driver Class B
		0.00	0.35	V	Max value at $I_{OL} = +5.0$ mA for Driver Class B
Output characteristics: H-level output	1.60	1.80		V	Min value at $I_{OH} = -3.0$ mA for Driver Class A
	1.45	1.80		V	Min value at $I_{OH} = -6.0$ mA for Driver Class A
	1.60	1.80		V	Min value at $I_{OH} = -2.5$ mA for Driver Class B
	1.45	1.80		V	Min value at $I_{OH} = -5.0$ mA for Driver Class B
Input/Output leakage current			0.7	μA	$0.2\text{ V} < V_{IN} < 1.95\text{ V}$
Pull-up input current		-102		μA	PU Class a
		-55		μA	PU Class b
		-31		μA	PU Class c
Pull-down input current		103		μA	PD Class a
		51		μA	PD Class b
		27		μA	PD Class c

Table 22: GDI pins characteristics

#### 4.2.11.1 AC characteristics of digital audio interfaces pins

The 4-wire I<sup>2</sup>S digital audio interfaces can be configured in 2 different modes:

- Normal I<sup>2</sup>S mode with Master role
- PCM mode with Master role

##### Normal I<sup>2</sup>S mode with Master role

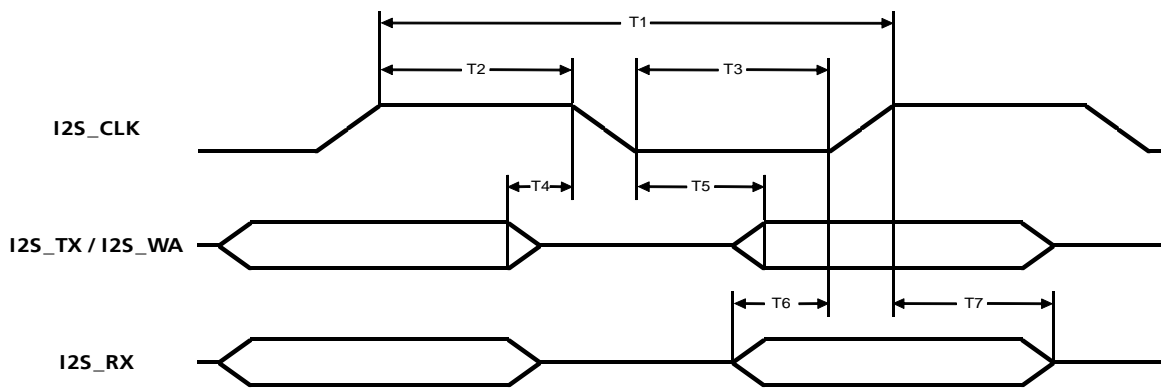


Figure 4: AC characteristics of digital audio interface in normal I2S mode (<I2S\_mode> = 2, 4, 6, 8, 10, 12)

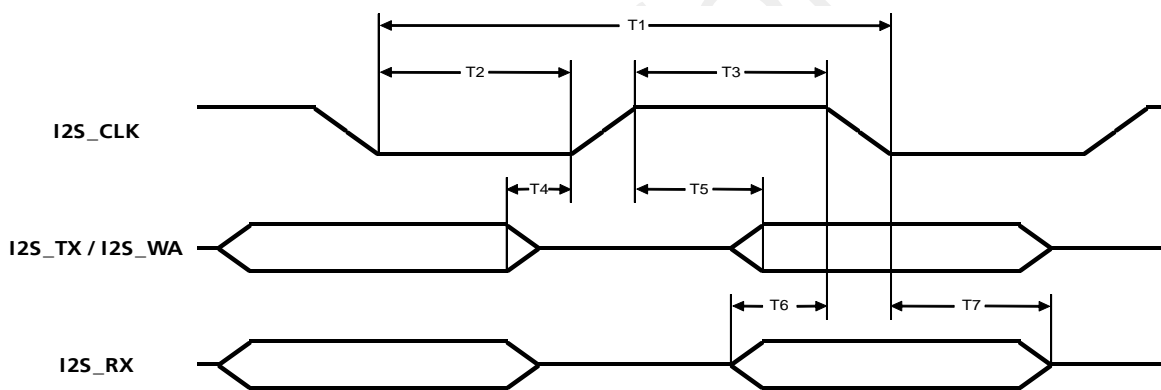


Figure 5: AC characteristics of digital audio interface in normal I2S mode (<I2S\_mode> = 3, 5, 7, 9, 11, 13)

Parameter	Description	Min	Typical	Max	Unit	Remarks
T1	I2S_CLK period	3.902	3.906		μs	<I2S_mode> = 2,...,13
1/T1	I2S_CLK frequency		256		kHz	<I2S_mode> = 2,...,13
T2	I2S_CLK high time	1.933	1.953		μs	<I2S_mode> = 2,...,13
T3	I2S_CLK low time	1.933	1.953		μs	<I2S_mode> = 2,...,13
	I2S_WA period		125.0		μs	<I2S_mode> = 2,...,13
	I2S_WA frequency		8		kHz	<I2S_mode> = 2,...,13
T4	I2S_TX invalid before I2S_CLK low end			24	ns	<I2S_mode> = 2,4,6,8,10,12
	I2S_TX invalid before I2S_CLK high end			24	ns	<I2S_mode> = 3,5,7,9,11,13
T5	I2S_TX valid after I2S_CLK high begin			31	ns	<I2S_mode> = 2,4,6,8,10,12
	I2S_TX valid after I2S_CLK low begin			31	ns	<I2S_mode> = 3,5,7,9,11,13
T6	I2S_RX setup time before I2S_CLK high end	60			ns	<I2S_mode> = 2,4,6,8,10,12
	I2S_RX setup time before I2S_CLK low end	60			ns	<I2S_mode> = 3,5,7,9,11,13
T7	I2S_RX hold time after I2S_CLK low begin	12			ns	<I2S_mode> = 2,4,6,8,10,12
	I2S_RX hold time after I2S_CLK high begin	12			ns	<I2S_mode> = 3,5,7,9,11,13

Table 23: AC characteristics of digital audio interface in normal I2S mode (<I2S\_mode> = 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13)

### PCM mode with Master role

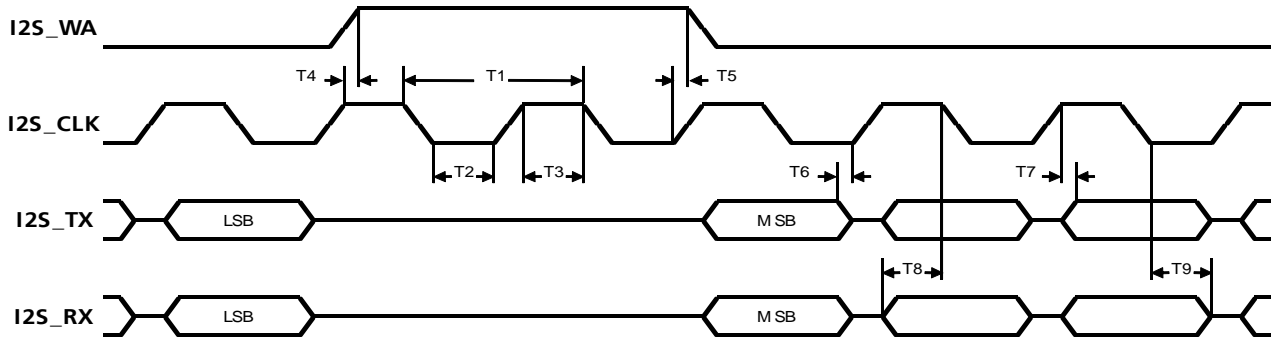


Figure 6: AC characteristics of digital audio interface in PCM mode (<I2S\_mode> = 0)

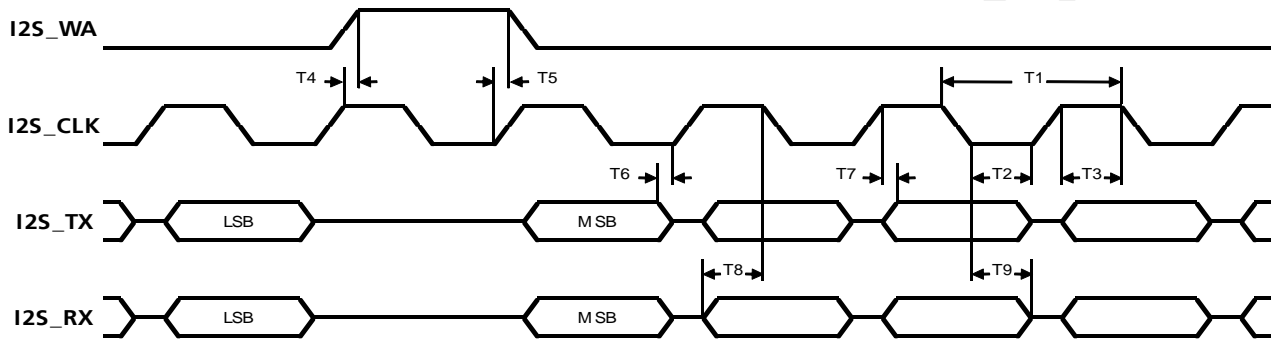


Figure 7: AC characteristics of digital audio interface in PCM mode (<I2S\_mode> = 1)

Parameter	Description	Min	Typical	Max	Unit	Remarks
T1	I2S_CLK period	6.940	6.944		μs	<I2S_mode> = 0
		7.349	7.353		μs	<I2S_mode> = 1
1/T1	I2S_CLK frequency		144		kHz	<I2S_mode> = 0
			136		kHz	<I2S_mode> = 1
T2	I2S_CLK low time	3.452	3.472		μs	<I2S_mode> = 0
		3.656	3.676		μs	<I2S_mode> = 1
T3	I2S_CLK high time	3.452	3.472		μs	<I2S_mode> = 0
		3.656	3.676		μs	<I2S_mode> = 1
	I2S_WA period		125.0		μs	<I2S_mode> = 0,1
	I2S_WA frequency		8		kHz	<I2S_mode> = 0,1
T4	I2S_CLK high begin to I2S_WA high begin	-24		31	ns	<I2S_mode> = 0,1
T5	I2S_CLK low end to I2S_WA high end	-24		31	ns	<I2S_mode> = 0,1
T6	I2S_TX invalid before I2S_CLK low end			24	ns	<I2S_mode> = 0,1
T7	I2S_TX valid after I2S_CLK high begin			22	ns	<I2S_mode> = 0,1
T8	I2S_RX setup time before I2S_CLK high end	60			ns	<I2S_mode> = 0,1
T9	I2S_RX hold time after I2S_CLK low begin	12			ns	<I2S_mode> = 0,1

Table 24: AC characteristics of digital audio interface in PCM mode (<I2S\_mode> = 0, 1)

#### 4.2.12 DDC (I<sup>2</sup>C) pins

Parameter	Min	Typical	Max	Unit	Remarks
Internal supply for DDC domain	1.75	1.80	1.85	V	Digital I/O Interfaces supply (V_INT)
L-level input	-0.20		0.35	V	In accordance with I <sup>2</sup> C bus specification
H-level input	1.30		3.30	V	In accordance with I <sup>2</sup> C bus specification
L-level output		0.00	0.40	V	Max value at I <sub>OL</sub> = +3.0 mA
Input/Output leakage current			0.7	μA	0.2 V < V <sub>IN</sub> < 3.3 V
Clock frequency on SCL		100		kHz	

**Table 25: DDC pins characteristics**

#### 4.2.13 Audio pins

Pin Name	Parameter	Min	Typical	Max	Unit	Remarks
MIC_BIAS	Microphone supply output voltage		2.2		V	Provided by MIC_BIAS output pin with MIC_GND pin as reference.
	Microphone supply output current			4.0	mA	Provided by MIC_BIAS output pin with MIC_GND pin as reference
	Load capacitance			0	nF	Proper internal bypass capacitor is already provided to guarantee stable operation: an external capacitor cannot be directly connected to MIC_BIAS pin, but can be provided after a proper series resistor.
	Load resistance	1			kΩ	
MIC_GND	Microphone ground		0		V	MIC_GND pin is internally connected to GND as a sense line

**Table 26: Microphone supply (MIC\_BIAS) characteristics**

Pin Name	Parameter	Min	Typical	Max	Unit	Remarks
MIC_P/MIC_N	Differential input voltage			0.8	V <sub>pp</sub>	Full scale differential voltage
	Differential input resistance		50		kΩ	
	Common mode DC voltage		0.5		V	No internal DC blocking capacitor
	Input capacitance		5	10	pF	No internal DC blocking capacitor
	Signal to noise	75			dB	Gain stage = +12 dB, Bandwidth 300-3900 Hz
	Signal to distortion (THD)	65			dB	
	Power supply rejection	49	85		dB	

**Table 27: Differential audio transmit path (MIC\_P, MIC\_N) input characteristics**

Pin Name	Parameter	Min	Typical	Max	Unit	Remarks
SPK_P/SPK_N	Maximum differential output voltage	4.0	4.1	4.2	V <sub>pp</sub>	Full scale differential open circuit voltage
	Common mode output voltage		1.25		V	
	Output current limit			200	mA	
	Output load resistance	14			Ω	
	Single-ended output load capacitance			250	pF	
	Signal to noise	70	80		dB	Load = 16 Ω, Gain stage = +0 dB, Input signal = 0 dBFS, Code 0, A-weighted
	Signal to distortion (THD)	60	70		dB	Load = 16 Ω, Input signal = 0 dBFS

**Table 28: Differential audio receive path (SPK\_P, SPK\_N) output characteristics**

## 5 Mechanical specifications

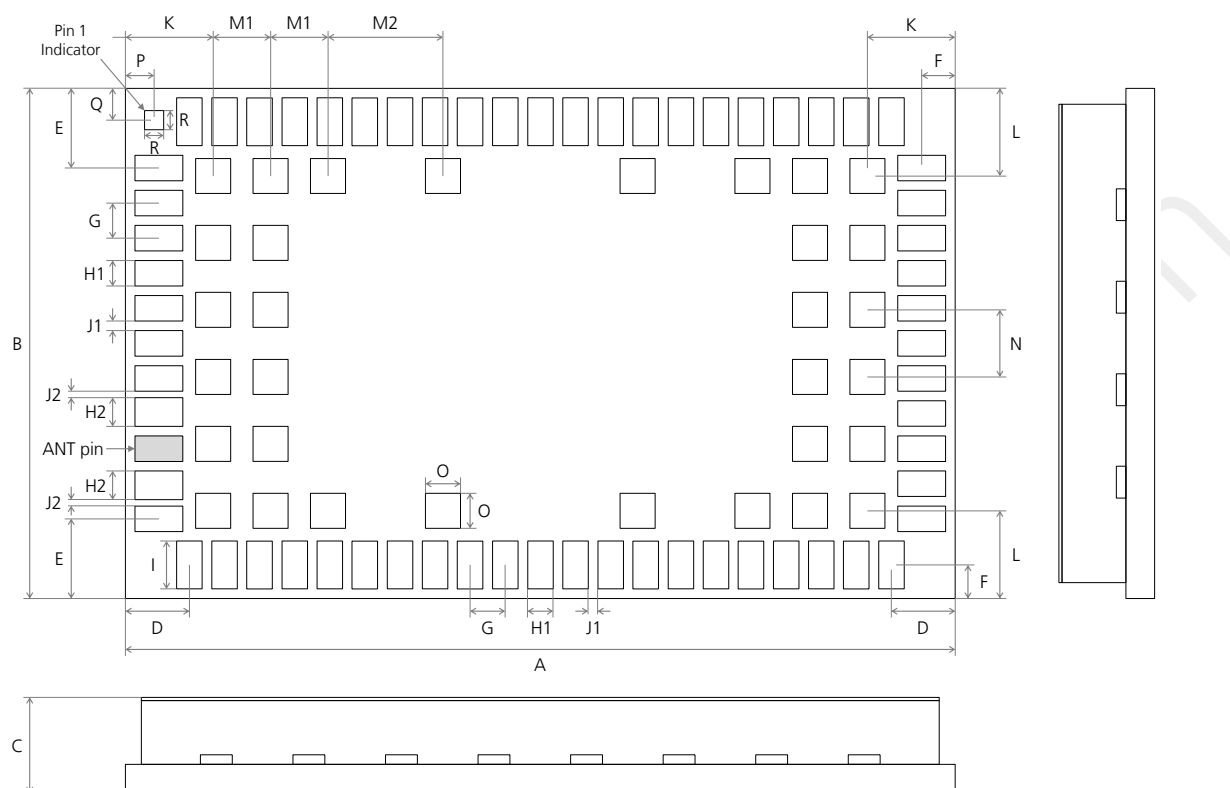


Figure 8: SARA-G3 series dimensions (bottom and sides views)

Parameter	Description	Typical	Tolerance
A	Module Height [mm]	26.0 (1023.6 mil)	+0.20/-0.20 (+7.9/-7.9 mil)
B	Module Width [mm]	16.0 (629.9 mil)	+0.20/-0.20 (+7.9/-7.9 mil)
C	Module Thickness [mm]	3.0 (118.4 mil)	+0.25/-0.15 (+9.8/-5.9 mil)
D	Horizontal Edge to Lateral Pin Pitch [mm]	2.0 (78.7 mil)	+0.20/-0.20 (+7.9/-7.9 mil)
E	Vertical Edge to Lateral Pin Pitch [mm]	2.5 (98.4 mil)	+0.20/-0.20 (+7.9/-7.9 mil)
F	Edge to Lateral Pin Pitch [mm]	1.05 (41.3 mil)	+0.20/-0.20 (+7.9/-7.9 mil)
G	Lateral Pin to Pin Pitch [mm]	1.1 (43.3 mil)	+0.02/-0.02 (+0.8/-0.8 mil)
H1	Lateral Pin Height [mm]	0.8 (31.5 mil)	+0.02/-0.02 (+0.8/-0.8 mil)
H2	Lateral Pin close to ANT Height [mm]	0.9 (35.4 mil)	+0.02/-0.02 (+0.8/-0.8 mil)
I	Lateral Pin Width [mm]	1.5 (59.1 mil)	+0.02/-0.02 (+0.8/-0.8 mil)
J1	Lateral Pin to Pin Distance [mm]	0.3 (11.8 mil)	+0.02/-0.02 (+0.8/-0.8 mil)
J2	Lateral Pin to Pin close to ANT Distance [mm]	0.2 (7.9 mil)	+0.02/-0.02 (+0.8/-0.8 mil)
K	Horizontal Edge to Central Pin Pitch [mm]	2.75 (108.3 mil)	+0.20/-0.20 (+7.9/-7.9 mil)
L	Vertical Edge to Central Pin Pitch [mm]	2.75 (108.3 mil)	+0.20/-0.20 (+7.9/-7.9 mil)
M1	Central Pin to Pin Horizontal Pitch [mm]	1.8 (70.9 mil)	+0.02/-0.02 (+0.8/-0.8 mil)
M2	Central Pin to Pin Horizontal Pitch [mm]	3.6 (141.7 mil)	+0.02/-0.02 (+0.8/-0.8 mil)
N	Central Pin to Pin Vertical Pitch [mm]	2.1 (82.7 mil)	+0.02/-0.02 (+0.8/-0.8 mil)
O	Central Pin Height and Width [mm]	1.1 (43.3 mil)	+0.02/-0.02 (+0.8/-0.8 mil)
P	Horizontal Edge to Pin 1 Indicator Pitch [mm]	0.9 (35.4 mil)	+0.20/-0.20 (+7.9/-7.9 mil)
Q	Vertical Edge to Pin 1 Indicator Pitch [mm]	1.0 (39.4 mil)	+0.20/-0.20 (+7.9/-7.9 mil)
R	Pin 1 Indicator Height and Width [mm]	0.6 (23.6 mil)	+0.02/-0.02 (+0.8/-0.8 mil)
Weight	Module Weight [g]	< 3	

Table 29: SARA-G3 series dimensions



For information regarding Footprint and Paste Mask see SARA-G3 / SARA-U2 series System Integration Manual [2].

## 6 Qualification and approvals

### 6.1 Reliability tests

Tests for product family qualifications are according to ISO 16750 "Road vehicles - Environmental conditions and testing for electrical and electronic equipment", and appropriate standards.

### 6.2 Approvals

#### 6.2.1 SARA-G3 series



Products marked with this lead-free symbol on the product label comply with the "Directive 2002/95/EC of the European Parliament and the Council on the Restriction of Use of certain Hazardous Substances in Electrical and Electronic Equipment" (RoHS).

SARA-G3 series GSM/GPRS modules are RoHS compliant.

No natural rubbers, hygroscopic materials, or materials containing asbestos are employed.

Table 30 lists the SARA-G3 series modules main approvals.

Directive / Standard / Regulatory	SARA-G300	SARA-G310	SARA-G340	SARA-G350
GCF (Global Certification Forum)	YES	YES		YES
PTCRB (PCS Type Certification Review Board)		YES		YES
R&TTE (Radio and Telecommunications Terminal Equipment EU Directive)	YES	YES	YES	YES
CE (Conformité Européenne)	YES	YES	YES	YES
FCC (US Federal Communications Commission)		YES		YES
IC (Industry Canada)		YES		YES

**Table 30: SARA-G3 series main certification approvals**



For all the certificates of compliancy and for the complete list of approvals (including countries' and network operators' approvals) of SARA-G3 series modules, refer to our website, [www.u-blox.com](http://www.u-blox.com).

#### 6.2.2 SARA-G350 ATEX

Additionally to the main approvals listed in the section 6.2.1 above and additionally to all the approvals listed in our website ([www.u-blox.com](http://www.u-blox.com)), the SARA-G350 ATEX modules are certified as components intended for use in potentially explosive atmospheres compliant to the following standards:

- IEC 60079-0: 2011
- IEC 60079-11: 2011
- IEC 60079-26: 2006

The certification number of SARA-G350 ATEX modules according to the ATEX directive 94/9/EC is:

- SIQ 13 ATEX 032 U

The certification number of SARA-G350 ATEX modules according to the IECEx conformity assessment system is:

- IECEx SIQ 13.0004U

According to the standards listed above, the SARA-G350 ATEX modules are certified with the following marking:

- Ex II 1G, Ex ia IIC/IIB

According to the marking stated above, the SARA-G350 ATEX modules are certified as electrical equipment of:

- group 'II': intended for use in areas with explosive gas atmosphere other than mines susceptible to firedamp
- category '1G': intended for use in zone 0 hazardous areas, in which explosive atmospheres caused by mixtures of air and gases, vapours or mists are present continuously, for long periods or frequently
- level of protection 'ia': intrinsically safe apparatus with very high level of protection, not capable of causing ignition in normal operation and with the application of one countable fault or a combination of any two countable fault plus those non-countable faults which give the most onerous condition
- subdivision 'IIC/IIB': intended for use in areas where the nature of the explosive gas atmosphere is considered very dangerous based on the Maximum Experimental Safe Gap or the Minimum Ignition Current ratio of the explosive gas atmosphere in which the equipment may be installed (a typical gas is hydrogen), so that the modules are also suitable for applications intended for use in subdivision IIB (typical gas is ethylene) and subdivision IIA (a typical gas is propane)

The section 4.2.1 defines the temperature range of use of SARA-G350 ATEX modules.

The RF radiating profile of SARA-G350 ATEX modules is compliant to all the applicable 3GPP / ETSI standards, with a maximum of 2 W RF pulse power and 1.15 mJ RF pulse energy according to the GSM/GPRS power class stated in Table 2.

The section 9.1 describes the nameplate of SARA-G350 ATEX modules (refer to Figure 12).

The following maximum input and equivalent parameters must be considered in sub-division IIC:

- $U_i = 3.8 \text{ V}$
- $I_i = 1.6 \text{ A}$  (burst)
- $P_i = 2.5 \text{ W}$
- $C_i = 103 \text{ }\mu\text{F}$
- $L_i = 4.1 \text{ }\mu\text{H}$

The following maximum input and equivalent parameters must be considered in sub-divisions IIB, IIA:

- $U_i = 4.2 \text{ V}$
- $I_i = 2.5 \text{ A}$  (burst)
- $P_i = 2.5 \text{ W}$
- $C_i = 103 \text{ }\mu\text{F}$
- $L_i = 4.1 \text{ }\mu\text{H}$



For more information about the integration of SARA-G350 ATEX modules in applications intended for use in potentially explosive atmospheres, refer to SARA-G3 / SARA-U2 Series System Integration Manual [2].



For all the certificates of compliancy and for the complete list of approvals (including countries' and network operators' approvals) of SARA-G350 ATEX modules, refer to our website [www.u-blox.com](http://www.u-blox.com).



## 7 Product handling

### 7.1 Packaging

SARA-G3 series modules are delivered as hermetically sealed, reeled tapes to enable efficient production, production lot set-up and tear-down. For more information about packaging, see the u-blox Package Information Guide [5].



Figure 9: Reeled SARA-G3 series modules

#### 7.1.1 Reels

SARA-G3 series modules are deliverable in quantities of 250 pieces on a reel. SARA-G3 series modules are delivered using reel Type B as described in the u-blox Package Information Guide [5].

Parameter	Specification
Reel Type	B
Delivery Quantity	250

Table 31: Reel information for SARA-G3 series modules



Quantities of less than 250 pieces are also available. Contact u-blox for more information.

## 7.1.2 Tapes

Figure 10 specifies the dimensions and orientations of the tapes for SARA-G3 series module.

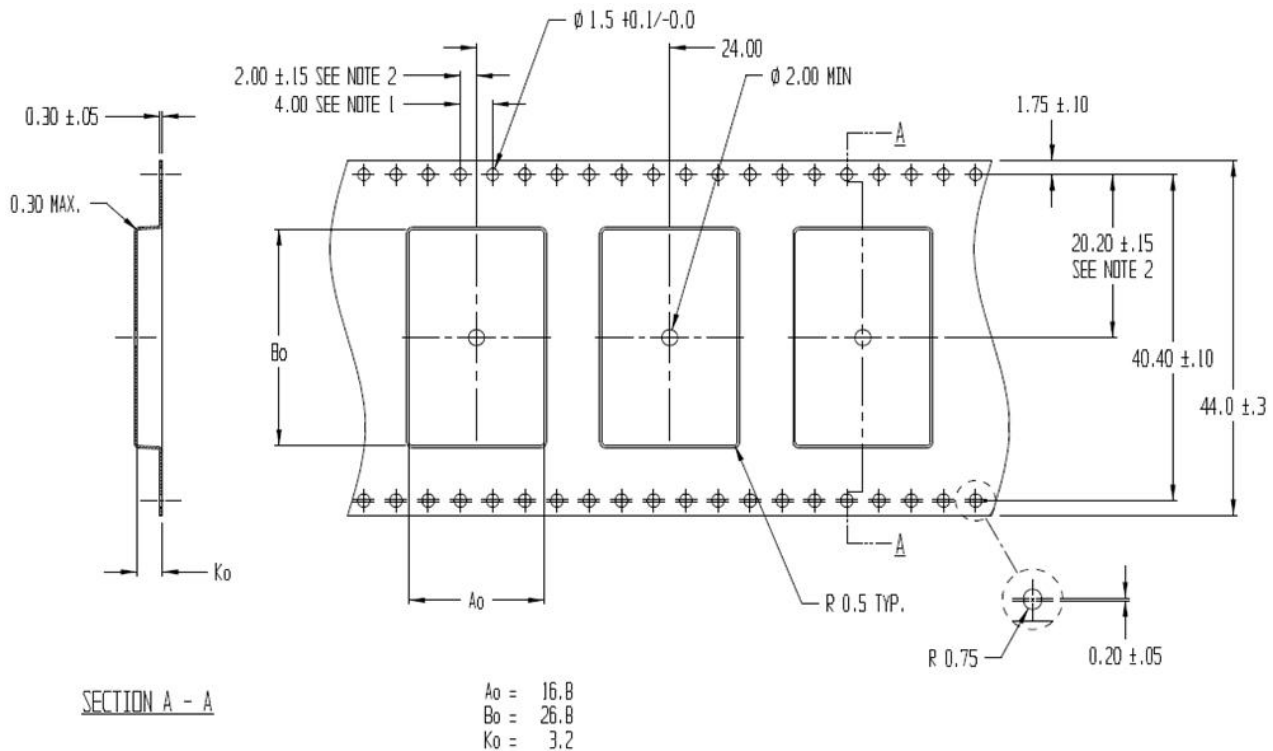


Figure 10: Dimensions for SARA-G3 series on tape

Parameter	Value
$A_0$	16.8
$B_0$	26.8
$K_0$	3.2

Table 32: SARA-G3 series tape dimensions (mm)



Note 1: 10 sprocket hole pitch cumulative tolerance  $\pm 0.2$ .



Note 2: Pocket position relative to sprocket hole measured as true position of pocket, not pocket hole.



Note 3:  $A_0$  and  $B_0$  are calculated on a plane at a distance "R" above the bottom of the pocket.

## 7.2 Moisture sensitivity levels

**SARA-G3 series modules are Moisture Sensitive Devices (MSD) in accordance to the IPC/JEDEC specification.**

The Moisture Sensitivity Level (MSL) relates to the packaging and handling precautions required. SARA-G3 series modules are rated at MSL level 4. For more information regarding moisture sensitivity levels, labeling, storage and drying see the u-blox Package Information Guide [5].



For MSL standard see IPC/JEDEC J-STD-020 (can be downloaded from [www.jedec.org](http://www.jedec.org)).

### 7.3 Reflow soldering

Reflow profiles are to be selected according to u-blox recommendations (see SARA-G3 / SARA-U2 Series System Integration Manual [2]).



**Failure to observe these precautions can result in severe damage to the device!**

### 7.4 ESD precautions



**SARA-G3 series modules contain highly sensitive electronic circuitry and are Electrostatic Sensitive Devices (ESD). Handling SARA-G3 series modules without proper ESD protection may destroy or damage them permanently.**

SARA-G3 series modules are Electrostatic Sensitive Devices (ESD) and require special ESD precautions typically applied to ESD sensitive components.

Table 8 reports the maximum ESD ratings of the SARA-G3 modules.

Proper ESD handling and packaging procedures must be applied throughout the processing, handling and operation of any application that incorporates SARA-G3 series modules.

ESD precautions should be implemented on the application board where the module is mounted, as described in the SARA-G3 / SARA-U2 Series System Integration Manual [2].



**Failure to observe these precautions can result in severe damage to the device!**

## 8 Default settings

Interface	Settings	Comments
UART interface	AT interface: enabled	AT command interface is by default enabled on the UART physical interface
	AT+IPR=0	Automatic baud rate detection enabled by default
	AT+ICF=0	Automatic frame format recognition enabled by default
	AT&K3	HW flow control enabled
	AT&S1	DSR line set ON in data mode and set OFF in command mode
	AT&D1	Upon an ON-to-OFF transition of DTR, the DCE enters online command state and issues an OK result code
	AT&C1	Circuit 109 changes in accordance with the Carrier detect status; ON if the Carrier is detected, OFF otherwise
	MUX protocol: disabled	<p>Multiplexing mode is by default disabled and it can be configured by AT+CMUX command.</p> <p>SARA-G340 and SARA-G350 modules define the following virtual channels:</p> <ul style="list-style-type: none"> <li>• Channel 0: control channel</li> <li>• Channel 1 – 5: AT commands / data connection</li> <li>• Channel 6: GNSS tunneling</li> </ul> <p>SARA-G300 and SARA-G310 modules define the following virtual channels:</p> <ul style="list-style-type: none"> <li>• Channel 0: control channel</li> <li>• Channel 1 – 2: AT commands / data connection</li> </ul>
Power Saving	AT+UPSV=0	Disabled
Network registration	AT+COPS=0	Self network registration

**Table 33: SARA-G3 series default settings**

## 9 Labeling and ordering information

### 9.1 Product labeling

The labels of SARA-G3 series modules include important product information as described in this section.

Figure 11 illustrates the label of all the SARA-G3 series modules except for SARA-G350 ATEX, and includes: u-blox logo, production lot, Pb-free marking, product Type Number, IMEI number, FCC and IC certification numbers (if applicable), CE marking with the Notified Body number, and production country.

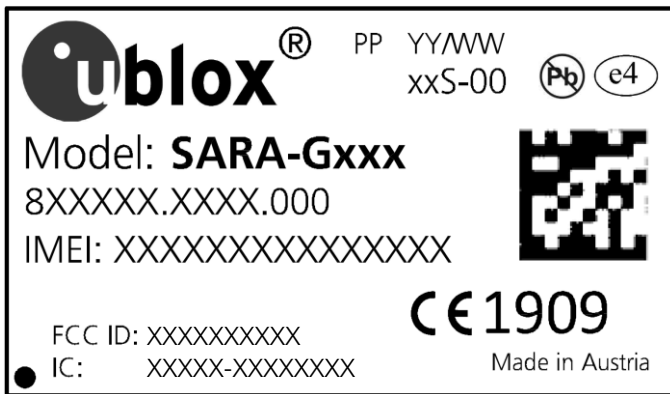


Figure 11: Label of all the SARA-G3 series modules except SARA-G350 ATEX

Figure 12 illustrates the label of SARA-G350 ATEX modules, and includes: u-blox logo, production lot, Pb-free marking, product Type Number, IMEI number, ATEX marking with the IECEx and ATEX certificate numbers and category classification, FCC and IC certification numbers, CE marking with the Notified Body number, and production country.



Figure 12: Label of SARA-G350 ATEX modules



For information about the approval codes and for all the certificates of compliancy of SARA-G3 series modules, refer to our website, [www.u-blox.com](http://www.u-blox.com).

## 9.2 Explanation of codes

Three different product code formats are used. The **Product Name** is used in documentation such as this data sheet and identifies all u-blox products, independent of packaging and quality grade. The **Ordering Code** includes options and quality, while the **Type Number** includes the hardware and firmware versions. Table 34 below details these three different formats:

Format	Structure
Product Name	SARA-TGVV
Ordering Code	SARA-TGVV-TTQ
Type Number	SARA-TGVV-TTQ-XX

Table 34: Product Code Formats

Table 35 explains the parts of the product code.

Code	Meaning	Example
T	Platform technology (i.e. G: GSM; E: EDGE; W: WEDGE; H: HSDPA; U: HSUPA; L: LTE; C: CDMA 1xRTT; D: EV-DO)	G: GSM
G	Platform generation, e.g. chip or function set; range: [0...9]	3
VV	Variant based on the same platform	50
TT	Major Product Version	00
Q	Quality grade/production site <ul style="list-style-type: none"> <li>S = standard / made in Austria</li> <li>A = automotive / made in Austria</li> <li>X = ATEX / made in Austria</li> <li>B = standard / made in Brazil</li> </ul>	S
XX		Default value is 00

Table 35: Part identification code

## 9.3 Ordering information

Ordering No.	Description
SARA-G300-00S	2-band GSM/GPRS class 10 module, 26.0 x 16.0 x 3.0 mm, 250 pcs/reel
SARA-G310-00S	4-band GSM/GPRS class 10 module, 26.0 x 16.0 x 3.0 mm, 250 pcs/reel
SARA-G340-00S	2-band GSM/GPRS class 10 module, full feature, 26.0 x 16.0 x 3.0 mm, 250 pcs/reel
SARA-G340-01S	
SARA-G350-00S	4-band GSM/GPRS class 10 module, full feature, 26.0 x 16.0 x 3.0 mm, 250 pcs/reel
SARA-G350-01S	
SARA-G350-00X	4-band GSM/GPRS class 10 module, full feature, ATEX certified, 26.0 x 16.0 x 3.0 mm, 250 pcs/reel
SARA-G350-01B	4-band GSM/GPRS class 10 module, full feature, made in Brazil, 26.0 x 16.0 x 3.0 mm, 250 pcs/reel

Table 36: Product ordering codes

# Appendix

## A Glossary

Name	Definition
ADC	Analog to Digital Converter
AT	AT Command Interpreter Software Subsystem, or attention
ATEX	Atmospheres Explosive
BER	Bit Error Rate
DCE	Data Communication Equipment
DDC	Display Data Channel (I <sup>2</sup> C compatible) Interface
DL	Down-link (Reception)
Driver Class	Output Driver Class: see Table 22 for definition
DRX	Discontinuous Reception
DTE	Data Terminal Equipment
ERS	External Reset Input Signal
GDI	Generic Digital Interfaces (power domain)
GND	Ground
GNSS	Global Navigation Satellite System
GPIO	General Purpose Input Output
GPRS	General Packet Radio Service
GPS	Global Positioning System
GSM	Global System for Mobile Communication
H	High logic digital level
HBM	Human Body Model
I <sup>2</sup> C	Inter-Integrated Circuit Interface
I <sup>2</sup> S	Inter-IC Sound Interface
L	Low logic digital level
LGA	Land Grid Array
N/A	Not Applicable
OD	Open Drain
PCN / IN	Product Change Notification / Information Note
PD	Pull-Down
POS	Power-On Input Signal
PU	Pull-Up
PU/PD Class	Pull-Up / Pull-Down Class: see Table 22 for definition
RMC	Reference Measurement Channel
SIM	Subscriber Identity Module
SPI	Serial Peripheral Interface
T	Tristate (Output of the pin set to tri-state, i.e. high impedance state)
T/PD	Tristate with internal active Pull-Down enabled
T/PU	Tristate with internal active Pull-Up enabled
TBD	To Be Defined
UART	Universal Asynchronous Receiver-Transmitter serial interface
UL	Up-link (Transmission)
USB	Universal Serial Bus interface

**Table 37: Explanation of abbreviations and terms used**

## Related documents

- [1] u-blox AT Commands Manual, Docu No WLS-SW-11000
- [2] u-blox SARA-G3 / SARA-U2 Series System Integration Manual, Docu No UBX-13000995
- [3] u-blox GNSS Implementation Application Note, Docu No UBX-13001849
- [4] u-blox Mux Implementation Application Note for wireless modules, Docu No UBX-13001887
- [5] u-blox Package Information Guide, Docu No UBX-14001652
- [6] ITU-T Recommendation V24, 02-2000. List of definitions for interchange circuits between Data Terminal Equipment (DTE) and Data Connection Equipment (DCE)
- [7] 3GPP TS 27.007 – Technical Specification Group Core Network and Terminals; AT command set for User Equipment (UE)
- [8] 3GPP TS 27.005 – Technical Specification Group Terminals; Use of Data Terminal Equipment – Data Circuit terminating Equipment (DTE-DCE) interface for Short Message Services (SMS) and Cell Broadcast Service (CBS)
- [9] 3GPP TS 27.010 – Terminal Equipment to User Equipment (TE-UE) multiplexer protocol
- [10] 3GPP TS 26.267 – Technical Specification Group Services and System Aspects; eCall Data Transfer; In-band modem solution; General description



For regular updates to u-blox documentation and to receive product change notifications, register on our homepage.

## Revision history

Revision	Date	Name	Status / Comments
-	Jun. 22, 2012	lpah / sses	Initial release
1	Sep. 05, 2012	sses	Corrected module pin numbering and dimensions
2	Oct. 19, 2012	sses	Added ESD immunity rating for ANT pin Added current consumption ratings
3	Nov. 07, 2012	sses	Added the technical description of SARA-G300 and SARA-G310 modules Updated current consumption ratings
4	Mar. 08, 2013	lpah / sses	Updated status to Advance Information
5	Apr. 12, 2013	lpah	Updated status to Preliminary
6	May 23, 2013	lpah	Updated status to Objective Specification CSD supported by SARA-G300 / SARA-G310
7	May 27, 2013	lpah	GPRS multi-slot class 10 supported by SARA-G300 / SARA-G310 Last revision with docu number GSM.G2-HW-12001
A	Jul. 05, 2013	sses	Updated status to Advance Information Added the technical description of SARA-G350 ATEX modules Added description of 32K_OUT pin for SARA-G300 / SARA-G310 Updated current consumption ratings
A1	Jul. 31, 2013	lpah	Updated status to Preliminary SIM detection not supported by SARA-G300 and SARA-G310 modules Last revision with old document number GSM.G2-HW-12001
R11	23-Jan-2014	lpah	Updated status to Objective Specification Extended the document applicability to SARA-G340 modules
R12	30-Jun-2014	mrod	Updated document status to Advance Information. Extended the document applicability to the SARA-G340-01S, SARA-G350-01S and SARA-G350-01B modules versions. Updated information about RIL and USSD. Updated current consumption ratings with AT+UPSV=0.



# Contact

For complete contact information visit us at [www.u-blox.com](http://www.u-blox.com)

## u-blox Offices

### North, Central and South America

#### u-blox America, Inc.

Phone: +1 703 483 3180  
E-mail: [info\\_us@u-blox.com](mailto:info_us@u-blox.com)

#### Regional Office West Coast:

Phone: +1 408 573 3640  
E-mail: [info\\_us@u-blox.com](mailto:info_us@u-blox.com)

#### Technical Support:

Phone: +1 703 483 3185  
E-mail: [support\\_us@u-blox.com](mailto:support_us@u-blox.com)

### Headquarters

#### Europe, Middle East, Africa

#### u-blox AG

Phone: +41 44 722 74 44  
E-mail: [info@u-blox.com](mailto:info@u-blox.com)  
Support: [support@u-blox.com](mailto:support@u-blox.com)

### Asia, Australia, Pacific

#### u-blox Singapore Pte. Ltd.

Phone: +65 6734 3811  
E-mail: [info\\_ap@u-blox.com](mailto:info_ap@u-blox.com)  
Support: [support\\_ap@u-blox.com](mailto:support_ap@u-blox.com)

#### Regional Office Australia:

Phone: +61 2 8448 2016  
E-mail: [info\\_au@u-blox.com](mailto:info_au@u-blox.com)  
Support: [support\\_ap@u-blox.com](mailto:support_ap@u-blox.com)

#### Regional Office China (Beijing):

Phone: +86 10 68 133 545  
E-mail: [info\\_cn@u-blox.com](mailto:info_cn@u-blox.com)  
Support: [support\\_cn@u-blox.com](mailto:support_cn@u-blox.com)

#### Regional Office China (Shenzhen):

Phone: +86 755 8627 1083  
E-mail: [info\\_cn@u-blox.com](mailto:info_cn@u-blox.com)  
Support: [support\\_cn@u-blox.com](mailto:support_cn@u-blox.com)

#### Regional Office India:

Phone: +91 959 1302 450  
E-mail: [info\\_in@u-blox.com](mailto:info_in@u-blox.com)  
Support: [support\\_in@u-blox.com](mailto:support_in@u-blox.com)

#### Regional Office Japan:

Phone: +81 3 5775 3850  
E-mail: [info\\_jp@u-blox.com](mailto:info_jp@u-blox.com)  
Support: [support\\_jp@u-blox.com](mailto:support_jp@u-blox.com)

#### Regional Office Korea:

Phone: +82 2 542 0861  
E-mail: [info\\_kr@u-blox.com](mailto:info_kr@u-blox.com)  
Support: [support\\_kr@u-blox.com](mailto:support_kr@u-blox.com)

#### Regional Office Taiwan:

Phone: +886 2 2657 1090  
E-mail: [info\\_tw@u-blox.com](mailto:info_tw@u-blox.com)  
Support: [support\\_tw@u-blox.com](mailto:support_tw@u-blox.com)