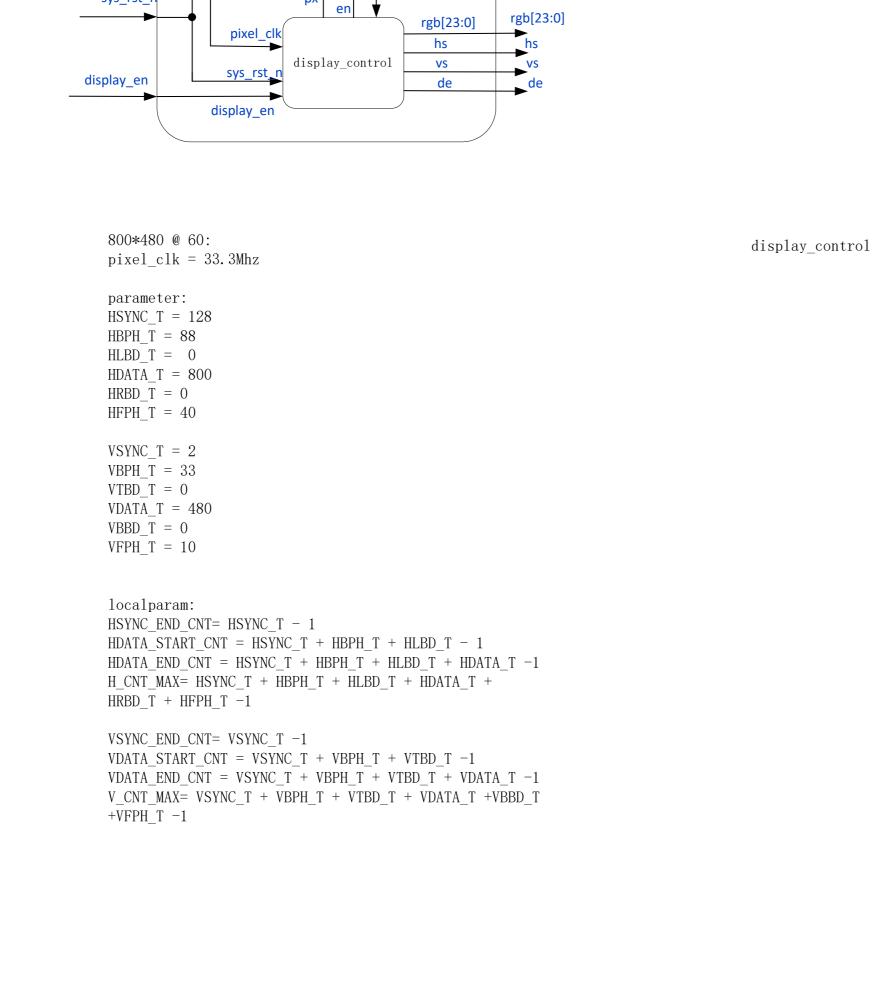
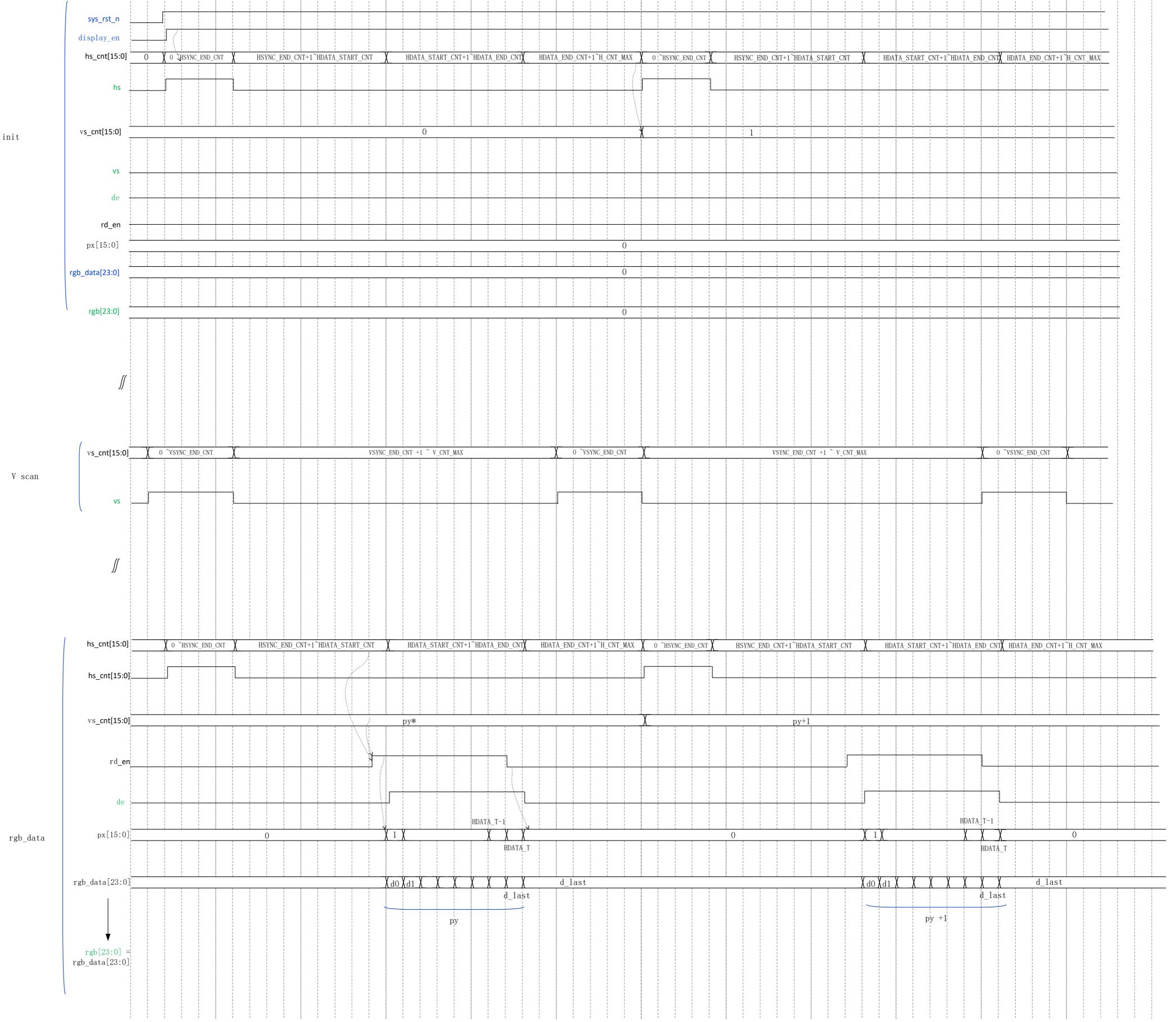
Design blocks design waveform hardware RGB LCD LCD_display display_en ____ init sys_rst_n | lcd_rst 1280*800 70 10 80 1280 70 1440 3 10 800 10 823 note: lcd_rst 低有效 display_en vs_cnt[15:0] X 0 ~VSYNC_END_CNT 0 ~VSYNC_END_CNT VSYNC_END_CNT +1 ~ V_CNT_MAX 800*480 @ 60: V scan





*py represent the vertical count number that in valid addressable video data VDATA_START_CNT < py <= VDATA_END_CNT

