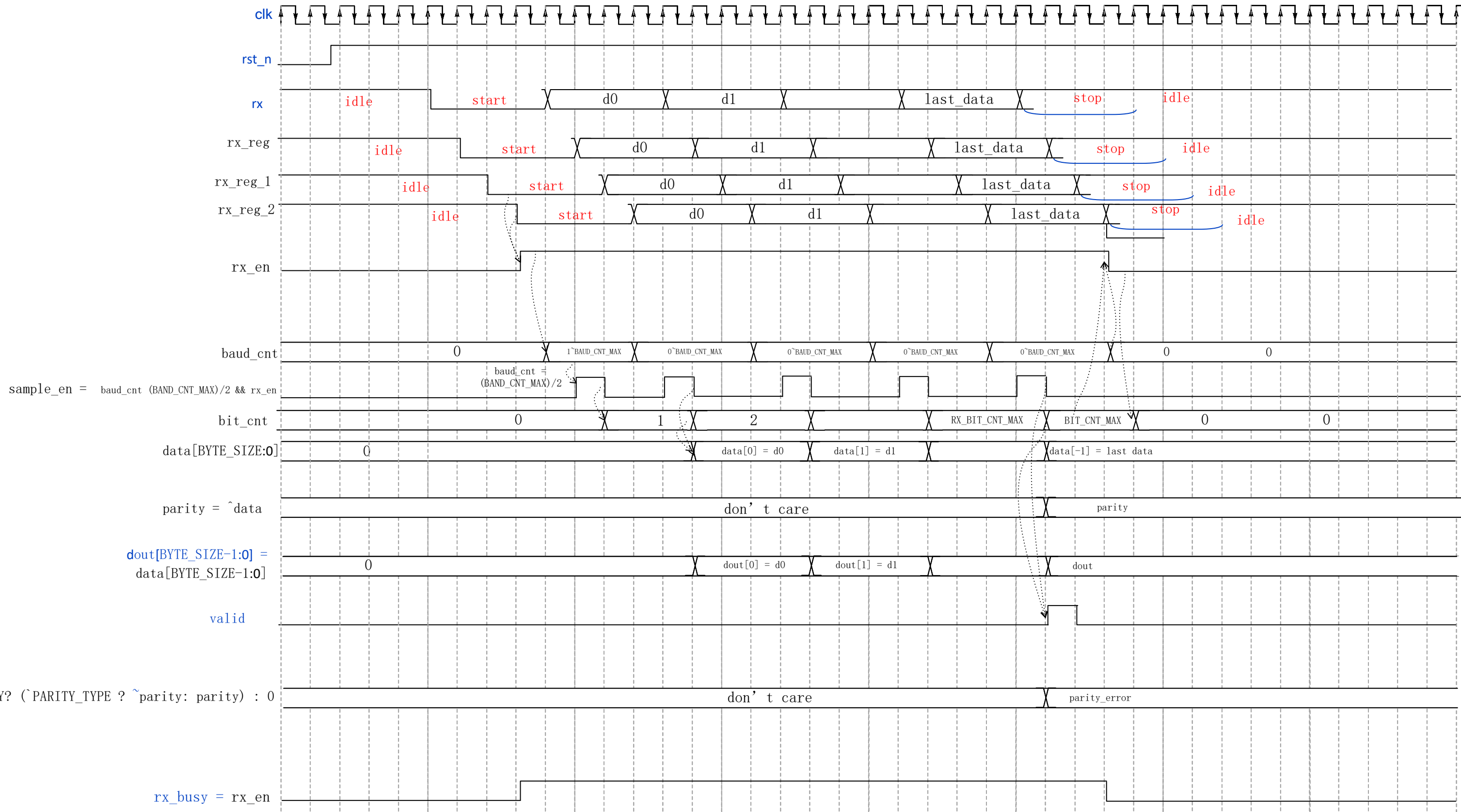
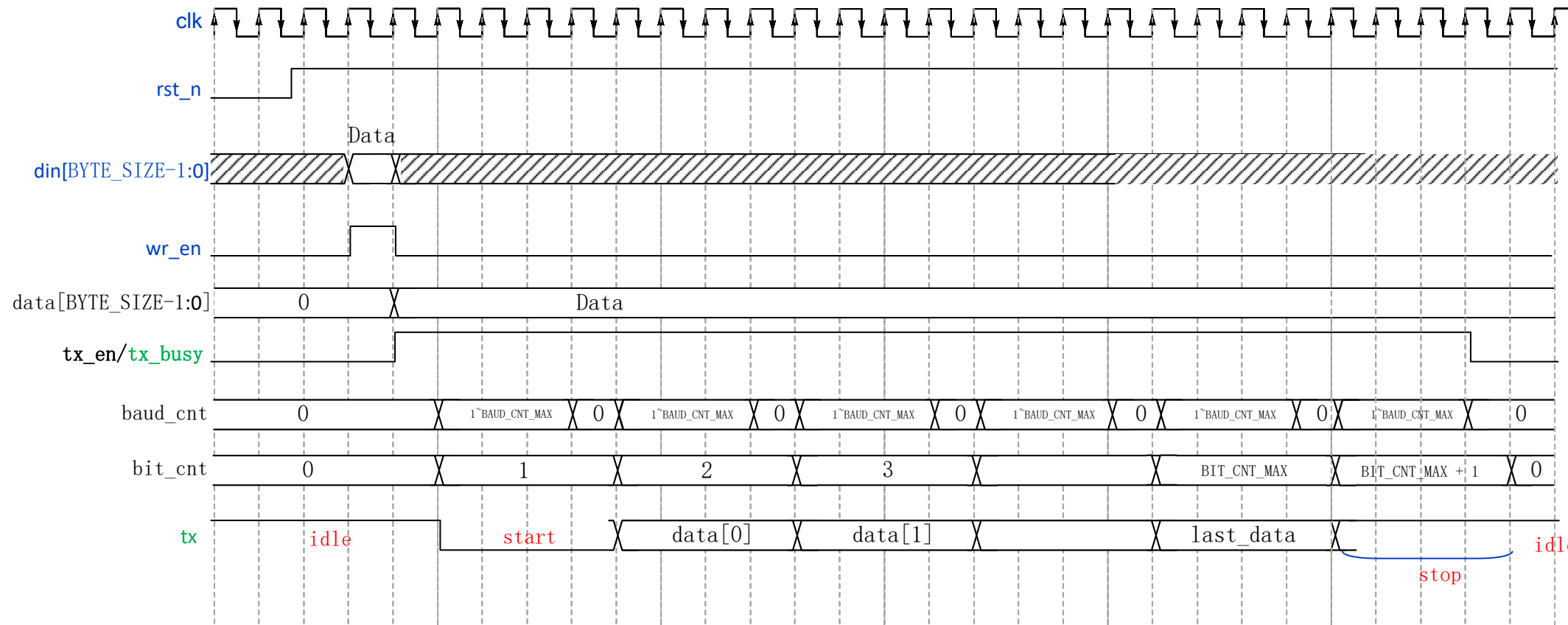
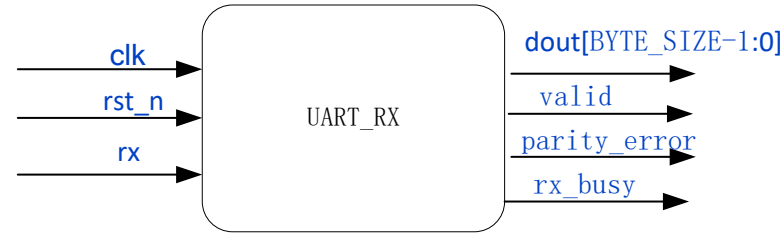
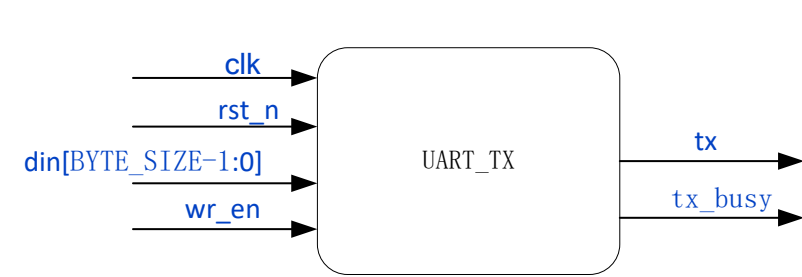
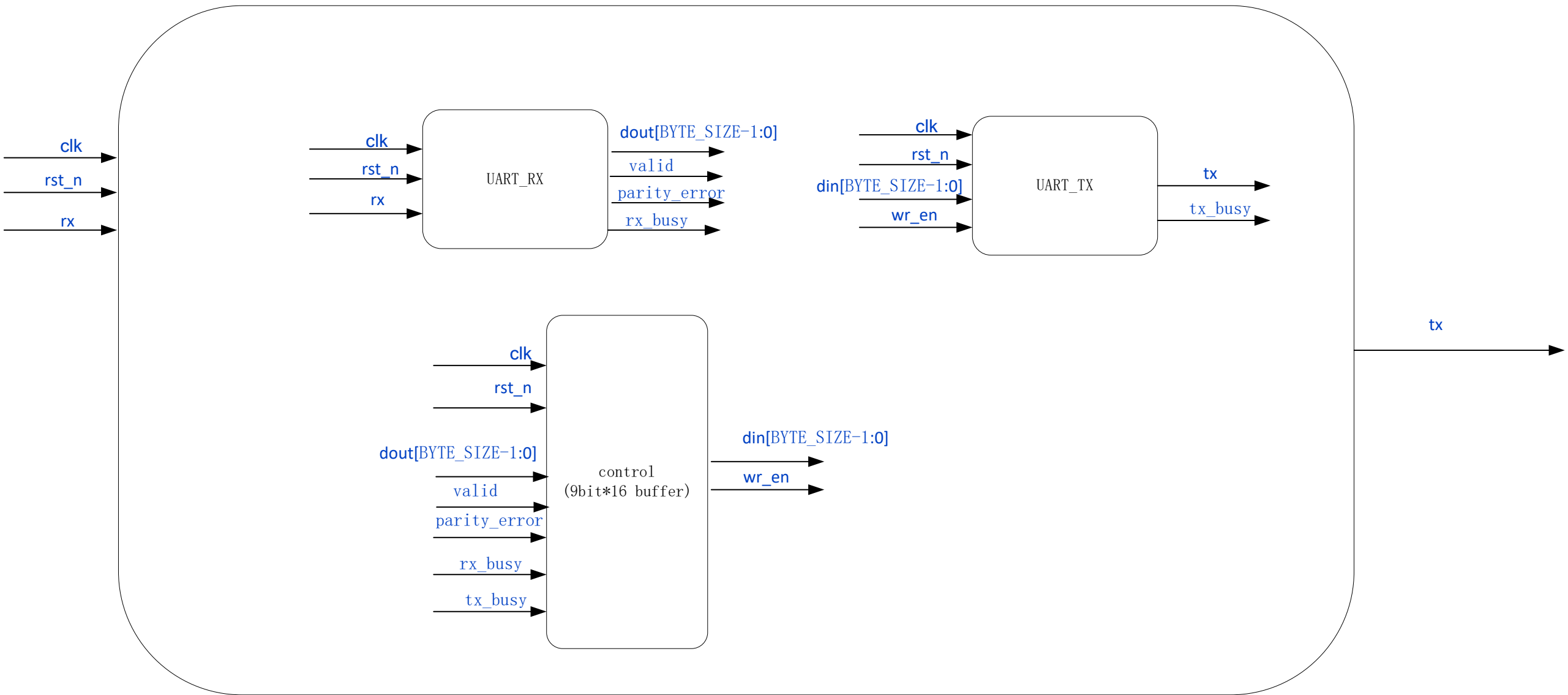
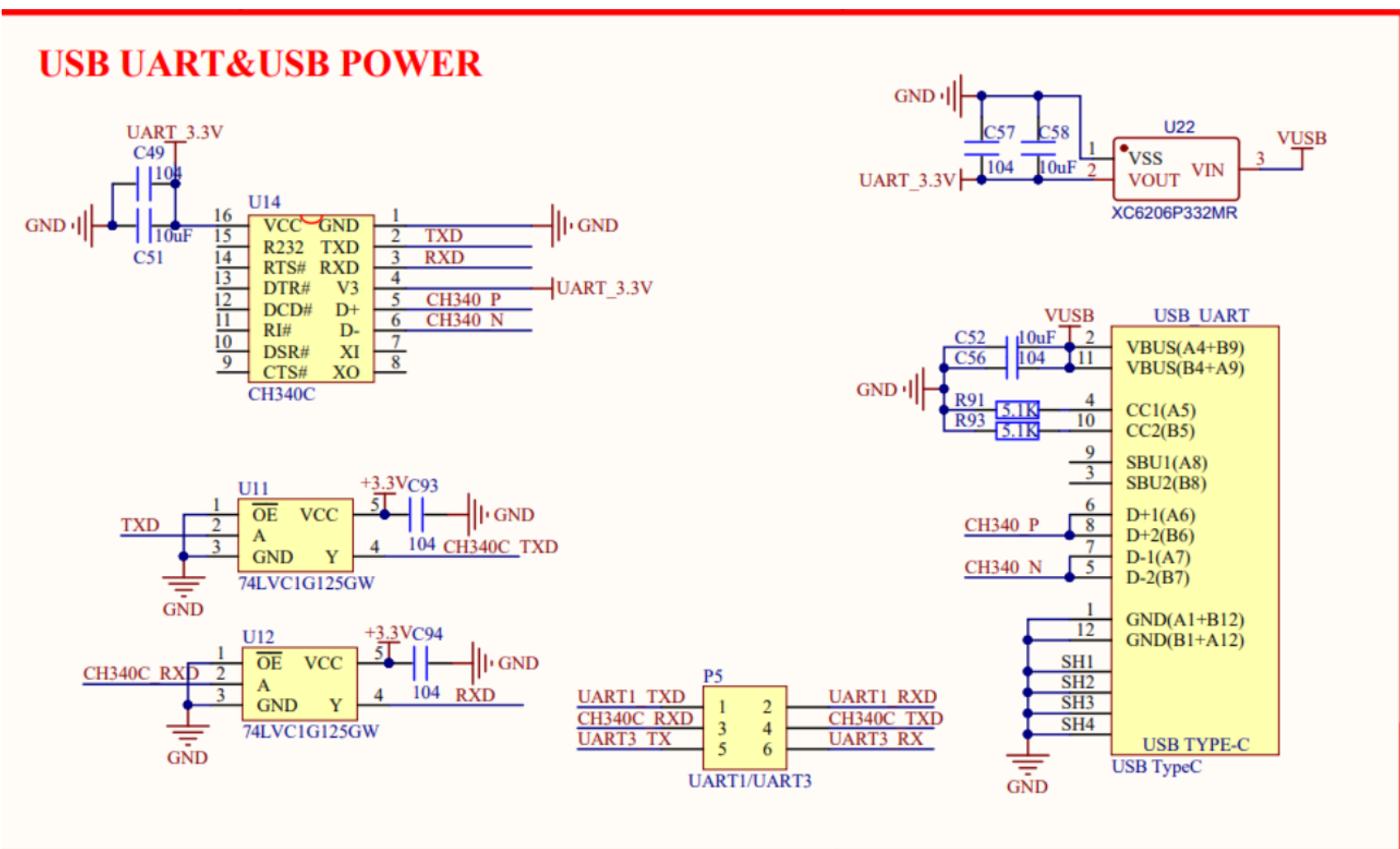
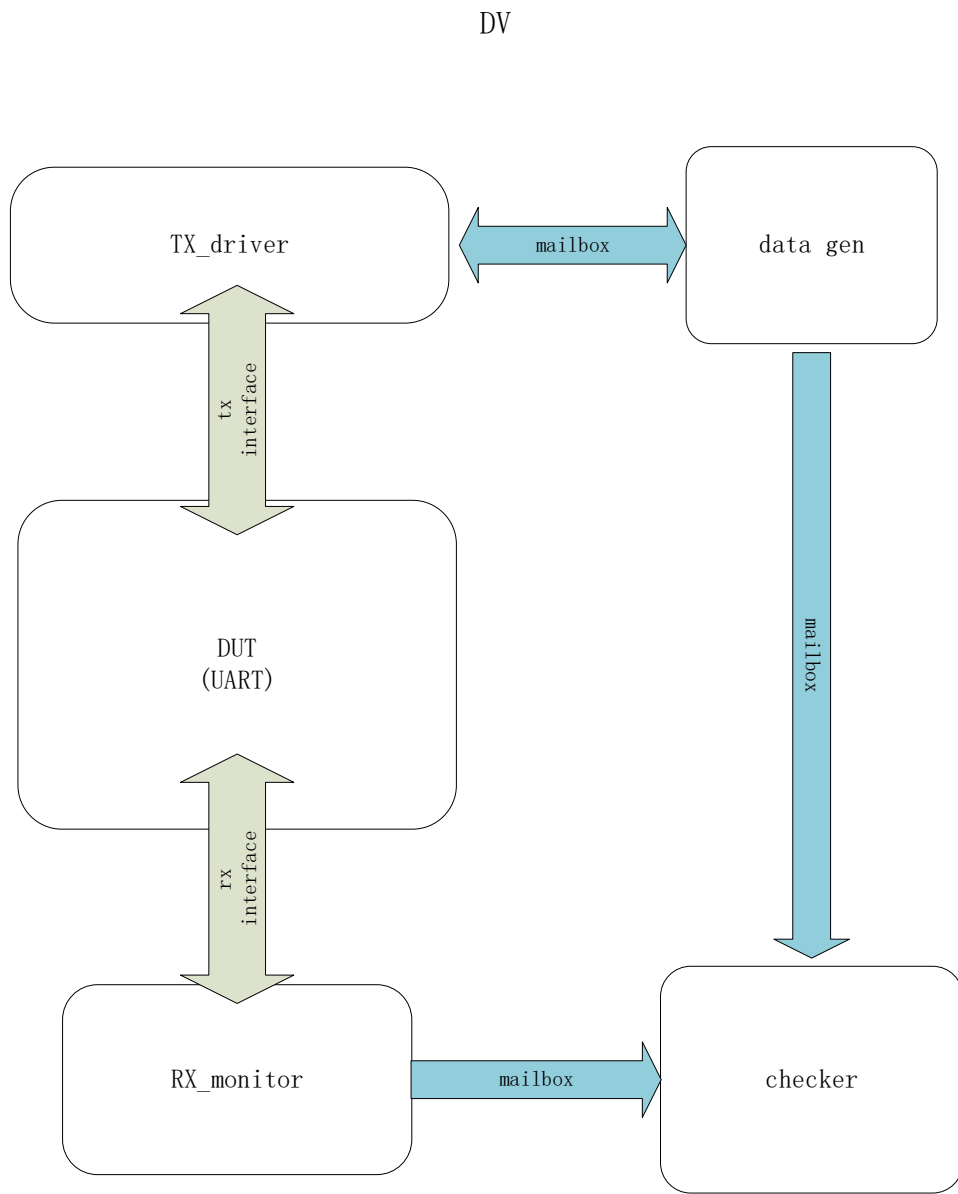


- Global Parameters:
1. BAUD_RATE_BPS = 9600
 2. CLK_MHZ = 50
 3. PARITY = 0
 4. STOP_SIZE = 1
 5. BYTESIZE = 8
- Local parameters:
6. BAUD_CNT_MAX = CLK_MHZ * 10⁶ / BAUD_RATE_BPS - 1
 7. BIT_CNT_MAX = BYTESIZE + PARITY + STOP_SIZE
 8. RX_BIT_CNT_MAX = BYTESIZE + PARITY



parity_error= 'PARITY? ('PARITY_TYPE ? `parity: parity) : 0



ATK MODULE			
uart rx	input	T19	RXD端口 UART3_RX
uart tx	output	J15	TXD端口

