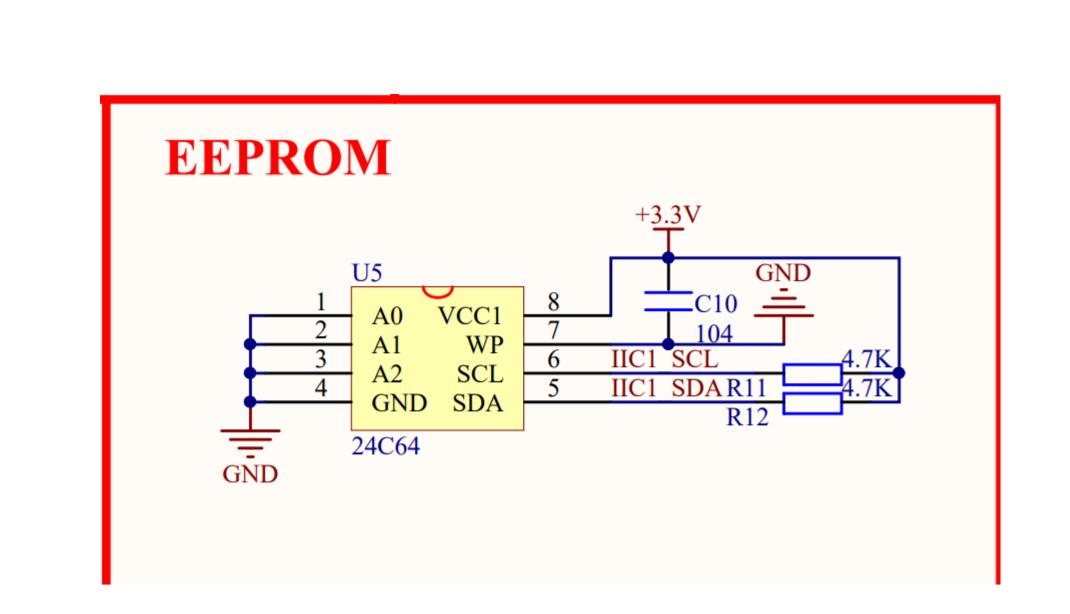
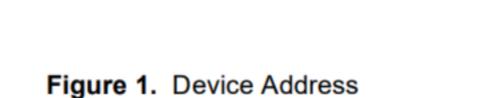


UART + EEPROM IIC

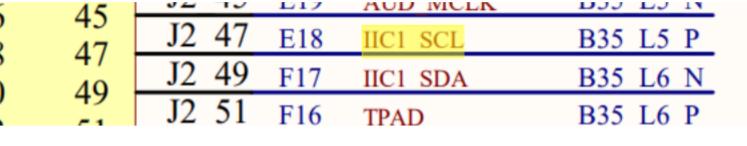
EEPROM IIC



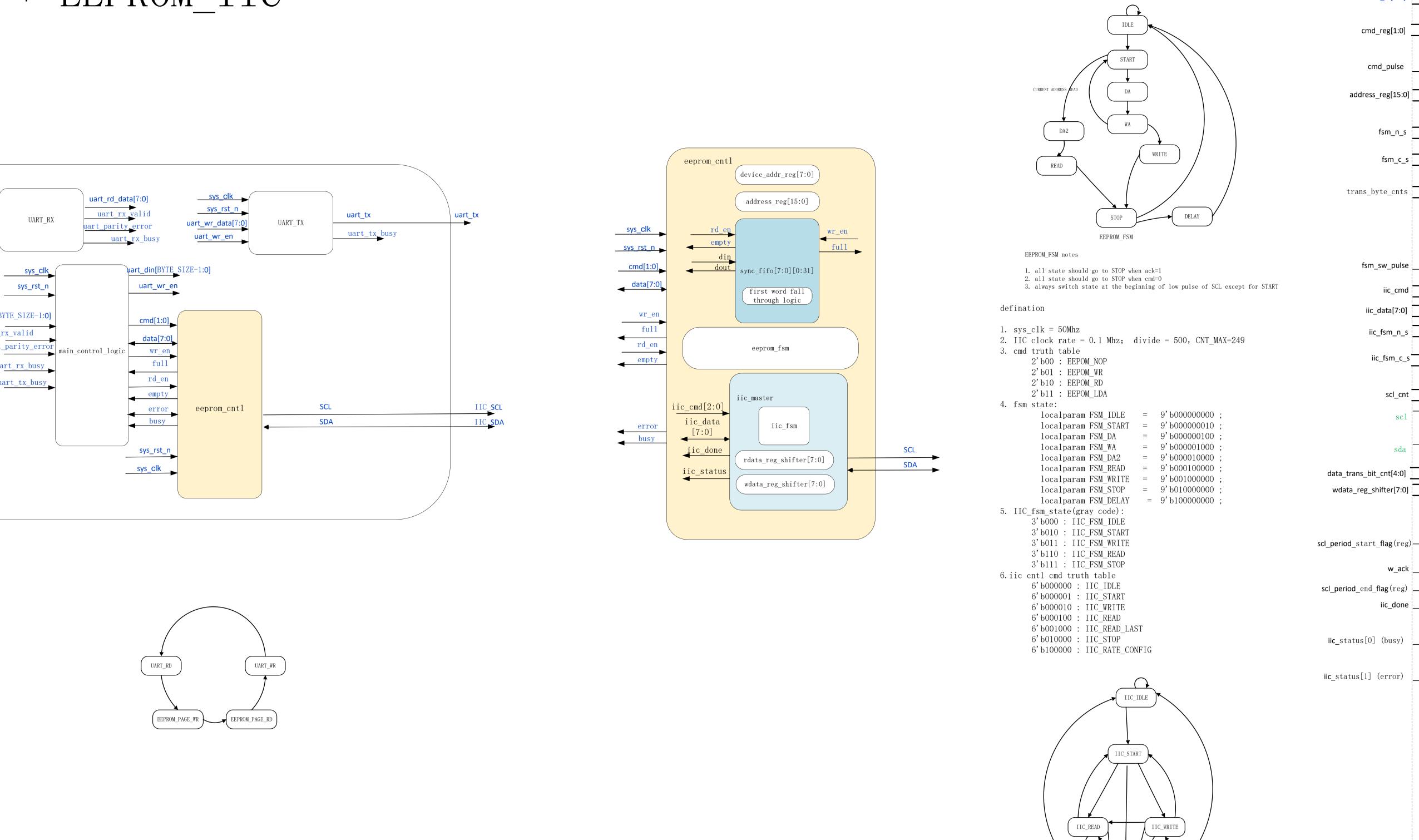


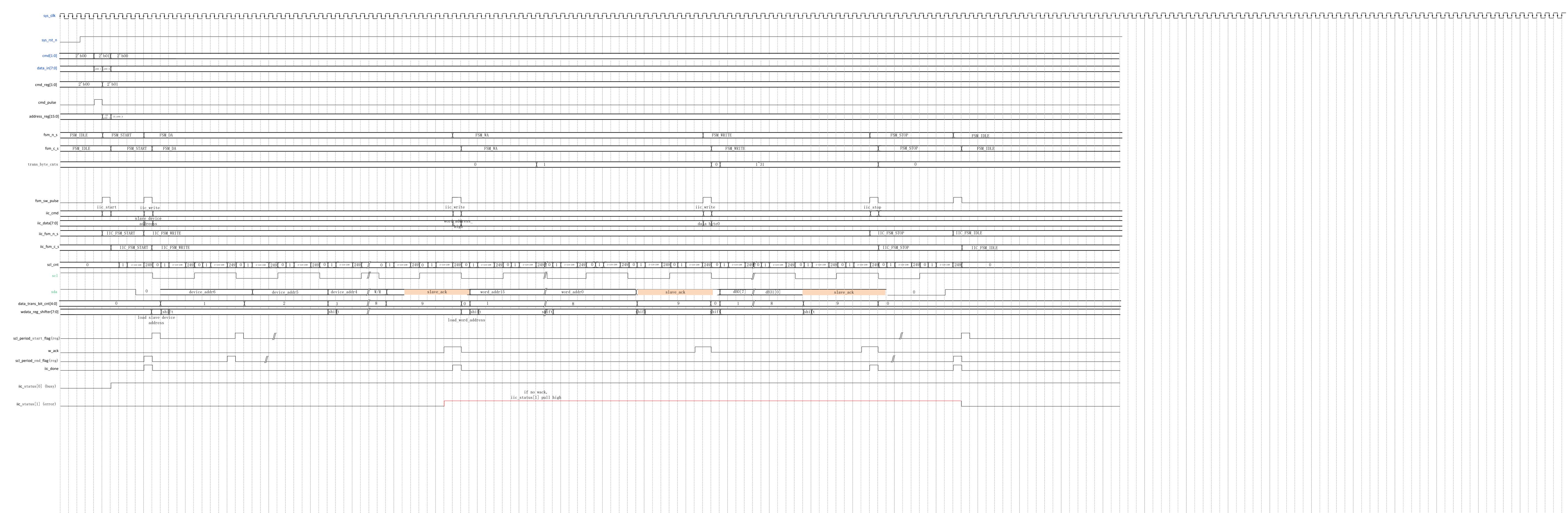
1 0 1 0 A₂ A₁ A₀ R/W

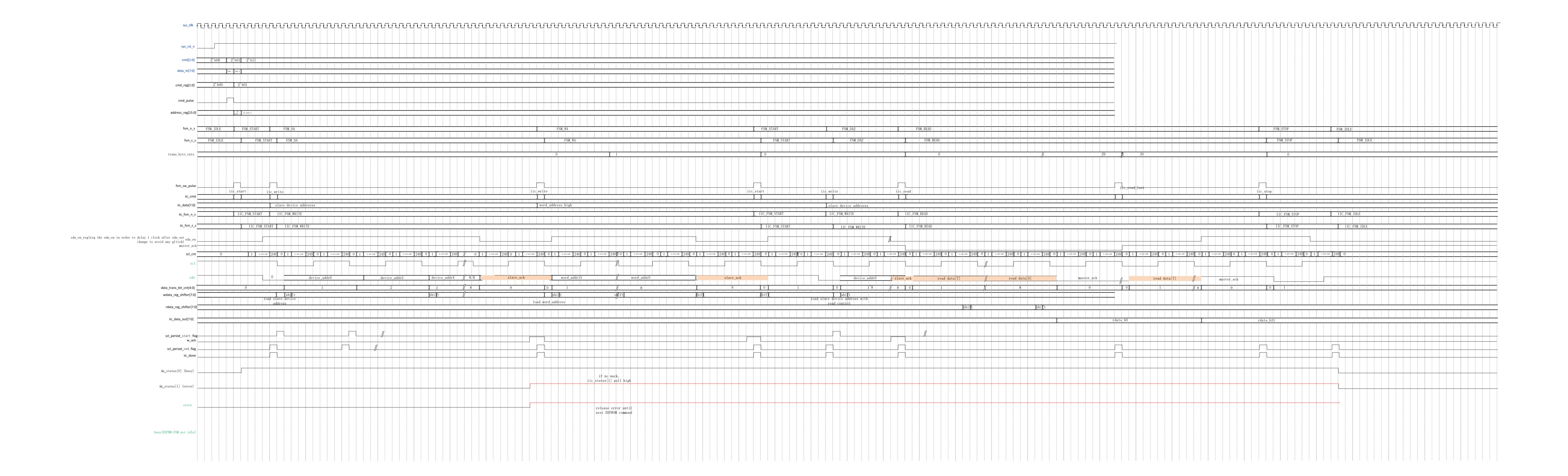
Device Addr = 7'b1010_000

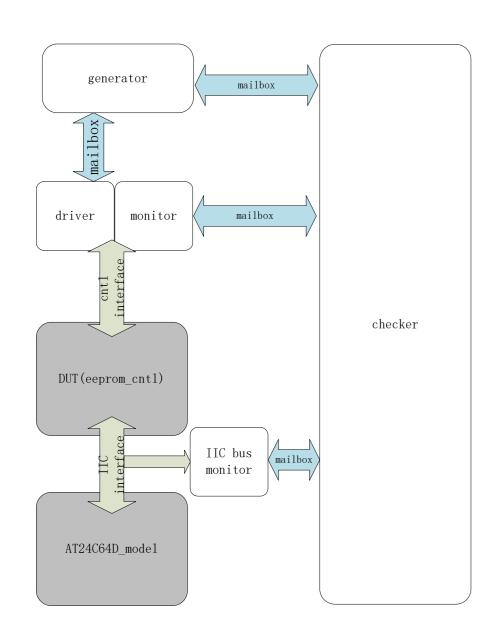


54 J2 J1 F10 IPAD BJ3 L0 F				
IIC总线 (EEPROM/RTC实时时钟				
/音频配置)				
iic_scl	output	E18	IIC时钟信号线	
iic_sda	inout	F17	IIC双向数据线	
	<u>'</u>	<u>'</u>	<u>+</u>	
ATK MODULE				
uart_rx	input	T19	RXD端口 UART3_RX	









note: 特别主义 clocking 采样在time-slot中的位置, 以及在module里和 program里不同的表现 a. clocking 的采样在module所有执行完后和program执行之前

b. 以下代码如果在module里执行会有问题,但在program里执行就没有问题