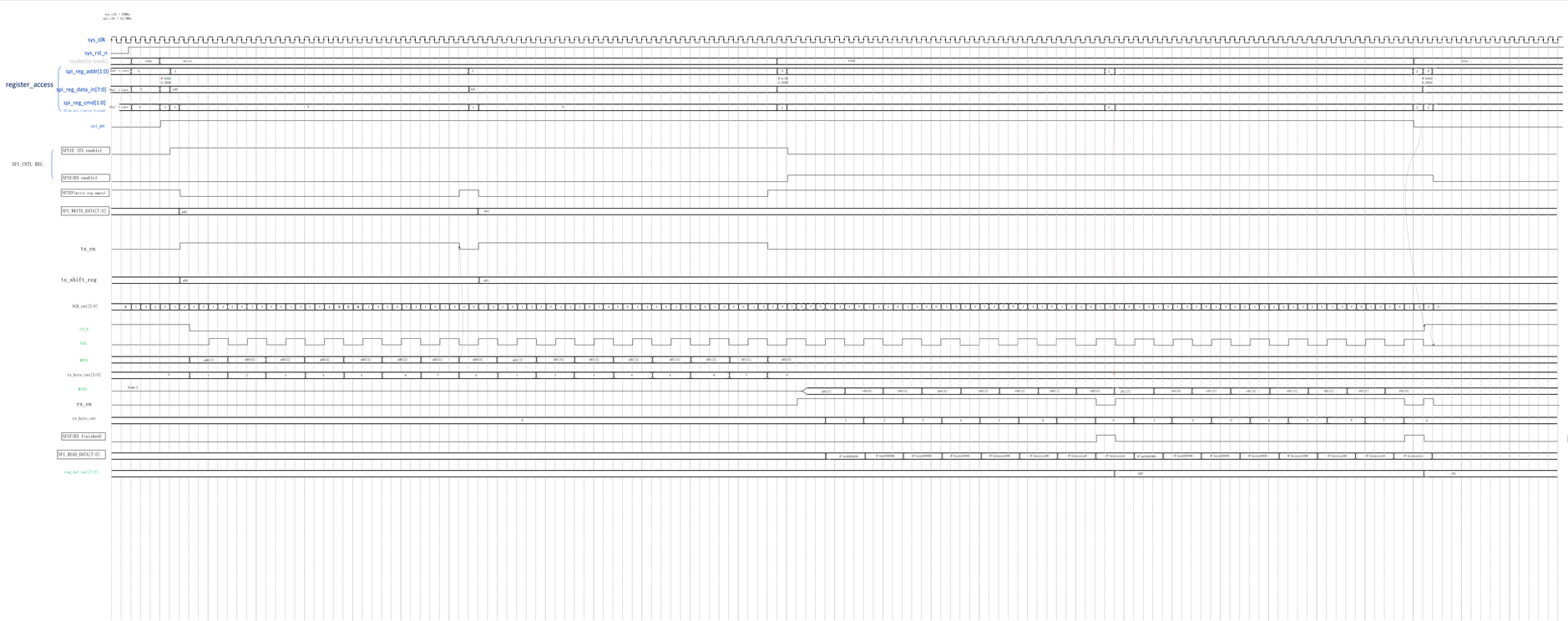
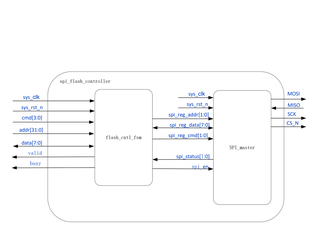
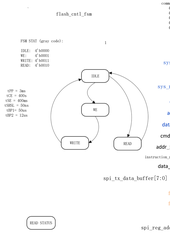
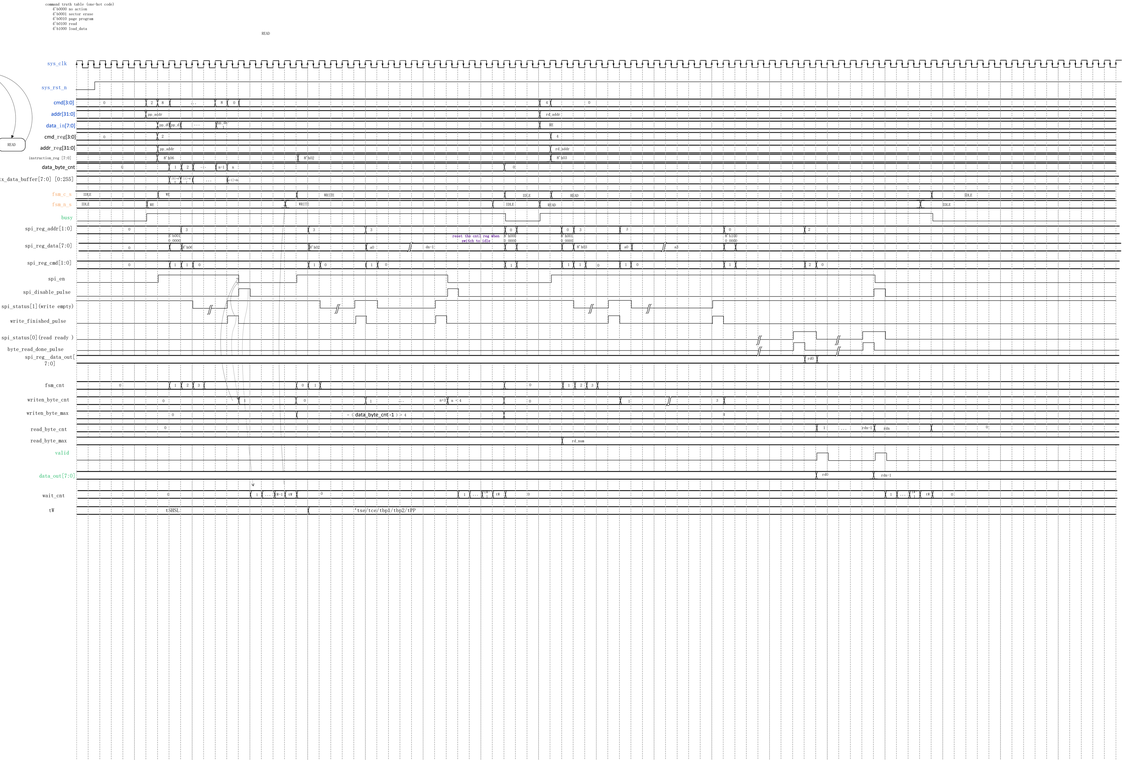

SPI registers defination

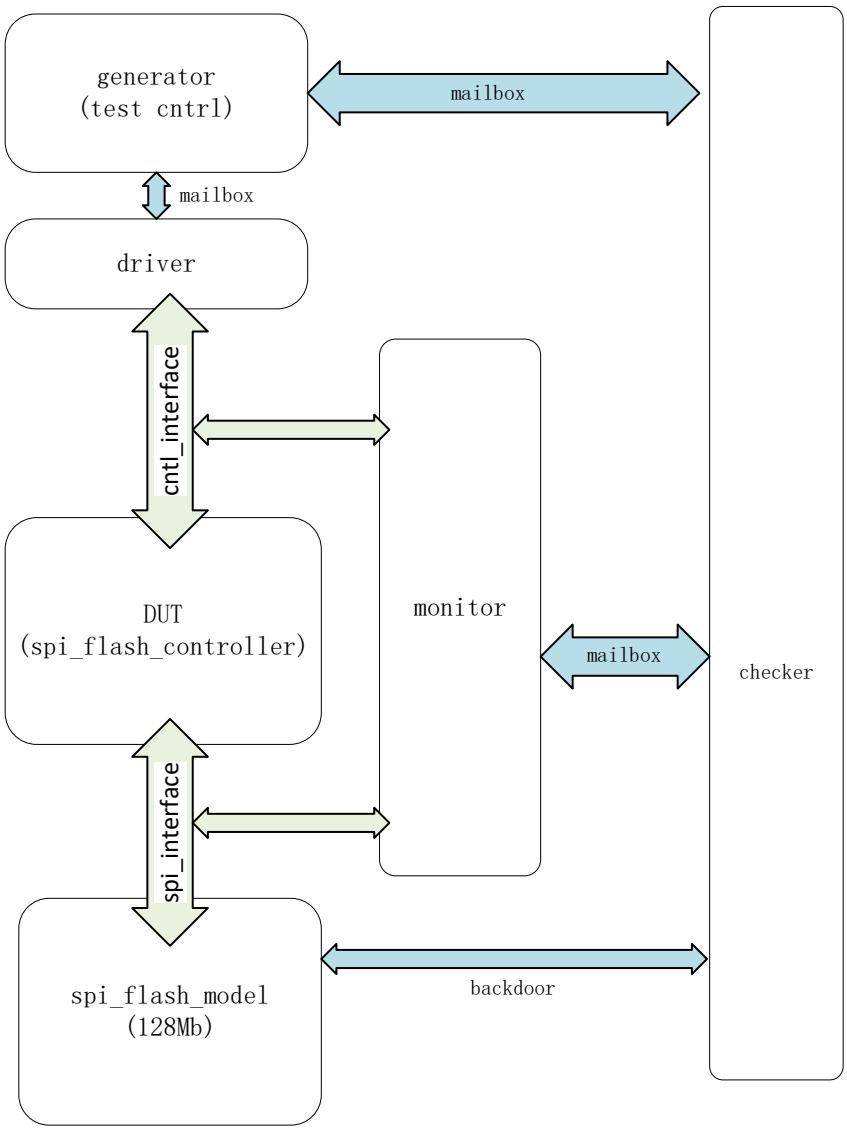
| | | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
|---|---|---|---|---|---|---|---|---|---|
| 0 | SPI_CNTL (r/w) | SPIE(RX enable) | SPE (Global enable) | SPTIE (TX enable) | MSTR | CPOL | CPHA | | LSBFE |
| | | | | | 0 | 0 | 0 | | 0 |
| 1 | SPI_STATUS (read only) | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| | | SPIF(RX finished) | | SPTEF(TX reg empty) | | | | | |
| | | 0 | | 1 | | | | | |
| 2 | SPI_READ_DATA | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| | (read only) | SPI read data | | | | | | | |
| 3 | SPI_WRITE_DATA (write only) | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| | | SPI read data | | | | | | | |




generator
(test cntrl)
mailbox
mailbox
driver
cntl_interface
monitor
DUT
(spi_flash_controller)
mailbox
checker
spi_interface
spi_flash_model
(128Mb)
backdoor