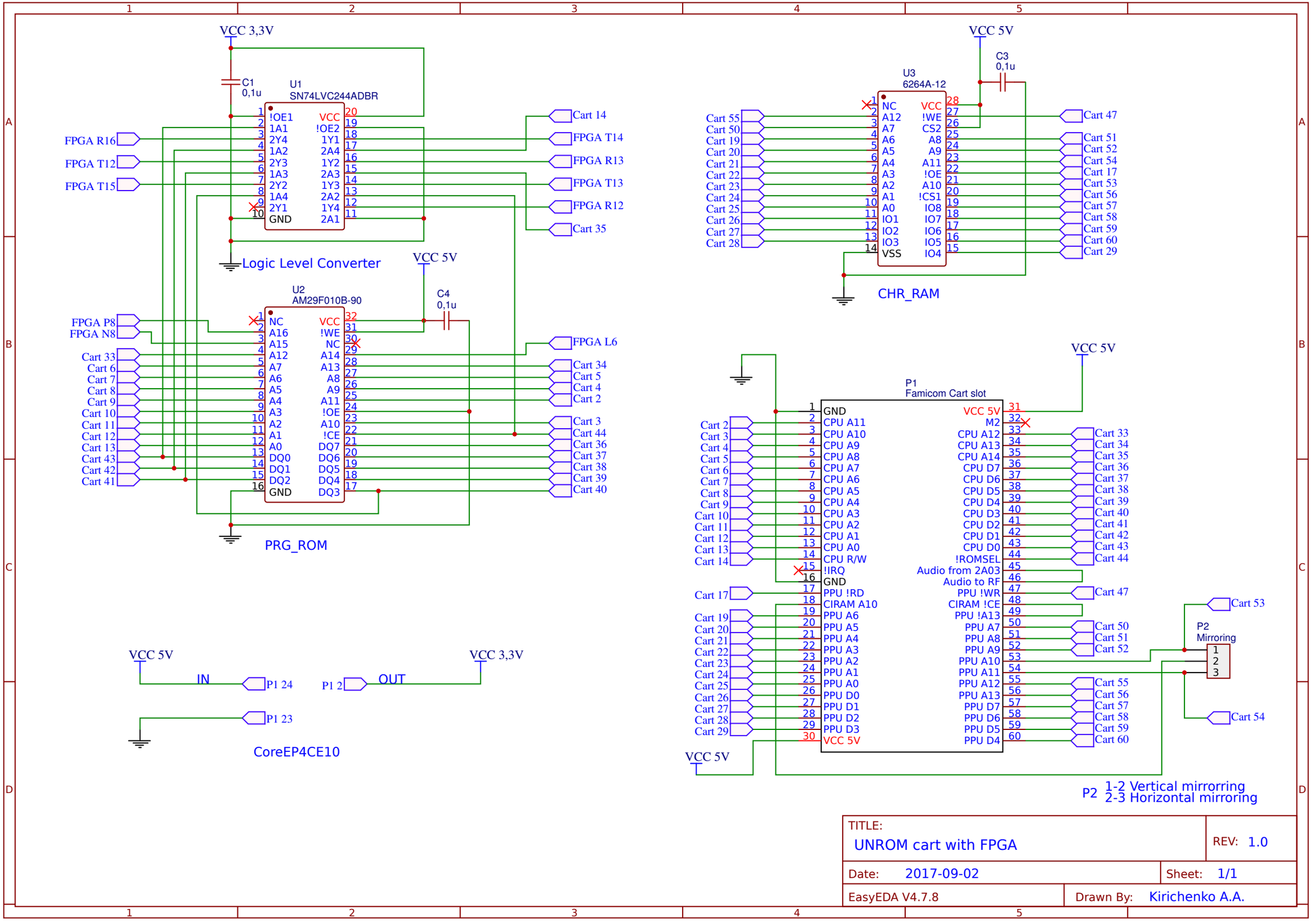
VCC 3,3V
C1
0,1u
U1
SN74LVC244ADBR
!OE1
1A1
2Y4
1A2
2Y3
1A3
2Y2
1A4
2Y1
GND
VCC
!OE2
1Y1
2A4
1Y2
2A3
1Y3
2A2
1Y4
2A1
FPGA R16
FPGA T12
FPGA T15
Cart 14
FPGA T14
FPGA R13
FPGA T13
FPGA R12
Cart 35
Logic Level Converter
VCC 5V
U2
AM29F010B-90
C4
0,1u
NC
A16
A15
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A7
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A2
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DQ0
DQ1
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GND
VCC
!WE
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A14
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A10
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DQ7
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DQ3
FPGA P8
FPGA N8
Cart 33
Cart 6
Cart 7
Cart 8
Cart 9
Cart 10
Cart 11
Cart 12
Cart 13
Cart 43
Cart 42
Cart 41
FPGA L6
Cart 34
Cart 5
Cart 4
Cart 2
Cart 3
Cart 44
Cart 36
Cart 37
Cart 38
Cart 39
Cart 40
PRG_ROM
VCC 5V
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U3
6264A-12
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A4
A3
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A0
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IO2
IO3
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VCC
!WE
CS2
A8
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A11
!OE
A10
!CS1
IO8
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IO5
IO4
Cart 55
Cart 50
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Cart 29
CHR_RAM
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Famicom Cart slot
GND
CPU A11
CPU A10
CPU A9
CPU A8
CPU A7
CPU A6
CPU A5
CPU A4
CPU A3
CPU A2
CPU A1
CPU A0
CPU R/W
!IRQ
GND
PPU !RD
CIRAM A10
PPU A6
PPU A5
PPU A4
PPU A3
PPU A2
PPU A1
PPU A0
PPU D0
PPU D1
PPU D2
PPU D3
VCC 5V
VCC 5V
M2
CPU A12
CPU A13
CPU A14
CPU D7
CPU D6
CPU D5
CPU D4
CPU D3
CPU D2
CPU D1
CPU D0
!ROMSEL
Audio from 2A03
Audio to RF
PPU !WR
CIRAM !CE
PPU !A13
PPU A7
PPU A8
PPU A9
PPU A10
PPU A11
PPU A12
PPU A13
PPU D7
PPU D6
PPU D5
PPU D4
Cart 2
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Cart 52
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Cart 57
Cart 58
Cart 59
Cart 60
VCC 5V
P2
Mirroring
Cart 53
Cart 54
P2 1-2 Vertical mirrorring
2-3 Horizontal mirroring
VCC 5V
IN
P1 24
P1 2
OUT
VCC 3,3V
P1 23
CoreEP4CE10
TITLE:
UNROM cart with FPGA
REV: 1.0
Date: 2017-09-02
Sheet: 1/1
EasyEDA V4.7.8
Drawn By: Kirichenko A.A.