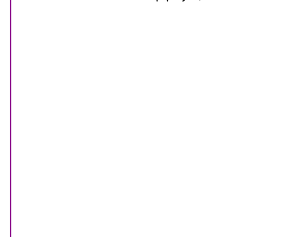


Sheet: SNES Slot



File: snesslot.sch

Sheet: Power Supply / Misc.



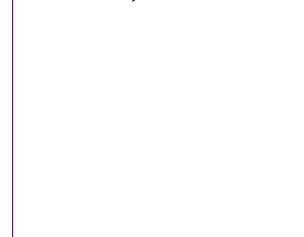
File: pwr_misc.sch

Sheet: MCU



File: mcu.sch

Sheet: Memory



File: memory.sch

Sheet: FPGA



File: fpga.sch

TODO

- [] Change FPGA to 12F
- [] Check buck inductor rating & 5v filter
- [] Feed forward cap on regulators?
- [] Fettle LED current limit resistors
- [] DRC with nightly 5.99
- [] Tidy ground stitching

sd2snes ECP5

Rework for ECP5 by @samlittlewood

Maximilian Rehkopf

Sheet: /

File: sd2snes_ecp5.sch

Title: sd2snes ECP5

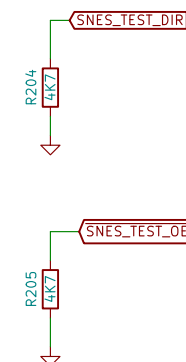
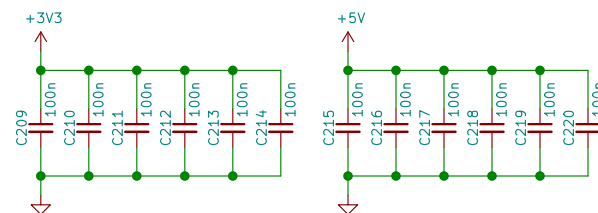
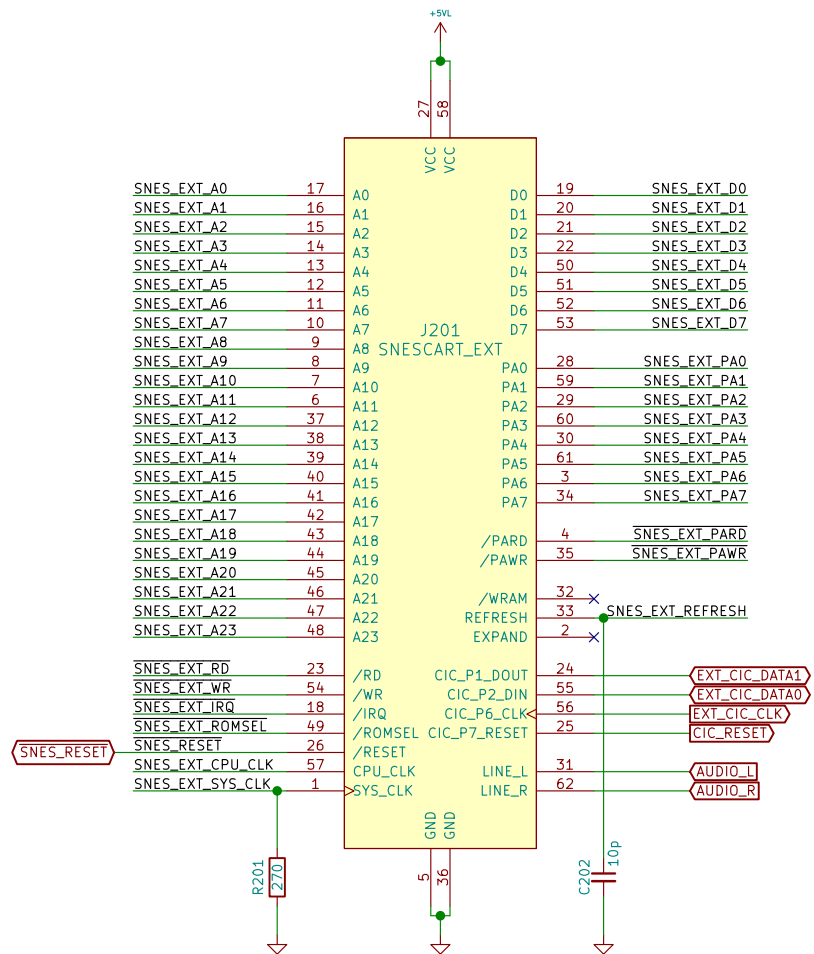
Size: A4

Date: 2021-02-09

Rev: B

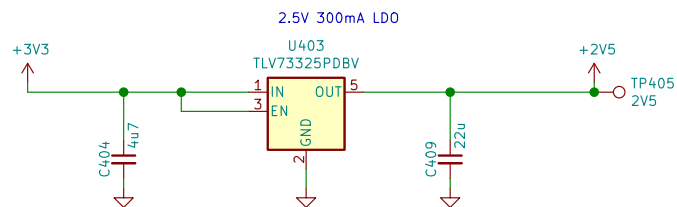
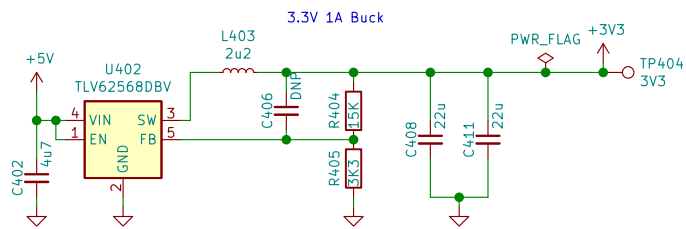
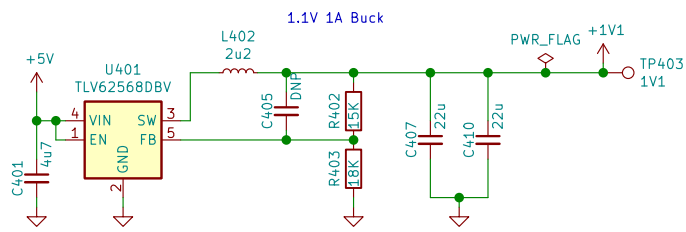
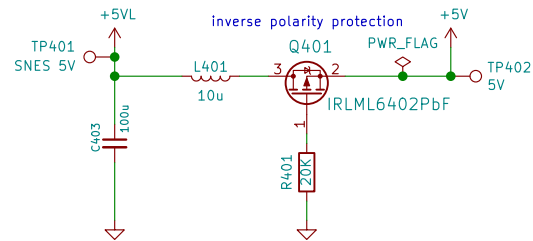
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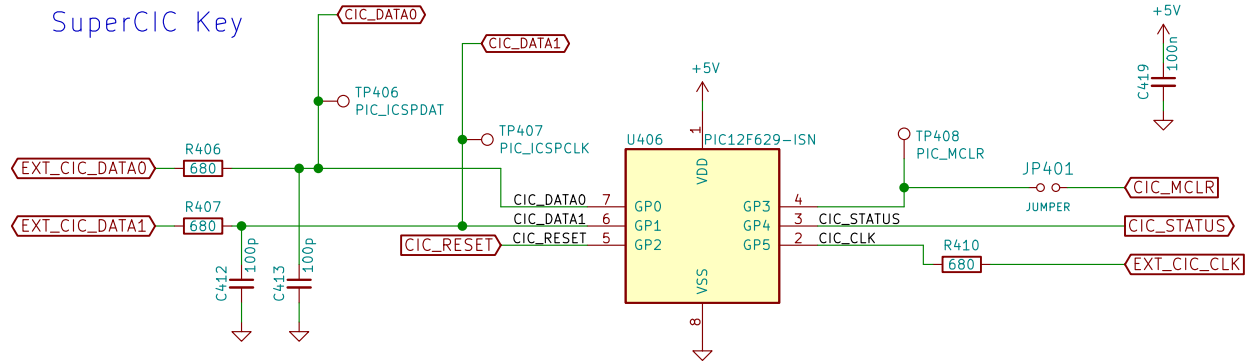


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Power Supply

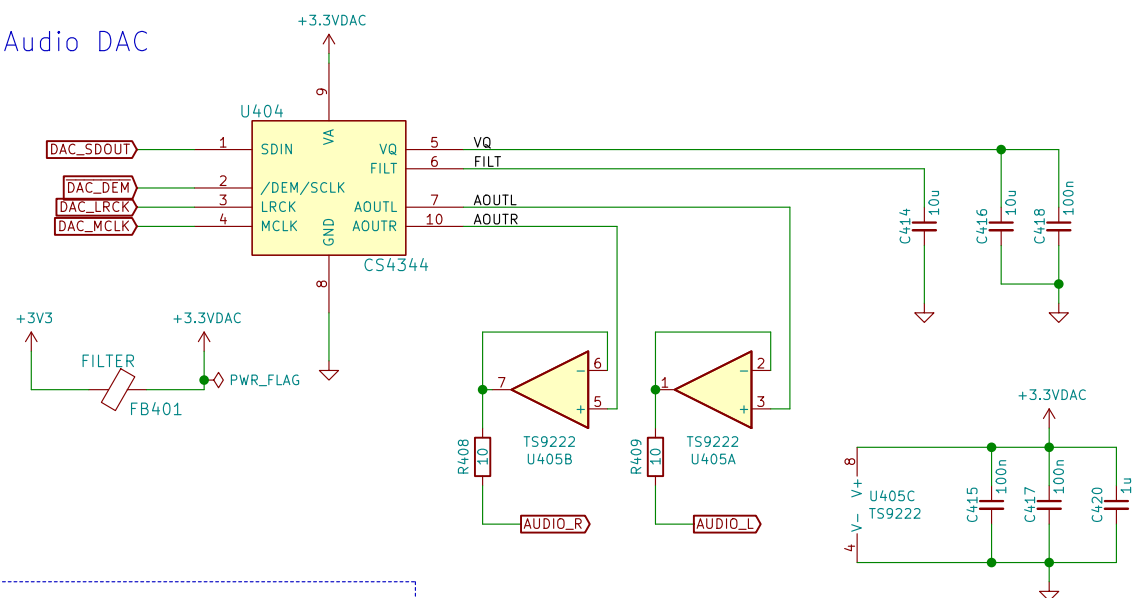


SuperCIC Key

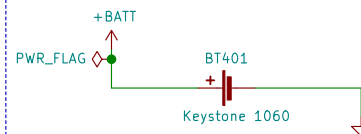


JP401 – open in case of in-circuit PIC programming

Audio DAC



Battery (SRAM + RTC)



Power, DAC, CIC, Batt

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Sheet: /Power Supply / Misc./

File: pwr_misc.sch

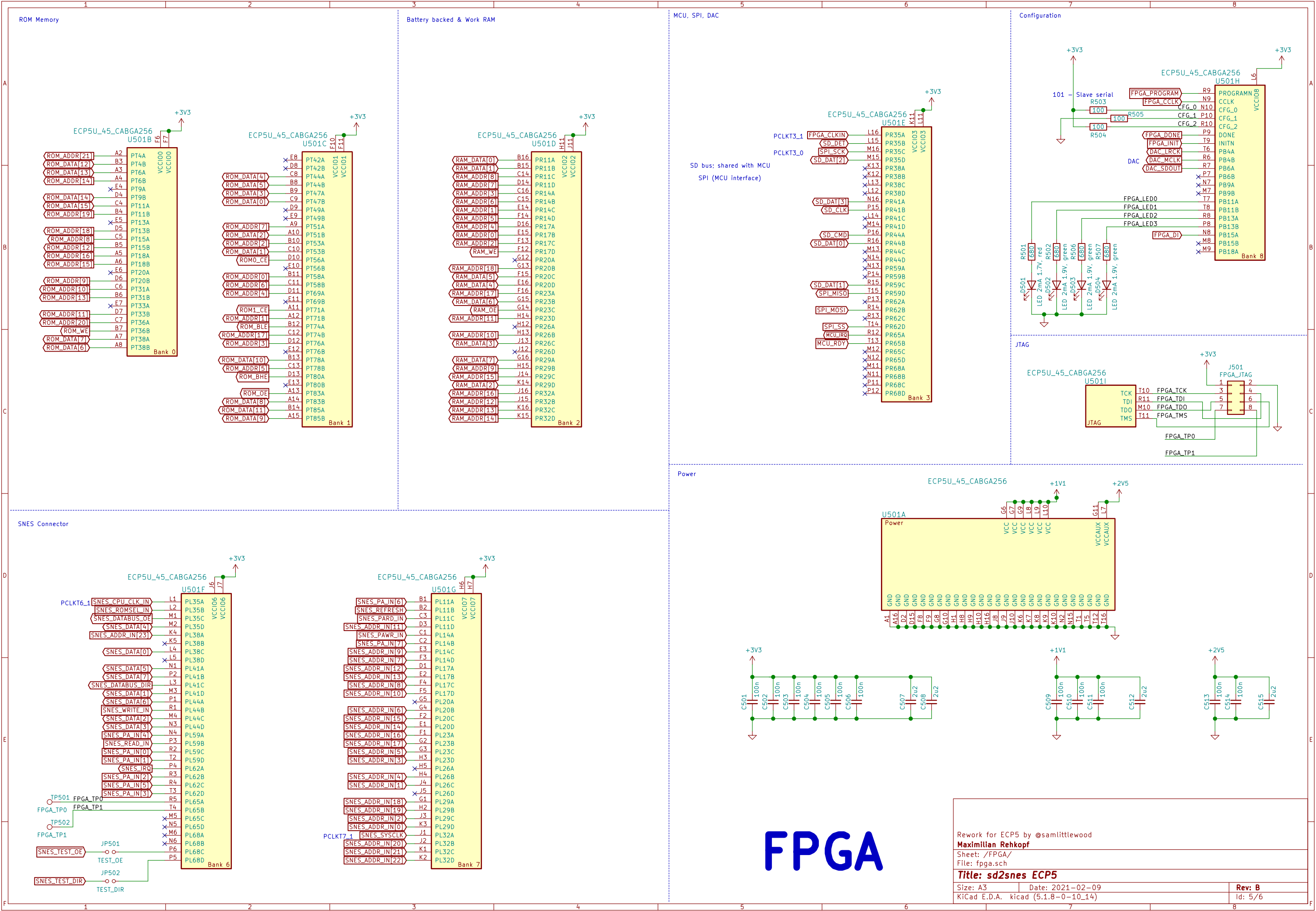
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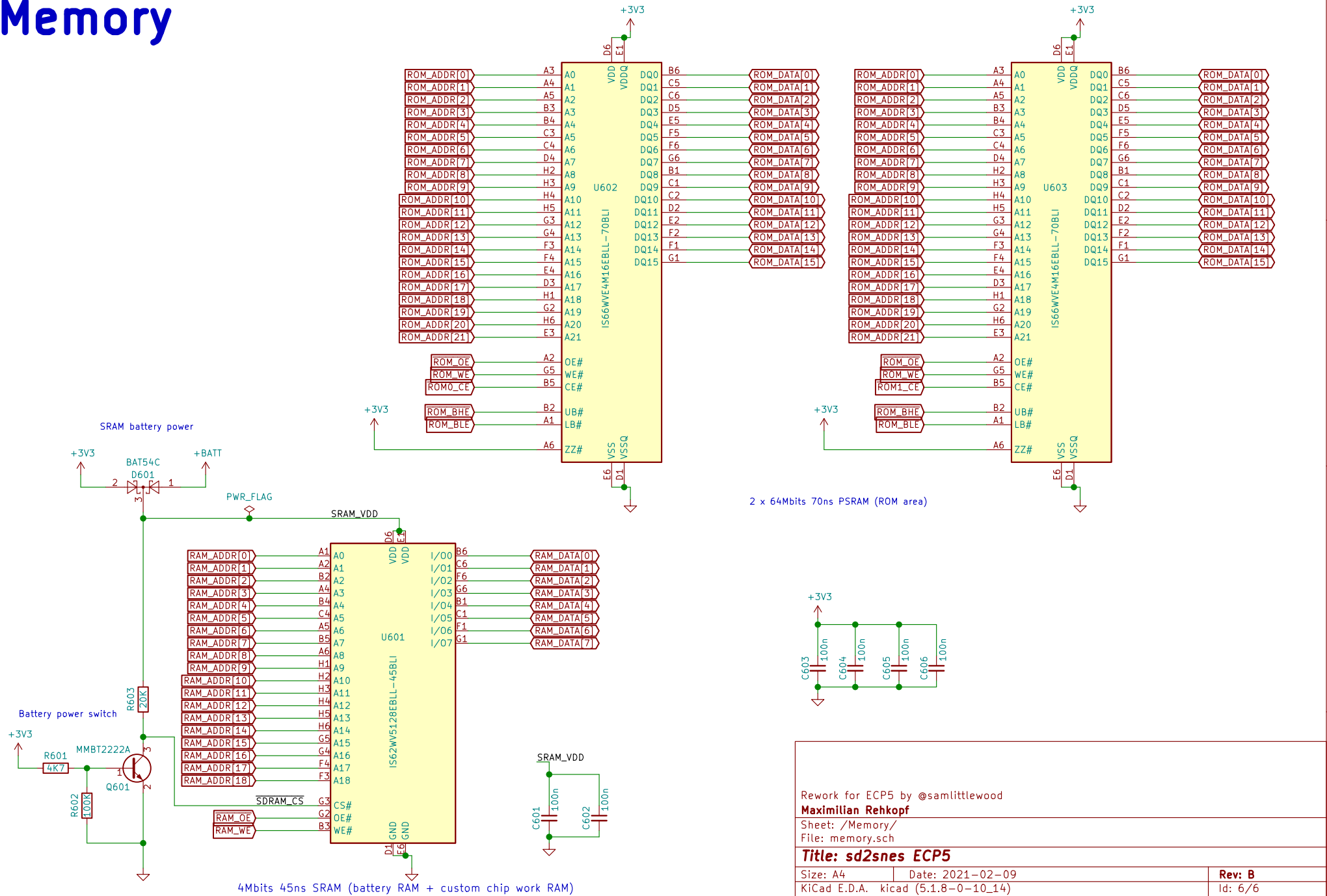
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Rev: B

Id: 4/6



Memory



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Sheet: /Memory/

File: memory.sch

Title: sd2snes ECP5

Size: A4 Date: 2021-02-09

KiCad E.D.A. kicad (5.1.8-0-10_14)

Rev: B

Id: 6/6