	LFUCache
ECE419 M1 Report	FIFOCache
LCLII3 WII 100port	LockManager
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Zhihao Sheng	2 Performance Evaluation
28 January 2018	3 Test Cases
4 D	o rest cases
1 Design and Decisions	3.1 CacheTests
Architecture Refer to Figure 1 in the Appendices for an architecture diagram.	3.2 CommModTests
	3.3 ConnectionTest
KVClient	3.4 InteractionTest
KVStore	3.5 KVDBTests
KVServer	3.6 LockManagerTest
CommMod	3.7 SocketTest
TLVMessage	3.8 StoreServerTests

LRUCache

3.9 TLVMessageTest

4 Appendices

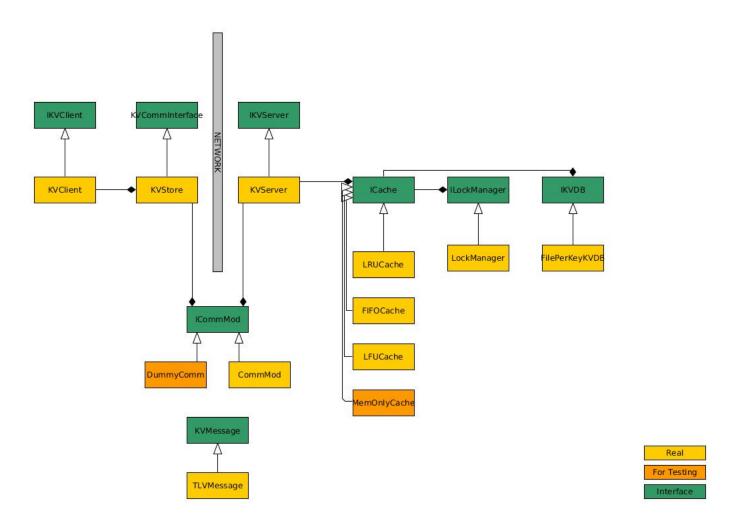


Figure 1: Architecture Diagram