

BGS22WL10

DPDT (Dual-Pole / Double-Throw) Differential RF Switch

Data Sheet

Revision 1.7 - June 1, 2015

Power Management & Multimarket

Edition June 1, 2015

Published by Infineon Technologies AG 81726 Munich, Germany

©2011 Infineon Technologies AG All Rights Reserved.

Legal Disclaimer

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Infineon Technologies hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party.

Information

For further information on technology, delivery terms and conditions and prices, please contact the nearest Infineon Technologies Office (www.infineon.com).

Warnings

Due to technical requirements, components may contain dangerous substances. For information on the types in question, please contact the nearest Infineon Technologies Office.

Infineon Technologies components may be used in life-support devices or systems only with the express written approval of Infineon Technologies, if a failure of such components can reasonably be expected to cause the failure of that life-support device or system or to affect the safety or effectiveness of that device or system. Life support devices or systems are intended to be implanted in the human body or to support and/or maintain and sustain and/or protect human life. If they fail, it is reasonable to assume that the health of the user or other persons may be endangered.



Revision History

Previous Version: v1.6, May 27, 2014									
Page	Page Subjects (major changes since last revision)								
9	Updated Maximum Ratings (Table 3)								

Trademarks of Infineon Technologies AG

 $AURIX^{TM}, \quad BlueMoon^{TM}, \quad COMNEON^{TM}, \quad C166^{TM}, \quad CROSSAVE^{TM}, \quad CanPAK^{TM}, \quad CIPOS^{TM}, \quad CoolMOS^{TM}, \quad CoolSET^{TM}, \quad CORECONTROL^{TM}, \quad DAVE^{TM}, \quad EasyPIM^{TM}, \quad EconoBRIDGE^{TM}, \quad EconoDUAL^{TM}, \quad EconoPACK^{TM}, \quad EconoPIM^{TM}, \quad EiceDRIVER^{TM}, \quad EUPEC^{TM}, \quad FCOS^{TM}, \quad HITFET^{TM}, \quad HybridPACK^{TM}, \quad ISOFACE^{TM}, \quad I^2RF^{TM}, \quad IsoPACK^{TM}, \quad MIPAQ^{TM}, \quad ModSTACK^{TM}, \quad my-d^{TM}, \quad NovalithIC^{TM}, \quad OmniTune^{TM}, \quad OptiMOS^{TM}, \quad ORIGA^{TM}, \quad PROFET^{TM}, \quad PRO-SIL^{TM}, \quad PRIMARION^{TM}, \quad PrimePACK^{TM}, \quad RASIC^{TM}, \quad ReverSave^{TM}, \quad SatRIC^{TM}, \quad SIEGET^{TM}, \quad SINDRION^{TM}, \quad SMARTi^{TM}, \quad SmartLEWIS^{TM}, \quad TEMPFET^{TM}, \quad thinQ!^{TM}, \quad TriCore^{TM}, \quad TRENCHSTOP^{TM}, \quad X-GOLD^{TM}, \quad XMM^{TM}, \quad X-PMU^{TM}, \quad XPOSYS^{TM}.$

Other Trademarks

Advance Design SystemTM (ADS) of Agilent Technologies, AMBATM, ARMTM, MULTI-ICETM, PRIMECELLTM, REALVIEWTM, THUMBTM of ARM Limited, UK. AUTOSARTM is licensed by AUTOSAR development partnership. BluetoothTM of Bluetooth SIG Inc. CAT-iqTM of DECT Forum. COLOSSUSTM, FirstGPSTM of Trimble Navigation Ltd. EMVTM of EMVCo, LLC (Visa Holdings Inc.). EPCOSTM of Epcos AG. FLEXGOTM of Microsoft Corporation. FlexRayTM is licensed by FlexRay Consortium. HYPERTERMINALTM of Hilgraeve Incorporated. IECTM of Commission Electrotechnique Internationale. IrDATM of Infrared Data Association Corporation. ISOTM of INTERNATIONAL ORGANIZATION FOR STANDARDIZATION. MATLABTM of MathWorks, Inc. MAXIMTM of Maxim Integrated Products, Inc. MICROTECTM, NUCLEUSTM of Mentor Graphics Corporation. MifareTM of NXP. MIPITM of MIPI Alliance, Inc. MIPSTM of MIPS Technologies, Inc., USA. muRataTM of MURATA MANUFACTURING CO., MICROWAVE OFFICETM (MWO) of Applied Wave Research Inc., OmniVisionTM of OmniVision Technologies, Inc. OpenwaveTM Openwave Systems Inc. RED HATTM Red Hat, Inc. RFMDTM RF Micro Devices, Inc. SIRIUSTM of Sirius Sattelite Radio Inc. SOLARISTM of Sun Microsystems, Inc. SPANSIONTM of Spansion LLC Ltd. SymbianTM of Symbian Software Limited. TAIYO YUDENTM of Taiyo Yuden Co. TEAKLITETM of CEVA, Inc. TEKTRONIXTM of Tektronix Inc. TOKOTM of TOKO KABUSHIKI KAISHA TA. UNIXTM of X/Open Company Limited. VERILOGTM, PALLADIUMTM of Cadence Design Systems, Inc. VLYNQTM of Texas Instruments Incorporated. VXWORKSTM, WIND RIVERTM of WIND RIVER SYSTEMS, INC. ZETEXTM of Diodes Zetex Limited.

Last Trademarks Update 2010-06-09

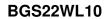
3





Contents

1	Features	7
2	Product Description	7
3	Maximum Ratings	9
4	Operation Ranges	9
5	RF Characteristics	10
6	Package Outline and Pin Configuration	11





List of Figures

1	BGS22WL10 block diagram	8
	Marking Layout (top view)	
	TSLP-10-1 Package Outline (top, side and bottom view)	
4	Footprint TSLP-10-1	13
5	Tape and Reel Dimensions for TSLP-10-1	13





List of Tables

1	Ordering Information
2	Truth Table
3	Maximum Ratings
4	Operation Ranges
5	RF Input Power
6	RF Characteristics
7	Pin Configuration
8	Mechanical Data



BGS22WL10 DPDT (Dual-Pole / Double-Throw) Differential RF Switch

1 Features

- DPDT (Dual-Pole / Double-Throw) differential RF switch
- Frequency range: 0.1 3 GHz
- High signal power up to 30 dBm
- Supply voltage 2.3 3.6 V
- Small package size of 1.55 x 1.15 mm²
- No decoupling capacitors required if no DC applied
- · RoHS compliant package





2 Product Description

The BGS22WL10 is a DPDT (Dual-Pole / Double Throw) RF switch which combines two differential signals into one differential output or splits one differential signal into two separate differential lines. The parallel paths of the switch are controlled simultaneously through the same signals. The switch is designed to operate in battery powered applications with a supply voltage range of 2.4 - 3.6 V. The highly symmetric design ensures best phase- and amplitude accuracy.

A typical application is to combine two Rx paths in a mobile cellular device after the Rx filters or duplexers into one input to the tranceiver IC. The IC can also be used for a wide variety of applications switching balanced signals in a frequency range of 0.1 - 3~GHz. The RF switch is packaged in a standard RoHS compliant TSLP-10-1 package with a small outline of only $1.55 \times 1.15~mm^2$.

No decoupling capacitors are required in typical applications as long as no DC is applied to any RF port.

Table 1: Ordering Information

Туре	Package	Marking
BGS22WL10	TSLP-10-1	22W

Data Sheet 7 Revision 1.7 - June 1, 2015



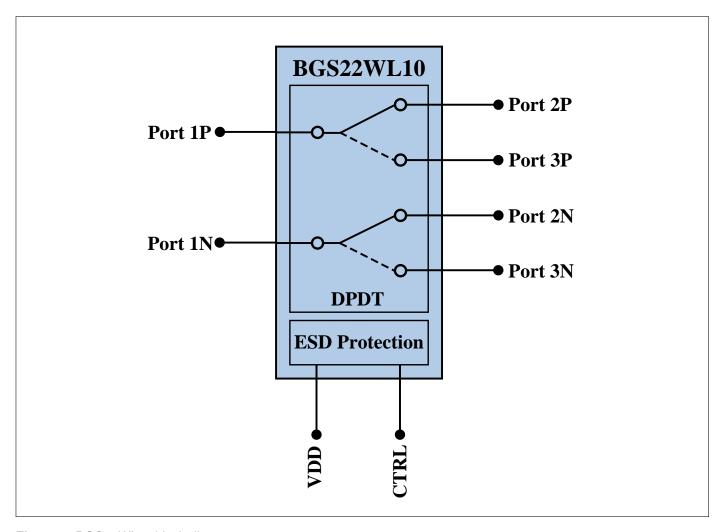


Figure 1: BGS22WL10 block diagram

Table 2: Truth Table

Pin No.	CRTL
Port 1 to Port 2	0
Port 1 to Port 3	1

8



3 Maximum Ratings

Table 3: Maximum Ratings at $T_A = 25$ °C, unless otherwise specified

Parameter	Symbol		Value	S	Unit	Note / Test Condition	
		Min.	Тур.	Max.		rest condition	
Supply voltage	V_{DD}	-0.5	-	5.5	V	_	
Control voltage	V _{Ctrl}	-0.3	-	3.6	V	_	
Storage temperature range	T _{STG}	-55	-	150	°C	_	
RF input power	P _{In}	_	-	+32	dBm	_	
ESD capability Human Body Model	V _{ESD_HBM}	1000	-	_	V	_	
Junction temperature	T_j	_	-	125	°C	_	
Thermal resistance junction - soldering point	R _{thJS}	-	T -	43	K/W	_	

Attention:

Stresses above the max. values listed here may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect device reliability. Maximum ratings are absolute ratings; exceeding only one of these values may cause irreversible damage to the integrated circuit.

4 Operation Ranges

Table 4: Operation Ranges

Parameter	Symbol	Values			Unit	Note / Test Condition
		Min.	Тур.	Max.		
Supply Voltage	V_{DD}	2.3	_	3.6	V	_
Control Voltage Low	V _{Ctrl_L}	-0.3	_	0.4	V	_
Control Voltage High	V _{Ctrl_H}	1.2	_	V _{DD}	V	_
RF frequency	f _{RF}	0.1		-	GHz	_
Ambient Temperature	T_A	-40	25	85	°C	_

Table 5: RF Input Power

Parameter	Symbol	Values			Unit	Note / Test Condition
		Min.	Тур.	Max.		
RF Input Power (50Ω)	P _{In}	_	_	30	dBm	_

9



5 RF Characteristics

Table 6: RF Characteristics:

Terminating port impedance: $Z_0 = 50 \Omega$

Measurement conditions unless otherwise specified:

 $T_A = 25 \,^{\circ}\text{C}$, $P_{IN} = 0 \, dBm$, Supply Voltage $V_{DD} = 2.3 - 3.6 \, V$

Parameter	eter Symbol Values			Unit	Note / Test Condition					
		Min.	Тур.	Тур. Мах.						
Insertion Loss - Typical Conditions: $T_A = 25 ^{\circ}\text{C}$, $V_{DD} = 3 V$										
		0.30	0.34	0.39	dB	824 - 915 MHz				
	IL	0.37	0.40	0.46	dB	1710 - 1910 MHz				
		0.43	0.48	0.59	dB	2170 - 2690 MHz				
Insertion Loss -	Min/Max Co	nditions:	$T_A = -40$. +85 °C,	$V_{DD} = 2.$	3 3.6 <i>V</i>				
		0.23	0.34	0.54	dB	824 - 915 MHz				
	IL	0.27	0.40	0.58	dB	1710 - 1910 MHz				
		0.31	0.48	0.75	dB	2170 - 2690 MHz				
Return Loss - Mi	n/Max Cond	ditions: T_A	= -40 +	85 °C, <i>V</i> _D	$_{D} = 2.3$.	3.6 <i>V</i>				
		25	27	35	dB	824 - 915 MHz				
	RL	22	26	30	dB	1710 - 1910 MHz				
		16	22	25	dB	2170 - 2690 MHz				
Isolation ¹	l	1								
		32	38	44	dB	824 - 915 MHz				
	ISO	25	30	37	dB	1710 - 1910 MHz				
		22	28	33	dB	2170 - 2690 MHz				
P0.1 dB Compre	ssion Point			'	'					
	$P_{0.1dB}$	33	34	35	dBm	1000 MHz				
Harmonic Gener	ation up to	12.75 GH	Z	'	'					
Any path	P _{Harm}	80	85	95	dBc	27 dBm, 50Ω, 25 °C, 25 % duty cy-				
						cle				
Intermodulation	Distortion	in Rx Ban	d							
IMD2_Low ²	P_{IMD2_L}	-125	-115	-105	dBm	Tx = 15 dBm, Interferer = -15 dBm				
IMD3	P_{IMD3}	-125	-115	-110	dBm	Tx = 10 dBm, Interferer = -15 dBm				
IMD2_High	P_{IMD2_H}	-125	-115	-110	dBm	Tx = 10 dBm, Interferer = -15 dBm				
Switching Time	and Curren	t Consum	ption	'						
RF Rise Time	t _{10%-90%}	_	250	 	ns	10% - 90% of RF Signal ($V_{DD} = 3 V$)				
Ctrl to RF Time	t _{Ctrl} _RF	_	600	_	ns	50% of Ctrl Signal to 90% of RF				
						Signal $(V_{DD} = 3 V)$				
Supply Current	I _{DD}	70	120	190	μ A	Current at T_A = 25 °C				
Phase Error	<u>I</u>	1	1	1	1	1				
Between any	Ph _{Err}	0.3	0.5	0.7	Deg.	27 dBm, 50Ω, 25 °C, 25 % duty cy-				
			1	1	_	1				

Note: All electrical characteristics are measured with all RF ports terminated in 50 Ω .

¹ Isolation values are not dependent on supply voltage and temperature as long as operated in the specified operation range.

² With external shunt inductor.



6 Package Outline and Pin Configuration

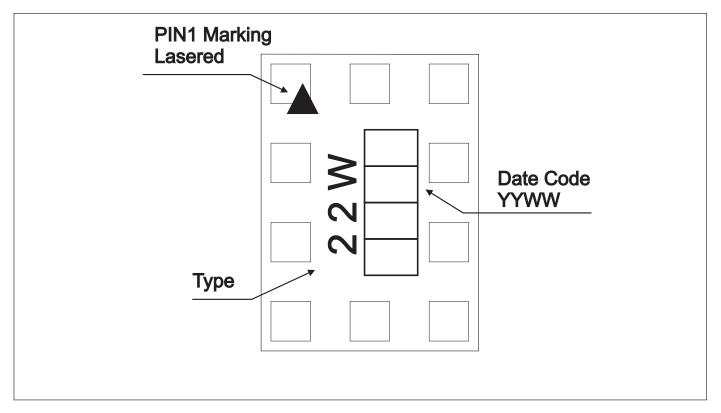


Figure 2: Marking Layout (top view)

Table 7: Pin Configuration

Pin	Name	Pin	Buffer	Function
No.		Туре	Type	
1	Port 3P			Differential Output P of Port 3
2	GND			Ground Pin
3	GND			Ground Pin
4	Port 2N			Differential Output N of Port 2
5	Port 2P			Differential Output P of Port 2
6	CTRL			Control Voltage
7	Port 1P			Differential Input P of Port 1
8	Port 1N			Differential Input N of Port 1
9	VDD			Supply Voltage
10	Port 3N			Differential Output N of Port 3



Table 8: Mechanical Data

Parameter	Symbol	Value	Unit
Package X-Dimension	X	1.55 ± 0.05	mm
Package Y-Dimension	Υ	1.15 ± 0.05	mm
Package Area	Α	1.783	mm ²
Package Height	Н	0.39 +0.01/-0.03	mm

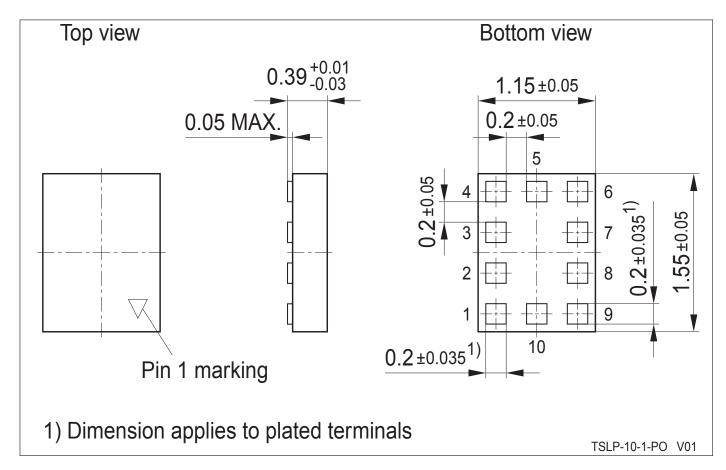


Figure 3: TSLP-10-1 Package Outline (top, side and bottom view)



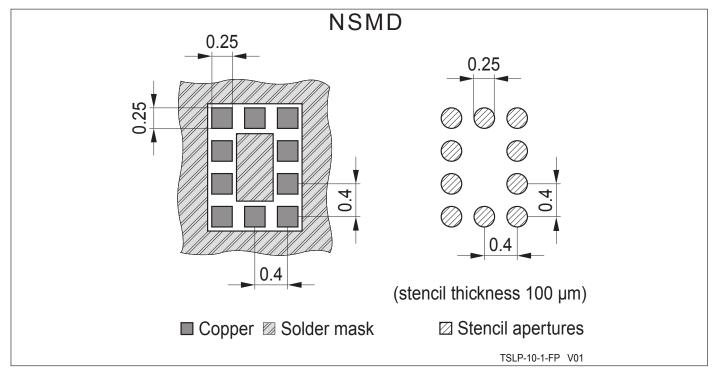


Figure 4: Footprint TSLP-10-1

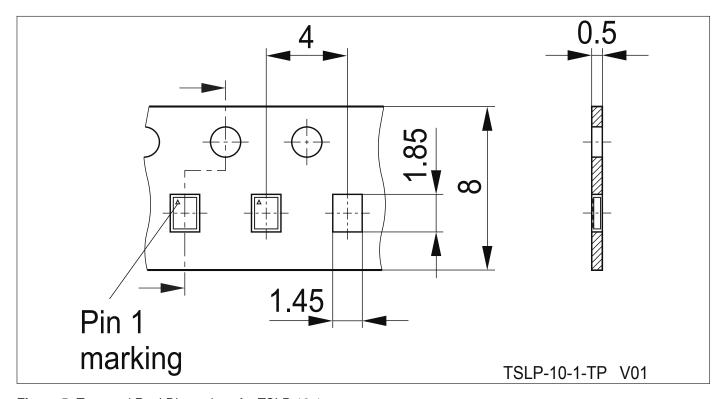


Figure 5: Tape and Reel Dimensions for TSLP-10-1

www.infineon.com