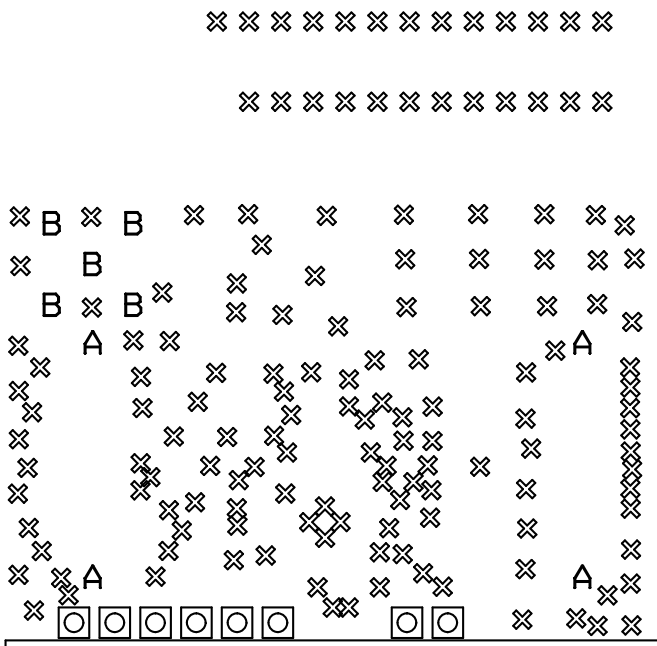
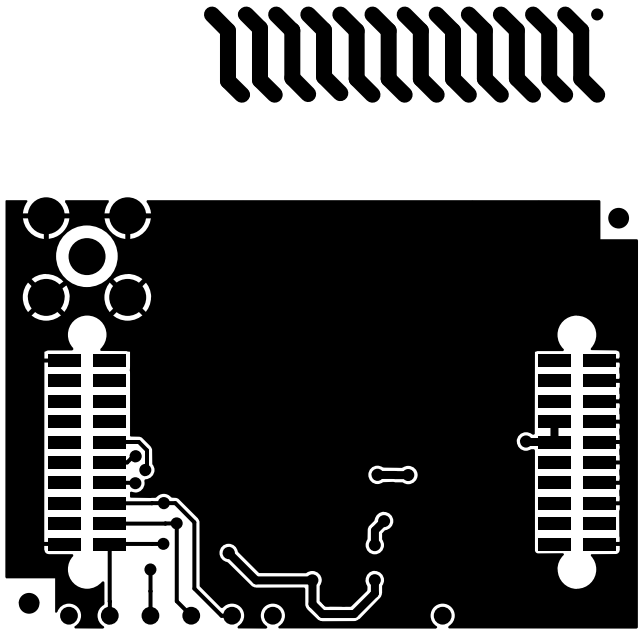
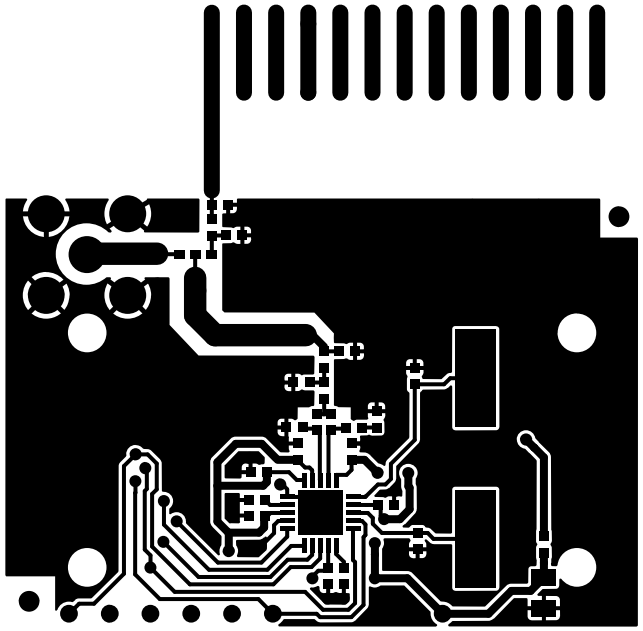


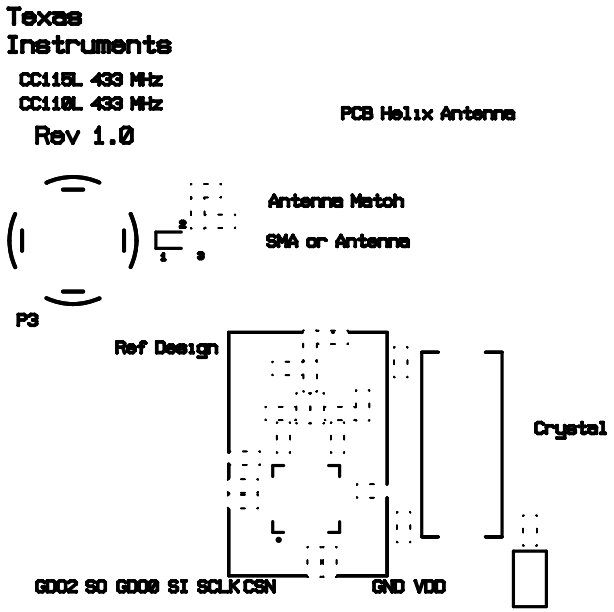
Through Holes						
Symbol	Diameter (in)	Tolerance (in)	Plated	Punched	Hole Name	Quantity
⊠	0.0150		Yes	No	Rnd 15	151
⊡	0.0200		Yes	No	Rnd 20 +Tol 3 -Tol -3	8
A	0.0550		Yes	No	Rnd 55	4
B	0.0670	+0.0030 / -0.0000	Yes	No	Rnd 67 +Tol 3 -Tol 0	5

EtchLayerBottom.gdo (Board layer 2) Scale=2.12 Mon May 23 14:32:25 2011

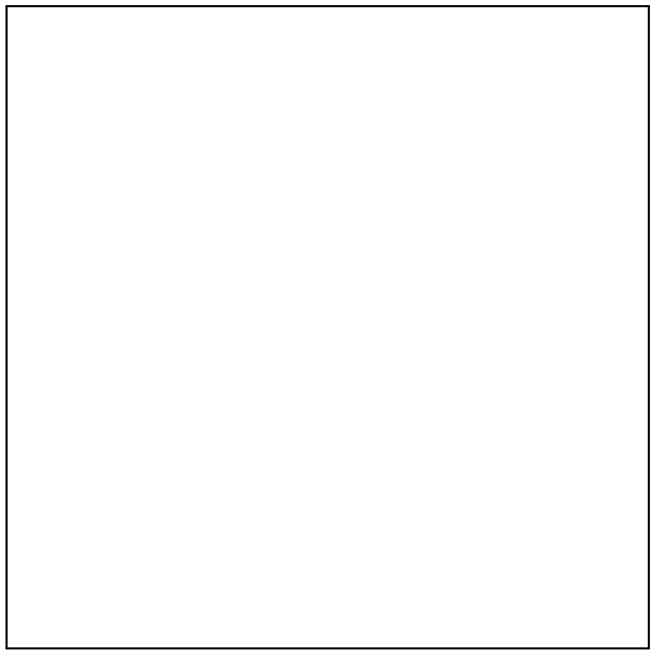


EtchLayerTop.gdo (Board layer 3) Scale=2.12 Mon May 23 14:32:25 2011

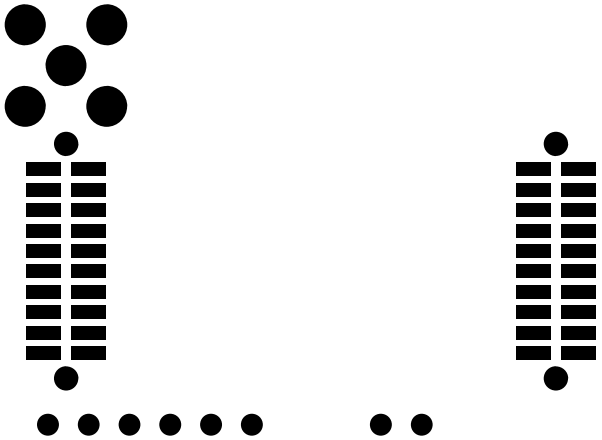



Texas
Instruments
CC115L 433 MHz
CC110L 433 MHz
Rev 1.0
PCB Helix Antenna
Antenna Match
SMA or Antenna
P3
Ref Design
Crystal
GD02 SO GD00 SI SCLK CSN
GND VDD

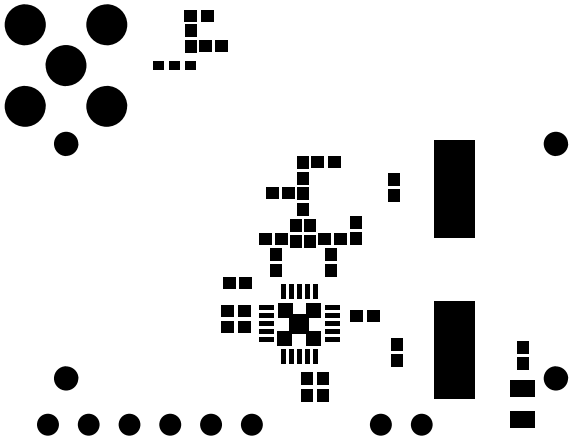
outline.gdo (Board layer 6) Scale=2.12 Mon May 23 14:32:25 2011

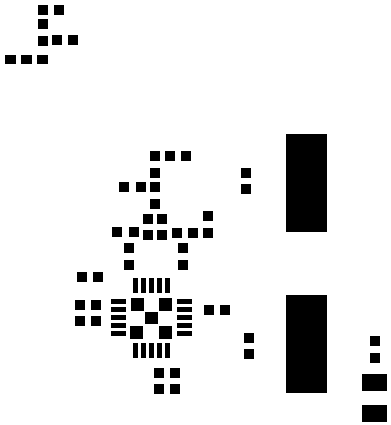


SoldermaskBottom.gdo (Board layer 7) Scale=2.12 Mon May 23 14:32:25 2011



SoldermaskTop.gdo (Board layer 8) Scale=2.12 Mon May 23 14:32:25 2011



SolderPasteBottom.gdo (Board layer 9) Scale=2.12 Mon May 23 14:32:25 2011

SolderPasteTop.gdo (Board layer 10) Scale=2.12 Mon May 23 14:32:25 2011