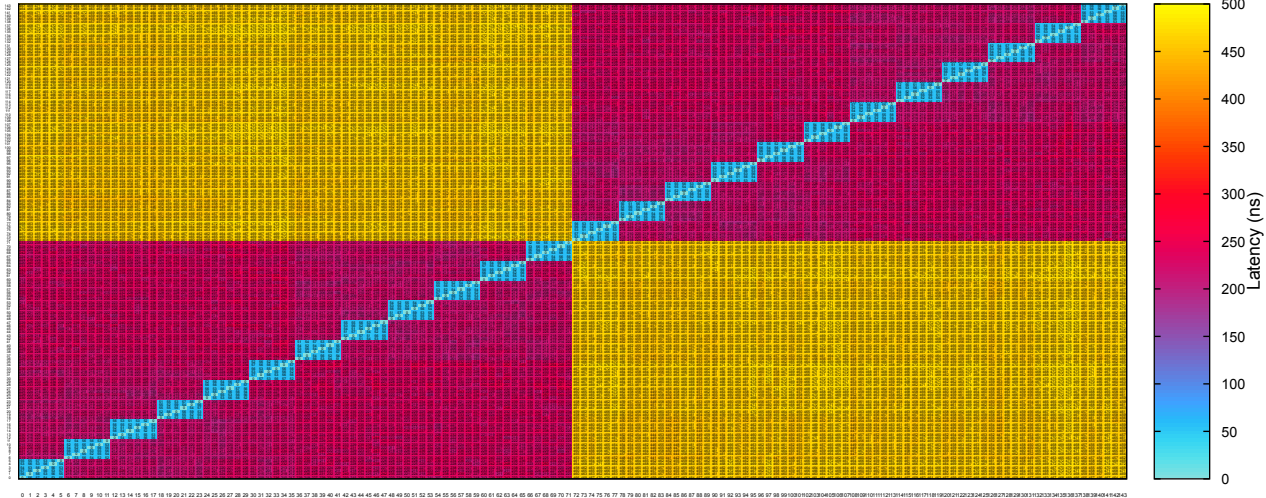


Inter-core one-way data latency between CPU cores

CPU



CPU