

MFRC522::PICC_DumpToSerial

MFRC522Extended::PICC
_DumpToSerial

MFRC522::PICC_DumpMifare
UltralightToSerial

```
graph LR; A[MFRC522::PICC_DumpToSerial] --> C[MFRC522::PICC_DumpMifare UltralightToSerial]; B[MFRC522Extended::PICC_DumpToSerial] --> C;
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The diagram illustrates a function mapping. On the left, two white rectangular boxes with black borders represent source functions. The top box contains the text 'MFRC522::PICC_DumpToSerial'. The bottom box contains the text 'MFRC522Extended::PICC_DumpToSerial'. On the right, a gray rectangular box with a black border represents the target function, containing the text 'MFRC522::PICC_DumpMifare UltralightToSerial'. Two blue arrows point from the right side of each source box to the left side of the target box, indicating that both source functions are mapped to the target function.