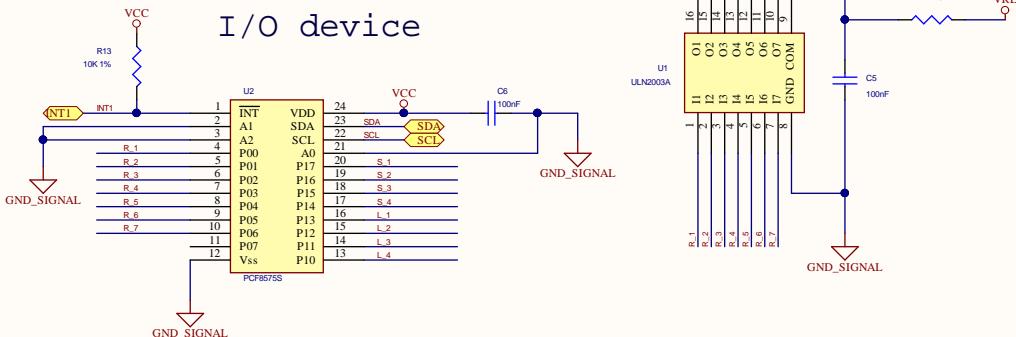
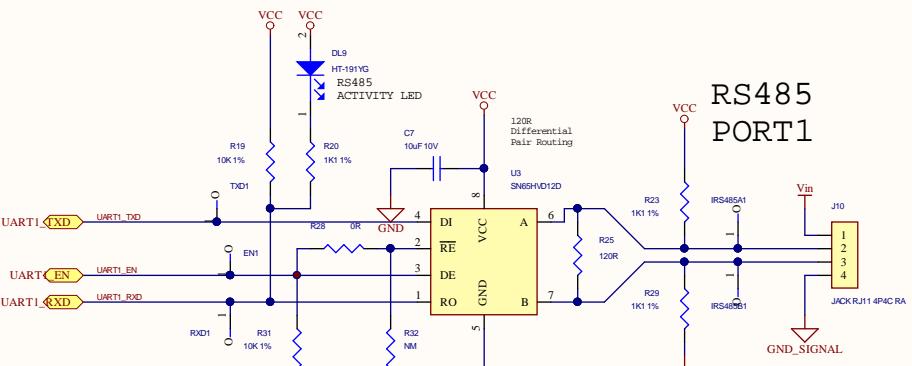


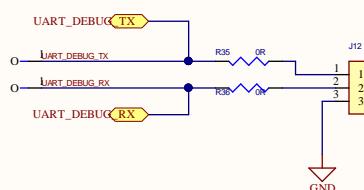
DIGITAL OUTPUT



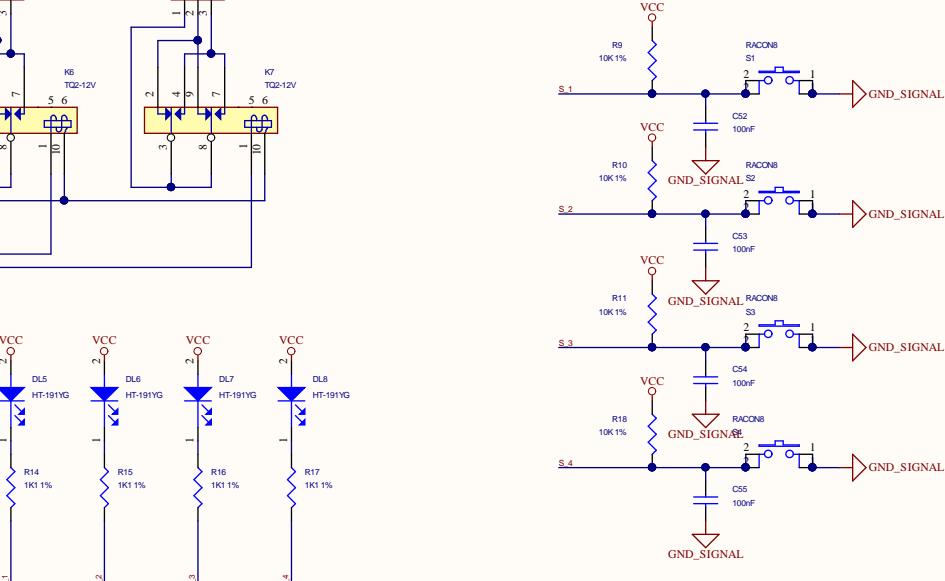
I/O device



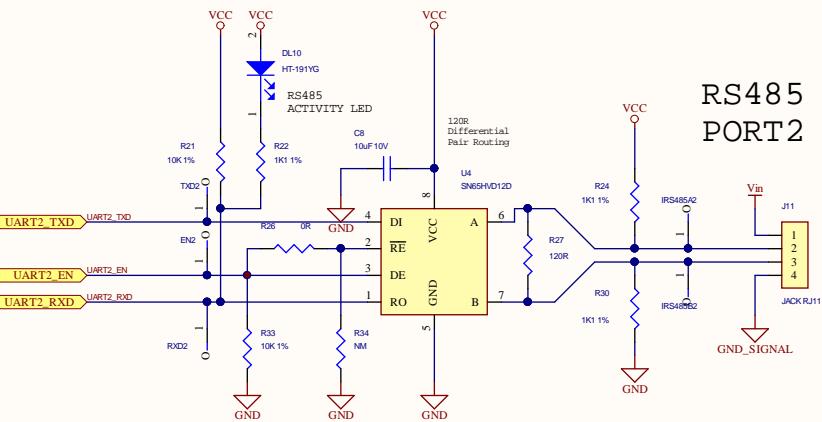
DEBUG TTL

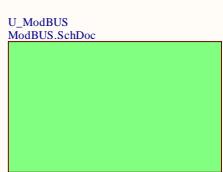
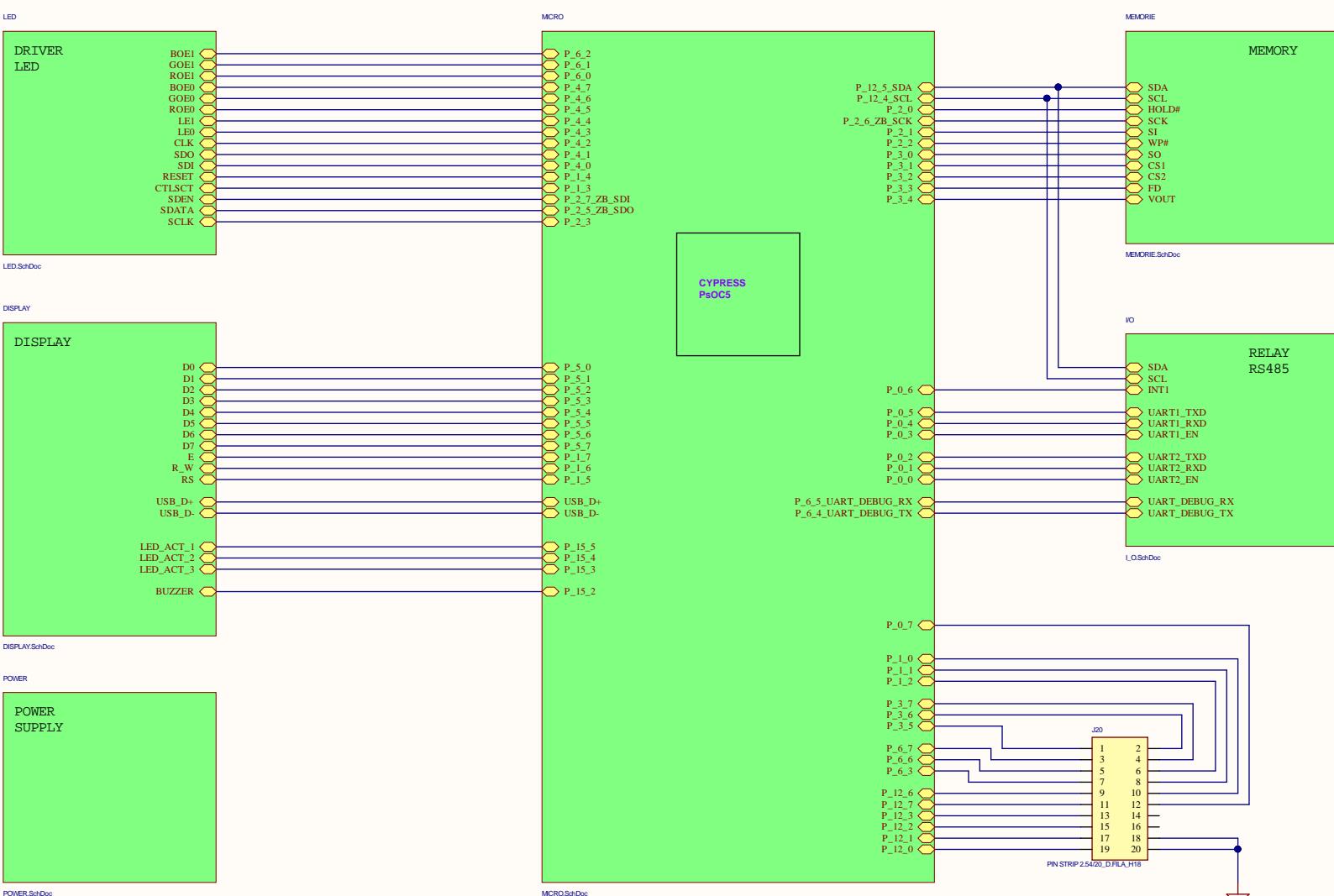


DIGITAL INPUT BUTTON

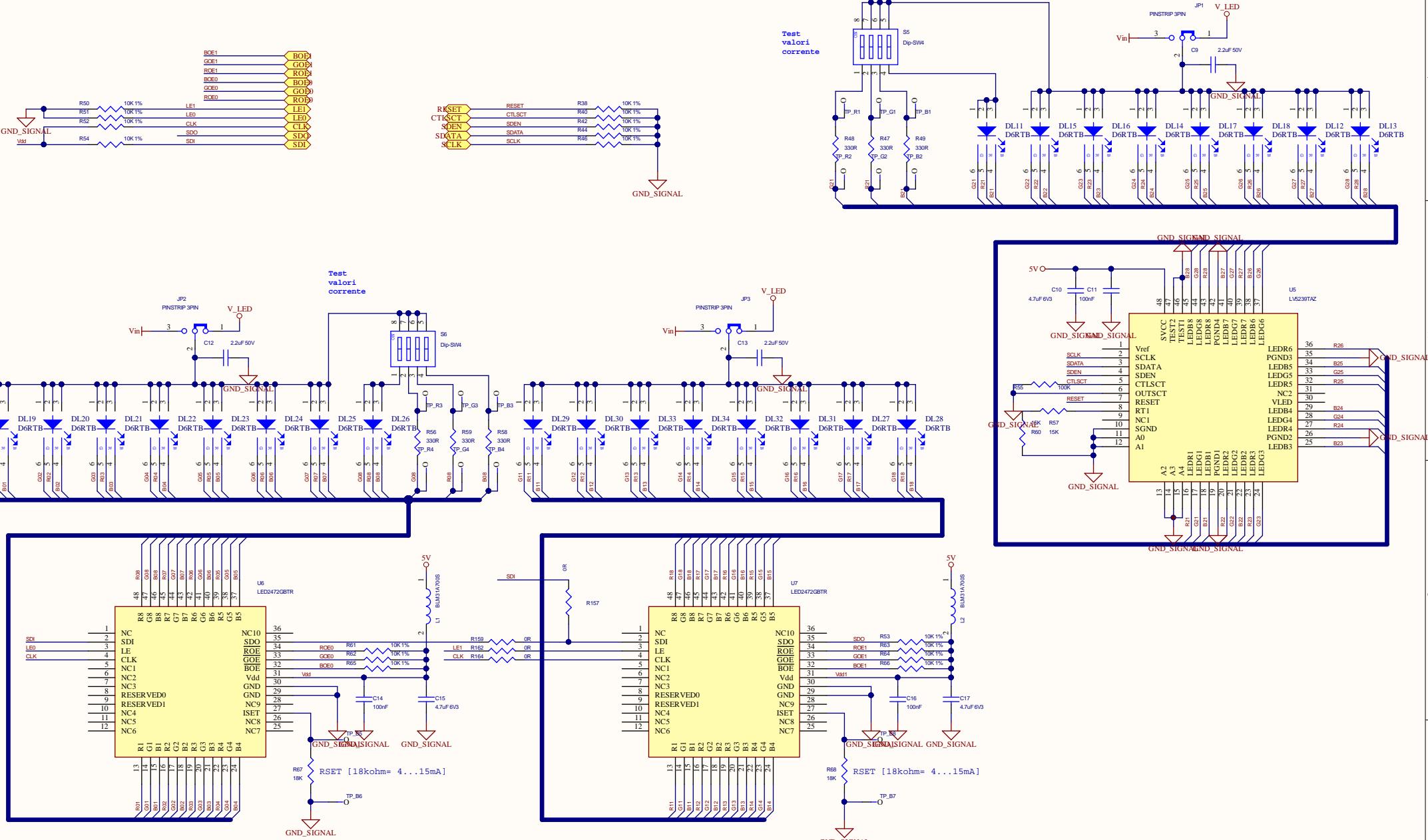


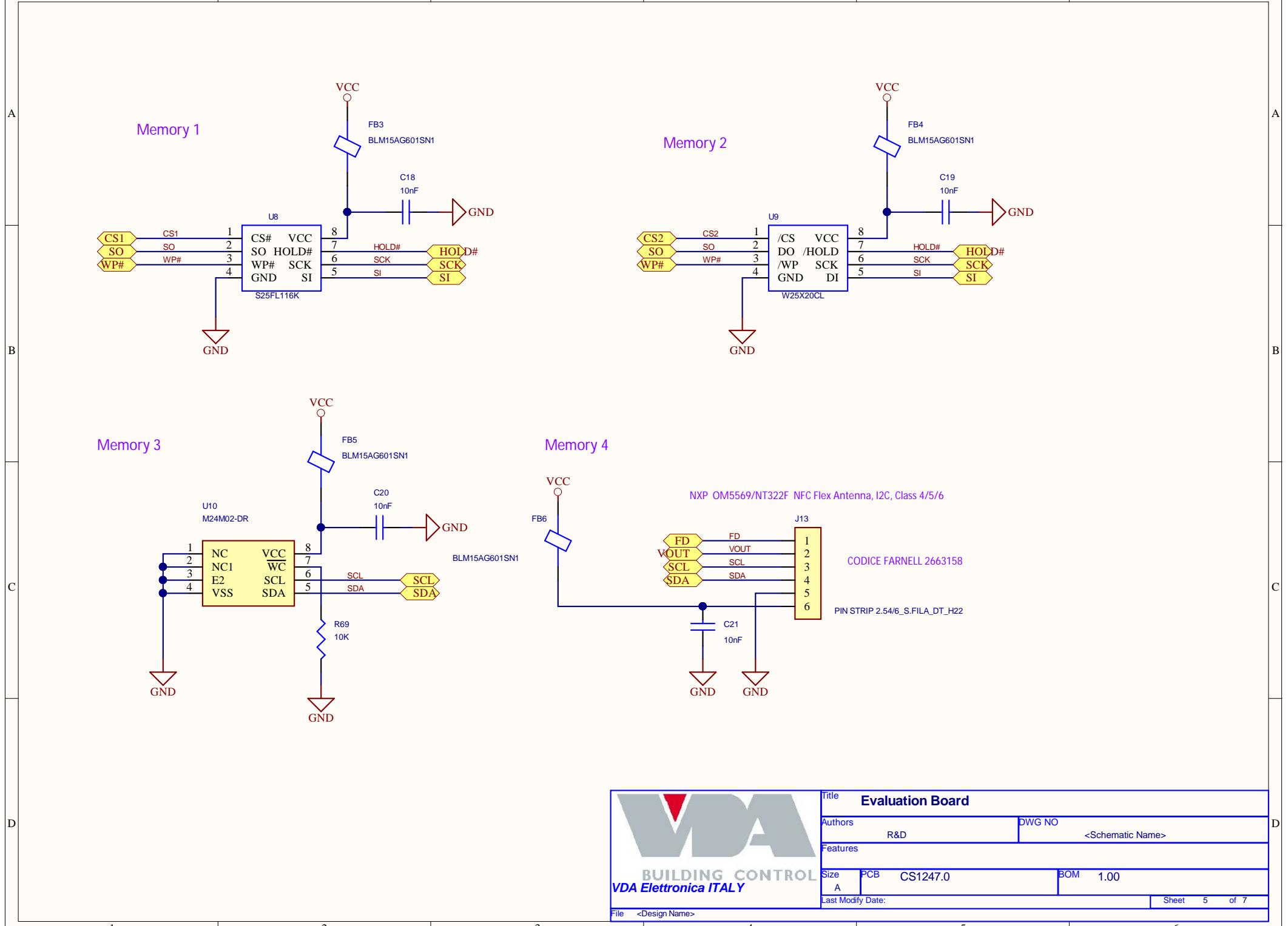
RS485
PORT2



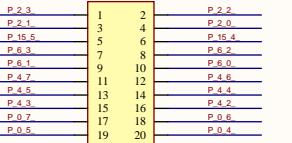
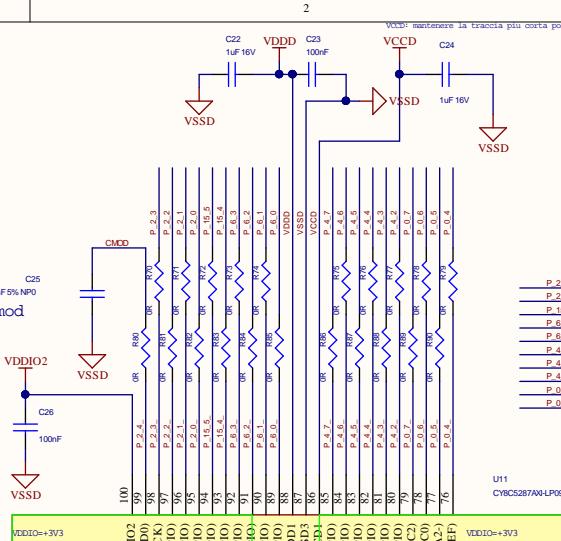
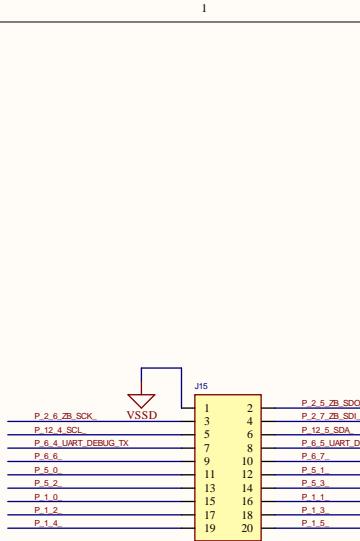


Evaluation Board		
Authors	DWG NO <Schematic Name>	
Features		
Size	PCB CS1249.0	BOM 1.00
LastModify Date:	Sheet 1 of 7	
File <Design Name>		

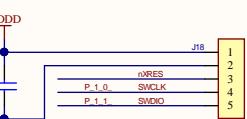




A



PROG
CONNECTOR



PIN STRIP 2.54/20_D.FL.A_H9

P.1.6	P.1.6
P.1.7	P.1.7
P.12.6	P.12.6
P.12.7	P.12.7
P.5.4	P.5.4
P.5.5	P.5.5
P.5.6	P.5.6
P.5.7	P.5.7
P.3.0	P.3.0
P.3.1	P.3.1
P.3.2	P.3.2
P.3.3	P.3.3
P.3.4	P.3.4
P.3.5	P.3.5

P.0.3	P.0.3
P.0.2	P.0.2
P.0.1	P.0.1
P.0.0	P.0.0
P.4.1	P.4.1
P.4.0	P.4.0
P.12.3	P.12.3
P.12.2	P.12.2
P.15.3	P.15.3
P.15.2	P.15.2
P.12.1	P.12.1
P.12.0	P.12.0
P.3.7	P.3.7
P.3.6	P.3.6

P.2.3	P.2.3
P.2.2	P.2.2
P.2.1	P.2.1
P.2.0	P.2.0
P.15.5	P.15.5
P.15.4	P.15.4
P.6.3	P.6.3
P.6.2	P.6.2
P.6.1	P.6.1
P.6.0	P.6.0
P.4.7	P.4.7
P.4.6	P.4.6
P.4.5	P.4.5
P.4.4	P.4.4
P.4.3	P.4.3
P.4.2	P.4.2
P.0.7	P.0.7
P.0.6	P.0.6
P.0.5	P.0.5
P.0.4	P.0.4

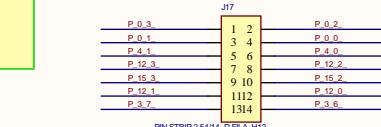
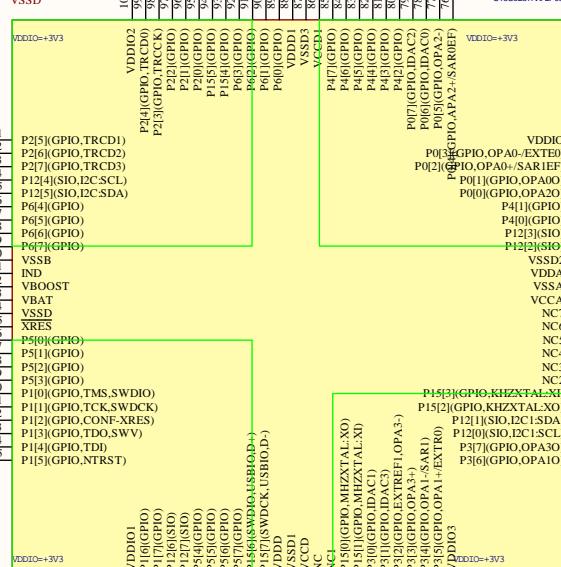
P.2.6.ZB.SDO	P.2.5.ZB.SDO
P.2.6.ZB.SCK	P.2.7.ZB.SDI
P.2.7.ZB.SDI	P.2.6.ZB.SCK
P.12.4.SCL	P.12.4.SCL
P.12.5.SDA	P.12.5.SDA

P.6.4.UART.DEBUG.TX	P.6.5.UART.DEBUG.RX
P.6.6	P.6.7
P.5.0	P.5.1
P.5.1	P.5.2
P.5.2	P.5.3

P.1.0	P.1.1
P.1.1	P.1.2
P.1.2	P.1.3
P.1.3	P.1.4
P.1.4	P.1.5

PIN STRIP 2.54/14_D.FL.A_H12

C

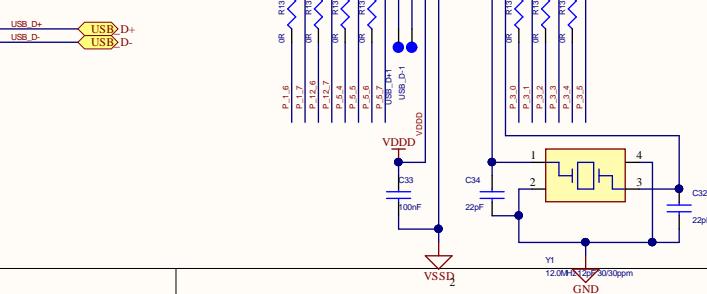


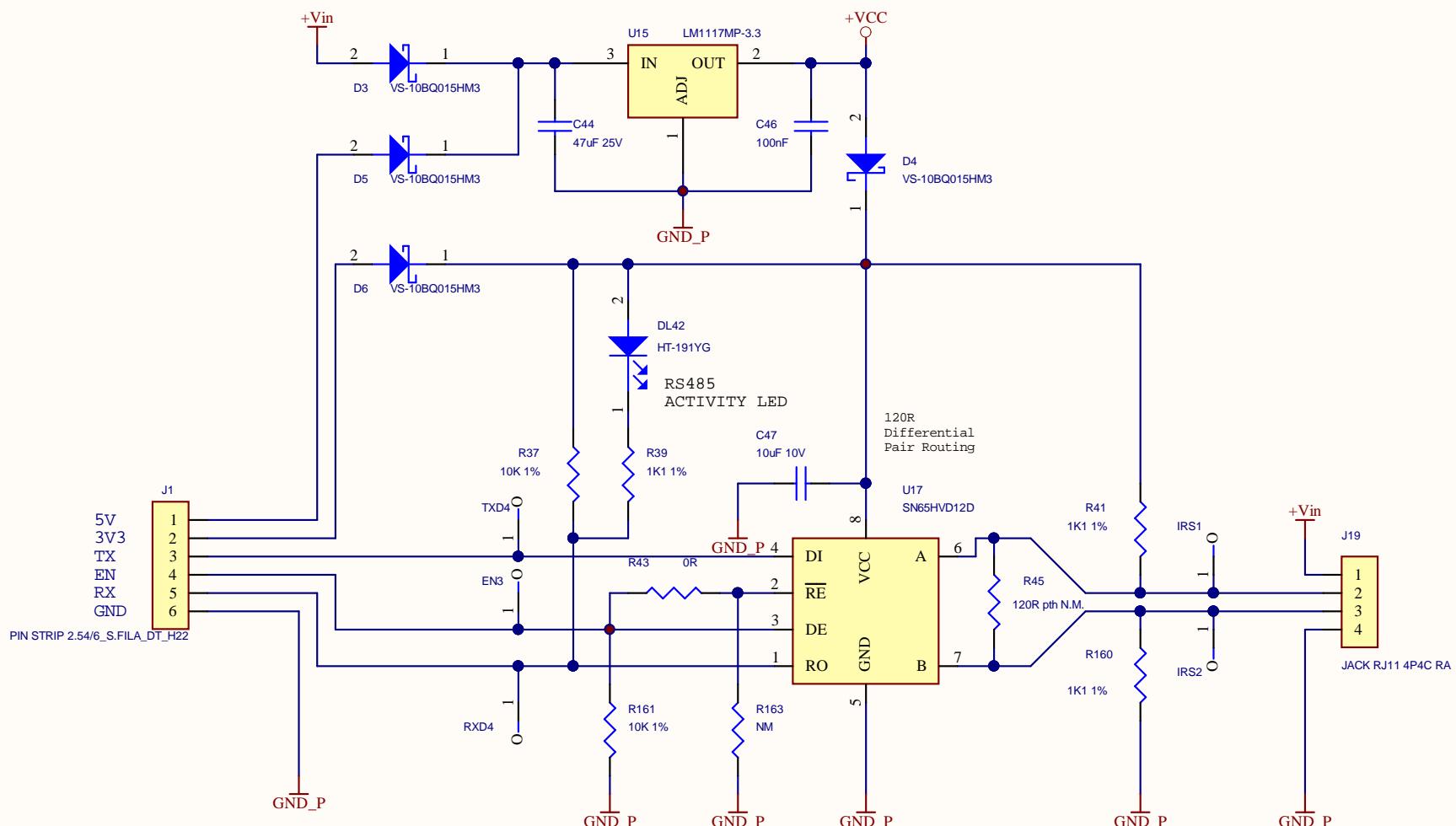
TEST Pads



P.1.6	P.1.6
P.1.7	P.1.7
P.12.6	P.12.6
P.12.7	P.12.7
P.5.4	P.5.4
P.5.5	P.5.5
P.5.6	P.5.6
P.5.7	P.5.7
P.3.0	P.3.0
P.3.1	P.3.1
P.3.2	P.3.2
P.3.3	P.3.3
P.3.4	P.3.4
P.3.5	P.3.5
P.3.6	P.3.6
P.1.0	P.1.0
P.1.1	P.1.1
P.1.2	P.1.2
P.1.3	P.1.3
P.1.4	P.1.4
P.1.5	P.1.5

D

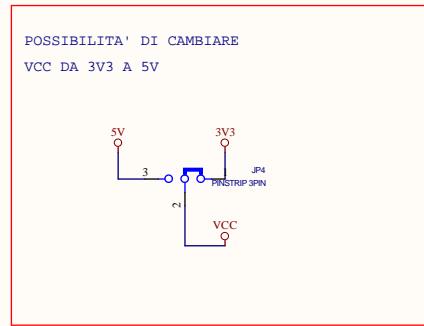
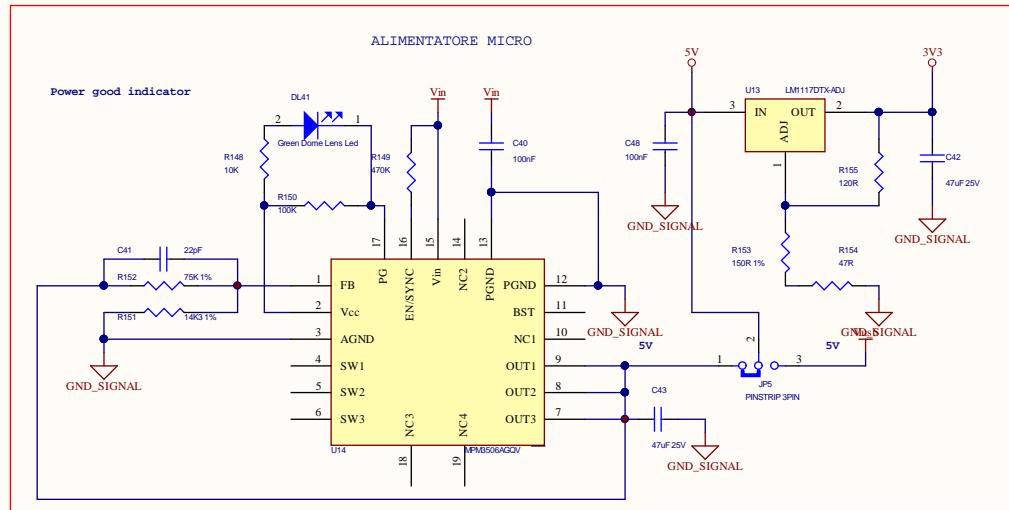
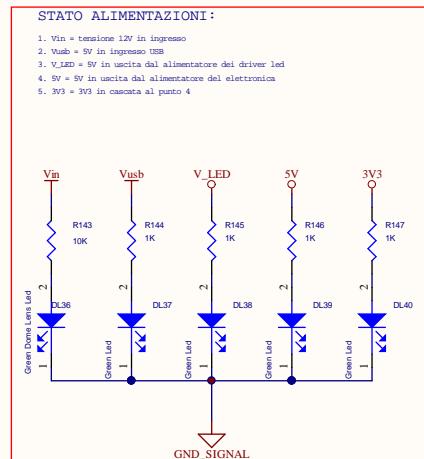
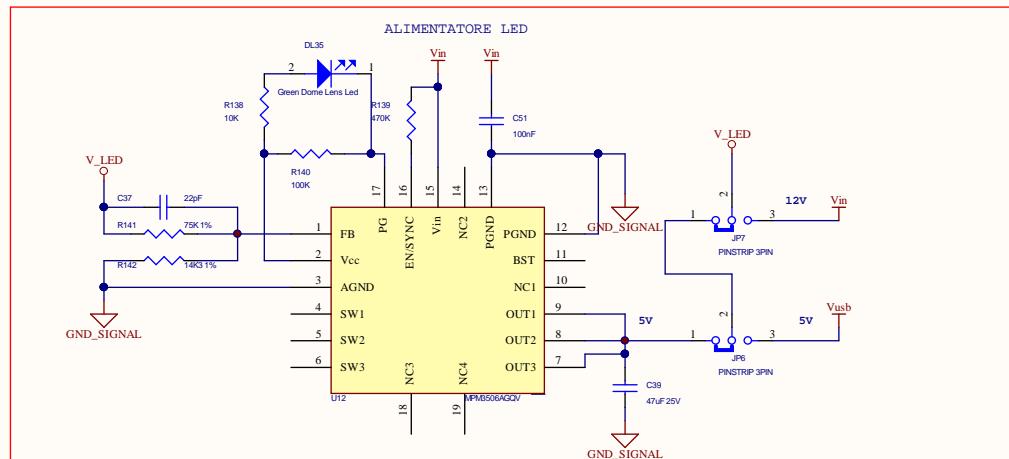
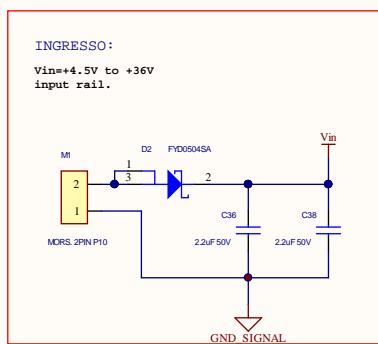




R45 THT 1/4W



Title		Additional ModBUS port	
Authors	R&D	DWG NO	<Schematic Name>
Features			
Size	PCB A	CS1247.0	BOM 1.00
Last Modify Date:		Sheet 5	of 7
File <Design Name>			



NOTE PER PCB DEI MP3506AGQV

Place the
No capacitor
connect
ion is
needed
for the
SW pins,
but a
large
copper
plane.
Avo
is
recommen
ded plac
ing on
pins 4,
5, and
6 for
improve
d heat
sink.

All
"NC"
pins
must
be
left
float
ing.

