

Hardware-Assisted Malware Analysis

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CTD - SBSEG

Topics

- 1 Introduction
 - The Problem
 - The Solution
 - The Challenges
- 2 Hardware-Assisted Solutions
 - The Benefits
 - A Summary
- 3 My Proposal
 - Background
 - Developments
- 4 Conclusions
 - Remarks
 - Future Work
 - Publications

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Malware Threats.



Europe

Malware, described in leaked NSA documents, cripples computers worldwide

Figure: Washington Post: <https://tinyurl.com/ljo7ekm>



Figure: BBC: <https://tinyurl.com/mfoggjhe>

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Malware Analysis.

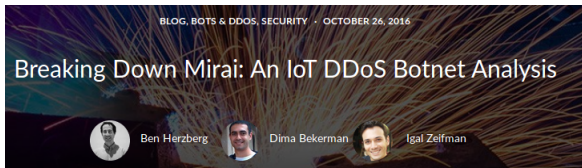


Figure: Imperva: <https://tinyurl.com/zkbsnl2>

Researchers Reverse Engineer Latest CryptoBit Ransomware to Decrypt Files

By GoldSparrow in [Computer Security](#)

User Rating: ★★★★★ (1 votes, average: 5.00 out of 5)



Figure: Enigma: <https://tinyurl.com/kydgvwe>

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The Challenges.

PetrWrap Crypto Ransomware Blocks Security Researchers From Reverse Engineering Code Samples

JP Buntinx 📅 March 16, 2017 News, Security

Figure: Themerkle: <https://tinyurl.com/kasuxcr>

MAY 13, 2017 @ 04:01 AM 95,046 🗨

The Little Black I

How One Simple Trick Just Put Out That Huge Ransomware Fire

Figure: Forbes: <https://tinyurl.com/17ecrex>

How to stop analysis?

Table: Anti-Analysis: Tricks summary. Malware samples may employ multiple techniques to evade distinct analysis procedures.

Technique	Description	Reason	Implementation
Anti Debug	Check if running inside a debugger	Blocks reverse engineering attempts	Fingerprinting
Anti VM	Check if running inside a VM	Analysts use VMs for scalability	Execution Side-effect
Anti Disassembly	Fool disassemblers to generate wrong opcodes	AV signatures may be based on opcodes	Undecidable Constructions

And then...

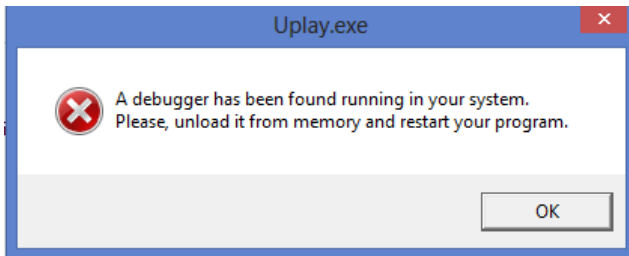


Figure: Commercial solution armored with anti-debug technique.

And then...

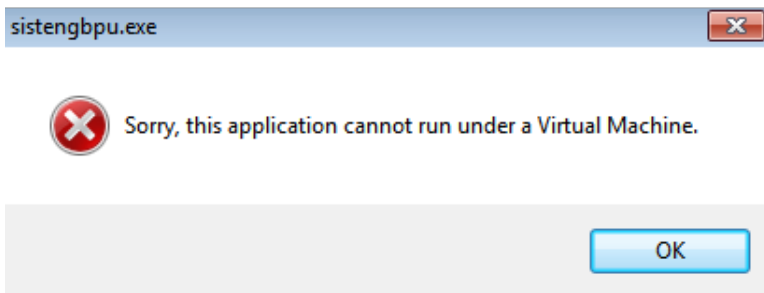


Figure: Real malware impersonating a secure solution which cannot run under an hypervisor.

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Transparency

- ① Higher privileged.
- ② No non-privileged side-effects.
- ③ Identical Basic Instruction Semantics.
- ④ Transparent Exception Handling.
- ⑤ Identical Measurement of Time.

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A Survey

Table: Hardware features.

Technique	PROS	CONS	Gaps
HVM	Ring -1	Hypervisor development	High overhead
SMM	Ring -2	BIOS development	High implementation cost
AMT	Ring -3	Chipset code change	No malware analysis solution
HPCs	Lightweight	Context-limited information	No malware analysis solution
GPU	Easy to program	No register data	No introspection procedures
TSX	Commit-based	Store only few KB	Overcome the KB barrier
SGX	Isolates goodwill	Also isolates malware	No enclave inspection
SOCs	Tamper-proof	Passive components	Raise alarms

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Branch Monitors

FROM	TO
0x0FFFF418	0x0FFF8F36
0x0FFF1510	0x0FFFCF2E
0x0FFF8014	0x0FFF0523
0x0FFF81b3	0x0FFFE057

Figure: Branch Stack.

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Proposed Framework

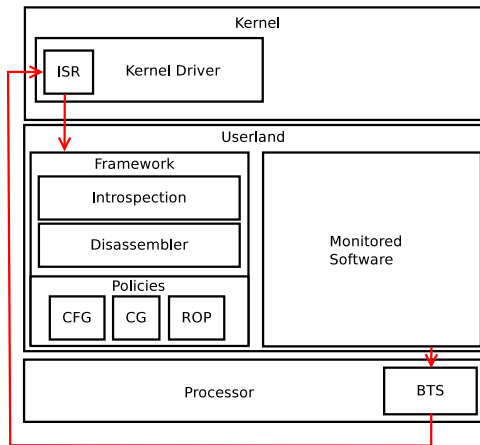


Figure: Proposed framework architecture.

Could I develop a performance-counter-based malware analyzer?

Could I isolate processes' actions?

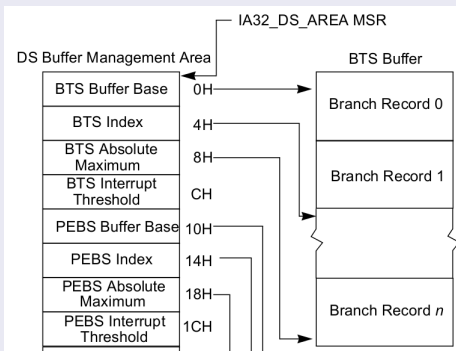


Figure: Data Storage (DS) AREA.

Could I develop a performance-counter-based malware analyzer?

Is CG reconstruction possible?

Table: ASLR - Library placement after two consecutive reboots.

Library	NTDLL	KERNEL32	KERNELBASE
Address 1	0xBAF80000	0xB9610000	0xB8190000
Address 2	0x987B0000	0x98670000	0x958C0000

Could I develop a performance-counter-based malware analyzer?

Is CG reconstruction possible?

Table: Function Offsets from ntdll.dll library.

Function	Offset
NtCreateProcess	0x3691
NtCreateProcessEx	0x30B0
NtCreateProfile	0x36A1
NtCreateResourceManager	0x36C1
NtCreateSemaphore	0x36D1
NtCreateSymbolicLinkObject	0x36E1
NtCreateThread	0x30C0
NtCreateThreadEx	0x36F1

Could I develop a performance-counter-based malware analyzer?

Is CG reconstruction possible?

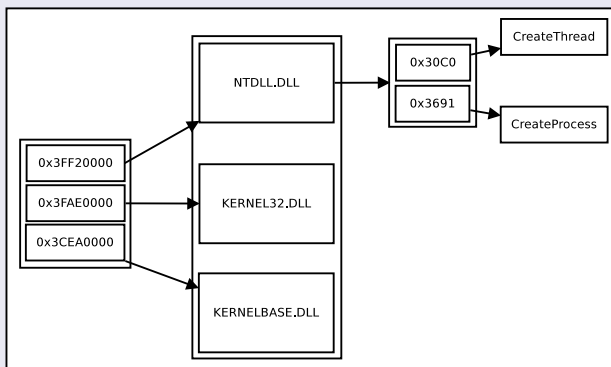


Figure: Introspection Mechanism.

Could I develop a performance-counter-based malware analyzer?

Is CG reconstruction possible?

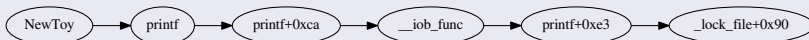


Figure: Step Into.



Figure: Step Over.

Could I retrieve all executed functions ?

Is CFG reconstruction possible?

FROM: SOMEWHERE
TO: 0x48ff5ab8



Block of Code A

Block of Code B

Block of Code C



8 bytes



FROM: 0x48ff5ac0
TO: SOMEWHERE

Figure: Code block identification.

Could I retrieve all executed functions ?

Is CFG reconstruction possible?

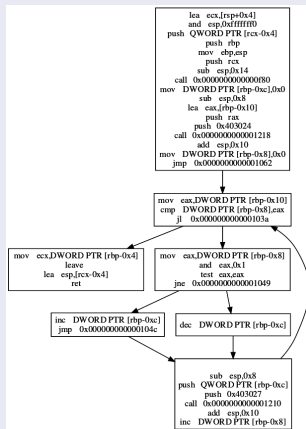


Figure: it is possible to reconstruct the whole execution flow.

Is the final solution transparent?

Deviating Behavior

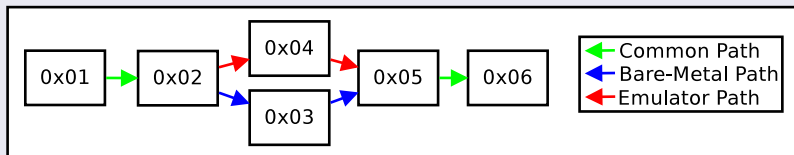


Figure: Deviating behavior identification.

Is the final solution transparent?

Deviating Behavior

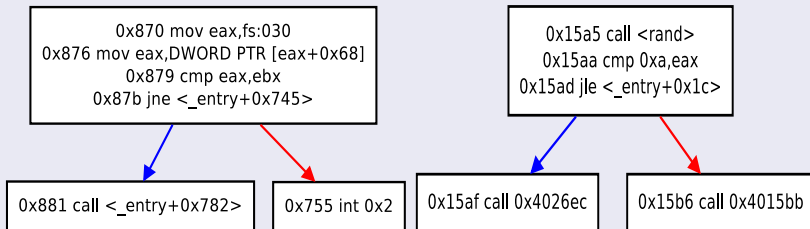


Figure: Divergence: True Positive.

Figure: Divergence: False Positive.

Could I develop a Debugger?

Inverted I/O

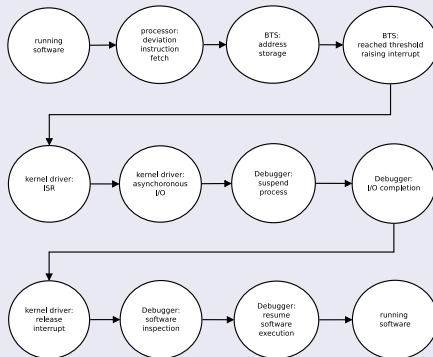
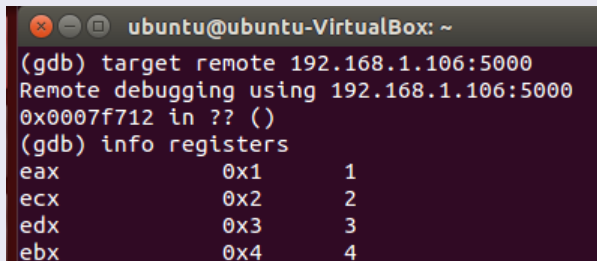


Figure: Debugger's working mechanism.

Could I develop a Debugger?

Integration

A screenshot of a terminal window titled 'ubuntu@ubuntu-VirtualBox: ~'. The terminal shows the following commands and output:

```
(gdb) target remote 192.168.1.106:5000
Remote debugging using 192.168.1.106:5000
0x0007f712 in ?? ()
(gdb) info registers
eax             0x1          1
ecx             0x2          2
edx             0x3          3
ebx             0x4          4
```

Figure: GDB integration.

Does the solution handle ROP attacks?

ROP Attacks

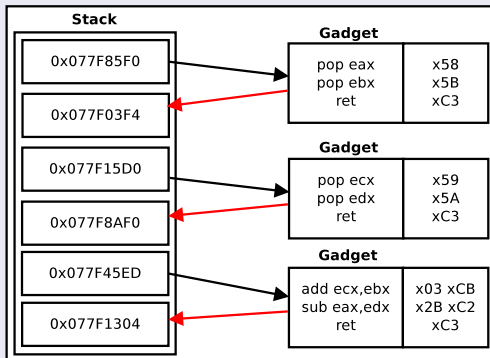


Figure: ROP chain example.

Does the solution handle ROP attacks?

CALL-RET Policy

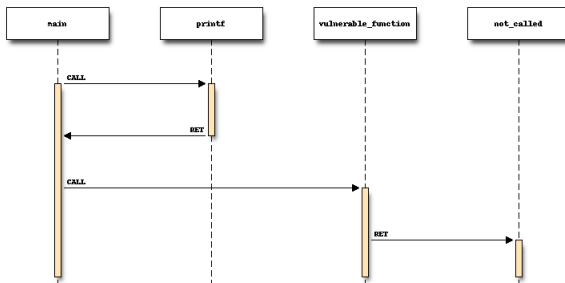


Figure: CALL-RET CFI policy.

Does the solution handle ROP attacks?

Exploit Analysis

Table: Excerpt of the branch window of the ROP payload.

FROM	TO
—	0x7c346c0a
0x7c346c0b	0x7c37a140
0x7c37a141	—

Does the solution handle ROP attacks?

Exploit Analysis

Listing 1: Static disassembly of the MSVCR71.dll library.

```
1 7c346c08: f2 0f 58 c3      addsd  %xmm3,%xmm0
2 7c346c0c: 66 0f 13 44 24 04 movlpd %xmm0,0x4(%esp)
```

Listing 2: Dynamic disassembly of the MSVC71.dll executed code.

```
1 0x1000 (size=1) pop    rax
2 0x1001 (size=1)  ret
```

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Lessons learned

- Transparency is essential.
- Hardware-assisted approaches may fulfill transparency requirements.
- There are open problems on hardware monitoring.
- Security, performance, and development efforts as trade-offs (really?).
- Performance monitors as lightweight alternatives.

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And now?

- Multi-core monitor.
- Linux Monitor.
- Malware clustering.

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Main Papers

- *Who watches the watchmen: A security-focused review on current state-of-the-art techniques, tools and methods for systems and binary analysis on modern platforms—ACM Computing Surveys (A1).*
- *Enhancing Branch Monitoring for Security Purposes: From Control Flow Integrity to Malware Analysis and Debugging—ACM Transactions on Privacy and Security (A2).*
- *The other guys: automated analysis of marginalized malware—Journal of Computer Virology and Hacking techniques (B1).*

Solution's Availability

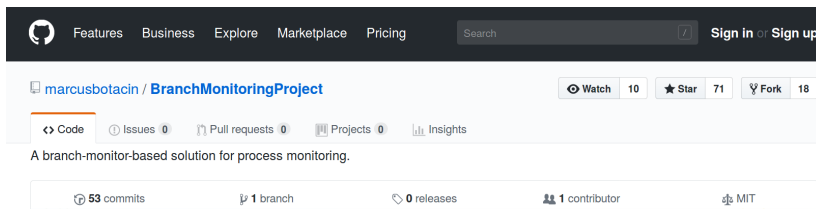


Figure: Solution's Availability. Solution is public on github.

<https://github.com/marcusbotacin/BranchMonitoringProject>

Questions ?

Contact

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