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**Class:** TE IT

**Batch:** B

## **Experiment No.: 1**

**Code:**

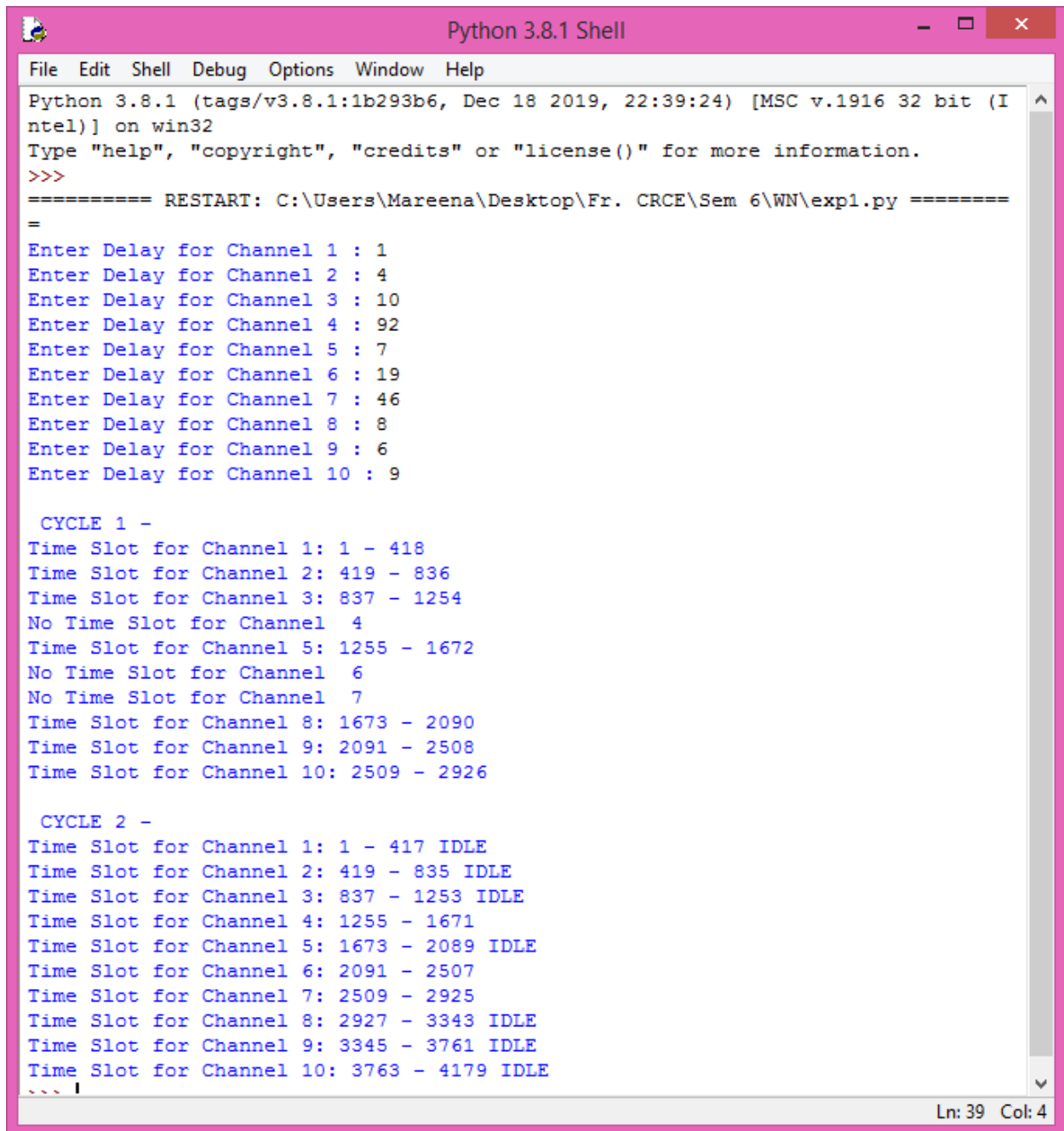
```
delays = []
prevTimeSlotEnd = 0

for i in range(0, 10):
    delays.append(int(input("Enter Delay for Channel " + str(i+1) + " : ")))

print("\n CYCLE 1 - ")
for i, delay in enumerate(delays):
    if delay > 10:
        print("No Time Slot for Channel ", i+1)
    else:
        print("Time Slot for Channel " + str(i+1) + ": " + str( prevTimeSlotEnd+1) + " - " + str(
prevTimeSlotEnd+418 ))
        prevTimeSlotEnd += 418

prevTimeSlotEnd = 0
print("\n CYCLE 2 - ")
for i, delay in enumerate(delays):
    if delay > 10:
        print("Time Slot for Channel " + str(i+1) + ": " + str( prevTimeSlotEnd+1) + " - " + str(
prevTimeSlotEnd+417 ))
        prevTimeSlotEnd += 418
    else:
        print("Time Slot for Channel " + str(i+1) + ": " + str( prevTimeSlotEnd+1) + " - " + str(
prevTimeSlotEnd+417 ) + " IDLE ")
        prevTimeSlotEnd += 418
```

## Output:



```
Python 3.8.1 (tags/v3.8.1:1b293b6, Dec 18 2019, 22:39:24) [MSC v.1916 32 bit (Intel)] on win32
Type "help", "copyright", "credits" or "license()" for more information.
>>>
===== RESTART: C:\Users\Mareena\Desktop\Fr. CRCE\Sem 6\WN\expl.py =====
=
Enter Delay for Channel 1 : 1
Enter Delay for Channel 2 : 4
Enter Delay for Channel 3 : 10
Enter Delay for Channel 4 : 92
Enter Delay for Channel 5 : 7
Enter Delay for Channel 6 : 19
Enter Delay for Channel 7 : 46
Enter Delay for Channel 8 : 8
Enter Delay for Channel 9 : 6
Enter Delay for Channel 10 : 9

CYCLE 1 -
Time Slot for Channel 1: 1 - 418
Time Slot for Channel 2: 419 - 836
Time Slot for Channel 3: 837 - 1254
No Time Slot for Channel 4
Time Slot for Channel 5: 1255 - 1672
No Time Slot for Channel 6
No Time Slot for Channel 7
Time Slot for Channel 8: 1673 - 2090
Time Slot for Channel 9: 2091 - 2508
Time Slot for Channel 10: 2509 - 2926

CYCLE 2 -
Time Slot for Channel 1: 1 - 417 IDLE
Time Slot for Channel 2: 419 - 835 IDLE
Time Slot for Channel 3: 837 - 1253 IDLE
Time Slot for Channel 4: 1255 - 1671
Time Slot for Channel 5: 1673 - 2089 IDLE
Time Slot for Channel 6: 2091 - 2507
Time Slot for Channel 7: 2509 - 2925
Time Slot for Channel 8: 2927 - 3343 IDLE
Time Slot for Channel 9: 3345 - 3761 IDLE
Time Slot for Channel 10: 3763 - 4179 IDLE
...
Ln: 39 Col: 4
```

**Post labs:**

Q1. TDMA is a multiple access technique that has

- a. Different users in different time slots
- b. Each user is assigned unique frequency slots
- c. Each user is assigned a unique code sequence
- d. Each signal is modulated with frequency modulation technique

**Answer: a. Different users in different time slots**

Q2. In TDMA, the user occupies the whole bandwidth during transmission.

- a. True
- b. False

**Answer: a. True**

Q3. TDMA allows the user to have

- a. Use of same frequency channel for same time slot
- b. Use of same frequency channel for different time slot
- c. Use of same time slot for different frequency channel
- d. Use of different time slot for different frequency channels

**Answer: a. Use of same frequency channel for different time slot**