

HR891156C

Electrical Specification @25°C

RoHS Compliant (PB Free)

Isolation: 1500Vrms (UTP Side to Chip Side)

OCL: 350uH Minimum @100KHz 100mV with 8mADC

Turns Ratio: (P1-P2) : (J1-J2) = 1CT:1CT $\pm 2\%$

Turns Ratio: (P4-P5) : (J3-J6) = 1CT:1CT $\pm 2\%$

Insertion Loss: -1.0dB Maximum @1MHz ~ 100MHz

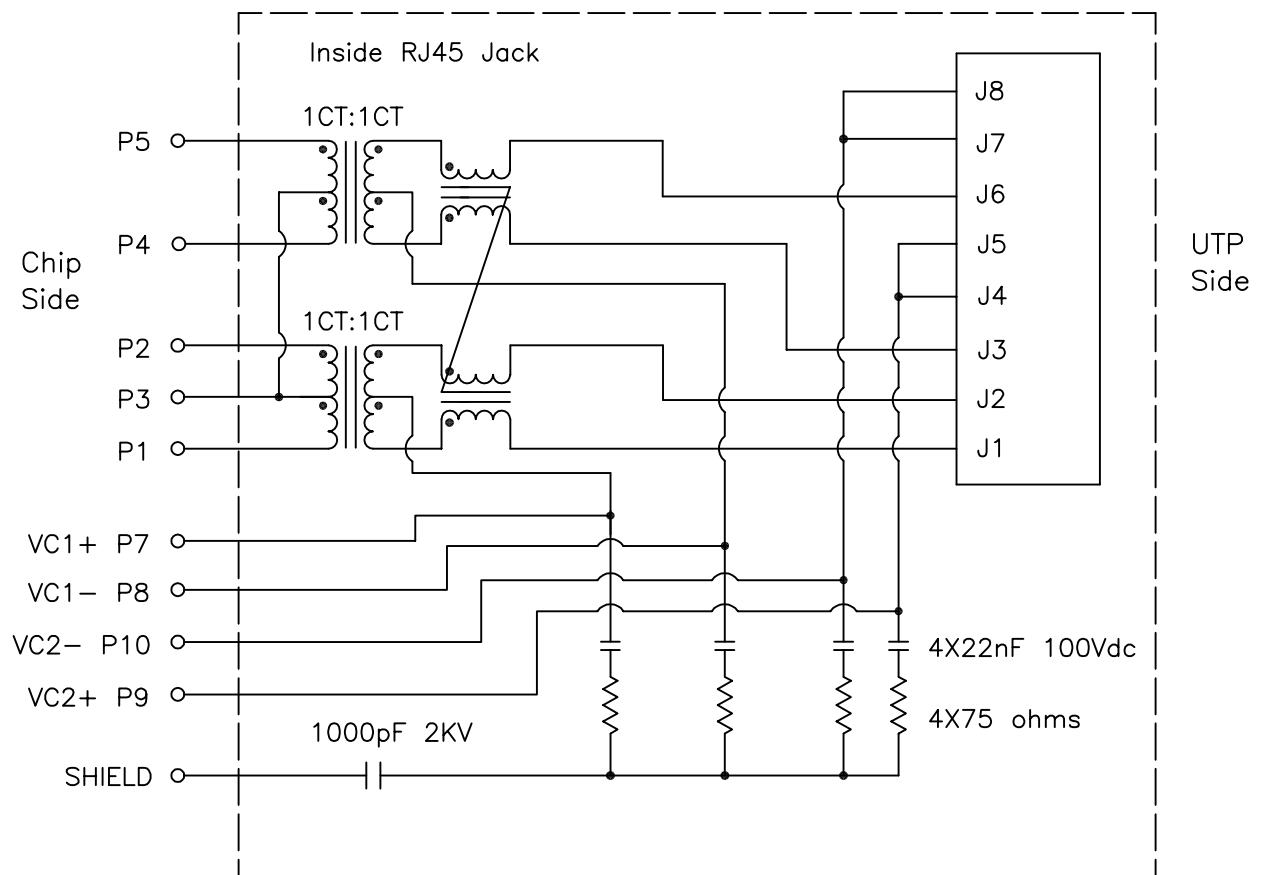
Return Loss: -18dB Minimum @1MHz ~ 30MHz / -16dB Minimum @30MHz ~ 45MHz

-14dB Minimum @45MHz ~ 60MHz / -12dB Minimum @60MHz ~ 80MHz

Common Mode Rejection: -30dB Minimum @1MHz ~ 100MHz

Crosstalk: -30dB Minimum @1MHz ~ 100MHz

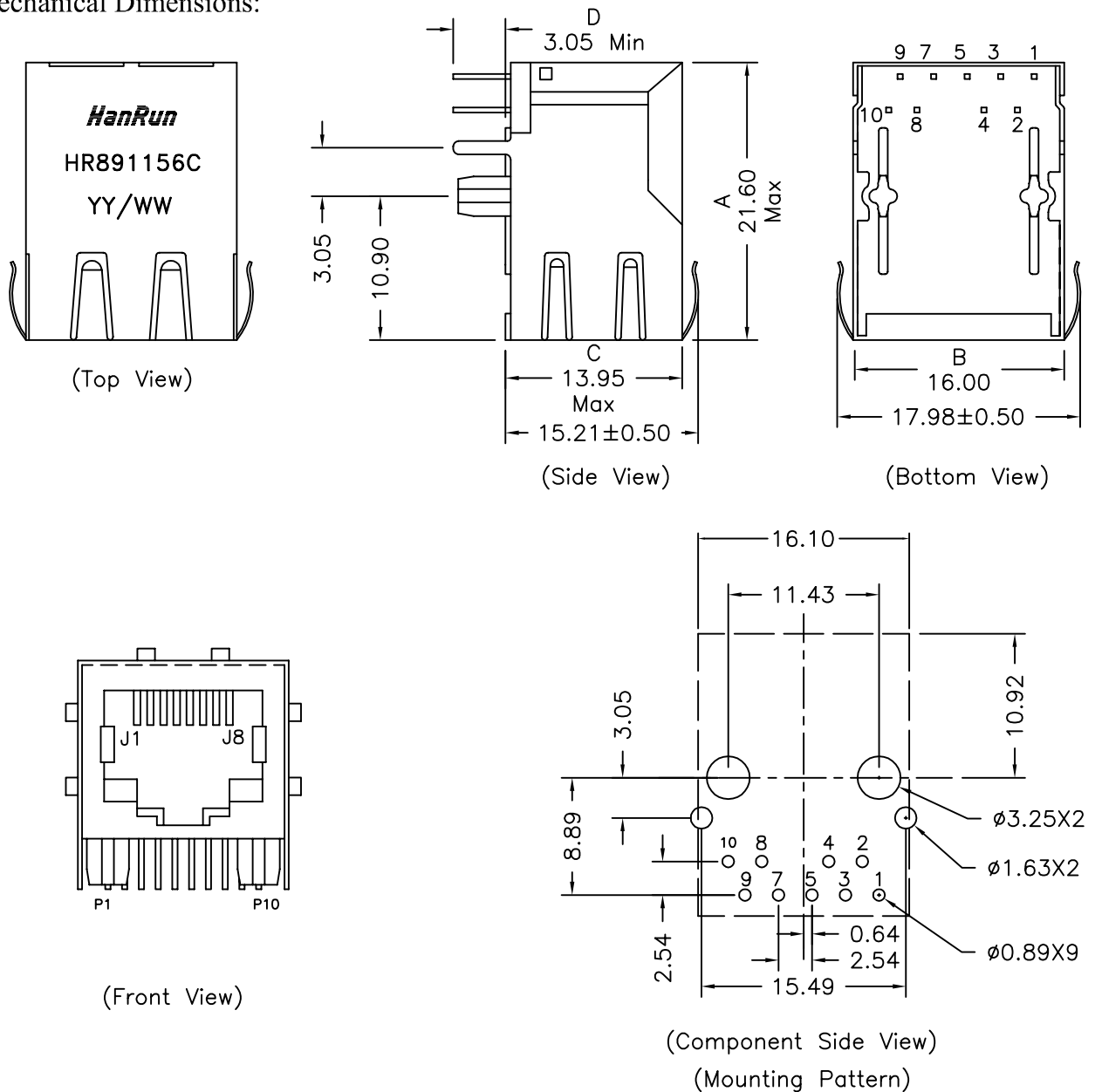
Schematics



HR891156C

- RoHS Compliant (PB Free)
- Meets or Exceeds IEEE802.3af standards including 350uH Min OCL with 8mADC
- High performance for EMI suppression, Crosstalk, Return Loss and Consistent Electrical
- Minimum 1500Vrms isolation per IEEE802.3 requirement
- Minimize PCB space and Simplify PCB Layout
- Less magnetic components to be placed on PCB, higher reliability and yields

Mechanical Dimensions:



Dimensions in mm

Unless otherwise specified, Tolerance: .xx ±0.25

REV.A

Remark:
1.Remove Pin#6

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