

Q3 Results for Mualla Argin

Score for this attempt: **5.5** out of 6

Submitted Jan 24 at 9:31pm

This attempt took 12 minutes.

Question 1

3 / 3 pts

1. After handling a fault successfully, the CPU always goes (when it does go back) to the instruction immediately after the faulting one False
2. Interrupts are traditionally implemented to handle asynchronous events True
3. Memory limit protection (within a private address space using base and bound) is implemented in the hardware instead of software True
4. Machine Check Error is an example of a fault False
5. Trap is a type of synchronous exception True
6. Faults are unintentional but possibly recoverable True

Answer 1:

Correct!

False

Answer 2:

Correct!

True

Answer 3:

Correct!

True

Answer 4:

Correct!

False

Answer 5:

Correct!

True

Answer 6:**Correct!**

True

Question 2

2.5 / 3 pts

Match the following operations to their correct privilege association (User is non-privileged whereas OS Kernel is privileged)

You Answered

Setting 0 to a large chunk of memory

Privileged



Non-Privileged

Correct!**Modifying the page table entries**

Privileged

**Correct!****Issuing the "trap" instruction**

Non-Privileged

**Correct!****Directly accessing I/O devices**

Privileged

**Correct!****Handling an Interrupt**

Privileged

**Correct!****Divide by zero**

Non-Privileged



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