

Q2 Results for Mualla Argin

Score for this attempt: **5** out of 5

Submitted Jan 22 at 7:52am

This attempt took 5 minutes.

Question 1

1 / 1 pts

Hardware Architecture Support for the OS is generally provided for the sake of

- ☐ Safety
- ☐ Efficiency
- ☐ Convenience
- ☒ All of the above

Correct!

Question 2

1 / 1 pts

Most operating systems handle asynchronous events using interrupts.

- ☒ True
- ☐ False

Correct!

Interrupt handling is simple and efficient because the CPU cycles are not 'wasted' running code to look for asynchronous events. Instead, an asynchronous event triggers the CPU to address it after it finishes its current instruction.

Question 3

1 / 1 pts

The timer feature may be used to implement hardware protection mechanism in operating systems

Correct!☒ True☐ False

Using the timer expiration, the OS can relinquish control from a job (process) that may be in an infinite loop or deadlocked.

Question 4

1 / 1 pts

Dual Mode Operation supported in computer architecture is used to restrict certain instructions to be available only to privileged software like an operating system.

Correct!☒ True☐ False

In dual mode operation (user, supervisor), certain CPU instructions are only permitted to be executed by the operating system so as to maintain hardware protection.

Question 5**1 / 1 pts**

The memory management unit in the CPU is a classic example of implementing support for address spaces for multiple jobs (processes) in the memory.

Correct!☒ True☐ False

Using the memory management hardware feature with checks for base and limit exceptions, the OS is able to implement support for multiple address spaces (multiple processes).

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